

Jinja with Threads

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Abstract. We extend the Jinja source code semantics by Klein and Nipkow with Java-style arrays and threads. Concurrency is captured in a generic framework semantics for adding concurrency through interleaving to a sequential semantics, which features dynamic thread creation, inter-thread communication via shared memory, lock synchronisation and joins. Also, threads can suspend themselves and be notified by others. We instantiate the framework with the adapted versions of both Jinja source and byte code and show type safety for the multithreaded case. Equally, the compiler from source to byte code is extended, for which we prove weak bisimilarity between the source code small step semantics and the defensive Jinja virtual machine. On top of this, we formalise the JMM and show the DRF guarantee and consistency.

For description of the different parts, see [1, 2, 4, 3].

Contents

1	The generic multithreaded semantics	7
1.1	State of the multithreaded semantics	7
1.2	All about a managing a single lock	17
1.3	Semantics of the thread actions for locking	23
1.4	Semantics of the thread actions for thread creation	27
1.5	Semantics of the thread actions for wait, notify and interrupt	32
1.6	Semantics of the thread actions for purely conditional purpose such as Join	36
1.7	Wellformedness conditions for the multithreaded state	38
1.8	Semantics of the thread action ReleaseAcquire for the thread state	42
1.9	Semantics of the thread actions for interruption	43
1.10	The multithreaded semantics	47
1.11	Auxiliary definitions for the progress theorem for the multithreaded semantics	55
1.12	Deadlock formalisation	61
1.13	Progress theorem for the multithreaded semantics	80
1.14	Lifting of thread-local properties to the multithreaded case	84
1.15	Labelled transition systems	89
1.16	The multithreaded semantics as a labelled transition system	116
1.17	Various notions of bisimulation	127
1.18	Bisimulation relations for the multithreaded semantics	152
1.19	Preservation of deadlock across bisimulations	186
1.20	Semantic properties of lifted predicates	201
1.21	Synthetic first and last actions for each thread	205
2	Data Flow Analysis Framework	223
2.1	Semilattices	223
2.2	The Error Type	227
2.3	More about Options	229
2.4	Products as Semilattices	230
2.5	Fixed Length Lists	231
2.6	Typing and Dataflow Analysis Framework	233
2.7	More on Semilattices	233
2.8	Lifting the Typing Framework to err, app, and eff	235
2.9	Kildall's Algorithm	236
2.10	The Lightweight Bytecode Verifier	240
2.11	Correctness of the LBV	244
2.12	Completeness of the LBV	245

3	Concepts for all JinjaThreads Languages	247
3.1	JinjaThreads types	247
3.2	Class Declarations and Programs	251
3.3	Relations between Jinja Types	253
3.4	Jinja Values	264
3.5	Exceptions	266
3.6	System Classes	270
3.7	An abstract heap model	271
3.8	Observable events in JinjaThreads	276
3.9	The initial configuration	278
3.10	Conformance Relations for Type Soundness Proofs	285
3.11	Semantics of method calls that cannot be defined inside JinjaThreads	291
3.12	Generic Well-formedness of programs	303
3.13	Properties of external calls in well-formed programs	314
3.14	Conformance for threads	330
3.15	Binary Operators	331
3.16	The Jinja Type System as a Semilattice	342
4	JinjaThreads source language	355
4.1	Program State	355
4.2	Expressions	355
4.3	Abstract heap locales for source code programs	364
4.4	Small Step Semantics	366
4.5	Weak well-formedness of Jinja programs	381
4.6	Well-typedness of Jinja expressions	381
4.7	Definite assignment	389
4.8	Well-formedness Constraints	392
4.9	The source language as an instance of the framework	393
4.10	Runtime Well-typedness	408
4.11	Progress of Small Step Semantics	413
4.12	Preservation of definite assignment	433
4.13	Type Safety Proof	434
4.14	Progress and type safety theorem for the multithreaded system	451
4.15	Preservation of Deadlock	464
4.16	Program annotation	472
5	Jinja Virtual Machine	481
5.1	State of the JVM	481
5.2	Instructions of the JVM	482
5.3	Abstract heap locales for byte code programs	484
5.4	JVM Instruction Semantics	487
5.5	Exception handling in the JVM	491
5.6	Program Execution in the JVM	491
5.7	A Defensive JVM	493
5.8	Instantiating the framework semantics with the JVM	498

6	Bytecode verifier	505
6.1	The JVM Type System as Semilattice	505
6.2	Effect of Instructions on the State Type	509
6.3	The Bytecode Verifier	521
6.4	BV Type Safety Invariant	522
6.5	BV Type Safety Proof	526
6.6	Welltyped Programs produce no Type Errors	560
6.7	Progress result for both of the multithreaded JVMs	560
6.8	Preservation of deadlock for the JVMs	581
6.9	Monotonicity of eff and app	587
6.10	The Typing Framework for the JVM	593
6.11	LBV for the JVM	595
6.12	Kildall for the JVM	597
6.13	Code generation for the byte code verifier	598
7	Compilation	601
7.1	Method calls in expressions	601
7.2	The JinjaThreads source language with explicit call stacks	604
7.3	Bisimulation proof for between source code small step semantics with and without callstacks for single threads	624
7.4	The intermediate language J1	641
7.5	Abstract heap locales for J1 programs	647
7.6	Semantics of the intermediate language	649
7.7	Deadlock perservation for the intermediate language	674
7.8	Program Compilation	678
7.9	Compilation Stage 2	685
7.10	Various Operations for Exception Tables	689
7.11	Type rules for the intermediate language	694
7.12	Well-Formedness of Intermediate Language	699
7.13	Preservation of Well-Typedness in Stage 2	700
7.14	Unobservable steps for the JVM	728
7.15	JVM Semantics for the delay bisimulation proof from intermediate language to byte code	739
7.16	The delay bisimulation between intermediate language and JVM	783
7.17	Correctness of Stage: From intermediate language to JVM	898
7.18	Correctness of Stage 2: From JVM to intermediate language	1011
7.19	Correctness of Stage 2: The multithreaded setting	1099
7.20	Indexing variables in variable lists	1109
7.21	Compilation Stage 1	1112
7.22	The bisimulation relation between source and intermediate language	1118
7.23	Unlocking a sync block never fails	1134
7.24	Semantic Correctness of Stage 1	1149
7.25	Preservation of well-formedness from source code to intermediate language	1197
7.26	Correctness of both stages	1206

8	Memory Models	1217
8.1	Sequential consistency	1218
8.2	Sequential consistency with efficient data structures	1228
8.3	Orders as predicates	1239
8.4	Axiomatic specification of the JMM	1250
8.5	The data race free guarantee of the JMM	1289
8.6	Sequentially consistent executions are legal	1296
8.7	Non-speculative prefixes of executions	1304
8.8	Sequentially consistent completion of executions in the JMM	1313
8.9	Happens-before consistent completion of executions in the JMM	1339
8.10	Locales for heap operations with set of allocated addresses	1353
8.11	Combination of locales for heap operations and interleaving	1358
8.12	Type-safety proof for the Java memory model	1426
8.13	JMM Instantiation with Jinja – common parts	1444
8.14	JMM Instantiation for J	1465
8.15	JMM Instantiation for bytecode	1487
8.16	JMM heap implementation 1	1509
8.17	Compiler correctness for the JMM	1516
8.18	JMM heap implementation 2	1522
8.19	Specialize type safety for JMM heap implementation 2	1531
8.20	JMM type safety for source code	1537
8.21	JMM type safety for bytecode	1547
8.22	Compiler correctness for JMM heap implementation 2	1557
9	Schedulers	1561
9.1	Refinement for multithreaded states	1561
9.2	Abstract scheduler	1562
9.3	Random scheduler	1586
9.4	Round robin scheduler	1590
9.5	Tabulation for lookup functions	1609
9.6	Executable semantics for J	1620
9.7	Executable semantics for the JVM	1622
9.8	An optimized JVM	1625
9.9	Code generator setup	1644
9.10	String representation of types	1654
9.11	Setup for converter Java2Jinja	1660
10	Examples	1663
10.1	Apprentice challenge	1663
10.2	Buffer example	1665

Chapter 1

The generic multithreaded semantics

1.1 State of the multithreaded semantics

```
theory FWState
imports
  ../Basic/Auxiliary
begin

datatype lock-action =
  Lock
  | Unlock
  | UnlockFail
  | ReleaseAcquire

datatype ('t,'x,'m) new-thread-action =
  NewThread 't 'x 'm
  | ThreadExists 't bool

datatype 't conditional-action =
  Join 't
  | Yield

datatype ('t, 'w) wait-set-action =
  Suspend 'w
  | Notify 'w
  | NotifyAll 'w
  | WakeUp 't
  | Notified
  | WokenUp

datatype 't interrupt-action
  = IsInterrupted 't bool
  | Interrupt 't
  | ClearInterrupt 't

type-synonym 'l lock-actions = 'l =>f lock-action list
```

translations

$(type) \ 'l \ lock\ actions \ \leq \ (type) \ 'l \ \Rightarrow f \ lock\ action \ list$

type-synonym

$(l, 't, 'x, 'm, 'w, 'o) \ thread\ action =$
 $\ 'l \ lock\ actions \ \times \ ('t, 'x, 'm) \ new\ thread\ action \ list \ \times$
 $\ 't \ conditional\ action \ list \ \times \ ('t, 'w) \ wait\ set\ action \ list \ \times$
 $\ 't \ interrupt\ action \ list \ \times \ 'o \ list$

print-translation <

```

let
  fun tr'
    [Const (@{type-syntax finfun}, -) $ l $
      (Const (@{type-syntax list}, -) $ Const (@{type-syntax lock-action}, -)),
      Const (@{type-syntax prod}, -) $
      (Const (@{type-syntax list}, -) $ (Const (@{type-syntax new-thread-action}, -) $ t1 $ x $ m))
    ] $
      (Const (@{type-syntax prod}, -) $
        (Const (@{type-syntax list}, -) $ (Const (@{type-syntax conditional-action}, -) $ t2)) $
        (Const (@{type-syntax prod}, -) $
          (Const (@{type-syntax list}, -) $ (Const (@{type-syntax wait-set-action}, -) $ t3 $ w)) $
          (Const (@{type-syntax prod}, -) $
            (Const (@{type-syntax list}, -) $ (Const (@{type-syntax interrupt-action}, -) $ t4)) $
            (Const (@{type-syntax list}, -) $ o1)))) =
        if t1 = t2 andalso t2 = t3 andalso t3 = t4 then Syntax.const @ {type-syntax thread-action} $ l $
        t1 $ x $ m $ w $ o1
      else raise Match;
    in [(@{type-syntax prod}, K tr')]
  end
  >
typ (l, 't, 'x, 'm, 'w, 'o) thread-action

```

definition $locks\ a :: (l, 't, 'x, 'm, 'w, 'o) \ thread\ action \ \Rightarrow \ 'l \ lock\ actions \ (\langle \! \! \! \{ \} \rangle [0] \ 1000) \ \mathbf{where}$
 $locks\ a \equiv fst$

definition $thr\ a :: (l, 't, 'x, 'm, 'w, 'o) \ thread\ action \ \Rightarrow \ ('t, 'x, 'm) \ new\ thread\ action \ list \ (\langle \! \! \! \{ \} \rangle [0] \ 1000) \ \mathbf{where}$
 $thr\ a \equiv fst \ o \ snd$

definition $cond\ a :: (l, 't, 'x, 'm, 'w, 'o) \ thread\ action \ \Rightarrow \ 't \ conditional\ action \ list \ (\langle \! \! \! \{ \} \rangle [0] \ 1000) \ \mathbf{where}$
 $cond\ a = fst \ o \ snd \ o \ snd$

definition $wset\ a :: (l, 't, 'x, 'm, 'w, 'o) \ thread\ action \ \Rightarrow \ ('t, 'w) \ wait\ set\ action \ list \ (\langle \! \! \! \{ \} \rangle [0] \ 1000) \ \mathbf{where}$
 $wset\ a = fst \ o \ snd \ o \ snd \ o \ snd$

definition $interrupt\ a :: (l, 't, 'x, 'm, 'w, 'o) \ thread\ action \ \Rightarrow \ 't \ interrupt\ action \ list \ (\langle \! \! \! \{ \} \rangle [0] \ 1000) \ \mathbf{where}$
 $interrupt\ a = fst \ o \ snd \ o \ snd \ o \ snd \ o \ snd$

definition $obs\ a :: (l, 't, 'x, 'm, 'w, 'o) \ thread\ action \ \Rightarrow \ 'o \ list \ (\langle \! \! \! \{ \} \rangle [0] \ 1000) \ \mathbf{where}$
 $obs\ a \equiv snd \ o \ snd \ o \ snd \ o \ snd \ o \ snd$

lemma *locks-a-conv* [simp]: *locks-a* (*ls*, *ntsjswss*) = *ls*
by(*simp add:locks-a-def*)

lemma *thr-a-conv* [simp]: *thr-a* (*ls*, *nts*, *jswss*) = *nts*
by(*simp add:thr-a-def*)

lemma *cond-a-conv* [simp]: *cond-a* (*ls*, *nts*, *js*, *wss*) = *js*
by(*simp add:cond-a-def*)

lemma *wset-a-conv* [simp]: *wset-a* (*ls*, *nts*, *js*, *wss*, *isobs*) = *wss*
by(*simp add:wset-a-def*)

lemma *interrupt-a-conv* [simp]: *interrupt-a* (*ls*, *nts*, *js*, *ws*, *is*, *obs*) = *is*
by(*simp add:interrupt-a-def*)

lemma *obs-a-conv* [simp]: *obs-a* (*ls*, *nts*, *js*, *wss*, *is*, *obs*) = *obs*
by(*simp add:obs-a-def*)

fun *ta-update-locks* :: ('l,'t,'x,'m,'w,'o) *thread-action* ⇒ *lock-action* ⇒ 'l ⇒ ('l,'t,'x,'m,'w,'o) *thread-action*
where
ta-update-locks (*ls*, *nts*, *js*, *wss*, *obs*) *lta* *l* = (*ls*(*l* \$:= *ls* \$ *l* @ [*lta*]), *nts*, *js*, *wss*, *obs*)

fun *ta-update-NewThread* :: ('l,'t,'x,'m,'w,'o) *thread-action* ⇒ ('t,'x,'m) *new-thread-action* ⇒ ('l,'t,'x,'m,'w,'o) *thread-action* **where**
ta-update-NewThread (*ls*, *nts*, *js*, *wss*, *is*, *obs*) *nt* = (*ls*, *nts* @ [*nt*], *js*, *wss*, *is*, *obs*)

fun *ta-update-Conditional* :: ('l,'t,'x,'m,'w,'o) *thread-action* ⇒ 't *conditional-action* ⇒ ('l,'t,'x,'m,'w,'o) *thread-action*
where
ta-update-Conditional (*ls*, *nts*, *js*, *wss*, *is*, *obs*) *j* = (*ls*, *nts*, *js* @ [*j*], *wss*, *is*, *obs*)

fun *ta-update-wait-set* :: ('l,'t,'x,'m,'w,'o) *thread-action* ⇒ ('t, 'w) *wait-set-action* ⇒ ('l,'t,'x,'m,'w,'o) *thread-action* **where**
ta-update-wait-set (*ls*, *nts*, *js*, *wss*, *is*, *obs*) *ws* = (*ls*, *nts*, *js*, *wss* @ [*ws*], *is*, *obs*)

fun *ta-update-interrupt* :: ('l,'t,'x,'m,'w,'o) *thread-action* ⇒ 't *interrupt-action* ⇒ ('l,'t,'x,'m,'w,'o) *thread-action*
where
ta-update-interrupt (*ls*, *nts*, *js*, *wss*, *is*, *obs*) *i* = (*ls*, *nts*, *js*, *wss*, *is* @ [*i*], *obs*)

fun *ta-update-obs* :: ('l,'t,'x,'m,'w,'o) *thread-action* ⇒ 'o ⇒ ('l,'t,'x,'m,'w,'o) *thread-action*
where
ta-update-obs (*ls*, *nts*, *js*, *wss*, *is*, *obs*) *ob* = (*ls*, *nts*, *js*, *wss*, *is*, *obs* @ [*ob*])

abbreviation *empty-ta* :: ('l,'t,'x,'m,'w,'o) *thread-action* **where**
empty-ta ≡ (*K*\$ [], [], [], [], [], [])

notation (*input*) *empty-ta* (<ε>)

Pretty syntax for specifying thread actions: Write $\{ Lock \rightarrow l, Unlock \rightarrow l, Suspend w, Interrupt t \}$ instead of $((K\$ []) (l \$:= [Lock, Unlock]), [], [Suspend w], [Interrupt t], [])$.

thread-action' is a type that contains of all basic thread actions. Automatically coerce basic thread actions into that type and then dispatch to the right update function by pattern matching. For coercion, adhoc overloading replaces the generic injection *inject-thread-action*

by the specific ones, i.e. constructors. To avoid ambiguities with observable actions, the observable actions must be of sort *obs-action*, which the basic thread action types are not.

class *obs-action*

datatype (*l,t,x,m,w,o*) *thread-action'*
 = *LockAction* *lock-action* × *l*
 | *NewThreadAction* (*t,x,m*) *new-thread-action*
 | *ConditionalAction* *t* *conditional-action*
 | *WaitSetAction* (*t,w*) *wait-set-action*
 | *InterruptAction* *t* *interrupt-action*
 | *ObsAction* *o*

setup <

Sign.add-const-constraint (@{*const-name ObsAction*}, *SOME* @{*typ 'o* :: *obs-action* ⇒ (*l,t,x,m,w,o*) *thread-action'*}

>

fun *thread-action'-to-thread-action* ::

(*l,t,x,m,w,o* :: *obs-action*) *thread-action'* ⇒ (*l,t,x,m,w,o*) *thread-action* ⇒ (*l,t,x,m,w,o*) *thread-action*

where

thread-action'-to-thread-action (*LockAction* (*la,l*)) *ta* = *ta-update-locks* *ta* *la l*
 | *thread-action'-to-thread-action* (*NewThreadAction* *nt*) *ta* = *ta-update-NewThread* *ta* *nt*
 | *thread-action'-to-thread-action* (*ConditionalAction* *ca*) *ta* = *ta-update-Conditional* *ta* *ca*
 | *thread-action'-to-thread-action* (*WaitSetAction* *wa*) *ta* = *ta-update-wait-set* *ta* *wa*
 | *thread-action'-to-thread-action* (*InterruptAction* *ia*) *ta* = *ta-update-interrupt* *ta* *ia*
 | *thread-action'-to-thread-action* (*ObsAction* *ob*) *ta* = *ta-update-obs* *ta* *ob*

consts *inject-thread-action* :: *a* ⇒ (*l,t,x,m,w,o*) *thread-action'*

nonterminal *ta-let* and *ta-lets*

syntax

-ta-snoc :: *ta-lets* ⇒ *ta-let* ⇒ *ta-lets* (⟨-,/ -⟩)
-ta-block :: *ta-lets* ⇒ *a* (⟨{-}⟩ [0] 1000)
-ta-empty :: *ta-lets* (⟨⟩)
-ta-single :: *ta-let* ⇒ *ta-lets* (⟨-⟩)
-ta-inject :: *logic* ⇒ *ta-let* (⟨(-)⟩)
-ta-lock :: *logic* ⇒ *logic* ⇒ *ta-let* (⟨-→-⟩)

translations

-ta-block -ta-empty == *CONST empty-ta*
-ta-block (-ta-single bta) == *-ta-block (-ta-snoc -ta-empty bta)*
-ta-inject bta == *CONST inject-thread-action bta*
-ta-lock la l == *CONST inject-thread-action (CONST Pair la l)*
-ta-block (-ta-snoc btas bta) == *CONST thread-action'-to-thread-action bta (-ta-block btas)*

ad hoc-overloading

inject-thread-action ⇐ *NewThreadAction ConditionalAction WaitSetAction InterruptAction ObsAction LockAction*

lemma *ta-upd-proj-simps* [*simp*]:

shows *ta-obs-proj-simps*:

$\{\{ta\}\}_l \{\{ta\}\}_l \{\{ta\}\}_t \{\{ta\}\}_t \{\{ta\}\}_w = \{\{ta\}\}_w$

$\{\{ta\text{-update-obs } ta \text{ obs}\}_c = \{\{ta\}_c \{\{ta\text{-update-obs } ta \text{ obs}\}_i = \{\{ta\}_i \{\{ta\text{-update-obs } ta \text{ obs}\}_o = \{\{ta\}_o @ [obs]$

and *ta-lock-proj-simps*:

$\{\{ta\text{-update-locks } ta \ x \ l\}_l = (let \ ls = \{\{ta\}_l \text{ in } ls(l \ \$:= \ ls \ \$ \ l @ [x]))$

$\{\{ta\text{-update-locks } ta \ x \ l\}_t = \{\{ta\}_t \{\{ta\text{-update-locks } ta \ x \ l\}_w = \{\{ta\}_w \{\{ta\text{-update-locks } ta \ x \ l\}_c = \{\{ta\}_c$

$\{\{ta\text{-update-locks } ta \ x \ l\}_i = \{\{ta\}_i \{\{ta\text{-update-locks } ta \ x \ l\}_o = \{\{ta\}_o$

and *ta-thread-proj-simps*:

$\{\{ta\text{-update-NewThread } ta \ t\}_l = \{\{ta\}_l \{\{ta\text{-update-NewThread } ta \ t\}_t = \{\{ta\}_t @ [t] \{\{ta\text{-update-NewThread } ta \ t\}_w = \{\{ta\}_w$

$\{\{ta\text{-update-NewThread } ta \ t\}_c = \{\{ta\}_c \{\{ta\text{-update-NewThread } ta \ t\}_i = \{\{ta\}_i \{\{ta\text{-update-NewThread } ta \ t\}_o = \{\{ta\}_o$

and *ta-wset-proj-simps*:

$\{\{ta\text{-update-wait-set } ta \ w\}_l = \{\{ta\}_l \{\{ta\text{-update-wait-set } ta \ w\}_t = \{\{ta\}_t \{\{ta\text{-update-wait-set } ta \ w\}_w = \{\{ta\}_w @ [w]$

$\{\{ta\text{-update-wait-set } ta \ w\}_c = \{\{ta\}_c \{\{ta\text{-update-wait-set } ta \ w\}_i = \{\{ta\}_i \{\{ta\text{-update-wait-set } ta \ w\}_o = \{\{ta\}_o$

and *ta-cond-proj-simps*:

$\{\{ta\text{-update-Conditional } ta \ c\}_l = \{\{ta\}_l \{\{ta\text{-update-Conditional } ta \ c\}_t = \{\{ta\}_t \{\{ta\text{-update-Conditional } ta \ c\}_w = \{\{ta\}_w$

$\{\{ta\text{-update-Conditional } ta \ c\}_c = \{\{ta\}_c @ [c] \{\{ta\text{-update-Conditional } ta \ c\}_i = \{\{ta\}_i \{\{ta\text{-update-Conditional } ta \ c\}_o = \{\{ta\}_o$

and *ta-interrupt-proj-simps*:

$\{\{ta\text{-update-interrupt } ta \ i\}_l = \{\{ta\}_l \{\{ta\text{-update-interrupt } ta \ i\}_t = \{\{ta\}_t \{\{ta\text{-update-interrupt } ta \ i\}_c = \{\{ta\}_c$

$\{\{ta\text{-update-interrupt } ta \ i\}_w = \{\{ta\}_w \{\{ta\text{-update-interrupt } ta \ i\}_i = \{\{ta\}_i @ [i] \{\{ta\text{-update-interrupt } ta \ i\}_o = \{\{ta\}_o$

by(cases *ta*, *simp*)+

lemma *thread-action'-to-thread-action-proj-simps* [*simp*]:

shows *thread-action'-to-thread-action-proj-locks-simps*:

$\{\{thread\text{-action}'\text{-to-thread-action } (LockAction \ (la, \ l)) \ ta\}_l = \{\{ta\text{-update-locks } ta \ la \ l\}_l$

$\{\{thread\text{-action}'\text{-to-thread-action } (NewThreadAction \ nt) \ ta\}_l = \{\{ta\text{-update-NewThread } ta \ nt\}_l$

$\{\{thread\text{-action}'\text{-to-thread-action } (ConditionalAction \ ca) \ ta\}_l = \{\{ta\text{-update-Conditional } ta \ ca\}_l$

$\{\{thread\text{-action}'\text{-to-thread-action } (WaitSetAction \ wa) \ ta\}_l = \{\{ta\text{-update-wait-set } ta \ wa\}_l$

$\{\{thread\text{-action}'\text{-to-thread-action } (InterruptAction \ ia) \ ta\}_l = \{\{ta\text{-update-interrupt } ta \ ia\}_l$

$\{\{thread\text{-action}'\text{-to-thread-action } (ObsAction \ ob) \ ta\}_l = \{\{ta\text{-update-obs } ta \ ob\}_l$

and *thread-action'-to-thread-action-proj-nt-simps*:

$\{\{thread\text{-action}'\text{-to-thread-action } (LockAction \ (la, \ l)) \ ta\}_t = \{\{ta\text{-update-locks } ta \ la \ l\}_t$

$\{\{thread\text{-action}'\text{-to-thread-action } (NewThreadAction \ nt) \ ta\}_t = \{\{ta\text{-update-NewThread } ta \ nt\}_t$

$\{\{thread\text{-action}'\text{-to-thread-action } (ConditionalAction \ ca) \ ta\}_t = \{\{ta\text{-update-Conditional } ta \ ca\}_t$

$\{\{thread\text{-action}'\text{-to-thread-action } (WaitSetAction \ wa) \ ta\}_t = \{\{ta\text{-update-wait-set } ta \ wa\}_t$

$\{\{thread\text{-action}'\text{-to-thread-action } (InterruptAction \ ia) \ ta\}_t = \{\{ta\text{-update-interrupt } ta \ ia\}_t$

$\{\{thread\text{-action}'\text{-to-thread-action } (ObsAction \ ob) \ ta\}_t = \{\{ta\text{-update-obs } ta \ ob\}_t$

and *thread-action'-to-thread-action-proj-cond-simps*:

$\{\{thread\text{-action}'\text{-to-thread-action } (LockAction \ (la, \ l)) \ ta\}_c = \{\{ta\text{-update-locks } ta \ la \ l\}_c$

$\{\{thread\text{-action}'\text{-to-thread-action } (NewThreadAction \ nt) \ ta\}_c = \{\{ta\text{-update-NewThread } ta \ nt\}_c$

$\{\{thread\text{-action}'\text{-to-thread-action } (ConditionalAction \ ca) \ ta\}_c = \{\{ta\text{-update-Conditional } ta \ ca\}_c$

$\{\{thread\text{-action}'\text{-to-thread-action } (WaitSetAction \ wa) \ ta\}_c = \{\{ta\text{-update-wait-set } ta \ wa\}_c$

$\{\{thread\text{-action}'\text{-to-thread-action } (InterruptAction \ ia) \ ta\}_c = \{\{ta\text{-update-interrupt } ta \ ia\}_c$

$\{\{thread\text{-action}'\text{-to-thread-action } (ObsAction \ ob) \ ta\}_c = \{\{ta\text{-update-obs } ta \ ob\}_c$

and *thread-action'-to-thread-action-proj-wset-simps*:

$\{\{thread\text{-action}'\text{-to-thread-action } (LockAction \ (la, \ l)) \ ta\}_w = \{\{ta\text{-update-locks } ta \ la \ l\}_w$

$\{\{thread\text{-action}'\text{-to-thread-action } (NewThreadAction \ nt) \ ta\}_w = \{\{ta\text{-update-NewThread } ta \ nt\}_w$

$\{\{ \text{thread-action}'\text{-to-thread-action } (\text{ConditionalAction } ca) \text{ ta} \}_w = \{\{ \text{ta-update-Conditional } ta \ ca \}_w$
 $\{\{ \text{thread-action}'\text{-to-thread-action } (\text{WaitSetAction } wa) \text{ ta} \}_w = \{\{ \text{ta-update-wait-set } ta \ wa \}_w$
 $\{\{ \text{thread-action}'\text{-to-thread-action } (\text{InterruptAction } ia) \text{ ta} \}_w = \{\{ \text{ta-update-interrupt } ta \ ia \}_w$
 $\{\{ \text{thread-action}'\text{-to-thread-action } (\text{ObsAction } ob) \text{ ta} \}_w = \{\{ \text{ta-update-obs } ta \ ob \}_w$

and *thread-action'-to-thread-action-proj-interrupt-simps*:

$\{\{ \text{thread-action}'\text{-to-thread-action } (\text{LockAction } (la, l)) \text{ ta} \}_i = \{\{ \text{ta-update-locks } ta \ la \ l \}_i$
 $\{\{ \text{thread-action}'\text{-to-thread-action } (\text{NewThreadAction } nt) \text{ ta} \}_i = \{\{ \text{ta-update-NewThread } ta \ nt \}_i$
 $\{\{ \text{thread-action}'\text{-to-thread-action } (\text{ConditionalAction } ca) \text{ ta} \}_i = \{\{ \text{ta-update-Conditional } ta \ ca \}_i$
 $\{\{ \text{thread-action}'\text{-to-thread-action } (\text{WaitSetAction } wa) \text{ ta} \}_i = \{\{ \text{ta-update-wait-set } ta \ wa \}_i$
 $\{\{ \text{thread-action}'\text{-to-thread-action } (\text{InterruptAction } ia) \text{ ta} \}_i = \{\{ \text{ta-update-interrupt } ta \ ia \}_i$
 $\{\{ \text{thread-action}'\text{-to-thread-action } (\text{ObsAction } ob) \text{ ta} \}_i = \{\{ \text{ta-update-obs } ta \ ob \}_i$

and *thread-action'-to-thread-action-proj-obs-simps*:

$\{\{ \text{thread-action}'\text{-to-thread-action } (\text{LockAction } (la, l)) \text{ ta} \}_o = \{\{ \text{ta-update-locks } ta \ la \ l \}_o$
 $\{\{ \text{thread-action}'\text{-to-thread-action } (\text{NewThreadAction } nt) \text{ ta} \}_o = \{\{ \text{ta-update-NewThread } ta \ nt \}_o$
 $\{\{ \text{thread-action}'\text{-to-thread-action } (\text{ConditionalAction } ca) \text{ ta} \}_o = \{\{ \text{ta-update-Conditional } ta \ ca \}_o$
 $\{\{ \text{thread-action}'\text{-to-thread-action } (\text{WaitSetAction } wa) \text{ ta} \}_o = \{\{ \text{ta-update-wait-set } ta \ wa \}_o$
 $\{\{ \text{thread-action}'\text{-to-thread-action } (\text{InterruptAction } ia) \text{ ta} \}_o = \{\{ \text{ta-update-interrupt } ta \ ia \}_o$
 $\{\{ \text{thread-action}'\text{-to-thread-action } (\text{ObsAction } ob) \text{ ta} \}_o = \{\{ \text{ta-update-obs } ta \ ob \}_o$

by(*simp-all*)

lemmas *ta-upd-simps* =

ta-update-locks.simps ta-update-NewThread.simps ta-update-Conditional.simps
ta-update-wait-set.simps ta-update-interrupt.simps ta-update-obs.simps
thread-action'-to-thread-action.simps

declare *ta-upd-simps* [*simp del*]

hide-const (**open**)

LockAction NewThreadAction ConditionalAction WaitSetAction InterruptAction ObsAction
thread-action'-to-thread-action

hide-type (**open**) *thread-action'*

datatype *wake-up-status* =

WSNotified
| *WSWokenUp*

datatype *'w wait-set-status* =

InWS 'w
| *PostWS wake-up-status*

type-synonym *'t lock* = (*'t × nat*) *option*

type-synonym (*'l, 't*) *locks* = *'l ⇒f 't lock*

type-synonym *'l released-locks* = *'l ⇒f nat*

type-synonym (*'l, 't, 'x*) *thread-info* = *'t → ('x × 'l released-locks)*

type-synonym (*'w, 't*) *wait-sets* = *'t → 'w wait-set-status*

type-synonym *'t interrupts* = *'t set*

type-synonym (*'l, 't, 'x, 'm, 'w*) *state* = (*'l, 't*) *locks* × ((*'l, 't, 'x*) *thread-info* × *'m*) × (*'w, 't*) *wait-sets*
× *'t interrupts*

translations

(*type*) (*'l, 't*) *locks* <= (*type*) *'l ⇒f ('t × nat) option*

(*type*) (*'l, 't, 'x*) *thread-info* <= (*type*) *'t → ('x × ('l ⇒f nat))*


```

print-translation <
  let
    fun tr'
      [Const (@{type-syntax finfun}, -) $ l1 $
        (Const (@{type-syntax option}, -) $
          (Const (@{type-syntax prod}, -) $ t1 $ Const (@{type-syntax nat}, -))),
        Const (@{type-syntax prod}, -) $
          (Const (@{type-syntax prod}, -) $
            (Const (@{type-syntax fun}, -) $ t2 $
              (Const (@{type-syntax option}, -) $
                (Const (@{type-syntax prod}, -) $ x $
                  (Const (@{type-syntax finfun}, -) $ l2 $ Const (@{type-syntax nat}, -)))))) $
            m) $
          (Const (@{type-syntax prod}, -) $
            (Const (@{type-syntax fun}, -) $ t3 $
              (Const (@{type-syntax option}, -) $ (Const (@{type-syntax wait-set-status}, -) $ w))) $
            (Const (@{type-syntax fun}, -) $ t4 $ (Const (@{type-syntax bool}, -)))] =
        if t1 = t2 andalso t1 = t3 andalso t1 = t4 andalso l1 = l2
        then Syntax.const @{:type-syntax state} $ l1 $ t1 $ x $ m $ w
        else raise Match;
    in [(@{:type-syntax prod}, K tr^)]
  end
>
typ ('l, 't, 'x, 'm, 'w) state

```

abbreviation *no-wait-locks* :: 'l ⇒ f nat
where *no-wait-locks* ≡ (K \$ 0)

lemma *neq-no-wait-locks-conv*:
 $\bigwedge ln. ln \neq \text{no-wait-locks} \longleftrightarrow (\exists l. ln \ \$ \ l > 0)$
by(*auto simp add: expand-funfun-eq fun-eq-iff*)

lemma *neq-no-wait-locksE*:
fixes *ln* **assumes** *ln* ≠ *no-wait-locks* **obtains** *l* **where** *ln* \$ *l* > 0
using *assms*
by(*auto simp add: neq-no-wait-locks-conv*)

Use type variables for components instead of ('l, 't, 'x, 'm, 'w) *state* in types for state projections to allow to reuse them for refined state implementations for code generation.

definition *locks* :: ('locks × ('thread-info × 'm) × 'wsets × 'interrupts) ⇒ 'locks **where**
locks lstsmws ≡ *fst lstsmws*

definition *thr* :: ('locks × ('thread-info × 'm) × 'wsets × 'interrupts) ⇒ 'thread-info **where**
thr lstsmws ≡ *fst (fst (snd lstsmws))*

definition *shr* :: ('locks × ('thread-info × 'm) × 'wsets × 'interrupts) ⇒ 'm **where**
shr lstsmws ≡ *snd (fst (snd lstsmws))*

definition *wset* :: ('locks × ('thread-info × 'm) × 'wsets × 'interrupts) ⇒ 'wsets **where**
wset lstsmws ≡ *fst (snd (snd lstsmws))*

definition *interrupts* :: ('locks × ('thread-info × 'm) × 'wsets × 'interrupts) ⇒ 'interrupts **where**
interrupts lstsmws ≡ *snd (snd (snd lstsmws))*

lemma *locks-conv* [simp]: $locks (ls, tsmws) = ls$
by(simp add: locks-def)

lemma *thr-conv* [simp]: $thr (ls, (ts, m), ws) = ts$
by(simp add: thr-def)

lemma *shr-conv* [simp]: $shr (ls, (ts, m), ws, is) = m$
by(simp add: shr-def)

lemma *wset-conv* [simp]: $wset (ls, (ts, m), ws, is) = ws$
by(simp add: wset-def)

lemma *interrupts-conv* [simp]: $interrupts (ls, (ts, m), ws, is) = is$
by(simp add: interrupts-def)

primrec *convert-new-thread-action* :: $(t \Rightarrow x) \Rightarrow (t, 'x, 'm) \text{ new-thread-action} \Rightarrow (t, 'x, 'm) \text{ new-thread-action}$
where

$convert\text{-new-thread-action } f (NewThread\ t\ x\ m) = NewThread\ t\ (f\ x)\ m$
 $| convert\text{-new-thread-action } f (ThreadExists\ t\ b) = ThreadExists\ t\ b$

lemma *convert-new-thread-action-inv* [simp]:

$NewThread\ t\ x\ h = convert\text{-new-thread-action } f\ nta \longleftrightarrow (\exists x'. nta = NewThread\ t\ x'\ h \wedge x = f\ x')$
 $ThreadExists\ t\ b = convert\text{-new-thread-action } f\ nta \longleftrightarrow nta = ThreadExists\ t\ b$
 $convert\text{-new-thread-action } f\ nta = NewThread\ t\ x\ h \longleftrightarrow (\exists x'. nta = NewThread\ t\ x'\ h \wedge x = f\ x')$
 $convert\text{-new-thread-action } f\ nta = ThreadExists\ t\ b \longleftrightarrow nta = ThreadExists\ t\ b$

by(cases nta, auto)+

lemma *convert-new-thread-action-eqI*:

$\llbracket \bigwedge t\ x\ m. nta = NewThread\ t\ x\ m \implies nta' = NewThread\ t\ (f\ x)\ m;$
 $\bigwedge t\ b. nta = ThreadExists\ t\ b \implies nta' = ThreadExists\ t\ b \rrbracket$
 $\implies convert\text{-new-thread-action } f\ nta = nta'$

apply(cases nta)

apply fastforce+

done

lemma *convert-new-thread-action-compose* [simp]:

$convert\text{-new-thread-action } f (convert\text{-new-thread-action } g\ ta) = convert\text{-new-thread-action } (f\ o\ g)\ ta$

apply(cases ta)

apply(simp-all add: convert-new-thread-action-def)

done

lemma *inj-convert-new-thread-action* [simp]:

$inj (convert\text{-new-thread-action } f) = inj\ f$

apply(rule iffI)

apply(rule injI)

apply(drule-tac $x = NewThread\ undefined\ x\ undefined$ in injD)

apply auto[2]

apply(rule injI)

apply(case-tac x)

apply(auto dest: injD)

done

lemma *convert-new-thread-action-id*:

convert-new-thread-action $id = (id :: ('t, 'x, 'm) \text{ new-thread-action} \Rightarrow ('t, 'x, 'm) \text{ new-thread-action})$
(is ?thesis1)

convert-new-thread-action $(\lambda x. x) = (id :: ('t, 'x, 'm) \text{ new-thread-action} \Rightarrow ('t, 'x, 'm) \text{ new-thread-action})$
(is ?thesis2)

proof –

show ?thesis1 **by**(rule ext)(case-tac x, simp-all)

thus ?thesis2 **by**(simp add: id-def)

qed

definition *convert-extTA* $:: ('x \Rightarrow 'x') \Rightarrow ('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \Rightarrow ('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action}$
where *convert-extTA* $f \ ta = (\{ta\}_l, \text{map } (\text{convert-new-thread-action } f) \ \{ta\}_t, \text{snd } (\text{snd } ta))$

lemma *convert-extTA-simps* [simp]:

convert-extTA $f \ \varepsilon = \varepsilon$

$\{ \text{convert-extTA } f \ ta \}_l = \{ta\}_l$

$\{ \text{convert-extTA } f \ ta \}_t = \text{map } (\text{convert-new-thread-action } f) \ \{ta\}_t$

$\{ \text{convert-extTA } f \ ta \}_c = \{ta\}_c$

$\{ \text{convert-extTA } f \ ta \}_w = \{ta\}_w$

$\{ \text{convert-extTA } f \ ta \}_i = \{ta\}_i$

convert-extTA $f \ (las, tas, was, cas, is, obs) = (las, \text{map } (\text{convert-new-thread-action } f) \ tas, was, cas, is, obs)$

apply(simp-all add: *convert-extTA-def*)

apply(cases ta, simp)+

done

lemma *convert-extTA-eq-conv*:

convert-extTA $f \ ta = ta' \iff$

$\{ta\}_l = \{ta'\}_l \wedge \{ta\}_c = \{ta'\}_c \wedge \{ta\}_w = \{ta'\}_w \wedge \{ta\}_o = \{ta'\}_o \wedge \{ta\}_i = \{ta'\}_i \wedge \text{length } \{ta\}_t = \text{length } \{ta'\}_t \wedge$

$(\forall n < \text{length } \{ta\}_t. \text{convert-new-thread-action } f \ (\{ta\}_t ! n) = \{ta'\}_t ! n)$

apply(cases ta, cases ta')

apply(auto simp add: *convert-extTA-def* map-eq-all-nth-conv)

done

lemma *convert-extTA-compose* [simp]:

convert-extTA $f \ (\text{convert-extTA } g \ ta) = \text{convert-extTA } (f \ o \ g) \ ta$

by(simp add: *convert-extTA-def*)

lemma *obs-a-convert-extTA* [simp]: *obs-a* $(\text{convert-extTA } f \ ta) = \text{obs-a } ta$

by(cases ta) simp

Actions for thread start/finish

datatype 'o action =

NormalAction 'o

| InitialThreadAction

| ThreadFinishAction

instance action :: (type) obs-action

proof qed

definition *convert-obs-initial* $:: ('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \Rightarrow ('l, 't, 'x, 'm, 'w, 'o) \text{ action} \text{ thread-action}$

where

convert-obs-initial $ta = (\{ta\}_l, \{ta\}_t, \{ta\}_c, \{ta\}_w, \{ta\}_i, \text{map } \text{NormalAction } \{ta\}_o)$

lemma *inj-NormalAction* [*simp*]: *inj NormalAction*
by(*rule injI*) *auto*

lemma *convert-obs-initial-inject* [*simp*]:
 $convert-obs-initial\ ta = convert-obs-initial\ ta' \iff ta = ta'$
by(*cases ta*)(*cases ta'*, *auto simp add: convert-obs-initial-def*)

lemma *convert-obs-initial-empty-TA* [*simp*]:
 $convert-obs-initial\ \varepsilon = \varepsilon$
by(*simp add: convert-obs-initial-def*)

lemma *convert-obs-initial-eq-empty-TA* [*simp*]:
 $convert-obs-initial\ ta = \varepsilon \iff ta = \varepsilon$
 $\varepsilon = convert-obs-initial\ ta \iff ta = \varepsilon$
by(*case-tac [!] ta*)(*auto simp add: convert-obs-initial-def*)

lemma *convert-obs-initial-simps* [*simp*]:
 $\{convert-obs-initial\ ta\}_o = map\ NormalAction\ \{ta\}_o$
 $\{convert-obs-initial\ ta\}_l = \{ta\}_l$
 $\{convert-obs-initial\ ta\}_t = \{ta\}_t$
 $\{convert-obs-initial\ ta\}_c = \{ta\}_c$
 $\{convert-obs-initial\ ta\}_w = \{ta\}_w$
 $\{convert-obs-initial\ ta\}_i = \{ta\}_i$
by(*simp-all add: convert-obs-initial-def*)

type-synonym
 $(l, 't, 'x, 'm, 'w, 'o)\ semantics =$
 $'t \Rightarrow 'x \times 'm \Rightarrow (l, 't, 'x, 'm, 'w, 'o)\ thread-action \Rightarrow 'x \times 'm \Rightarrow bool$

print-translation ‹

```

let
  fun tr'
    [t4,
      Const (@{type-syntax fun}, -) $
        (Const (@{type-syntax prod}, -) $ x1 $ m1) $
          (Const (@{type-syntax fun}, -) $
            (Const (@{type-syntax prod}, -) $
              (Const (@{type-syntax finfun}, -) $ l $
                (Const (@{type-syntax list}, -) $ Const (@{type-syntax lock-action}, -))) $
              (Const (@{type-syntax prod}, -) $
                (Const (@{type-syntax list}, -) $ (Const (@{type-syntax new-thread-action}, -) $ t1 $ x2
                  $ m2)) $
                (Const (@{type-syntax prod}, -) $
                  (Const (@{type-syntax list}, -) $ (Const (@{type-syntax conditional-action}, -) $ t2)) $
                  (Const (@{type-syntax prod}, -) $
                    (Const (@{type-syntax list}, -) $ (Const (@{type-syntax wait-set-action}, -) $ t3 $ w))
                  $
                    (Const (@{type-syntax prod}, -) $
                      (Const (@{type-syntax list}, -) $ (Const (@{type-syntax interrupt-action}, -) $ t5))
                    $
                      (Const (@{type-syntax list}, -) $ o1)))))) $
            (Const (@{type-syntax fun}, -) $ (Const (@{type-syntax prod}, -) $ x3 $ m3) $
              Const (@{type-syntax bool}, -))] =

```

```

    if  $x1 = x2$  andalso  $x1 = x3$  andalso  $m1 = m2$  andalso  $m1 = m3$ 
      andalso  $t1 = t2$  andalso  $t2 = t3$  andalso  $t3 = t4$  andalso  $t4 = t5$ 
    then Syntax.const @{type-syntax semantics} $ l $ t1 $ x1 $ m1 $ w $ o1
    else raise Match;
  in [(@{type-syntax fun}, K tr')]
  end
>
typ ('l,'t,'x,'m,'w,'o) semantics

end

```

1.2 All about a managing a single lock

theory *FWLock*

imports

FWState

begin

fun *has-locks* :: 't lock \Rightarrow 't \Rightarrow nat **where**
has-locks None t = 0
| *has-locks* [(t', n)] t = (if t = t' then Suc n else 0)

lemma *has-locks-iff*:

has-locks l t = n \longleftrightarrow

(l = None \wedge n = 0) \vee

($\exists n'. l = [(t, n')] \wedge$ Suc n' = n) \vee ($\exists t' n'. l = [(t', n')] \wedge t' \neq t \wedge n = 0$)

by(*cases l, auto*)

lemma *has-locksE*:

\llbracket *has-locks* l t = n;

\llbracket l = None; n = 0 $\rrbracket \Longrightarrow$ P;

$\bigwedge n'. \llbracket$ l = [(t, n)]; Suc n' = n $\rrbracket \Longrightarrow$ P;

$\bigwedge t' n'. \llbracket$ l = [(t', n)]; t' \neq t; n = 0 $\rrbracket \Longrightarrow$ P \rrbracket

\Longrightarrow P

by(*auto simp add: has-locks-iff split: if-split-asm prod.split-asm*)

inductive *may-lock* :: 't lock \Rightarrow 't \Rightarrow bool **where**

may-lock None t

| *may-lock* [(t, n)] t

lemma *may-lock-iff* [*code*]:

may-lock l t = (case l of None \Rightarrow True | [(t', n)] \Rightarrow t = t')

by(*auto intro: may-lock.intros elim: may-lock.cases*)

lemma *may-lockI*:

l = None \vee ($\exists n. l = [(t, n)]$) \Longrightarrow *may-lock* l t

by(*cases l, auto intro: may-lock.intros*)

lemma *may-lockE* [*consumes 1, case-names None Locked*]:

\llbracket *may-lock* l t; l = None \Longrightarrow P; $\bigwedge n. l = [(t, n)] \Longrightarrow$ P $\rrbracket \Longrightarrow$ P

by(*auto elim: may-lock.cases*)

lemma *may-lock-may-lock-t-eq*:

$\llbracket \text{may-lock } l \ t; \text{ may-lock } l \ t' \rrbracket \implies (l = \text{None}) \vee (t = t')$
by(*auto elim!*: *may-lockE*)

abbreviation *has-lock* :: 't lock \Rightarrow 't \Rightarrow bool

where *has-lock* l t \equiv 0 < *has-locks* l t

lemma *has-locks-Suc-has-lock*:

has-locks l t = *Suc* n \implies *has-lock* l t

by(*auto*)

lemmas *has-lock-has-locks-Suc* = *gr0-implies-Suc*[**where** n = *has-locks* l t] **for** l t

lemma *has-lock-has-locks-conv*:

has-lock l t \longleftrightarrow (\exists n. *has-locks* l t = (*Suc* n))

by(*auto intro*: *has-locks-Suc-has-lock has-lock-has-locks-Suc*)

lemma *has-lock-may-lock*:

has-lock l t \implies *may-lock* l t

by(*cases* l, *auto intro*: *may-lockI*)

lemma *has-lock-may-lock-t-eq*:

$\llbracket \text{has-lock } l \ t; \text{ may-lock } l \ t' \rrbracket \implies t = t'$

by(*auto elim!*: *may-lockE split*: *if-split-asm*)

lemma *has-locks-has-locks-t-eq*:

$\llbracket \text{has-locks } l \ t = \text{Suc } n; \text{ has-locks } l \ t' = \text{Suc } n \rrbracket \implies t = t'$

by(*auto elim*: *has-locksE*)

lemma *has-lock-has-lock-t-eq*:

$\llbracket \text{has-lock } l \ t; \text{ has-lock } l \ t' \rrbracket \implies t = t'$

unfolding *has-lock-has-locks-conv*

by(*auto intro*: *has-locks-has-locks-t-eq*)

lemma *not-may-lock-conv*:

$\neg \text{may-lock } l \ t \longleftrightarrow (\exists t'. t' \neq t \wedge \text{has-lock } l \ t')$

by(*cases* l, *auto intro*: *may-lock.intros elim*: *may-lockE*)

fun *lock-lock* :: 't lock \Rightarrow 't \Rightarrow 't lock **where**

lock-lock None t = [(t, 0)]

| *lock-lock* [(t', n)] t = [(t', *Suc* n)]

fun *unlock-lock* :: 't lock \Rightarrow 't lock **where**

unlock-lock None = None

| *unlock-lock* [(t, n)] = (case n of 0 \Rightarrow None | *Suc* n' \Rightarrow [(t, n')])

fun *release-all* :: 't lock \Rightarrow 't \Rightarrow 't lock **where**

release-all None t = None

| *release-all* [(t', n)] t = (if t = t' then None else [(t', n)])

fun *acquire-locks* :: 't lock \Rightarrow 't \Rightarrow nat \Rightarrow 't lock **where**
acquire-locks L t 0 = L
| *acquire-locks* L t (Suc m) = *acquire-locks* (lock-lock L t) t m

lemma *acquire-locks-conv*:

acquire-locks L t n = (case L of None \Rightarrow (case n of 0 \Rightarrow None | Suc m \Rightarrow [(t, m)]) | [(t', m)] \Rightarrow [(t', n + m)])
by(*induct n arbitrary: L*)(*auto*)

lemma *lock-lock-ls-Some*:

$\exists t' n. \text{lock-lock } l t = [(t', n)]$
by(*cases l, auto*)

lemma *unlock-lock-SomeD*:

unlock-lock l = [(t', n)] \Longrightarrow l = [(t', Suc n)]
by(*cases l, auto split: nat.split-asm*)

lemma *has-locks-Suc-lock-lock-has-locks-Suc-Suc*:

has-locks l t = Suc n \Longrightarrow *has-locks* (lock-lock l t) t = Suc (Suc n)
by(*auto elim!: has-locksE*)

lemma *has-locks-lock-lock-conv [simp]*:

may-lock l t \Longrightarrow *has-locks* (lock-lock l t) t = Suc (*has-locks* l t)
by(*auto elim: may-lockE*)

lemma *has-locks-release-all-conv [simp]*:

has-locks (release-all l t) t = 0
by(*cases l, auto split: if-split-asm*)

lemma *may-lock-lock-lock-conv [simp]*: *may-lock* (lock-lock l t) t = *may-lock* l t

by(*cases l, auto elim!: may-lock.cases intro: may-lock.intros*)

lemma *has-locks-acquire-locks-conv [simp]*:

may-lock l t \Longrightarrow *has-locks* (acquire-locks l t n) t = *has-locks* l t + n
by(*induct n arbitrary: l, auto*)

lemma *may-lock-unlock-lock-conv [simp]*:

has-lock l t \Longrightarrow *may-lock* (unlock-lock l) t = *may-lock* l t
by(*cases l*)(*auto split: if-split-asm nat.splits elim!: may-lock.cases intro: may-lock.intros*)

lemma *may-lock-release-all-conv [simp]*:

may-lock (release-all l t) t = *may-lock* l t
by(*cases l, auto split: if-split-asm intro!: may-lockI elim: may-lockE*)

lemma *may-lock-t-may-lock-unlock-lock-t*:

may-lock l t \Longrightarrow *may-lock* (unlock-lock l) t
by(*auto intro: may-lock.intros elim!: may-lockE split: nat.split*)

lemma *may-lock-has-locks-lock-lock-0*:

$\llbracket \text{may-lock } l t'; t \neq t' \rrbracket \Longrightarrow$ *has-locks* (lock-lock l t') t = 0
by(*auto elim!: may-lock.cases*)

lemma *has-locks-unlock-lock-conv [simp]*:

$has-lock\ l\ t \implies has-locks\ (unlock-lock\ l)\ t = has-locks\ l\ t - 1$
by(cases l)(auto split: nat.split)

lemma *has-lock-lock-lock-unlock-lock-id* [simp]:
 $has-lock\ l\ t \implies lock-lock\ (unlock-lock\ l)\ t = l$
by(cases l)(auto split: if-split-asm nat.split)

lemma *may-lock-unlock-lock-lock-lock-id* [simp]:
 $may-lock\ l\ t \implies unlock-lock\ (lock-lock\ l\ t) = l$
by(cases l) auto

lemma *may-lock-has-locks-0*:
 $\llbracket may-lock\ l\ t; t \neq t' \rrbracket \implies has-locks\ l\ t' = 0$
by(auto elim!: may-lockE)

fun *upd-lock* :: 't lock \Rightarrow 't \Rightarrow lock-action \Rightarrow 't lock
where
 $upd-lock\ l\ t\ Lock = lock-lock\ l\ t$
 $|\ upd-lock\ l\ t\ Unlock = unlock-lock\ l$
 $|\ upd-lock\ l\ t\ UnlockFail = l$
 $|\ upd-lock\ l\ t\ ReleaseAcquire = release-all\ l\ t$

fun *upd-locks* :: 't lock \Rightarrow 't \Rightarrow lock-action list \Rightarrow 't lock
where
 $upd-locks\ l\ t\ [] = l$
 $|\ upd-locks\ l\ t\ (L \# Ls) = upd-locks\ (upd-lock\ l\ t\ L)\ t\ Ls$

lemma *upd-locks-append* [simp]:
 $upd-locks\ l\ t\ (Ls\ @\ Ls') = upd-locks\ (upd-locks\ l\ t\ Ls)\ t\ Ls'$
by(induct Ls arbitrary: l, auto)

lemma *upd-lock-Some-thread-idD*:
assumes $ul: upd-lock\ l\ t\ L = \llbracket (t', n) \rrbracket$
and $tt': t \neq t'$
shows $\exists n. l = \llbracket (t', n) \rrbracket$
proof(cases L)
case Lock
with $ul\ tt'$ **show** ?thesis
by(cases l, auto)
next
case Unlock
with $ul\ tt'$ **show** ?thesis
by(auto dest: unlock-lock-SomeD)
next
case UnlockFail
with ul **show** ?thesis **by**(simp)
next
case ReleaseAcquire
with ul **show** ?thesis
by(cases l, auto split: if-split-asm)
qed

lemma *has-lock-upd-lock-implies-has-lock*:

$\llbracket \text{has-lock } (\text{upd-lock } l \ t \ L) \ t'; \ t \neq \ t' \rrbracket \implies \text{has-lock } l \ t'$

by(cases $l \ L$ rule: *option.exhaust*[*case-product lock-action.exhaust*])(*auto split: if-split-asm nat.split-asm*)

lemma *has-lock-upd-locks-implies-has-lock*:

$\llbracket \text{has-lock } (\text{upd-locks } l \ t \ Ls) \ t'; \ t \neq \ t' \rrbracket \implies \text{has-lock } l \ t'$

by(*induct* $l \ t \ Ls$ rule: *upd-locks.induct*)(*auto intro: has-lock-upd-lock-implies-has-lock*)

fun *lock-action-ok* :: $'t \ \text{lock} \Rightarrow 't \Rightarrow \text{lock-action} \Rightarrow \text{bool}$ **where**

lock-action-ok $l \ t \ \text{Lock} = \text{may-lock } l \ t$

| *lock-action-ok* $l \ t \ \text{Unlock} = \text{has-lock } l \ t$

| *lock-action-ok* $l \ t \ \text{UnlockFail} = (\neg \text{has-lock } l \ t)$

| *lock-action-ok* $l \ t \ \text{ReleaseAcquire} = \text{True}$

fun *lock-actions-ok* :: $'t \ \text{lock} \Rightarrow 't \Rightarrow \text{lock-action list} \Rightarrow \text{bool}$ **where**

lock-actions-ok $l \ t \ [] = \text{True}$

| *lock-actions-ok* $l \ t \ (L \ # \ Ls) = (\text{lock-action-ok } l \ t \ L \wedge \text{lock-actions-ok } (\text{upd-lock } l \ t \ L) \ t \ Ls)$

lemma *lock-actions-ok-append* [*simp*]:

$\text{lock-actions-ok } l \ t \ (Ls \ @ \ Ls') \longleftrightarrow \text{lock-actions-ok } l \ t \ Ls \wedge \text{lock-actions-ok } (\text{upd-locks } l \ t \ Ls) \ t \ Ls'$

by(*induct* Ls *arbitrary: l*) *auto*

lemma *not-lock-action-okE* [*consumes 1, case-names Lock Unlock UnlockFail*]:

$\llbracket \neg \text{lock-action-ok } l \ t \ L;$

$\llbracket L = \text{Lock}; \neg \text{may-lock } l \ t \rrbracket \implies Q;$

$\llbracket L = \text{Unlock}; \neg \text{has-lock } l \ t \rrbracket \implies Q;$

$\llbracket L = \text{UnlockFail}; \text{has-lock } l \ t \rrbracket \implies Q \rrbracket$

$\implies Q$

by(cases L) *auto*

lemma *may-lock-upd-lock-conv* [*simp*]:

$\text{lock-action-ok } l \ t \ L \implies \text{may-lock } (\text{upd-lock } l \ t \ L) \ t = \text{may-lock } l \ t$

by(cases L) *auto*

lemma *may-lock-upd-locks-conv* [*simp*]:

$\text{lock-actions-ok } l \ t \ Ls \implies \text{may-lock } (\text{upd-locks } l \ t \ Ls) \ t = \text{may-lock } l \ t$

by(*induct* $l \ t \ Ls$ rule: *upd-locks.induct*) *simp-all*

lemma *lock-actions-ok-Lock-may-lock*:

$\llbracket \text{lock-actions-ok } l \ t \ Ls; \text{Lock} \in \text{set } Ls \rrbracket \implies \text{may-lock } l \ t$

by(*induct* $l \ t \ Ls$ rule: *lock-actions-ok.induct*) *auto*

lemma *has-locks-lock-lock-conv'* [*simp*]:

$\llbracket \text{may-lock } l \ t'; \ t \neq \ t' \rrbracket \implies \text{has-locks } (\text{lock-lock } l \ t') \ t = \text{has-locks } l \ t$

by(cases l)(*auto elim: may-lock.cases*)

lemma *has-locks-unlock-lock-conv'* [*simp*]:

$\llbracket \text{has-lock } l \ t'; \ t \neq \ t' \rrbracket \implies \text{has-locks } (\text{unlock-lock } l) \ t = \text{has-locks } l \ t$

by(cases l)(*auto split: if-split-asm nat.split*)

lemma *has-locks-release-all-conv'* [*simp*]:

$t \neq t' \implies \text{has-locks } (\text{release-all } l \ t') \ t = \text{has-locks } l \ t$
by(cases l) auto

lemma *has-locks-acquire-locks-conv'* [simp]:
 $\llbracket \text{may-lock } l \ t; t \neq t' \rrbracket \implies \text{has-locks } (\text{acquire-locks } l \ t \ n) \ t' = \text{has-locks } l \ t'$
by(induct l t n rule: acquire-locks.induct) simp-all

lemma *lock-action-ok-has-locks-upd-lock-eq-has-locks* [simp]:
 $\llbracket \text{lock-action-ok } l \ t' \ L; t \neq t' \rrbracket \implies \text{has-locks } (\text{upd-lock } l \ t' \ L) \ t = \text{has-locks } l \ t$
by(cases L) auto

lemma *lock-actions-ok-has-locks-upd-locks-eq-has-locks* [simp]:
 $\llbracket \text{lock-actions-ok } l \ t' \ Ls; t \neq t' \rrbracket \implies \text{has-locks } (\text{upd-locks } l \ t' \ Ls) \ t = \text{has-locks } l \ t$
by(induct l t' Ls rule: upd-locks.induct) simp-all

lemma *has-lock-acquire-locks-implies-has-lock*:
 $\llbracket \text{has-lock } (\text{acquire-locks } l \ t \ n) \ t'; t \neq t' \rrbracket \implies \text{has-lock } l \ t'$
unfolding acquire-locks-conv
by(cases n)(auto split: if-split-asm)

lemma *has-lock-has-lock-acquire-locks*:
 $\text{has-lock } l \ T \implies \text{has-lock } (\text{acquire-locks } l \ t \ n) \ T$
unfolding acquire-locks-conv
by(auto)

fun *lock-actions-ok'* :: 't lock \Rightarrow 't \Rightarrow lock-action list \Rightarrow bool **where**
 $\text{lock-actions-ok}' \ l \ t \ \llbracket \ \rceil = \text{True}$
 $\mid \text{lock-actions-ok}' \ l \ t \ (L \# Ls) = ((L = \text{Lock} \wedge \neg \text{may-lock } l \ t) \vee$
 $\text{lock-action-ok } l \ t \ L \wedge \text{lock-actions-ok}' \ (\text{upd-lock } l \ t \ L) \ t \ Ls)$

lemma *lock-actions-ok'-iff*:
 $\text{lock-actions-ok}' \ l \ t \ las \longleftrightarrow$
 $\text{lock-actions-ok } l \ t \ las \vee (\exists xs \ ys. las = xs \ @ \ \text{Lock} \ \# \ ys \wedge \text{lock-actions-ok } l \ t \ xs \wedge \neg \text{may-lock}$
 $(\text{upd-locks } l \ t \ xs) \ t)$

proof(induct l t las rule: lock-actions-ok.induct)
case (2 L t LA LAS)
show ?case
proof(cases LA = Lock \wedge \neg may-lock L t)
case True
hence ($\exists ys. \text{Lock} \ \# \ LAS = \llbracket \ \rceil \ @ \ \text{Lock} \ \# \ ys$) \wedge lock-actions-ok L t $\llbracket \ \rceil \wedge$ \neg may-lock (upd-locks L t $\llbracket \ \rceil$) t
by(simp)
with True **show** ?thesis **by**(simp (no-asm))(blast)
next
case False
with 2 **show** ?thesis
by(fastforce simp add: Cons-eq-append-conv elim: allE[**where** x=LA $\#$ xs **for** xs])
qed
qed simp

lemma *lock-actions-ok'E*[consumes 1, case-names ok Lock]:
 $\llbracket \text{lock-actions-ok}' \ l \ t \ las;$
 $\text{lock-actions-ok } l \ t \ las \implies P;$

$\bigwedge xs \ ys. \llbracket las = xs @ Lock \# ys; lock\text{-}actions\text{-}ok \ l \ t \ xs; \neg may\text{-}lock (upd\text{-}locks \ l \ t \ xs) \ t \rrbracket \implies P \rrbracket$
 $\implies P$
by(*auto simp add: lock-actions-ok'-iff*)

end

1.3 Semantics of the thread actions for locking

theory *FWLocking*

imports

FWLock

begin

definition *redT-updLs* :: (l, t) locks $\Rightarrow t \Rightarrow l$ lock-actions $\Rightarrow (l, t)$ locks **where**
 $redT\text{-}updLs \ ls \ t \ las \equiv (\lambda(l, la). upd\text{-}locks \ l \ t \ la) \circ \$ (\$ls, las\$)$

lemma *redT-updLs-iff* [*simp*]: $redT\text{-}updLs \ ls \ t \ las \ \$ \ l = upd\text{-}locks (ls \ \$ \ l) \ t (las \ \$ \ l)$
by(*simp add: redT-updLs-def*)

lemma *upd-locks-empty-conv* [*simp*]: $(\lambda(l, las). upd\text{-}locks \ l \ t \ las) \circ \$ (\$ls, K\$ \ \ \ \$) = ls$
by(*auto intro: finfun-ext*)

lemma *redT-updLs-Some-thread-idD*:

$\llbracket has\text{-}lock (redT\text{-}updLs \ ls \ t \ las \ \$ \ l) \ t'; t \neq t' \rrbracket \implies has\text{-}lock (ls \ \$ \ l) \ t'$
by(*auto simp add: redT-updLs-def intro: has-lock-upd-locks-implies-has-lock*)

definition *acquire-all* :: (l, t) locks $\Rightarrow t \Rightarrow (l \Rightarrow f \ nat) \Rightarrow (l, t)$ locks
where $\bigwedge ln. acquire\text{-}all \ ls \ t \ ln \equiv (\lambda(l, la). acquire\text{-}locks \ l \ t \ la) \circ \$ (\$ls, ln\$)$

lemma *acquire-all-iff* [*simp*]:

$\bigwedge ln. acquire\text{-}all \ ls \ t \ ln \ \$ \ l = acquire\text{-}locks (ls \ \$ \ l) \ t (ln \ \$ \ l)$
by(*simp add: acquire-all-def*)

definition *lock-ok-las* :: (l, t) locks $\Rightarrow t \Rightarrow l$ lock-actions $\Rightarrow bool$ **where**
 $lock\text{-}ok\text{-}las \ ls \ t \ las \equiv \forall l. lock\text{-}actions\text{-}ok (ls \ \$ \ l) \ t (las \ \$ \ l)$

lemma *lock-ok-lasI* [*intro*]:

$(\bigwedge l. lock\text{-}actions\text{-}ok (ls \ \$ \ l) \ t (las \ \$ \ l)) \implies lock\text{-}ok\text{-}las \ ls \ t \ las$
by(*simp add: lock-ok-las-def*)

lemma *lock-ok-lasE*:

$\llbracket lock\text{-}ok\text{-}las \ ls \ t \ las; (\bigwedge l. lock\text{-}actions\text{-}ok (ls \ \$ \ l) \ t (las \ \$ \ l)) \implies Q \rrbracket \implies Q$
by(*simp add: lock-ok-las-def*)

lemma *lock-ok-lasD*:

$lock\text{-}ok\text{-}las \ ls \ t \ las \implies lock\text{-}actions\text{-}ok (ls \ \$ \ l) \ t (las \ \$ \ l)$
by(*simp add: lock-ok-las-def*)

lemma *lock-ok-las-code* [*code*]:

$lock\text{-}ok\text{-}las \ ls \ t \ las = finfun\text{-}All ((\lambda(l, la). lock\text{-}actions\text{-}ok \ l \ t \ la) \circ \$ (\$ls, las\$))$
by(*simp add: lock-ok-las-def finfun-All-All o-def*)

lemma *lock-ok-las-may-lock*:

$\llbracket \text{lock-ok-las } ls \ t \ las; \text{Lock} \in \text{set } (las \ \$ \ l) \rrbracket \implies \text{may-lock } (ls \ \$ \ l) \ t$
by(*erule lock-ok-lasE*)(*rule lock-actions-ok-Lock-may-lock*)

lemma *redT-updLs-may-lock* [*simp*]:

$\text{lock-ok-las } ls \ t \ las \implies \text{may-lock } (\text{redT-updLs } ls \ t \ las \ \$ \ l) \ t = \text{may-lock } (ls \ \$ \ l) \ t$
by(*auto dest!*: *lock-ok-lasD*[**where** $l=l$])

lemma *redT-updLs-has-locks* [*simp*]:

$\llbracket \text{lock-ok-las } ls \ t' \ las; \ t \neq t' \rrbracket \implies \text{has-locks } (\text{redT-updLs } ls \ t' \ las \ \$ \ l) \ t = \text{has-locks } (ls \ \$ \ l) \ t$
by(*auto dest!*: *lock-ok-lasD*[**where** $l=l$])

definition *may-acquire-all* :: $(l, t) \text{ locks} \Rightarrow t \Rightarrow (l \Rightarrow f \text{ nat}) \Rightarrow \text{bool}$

where $\bigwedge ln. \text{may-acquire-all } ls \ t \ ln \equiv \forall l. ln \ \$ \ l > 0 \longrightarrow \text{may-lock } (ls \ \$ \ l) \ t$

lemma *may-acquire-allI* [*intro*]:

$\bigwedge ln. (\bigwedge l. ln \ \$ \ l > 0 \implies \text{may-lock } (ls \ \$ \ l) \ t) \implies \text{may-acquire-all } ls \ t \ ln$
by(*simp add*: *may-acquire-all-def*)

lemma *may-acquire-allE*:

$\bigwedge ln. \llbracket \text{may-acquire-all } ls \ t \ ln; \forall l. ln \ \$ \ l > 0 \longrightarrow \text{may-lock } (ls \ \$ \ l) \ t \implies P \rrbracket \implies P$
by(*auto simp add*: *may-acquire-all-def*)

lemma *may-acquire-allD* [*dest*]:

$\bigwedge ln. \llbracket \text{may-acquire-all } ls \ t \ ln; \ln \ \$ \ l > 0 \rrbracket \implies \text{may-lock } (ls \ \$ \ l) \ t$
by(*auto simp add*: *may-acquire-all-def*)

lemma *may-acquire-all-has-locks-acquire-locks* [*simp*]:

fixes ln
shows $\llbracket \text{may-acquire-all } ls \ t \ ln; \ t \neq t' \rrbracket \implies \text{has-locks } (\text{acquire-locks } (ls \ \$ \ l) \ t \ (ln \ \$ \ l)) \ t' = \text{has-locks } (ls \ \$ \ l) \ t'$
by(*cases* $ln \ \$ \ l > 0$)(*auto dest*: *may-acquire-allD*)

lemma *may-acquire-all-code* [*code*]:

$\bigwedge ln. \text{may-acquire-all } ls \ t \ ln \longleftrightarrow \text{finfun-All } ((\lambda(\text{lock}, n). n > 0 \longrightarrow \text{may-lock } \text{lock } t) \circ \$) (\$ls, ln\$)$
by(*auto simp add*: *may-acquire-all-def finfun-All-All o-def*)

definition *collect-locks* :: $l \text{ lock-actions} \Rightarrow l \text{ set}$ **where**

$\text{collect-locks } las = \{l. \text{Lock} \in \text{set } (las \ \$ \ l)\}$

lemma *collect-locksI*:

$\text{Lock} \in \text{set } (las \ \$ \ l) \implies l \in \text{collect-locks } las$
by(*simp add*: *collect-locks-def*)

lemma *collect-locksE*:

$\llbracket l \in \text{collect-locks } las; \text{Lock} \in \text{set } (las \ \$ \ l) \rrbracket \implies P \implies P$
by(*simp add*: *collect-locks-def*)

lemma *collect-locksD*:

$l \in \text{collect-locks } las \implies \text{Lock} \in \text{set } (las \ \$ \ l)$
by(*simp add*: *collect-locks-def*)

fun *must-acquire-lock* :: *lock-action list* \Rightarrow *bool* **where**
must-acquire-lock [] = *False*
| *must-acquire-lock* (*Lock* # *las*) = *True*
| *must-acquire-lock* (*Unlock* # *las*) = *False*
| *must-acquire-lock* (- # *las*) = *must-acquire-lock las*

lemma *must-acquire-lock-append*:

must-acquire-lock (xs @ ys) \longleftrightarrow (if Lock \in set xs \vee Unlock \in set xs then must-acquire-lock xs else must-acquire-lock ys)

proof(*induct xs*)

case *Nil* **thus** ?*case* **by** *simp*

next

case (*Cons L Ls*)

thus ?*case* **by** (*cases L, simp-all*)

qed

lemma *must-acquire-lock-contains-lock*:

must-acquire-lock las \implies Lock \in set las

proof(*induct las*)

case (*Cons l las*) **thus** ?*case* **by**(*cases l*) *auto*

qed *simp*

lemma *must-acquire-lock-conv*:

*must-acquire-lock las = (case (filter ($\lambda L. L = \text{Lock} \vee L = \text{Unlock}$) las) of [] \Rightarrow *False* | *L* # *Ls* \Rightarrow *L = Lock*)*

proof(*induct las*)

case *Nil* **thus** ?*case* **by** *simp*

next

case (*Cons LA LAS*) **thus** ?*case*

by(*cases LA, auto split: list.split-asm*)

qed

definition *collect-locks'* :: '*l lock-actions* \Rightarrow '*l set* **where**

collect-locks' las \equiv {l. must-acquire-lock (las \$ l)}

lemma *collect-locks'I*:

must-acquire-lock (las \$ l) \implies l \in collect-locks' las

by(*simp add: collect-locks'-def*)

lemma *collect-locks'E*:

$\llbracket l \in \text{collect-locks}' \text{ las}; \text{must-acquire-lock (las } \$ \text{ l)} \implies P \rrbracket \implies P$

by(*simp add: collect-locks'-def*)

lemma *collect-locks'-subset-collect-locks*:

collect-locks' las \subseteq collect-locks las

by(*auto simp add: collect-locks'-def collect-locks-def intro: must-acquire-lock-contains-lock*)

definition *lock-ok-las'* :: ('*l,t*) *locks* \Rightarrow '*t* \Rightarrow '*l lock-actions* \Rightarrow *bool* **where**

lock-ok-las' ls t las \equiv $\forall l. \text{lock-actions-ok}' (ls \$ l) t (las \$ l)$

lemma *lock-ok-las'I*: ($\bigwedge l. \text{lock-actions-ok}' (ls \$ l) t (las \$ l)$) \implies *lock-ok-las' ls t las*

by(*simp add: lock-ok-las'-def*)

lemma *lock-ok-las'D*: $\text{lock-ok-las}'\ l\ t\ las \implies \text{lock-actions-ok}'\ (l\ \$\ l)\ t\ (las\ \$\ l)$
by(*simp add: lock-ok-las'-def*)

lemma *not-lock-ok-las'-conv*:
 $\neg \text{lock-ok-las}'\ l\ t\ las \iff (\exists l. \neg \text{lock-actions-ok}'\ (l\ \$\ l)\ t\ (las\ \$\ l))$
by(*simp add: lock-ok-las'-def*)

lemma *lock-ok-las'-code*:
 $\text{lock-ok-las}'\ l\ t\ las = \text{finfun-All}\ ((\lambda(l, la). \text{lock-actions-ok}'\ l\ t\ la) \circ \$\ (\$l, las\$))$
by(*simp add: lock-ok-las'-def finfun-All-All o-def*)

lemma *lock-ok-las'-collect-locks'-may-lock*:
assumes *lot'*: $\text{lock-ok-las}'\ l\ t\ las$
and *mayl*: $\forall l \in \text{collect-locks}'\ las. \text{may-lock}\ (l\ \$\ l)\ t$
and *l*: $l \in \text{collect-locks}\ las$
shows $\text{may-lock}\ (l\ \$\ l)\ t$
proof(*cases l \in collect-locks' las*)
case *True* **thus** *?thesis* **using** *mayl* **by** *auto*
next
case *False*
hence *nmal*: $\neg \text{must-acquire-lock}\ (l\ \$\ l)$
by(*auto intro: collect-locks'I*)
from *l* **have** *locklasl*: $\text{Lock} \in \text{set}\ (l\ \$\ l)$
by(*rule collect-locksD*)
then obtain *ys zs*
where *las*: $las\ \$\ l = ys\ @\ \text{Lock}\ \#\ zs$
and *notin*: $\text{Lock} \notin \text{set}\ ys$
by(*auto dest: split-list-first*)
from *lot'* **have** $\text{lock-actions-ok}'\ (l\ \$\ l)\ t\ (las\ \$\ l)$
by(*auto simp add: lock-ok-las'-def*)
thus *?thesis*
proof(*induct rule: lock-actions-ok'E*)
case *ok*
with *locklasl* **show** *?thesis*
by $\neg(\text{rule lock-actions-ok-Lock-may-lock})$
next
case (*Lock YS ZS*)
note *LAS* = $\langle las\ \$\ l = YS\ @\ \text{Lock}\ \#\ ZS \rangle$
note *lao* = $\langle \text{lock-actions-ok}\ (l\ \$\ l)\ t\ YS \rangle$
note *nml* = $\langle \neg \text{may-lock}\ (\text{upd-locks}\ (l\ \$\ l)\ t\ YS)\ t \rangle$
from *LAS las nmal notin* **have** *Unlock* $\in \text{set}\ YS$
by $\neg(\text{erule contrapos-np, auto simp add: must-acquire-lock-append append-eq-append-conv2 append-eq-Cons-conv})$
then obtain *ys' zs'*
where *YS*: $YS = ys'\ @\ \text{Unlock}\ \#\ zs'$
and *unlock*: $\text{Unlock} \notin \text{set}\ ys'$
by(*auto dest: split-list-first*)
from *YS las LAS lao* **have** *lao'*: $\text{lock-actions-ok}\ (l\ \$\ l)\ t\ (ys'\ @\ [\text{Unlock}])$ **by**(*auto*)
hence $\text{has-lock}\ (\text{upd-locks}\ (l\ \$\ l)\ t\ ys')\ t$ **by** *simp*
hence $\text{may-lock}\ (\text{upd-locks}\ (l\ \$\ l)\ t\ ys')\ t$
by(*rule has-lock-may-lock*)
moreover from *lao'* **have** $\text{lock-actions-ok}\ (l\ \$\ l)\ t\ ys'$ **by** *simp*
ultimately show *?thesis* **by** *simp*

qed
qed

lemma *lock-actions-ok'-must-acquire-lock-lock-actions-ok:*

$\llbracket \text{lock-actions-ok}'\ l\ t\ Ls; \text{must-acquire-lock}\ Ls \longrightarrow \text{may-lock}\ l\ t \rrbracket \Longrightarrow \text{lock-actions-ok}\ l\ t\ Ls$

proof(*induct* $l\ t\ Ls$ *rule: lock-actions-ok.induct*)

case 1 thus ?*case* **by** *simp*

next

case ($2\ l\ t\ L\ LS$) **thus** ?*case*

proof(*cases* $L = \text{Lock} \vee L = \text{Unlock}$)

case *True*

with 2 **show** ?*thesis* **by**(*auto simp add: lock-actions-ok'-iff Cons-eq-append-conv intro: has-lock-may-lock*)

qed(*cases* L , *auto*)

qed

lemma *lock-ok-las'-collect-locks-lock-ok-las:*

assumes *lol'*: *lock-ok-las'* $ls\ t\ las$

and *clml*: $\bigwedge l. l \in \text{collect-locks}\ las \Longrightarrow \text{may-lock}\ (ls\ \$\ l)\ t$

shows *lock-ok-las* $ls\ t\ las$

proof(*rule lock-ok-lasI*)

fix l

from *lol'* **have** *lock-actions-ok'* $(ls\ \$\ l)\ t\ (las\ \$\ l)$ **by**(*rule lock-ok-las'D*)

thus *lock-actions-ok* $(ls\ \$\ l)\ t\ (las\ \$\ l)$

proof(*rule lock-actions-ok'-must-acquire-lock-lock-actions-ok[OF - impI]*)

assume *mal*: *must-acquire-lock* $(las\ \$\ l)$

thus *may-lock* $(ls\ \$\ l)\ t$

by(*auto intro!: clml collect-locksI elim: must-acquire-lock-contains-lock*)

qed

qed

lemma *lock-ok-las'-into-lock-on-las:*

$\llbracket \text{lock-ok-las}'\ ls\ t\ las; \bigwedge l. l \in \text{collect-locks}'\ las \Longrightarrow \text{may-lock}\ (ls\ \$\ l)\ t \rrbracket \Longrightarrow \text{lock-ok-las}\ ls\ t\ las$

by (*metis lock-ok-las'-collect-locks'-may-lock lock-ok-las'-collect-locks-lock-ok-las*)

end

1.4 Semantics of the thread actions for thread creation

theory *FWThread*

imports

FWState

begin

Abstractions for thread ids

context

notes [*inductive-internals*]

begin

inductive *free-thread-id* :: $(l, t, x)\ \text{thread-info} \Rightarrow t \Rightarrow \text{bool}$

for ts :: $(l, t, x)\ \text{thread-info}$ **and** t :: t

where $ts\ t = \text{None} \Longrightarrow \text{free-thread-id}\ ts\ t$

declare *free-thread-id.cases* [*elim*]

end

lemma *free-thread-id-iff*: *free-thread-id* $ts\ t = (ts\ t = None)$
by(*auto elim: free-thread-id.cases intro: free-thread-id.intros*)

Update functions for the multithreaded state

fun *redT-updT* :: ($'l, 't, 'x$) *thread-info* \Rightarrow ($'t, 'x, 'm$) *new-thread-action* \Rightarrow ($'l, 't, 'x$) *thread-info*
where
redT-updT ts (*NewThread* $t' x m$) = $ts(t' \mapsto (x, no-wait-locks))$
| *redT-updT* ts - = ts

fun *redT-updTs* :: ($'l, 't, 'x$) *thread-info* \Rightarrow ($'t, 'x, 'm$) *new-thread-action list* \Rightarrow ($'l, 't, 'x$) *thread-info*
where
redT-updTs ts [] = ts
| *redT-updTs* ts ($ta \# tas$) = *redT-updTs* (*redT-updT* ts ta) tas

lemma *redT-updTs-append* [*simp*]:
redT-updTs ts ($tas @ tas'$) = *redT-updTs* (*redT-updTs* ts tas) tas'
by(*induct* ts tas *rule: redT-updTs.induct*) *auto*

lemma *redT-updT-None*:
redT-updT ts $ta\ t = None \Longrightarrow ts\ t = None$
by(*cases* ta)(*auto split: if-splits*)

lemma *redT-updTs-None*: *redT-updTs* ts $tas\ t = None \Longrightarrow ts\ t = None$
by(*induct* ts tas *rule: redT-updTs.induct*)(*auto intro: redT-updT-None*)

lemma *redT-updT-Some1*:
 $ts\ t = \lfloor xw \rfloor \Longrightarrow \exists xw. redT-updT\ ts\ ta\ t = \lfloor xw \rfloor$
by(*cases* ta) *auto*

lemma *redT-updTs-Some1*:
 $ts\ t = \lfloor xw \rfloor \Longrightarrow \exists xw. redT-updTs\ ts\ tas\ t = \lfloor xw \rfloor$
unfolding *not-None-eq[symmetric]*
by(*induct* ts tas *arbitrary: xw rule: redT-updTs.induct*)(*simp-all del: split-paired-Ex, blast dest: redT-updT-Some1*)

lemma *redT-updT-finite-dom-inv*:
finite (*dom* (*redT-updT* ts ta)) = *finite* (*dom* ts)
by(*cases* ta) *auto*

lemma *redT-updTs-finite-dom-inv*:
finite (*dom* (*redT-updTs* ts tas)) = *finite* (*dom* ts)
by(*induct* ts tas *rule: redT-updTs.induct*)(*simp-all add: redT-updT-finite-dom-inv*)

Preconditions for thread creation actions

These primed versions are for checking preconditions only. They allow the thread actions to have a type for thread-local information that is different than the thread info state itself.

fun *redT-updT'* :: ($'l, 't, 'x$) *thread-info* \Rightarrow ($'t, 'x', 'm$) *new-thread-action* \Rightarrow ($'l, 't, 'x$) *thread-info*
where
redT-updT' ts (*NewThread* $t' x m$) = $ts(t' \mapsto (undefined, no-wait-locks))$
| *redT-updT'* ts - = ts

fun *redT-updTs'* :: ($'l, 't, 'x$) *thread-info* \Rightarrow ($'t, 'x', 'm$) *new-thread-action list* \Rightarrow ($'l, 't, 'x$) *thread-info*

where

$redT\text{-updTs}' ts [] = ts$
 $| redT\text{-updTs}' ts (ta\#tas) = redT\text{-updTs}' (redT\text{-updT}' ts ta) tas$

lemma *redT-updT'-None*:

$redT\text{-updT}' ts ta t = None \implies ts t = None$

by(cases ta)(auto split: if-splits)

primrec *thread-ok* :: $(l, 't, 'x)$ *thread-info* \Rightarrow $(t, 'x', 'm)$ *new-thread-action* \Rightarrow *bool*

where

$thread\text{-ok} ts (NewThread t x m) = free\text{-thread-id} ts t$
 $| thread\text{-ok} ts (ThreadExists t b) = (b \neq free\text{-thread-id} ts t)$

fun *thread-oks* :: $(l, 't, 'x)$ *thread-info* \Rightarrow $(t, 'x', 'm)$ *new-thread-action list* \Rightarrow *bool*

where

$thread\text{-oks} ts [] = True$
 $| thread\text{-oks} ts (ta\#tas) = (thread\text{-ok} ts ta \wedge thread\text{-oks} (redT\text{-updT}' ts ta) tas)$

lemma *thread-ok-ts-change*:

$(\bigwedge t. ts t = None \longleftrightarrow ts' t = None) \implies thread\text{-ok} ts ta \longleftrightarrow thread\text{-ok} ts' ta$

by(cases ta)(auto simp add: free-thread-id-iff)

lemma *thread-oks-ts-change*:

$(\bigwedge t. ts t = None \longleftrightarrow ts' t = None) \implies thread\text{-oks} ts tas \longleftrightarrow thread\text{-oks} ts' tas$

proof(induct tas arbitrary: ts ts')

case Nil thus ?case **by** simp

next

case (Cons ta tas ts ts')

note IH = $\langle \bigwedge ts ts'. (\bigwedge t. (ts t = None) = (ts' t = None)) \implies thread\text{-oks} ts tas = thread\text{-oks} ts' tas \rangle$

note eq = $\langle \bigwedge t. (ts t = None) = (ts' t = None) \rangle$

from eq **have** $thread\text{-ok} ts ta \longleftrightarrow thread\text{-ok} ts' ta$ **by**(rule thread-ok-ts-change)

moreover from eq **have** $\bigwedge t. (redT\text{-updT}' ts ta t = None) = (redT\text{-updT}' ts' ta t = None)$

by(cases ta)(auto)

hence $thread\text{-oks} (redT\text{-updT}' ts ta) tas = thread\text{-oks} (redT\text{-updT}' ts' ta) tas$ **by**(rule IH)

ultimately show ?case **by** simp

qed

lemma *redT-updT'-eq-None-conv*:

$(\bigwedge t. ts t = None \longleftrightarrow ts' t = None) \implies redT\text{-updT}' ts ta t = None \longleftrightarrow redT\text{-updT} ts' ta t = None$

by(cases ta) simp-all

lemma *redT-updTs'-eq-None-conv*:

$(\bigwedge t. ts t = None \longleftrightarrow ts' t = None) \implies redT\text{-updTs}' ts tas t = None \longleftrightarrow redT\text{-updTs} ts' tas t = None$

apply(induct tas arbitrary: ts ts')

apply simp-all

apply(blast intro: redT-updT'-eq-None-conv del: iffI)

done

lemma *thread-oks-redT-updT-conv* [simp]:

$thread\text{-oks} (redT\text{-updT}' ts ta) tas = thread\text{-oks} (redT\text{-updT} ts ta) tas$

by(rule thread-oks-ts-change)(rule redT-updT'-eq-None-conv refl)+

lemma *thread-oks-append* [simp]:

$thread\text{-}oks\ ts\ (tas\ @\ tas') = (thread\text{-}oks\ ts\ tas \wedge thread\text{-}oks\ (redT\text{-}updTs'\ ts\ tas)\ tas')$
by(*induct tas arbitrary: ts, auto*)

lemma *thread-oks-redT-updTs-conv [simp]:*
 $thread\text{-}oks\ (redT\text{-}updTs'\ ts\ ta)\ tas = thread\text{-}oks\ (redT\text{-}updTs\ ts\ ta)\ tas$
by(*rule thread-oks-ts-change*)(*rule redT-updTs'-eq-None-conv refl*)**+**

lemma *redT-updT-Some:*
 $\llbracket ts\ t = \lfloor xw \rfloor; thread\text{-}ok\ ts\ ta \rrbracket \implies redT\text{-}updT\ ts\ ta\ t = \lfloor xw \rfloor$
by(*cases ta*) *auto*

lemma *redT-updTs-Some:*
 $\llbracket ts\ t = \lfloor xw \rfloor; thread\text{-}oks\ ts\ tas \rrbracket \implies redT\text{-}updTs\ ts\ tas\ t = \lfloor xw \rfloor$
by(*induct ts tas rule: redT-updTs.induct*)(*auto intro: redT-updT-Some*)

lemma *redT-updT'-Some:*
 $\llbracket ts\ t = \lfloor xw \rfloor; thread\text{-}ok\ ts\ ta \rrbracket \implies redT\text{-}updT'\ ts\ ta\ t = \lfloor xw \rfloor$
by(*cases ta*) *auto*

lemma *redT-updTs'-Some:*
 $\llbracket ts\ t = \lfloor xw \rfloor; thread\text{-}oks\ ts\ tas \rrbracket \implies redT\text{-}updTs'\ ts\ tas\ t = \lfloor xw \rfloor$
by(*induct ts tas rule: redT-updTs'.induct*)(*auto intro: redT-updT'-Some*)

lemma *thread-ok-new-thread:*
 $thread\text{-}ok\ ts\ (NewThread\ t\ m'\ x) \implies ts\ t = None$
by(*auto*)

lemma *thread-oks-new-thread:*
 $\llbracket thread\text{-}oks\ ts\ tas; NewThread\ t\ x\ m \in set\ tas \rrbracket \implies ts\ t = None$
by(*induct ts tas rule: thread-oks.induct*)(*auto intro: redT-updT'-None*)

lemma *redT-updT-new-thread-ts:*
 $thread\text{-}ok\ ts\ (NewThread\ t\ x\ m) \implies redT\text{-}updT\ ts\ (NewThread\ t\ x\ m)\ t = \lfloor (x, no\text{-}wait\text{-}locks) \rfloor$
by(*simp*)

lemma *redT-updTs-new-thread-ts:*
 $\llbracket thread\text{-}oks\ ts\ tas; NewThread\ t\ x\ m \in set\ tas \rrbracket \implies redT\text{-}updTs\ ts\ tas\ t = \lfloor (x, no\text{-}wait\text{-}locks) \rfloor$
by(*induct ts tas rule: redT-updTs.induct*)(*auto intro: redT-updTs-Some*)

lemma *redT-updT-new-thread:*
 $\llbracket redT\text{-}updT\ ts\ ta\ t = \lfloor (x, w) \rfloor; thread\text{-}ok\ ts\ ta; ts\ t = None \rrbracket \implies \exists m. ta = NewThread\ t\ x\ m \wedge w = no\text{-}wait\text{-}locks$
by(*cases ta*)(*auto split: if-split-asm*)

lemma *redT-updTs-new-thread:*
 $\llbracket redT\text{-}updTs\ ts\ tas\ t = \lfloor (x, w) \rfloor; thread\text{-}oks\ ts\ tas; ts\ t = None \rrbracket \implies \exists m. NewThread\ t\ x\ m \in set\ tas \wedge w = no\text{-}wait\text{-}locks$
proof(*induct tas arbitrary: ts*)
case Nil thus ?case by simp
next

case (*Cons* *TA* *TAS* *TS*)
note $IH = \langle \bigwedge ts. \llbracket redT\text{-upd}Ts \ ts \ TAS \ t = \lfloor (x, w) \rfloor; \text{thread-oks } ts \ TAS; ts \ t = None \rrbracket \implies \exists m. \text{NewThread } t \ x \ m \in \text{set } TAS \wedge w = \text{no-wait-locks} \rangle$
note $es't = \langle redT\text{-upd}Ts \ TS \ (TA \ \# \ TAS) \ t = \lfloor (x, w) \rfloor \rangle$
note $cct = \langle \text{thread-oks } TS \ (TA \ \# \ TAS) \rangle$
hence $cctta: \text{thread-ok } TS \ TA$ **and** $ccts: \text{thread-oks } (redT\text{-upd}T \ TS \ TA) \ TAS$ **by** *auto*
note $est = \langle TS \ t = None \rangle$
{ fix *X* *W*
assume $rest: redT\text{-upd}T \ TS \ TA \ t = \lfloor (X, W) \rfloor$
then obtain *m* **where** $TA = \text{NewThread } t \ X \ m \wedge W = \text{no-wait-locks}$ **using** $cctta \ est$
by (*auto dest!*: *redT-updT-new-thread*)
then obtain $TA = \text{NewThread } t \ X \ m \ W = \text{no-wait-locks} \dots$
moreover from $rest \ ccts$
have $redT\text{-upd}Ts \ TS \ (TA \ \# \ TAS) \ t = \lfloor (X, W) \rfloor$
by(*auto intro:redT-updTs-Some*)
with $es't$ **have** $X = x \ W = w$ **by** *auto*
ultimately have $?case$ **by** *auto* **}**
moreover
{ assume $rest: redT\text{-upd}T \ TS \ TA \ t = None$
hence $\bigwedge m. TA \neq \text{NewThread } t \ x \ m$ **using** $est \ cct$
by(*clarsimp*)
with $rest \ ccts \ es't$ **have** $?case$ **by**(*auto dest: IH*) **}**
ultimately show $?case$ **by**(*cases redT-updT TS TA t, auto*)
qed

lemma *redT-updT-upd*:

$\llbracket ts \ t = \lfloor xw \rfloor; \text{thread-ok } ts \ ta \rrbracket \implies (redT\text{-upd}T \ ts \ ta)(t \mapsto xw') = redT\text{-upd}T \ (ts(t \mapsto xw')) \ ta$
by(*cases ta*)(*fastforce intro: fun-upd-twist*)**+**

lemma *redT-updTs-upd*:

$\llbracket ts \ t = \lfloor xw \rfloor; \text{thread-oks } ts \ tas \rrbracket \implies (redT\text{-upd}Ts \ ts \ tas)(t \mapsto xw') = redT\text{-upd}Ts \ (ts(t \mapsto xw')) \ tas$
by(*induct ts tas rule: redT-updTs.induct*)(*auto simp del: fun-upd-apply simp add: redT-updT-upd dest: redT-updT-Some*)

lemma *thread-ok-upd*:

$ts \ t = \lfloor xln \rfloor \implies \text{thread-ok } (ts(t \mapsto xln')) \ ta = \text{thread-ok } ts \ ta$
by(*rule thread-ok-ts-change*) *simp*

lemma *thread-oks-upd*:

$ts \ t = \lfloor xln \rfloor \implies \text{thread-oks } (ts(t \mapsto xln')) \ tas = \text{thread-oks } ts \ tas$
by(*rule thread-oks-ts-change*) *simp*

lemma *thread-ok-convert-new-thread-action* [*simp*]:

$\text{thread-ok } ts \ (\text{convert-new-thread-action } f \ ta) = \text{thread-ok } ts \ ta$
by(*cases ta*) *auto*

lemma *redT-updT'-convert-new-thread-action-eq-None*:

$redT\text{-upd}T' \ ts \ (\text{convert-new-thread-action } f \ ta) \ t = None \iff redT\text{-upd}T' \ ts \ ta \ t = None$
by(*cases ta*) *auto*

lemma *thread-oks-convert-new-thread-action* [*simp*]:

$\text{thread-oks } ts \ (\text{map } (\text{convert-new-thread-action } f) \ tas) = \text{thread-oks } ts \ tas$
by(*induct ts tas rule: thread-oks.induct*)(*simp-all add: thread-oks-ts-change*)[*OF redT-updT'-convert-new-thread-action-eq-None*]

lemma *map-redT-updT*:

map-option (map-prod f id) (redT-updT ts ta t) =
redT-updT (λt. map-option (map-prod f id) (ts t)) (convert-new-thread-action f ta) t
by(cases ta) auto

lemma *map-redT-updTs*:

map-option (map-prod f id) (redT-updTs ts tas t) =
redT-updTs (λt. map-option (map-prod f id) (ts t)) (map (convert-new-thread-action f) tas) t
by(induct tas arbitrary: ts)(auto simp add: map-redT-updT)

end

1.5 Semantics of the thread actions for wait, notify and interrupt

theory *FWWait*

imports

FWState

begin

Update functions for the wait sets in the multithreaded state

inductive *redT-updW* :: 't ⇒ ('w, 't) wait-sets ⇒ ('t, 'w) wait-set-action ⇒ ('w, 't) wait-sets ⇒ bool
for t :: 't **and** ws :: ('w, 't) wait-sets

where

ws t' = [InWS w] ⇒ redT-updW t ws (Notify w) (ws(t' ↦ PostWS WSNotified))
| ($\bigwedge t'. ws t' \neq [InWS w]$) ⇒ *redT-updW t ws (Notify w) ws*
| *redT-updW t ws (NotifyAll w) (λt. if ws t = [InWS w] then [PostWS WSNotified] else ws t)*
| *redT-updW t ws (Suspend w) (ws(t ↦ InWS w))*
| *ws t' = [InWS w] ⇒ redT-updW t ws (WakeUp t') (ws(t' ↦ PostWS WSInterrupted))*
| ($\bigwedge w. ws t' \neq [InWS w]$) ⇒ *redT-updW t ws (WakeUp t') ws*
| *redT-updW t ws Notified (ws(t := None))*
| *redT-updW t ws WokenUp (ws(t := None))*

definition *redT-updWs* :: 't ⇒ ('w, 't) wait-sets ⇒ ('t, 'w) wait-set-action list ⇒ ('w, 't) wait-sets ⇒ bool

where *redT-updWs t = rtrancl3p (redT-updW t)*

inductive-simps *redT-updW-simps* [simp]:

redT-updW t ws (Notify w) ws'
redT-updW t ws (NotifyAll w) ws'
redT-updW t ws (Suspend w) ws'
redT-updW t ws (WakeUp t') ws'
redT-updW t ws WokenUp ws'
redT-updW t ws Notified ws'

lemma *redT-updW-total*: ∃ ws'. *redT-updW t ws wa ws'*

by(cases wa)(auto simp add: redT-updW.simps)

lemma *redT-updWs-total*: ∃ ws'. *redT-updWs t ws was ws'*

proof(induct was rule: rev-induct)

case Nil **thus** ?case **by**(auto simp add: redT-updWs-def)

next

case (snoc wa was)

then obtain ws' **where** $redT\text{-upd}Ws\ t\ ws\ was\ ws' ..$
also from $redT\text{-upd}W\text{-total}[of\ t\ ws'\ wa]$
obtain ws'' **where** $redT\text{-upd}W\ t\ ws'\ wa\ ws'' ..$
ultimately show $?case$ **unfolding** $redT\text{-upd}Ws\text{-def}$ **by**($auto\ intro: rtrancl3p\text{-step}$)
qed

lemma $redT\text{-upd}Ws\text{-trans}$: $\llbracket redT\text{-upd}Ws\ t\ ws\ was\ ws'; redT\text{-upd}Ws\ t\ ws'\ was'\ ws'' \rrbracket \implies redT\text{-upd}Ws\ t\ ws\ (was\ @\ was')\ ws''$
unfolding $redT\text{-upd}Ws\text{-def}$ **by**($rule\ rtrancl3p\text{-trans}$)

lemma $redT\text{-upd}W\text{-None}\text{-implies}\text{-None}$:
 $\llbracket redT\text{-upd}W\ t'\ ws\ wa\ ws'; ws\ t = None; t \neq t' \rrbracket \implies ws'\ t = None$
by($auto\ simp\ add: redT\text{-upd}W.simps$)

lemma $redT\text{-upd}Ws\text{-None}\text{-implies}\text{-None}$:
assumes $redT\text{-upd}Ws\ t'\ ws\ was\ ws'$
and $t \neq t'$ **and** $ws\ t = None$
shows $ws'\ t = None$
using $\langle redT\text{-upd}Ws\ t'\ ws\ was\ ws' \rangle\ \langle ws\ t = None \rangle$ **unfolding** $redT\text{-upd}Ws\text{-def}$
by $induct(auto\ intro: redT\text{-upd}W\text{-None}\text{-implies}\text{-None}[OF\ -\ -\ \langle t \neq t' \rangle])$

lemma $redT\text{-upd}W\text{-Post}WS\text{-imp}\text{-Post}WS$:
 $\llbracket redT\text{-upd}W\ t\ ws\ wa\ ws'; ws\ t'' = \lfloor PostWS\ w \rfloor; t'' \neq t \rrbracket \implies ws'\ t'' = \lfloor PostWS\ w \rfloor$
by($auto\ simp\ add: redT\text{-upd}W.simps$)

lemma $redT\text{-upd}Ws\text{-Post}WS\text{-imp}\text{-Post}WS$:
 $\llbracket redT\text{-upd}Ws\ t\ ws\ was\ ws'; t'' \neq t; ws\ t'' = \lfloor PostWS\ w \rfloor \rrbracket \implies ws'\ t'' = \lfloor PostWS\ w \rfloor$
unfolding $redT\text{-upd}Ws\text{-def}$
by($induct\ rule: rtrancl3p.induct$)($auto\ dest: redT\text{-upd}W\text{-Post}WS\text{-imp}\text{-Post}WS$)

lemma $redT\text{-upd}W\text{-Some}\text{-other}D$:
 $\llbracket redT\text{-upd}W\ t'\ ws\ wa\ ws'; ws'\ t = \lfloor w \rfloor; t \neq t' \rrbracket$
 $\implies (case\ w\ of\ InWS\ w' \Rightarrow ws\ t = \lfloor InWS\ w' \rfloor \mid - \Rightarrow ws\ t = \lfloor w \rfloor \vee (\exists w'. ws\ t = \lfloor InWS\ w' \rfloor))$
by($auto\ simp\ add: redT\text{-upd}W.simps\ split: if\text{-split}\text{-asm}\ wait\text{-set}\text{-status.split}$)

lemma $redT\text{-upd}Ws\text{-Some}\text{-other}D$:
 $\llbracket redT\text{-upd}Ws\ t'\ ws\ was\ ws'; ws'\ t = \lfloor w \rfloor; t \neq t' \rrbracket$
 $\implies (case\ w\ of\ InWS\ w' \Rightarrow ws\ t = \lfloor InWS\ w' \rfloor \mid - \Rightarrow ws\ t = \lfloor w \rfloor \vee (\exists w'. ws\ t = \lfloor InWS\ w' \rfloor))$
unfolding $redT\text{-upd}Ws\text{-def}$
apply($induct\ arbitrary: w\ rule: rtrancl3p.induct$)
apply($fastforce\ split: wait\text{-set}\text{-status.splits\ dest: redT\text{-upd}W\text{-Some}\text{-other}D$)
done

lemma $redT\text{-upd}W\text{-None}\text{-Some}D$:
 $\llbracket redT\text{-upd}W\ t\ ws\ wa\ ws'; ws'\ t' = \lfloor w \rfloor; ws\ t' = None \rrbracket \implies t = t' \wedge (\exists w'. w = InWS\ w' \wedge wa = Suspend\ w')$
by($auto\ simp\ add: redT\text{-upd}W.simps\ split: if\text{-split}\text{-asm}$)

lemma $redT\text{-upd}Ws\text{-None}\text{-Some}D$:
 $\llbracket redT\text{-upd}Ws\ t\ ws\ was\ ws'; ws'\ t' = \lfloor w \rfloor; ws\ t' = None \rrbracket \implies t = t' \wedge (\exists w'. Suspend\ w' \in set\ was)$
unfolding $redT\text{-upd}Ws\text{-def}$
proof($induct\ arbitrary: w\ rule: rtrancl3p.induct$)
case ($rtrancl3p\text{-refl}\ ws$) **thus** $?case$ **by** $simp$
next

```

case (rtrancl3p-step ws was ws' wa ws'')
show ?case
proof(cases ws' t')
  case None
  from redT-updW-None-SomeD[OF ‹redT-updW t ws' wa ws''›, OF ‹ws'' t' = [w]› this]
  show ?thesis by auto
next
  case (Some w')
  with ‹ws t' = None› rtrancl3p-step.hyps(2) show ?thesis by auto
qed
qed

```

lemma redT-updW-neq-Some-SomeD:

$\llbracket \text{redT-updW } t' \text{ ws wa ws}'; \text{ws}' t' = \lfloor \text{InWS } w \rfloor; \text{ws } t' \neq \lfloor \text{InWS } w \rfloor \rrbracket \implies t = t' \wedge \text{wa} = \text{Suspend } w$
by(auto simp add: redT-updW.simps split: if-split-asm)

lemma redT-updWs-neq-Some-SomeD:

$\llbracket \text{redT-updWs } t \text{ ws was ws}'; \text{ws}' t' = \lfloor \text{InWS } w \rfloor; \text{ws } t' \neq \lfloor \text{InWS } w \rfloor \rrbracket \implies t = t' \wedge \text{Suspend } w \in \text{set } was$

unfolding redT-updWs-def

proof(induct rule: rtrancl3p.induct)

case rtrancl3p-refl **thus** ?case **by** simp

next

case (rtrancl3p-step ws was ws' wa ws'')

show ?case

proof(cases ws' t' = $\lfloor \text{InWS } w \rfloor$)

case True

with ‹ws t' $\neq \lfloor \text{InWS } w \rfloor$ › ‹ $\llbracket \text{ws}' t' = \lfloor \text{InWS } w \rfloor; \text{ws } t' \neq \lfloor \text{InWS } w \rfloor \rrbracket \implies t = t' \wedge \text{Suspend } w \in \text{set } was$ ›

show ?thesis **by** simp

next

case False

with ‹redT-updW t ws' wa ws''› ‹ws'' t' = $\lfloor \text{InWS } w \rfloor$ ›

have $t' = t \wedge \text{wa} = \text{Suspend } w$ **by**(rule redT-updW-neq-Some-SomeD)

thus ?thesis **by** auto

qed

qed

lemma redT-updW-not-Suspend-Some:

$\llbracket \text{redT-updW } t \text{ ws wa ws}'; \text{ws}' t = \lfloor w' \rfloor; \text{ws } t = \lfloor w \rfloor; \bigwedge w. \text{wa} \neq \text{Suspend } w \rrbracket$
 $\implies w' = w \vee (\exists w'' w'''. w = \text{InWS } w'' \wedge w' = \text{PostWS } w''')$

by(auto simp add: redT-updW.simps split: if-split-asm)

lemma redT-updWs-not-Suspend-Some:

$\llbracket \text{redT-updWs } t \text{ ws was ws}'; \text{ws}' t = \lfloor w' \rfloor; \text{ws } t = \lfloor w \rfloor; \bigwedge w. \text{Suspend } w \notin \text{set } was \rrbracket$
 $\implies w' = w \vee (\exists w'' w'''. w = \text{InWS } w'' \wedge w' = \text{PostWS } w''')$

unfolding redT-updWs-def

proof(induct arbitrary: w rule: rtrancl3p-converse-induct)

case refl **thus** ?case **by** simp

next

case (step ws wa ws' was ws'')

note ‹ws'' t = $\lfloor w' \rfloor$ ›

moreover

have $\text{ws}' t \neq \text{None}$

proof

assume $ws' t = None$

with $\langle rtrancl3p (redT-updW t) ws' was ws'' \rangle \langle ws'' t = \lfloor w' \rfloor \rangle$

obtain w' **where** $Suspend w' \in set was$ **unfolding** $redT-updWs-def[symmetric]$

by($auto dest: redT-updWs-None-SomeD$)

with $\langle Suspend w' \notin set (wa \# was) \rangle$ **show** $False$ **by** $simp$

qed

then obtain w'' **where** $ws' t = \lfloor w'' \rfloor$ **by** $auto$

moreover {

fix w

from $\langle Suspend w \notin set (wa \# was) \rangle$ **have** $Suspend w \notin set was$ **by** $simp$ }

ultimately have $w' = w'' \vee (\exists w''' w'''' . w'' = InWS w''' \wedge w' = PostWS w'''')$ **by**($rule step.hyps$)

moreover { **fix** w

from $\langle Suspend w \notin set (wa \# was) \rangle$ **have** $wa \neq Suspend w$ **by** $auto$ }

note $redT-updW-not-Suspend-Some[OF \langle redT-updW t ws wa ws' \rangle, OF \langle ws' t = \lfloor w'' \rfloor \rangle \langle ws t = \lfloor w \rfloor \rangle$
 $this]$

ultimately show $?case$ **by** $auto$

qed

lemma $redT-updWs-WokenUp-SuspendD$:

$\llbracket redT-updWs t ws was ws'; Notified \in set was \vee WokenUp \in set was; ws' t = \lfloor w \rfloor \rrbracket \implies \exists w. Suspend w \in set was$

unfolding $redT-updWs-def$

by($induct rule: rtrancl3p-converse-induct$)($auto dest: redT-updWs-None-SomeD[unfolded redT-updWs-def]$)

lemma $redT-updW-Woken-Up-same-no-Notified-Interrupted$:

$\llbracket redT-updW t ws wa ws'; ws' t = \lfloor PostWS w \rfloor; ws t = \lfloor PostWS w \rfloor; \wedge w. wa \neq Suspend w \rrbracket$
 $\implies wa \neq Notified \wedge wa \neq WokenUp$

by($fastforce$)

lemma $redT-updWs-Woken-Up-same-no-Notified-Interrupted$:

$\llbracket redT-updWs t ws was ws'; ws' t = \lfloor PostWS w \rfloor; ws t = \lfloor PostWS w \rfloor; \wedge w. Suspend w \notin set was \rrbracket$
 $\implies Notified \notin set was \wedge WokenUp \notin set was$

unfolding $redT-updWs-def$

proof($induct rule: rtrancl3p-converse-induct$)

case refl **thus** $?case$ **by** $simp$

next

case ($step ws wa ws' was ws''$)

note $Suspend = \langle \wedge w. Suspend w \notin set (wa \# was) \rangle$

note $\langle ws'' t = \lfloor PostWS w \rfloor \rangle$

moreover have $ws' t = \lfloor PostWS w \rfloor$

proof($cases ws' t$)

case $None$

with $\langle rtrancl3p (redT-updW t) ws' was ws'' \rangle \langle ws'' t = \lfloor PostWS w \rfloor \rangle$

obtain w **where** $Suspend w \in set was$ **unfolding** $redT-updWs-def[symmetric]$

by($auto dest: redT-updWs-None-SomeD$)

with $Suspend[of w]$ **have** $False$ **by** $simp$

thus $?thesis ..$

next

case ($Some w'$)

thus $?thesis$ **using** $\langle ws t = \lfloor PostWS w \rfloor \rangle$ $Suspend \langle redT-updW t ws wa ws' \rangle$

by($auto simp add: redT-updW.simps split: if-split-asm$)

qed

moreover

```

{ fix w from Suspend[of w] have Suspend w  $\notin$  set was by simp }
ultimately have Notified  $\notin$  set was  $\wedge$  WokenUp  $\notin$  set was by(rule step.hyps)
moreover
{ fix w from Suspend[of w] have wa  $\neq$  Suspend w by auto }
with  $\langle \text{redT-updW } t \text{ ws wa ws'} \rangle \langle \text{ws}' t = \lfloor \text{PostWS } w \rfloor \rangle \langle \text{ws } t = \lfloor \text{PostWS } w \rfloor \rangle$ 
have wa  $\neq$  Notified  $\wedge$  wa  $\neq$  WokenUp by(rule redT-updW-Woken-Up-same-no-Notified-Interrupted)
ultimately show ?case by auto
qed

```

Preconditions for wait set actions

definition *wset-actions-ok* :: ('w,'t) wait-sets \Rightarrow 't \Rightarrow ('t,'w) wait-set-action list \Rightarrow bool

where

```

wset-actions-ok ws t was  $\longleftrightarrow$ 
(if Notified  $\in$  set was then ws t =  $\lfloor \text{PostWS } \text{WSNotified} \rfloor$ 
 else if WokenUp  $\in$  set was then ws t =  $\lfloor \text{PostWS } \text{WSWokenUp} \rfloor$ 
 else ws t = None)

```

lemma *wset-actions-ok-Nil* [*simp*]:

```
wset-actions-ok ws t []  $\longleftrightarrow$  ws t = None
```

by(*simp add: wset-actions-ok-def*)

definition *waiting* :: 'w wait-set-status option \Rightarrow bool

where *waiting* w \longleftrightarrow ($\exists w'$. w = $\lfloor \text{InWS } w' \rfloor$)

lemma *not-waiting-iff*:

```
 $\neg$  waiting w  $\longleftrightarrow$  w = None  $\vee$  ( $\exists w'$ . w =  $\lfloor \text{PostWS } w' \rfloor$ )
```

apply(*cases w*)

apply(*case-tac [2] a*)

apply(*auto simp add: waiting-def*)

done

lemma *waiting-code* [*code*]:

```

waiting None = False
 $\wedge w$ . waiting  $\lfloor \text{PostWS } w \rfloor$  = False
 $\wedge w$ . waiting  $\lfloor \text{InWS } w \rfloor$  = True

```

by(*simp-all add: waiting-def*)

end

1.6 Semantics of the thread actions for purely conditional purpose such as Join

theory *FWCondAction*

imports

```
FWState
```

begin

locale *final-thread* =

```
fixes final :: 'x  $\Rightarrow$  bool
```

begin

primrec *cond-action-ok* :: ('l,'t,'x,'m,'w) state \Rightarrow 't \Rightarrow 't conditional-action \Rightarrow bool **where**

```
 $\wedge \ln$ . cond-action-ok s t (Join T) =
```


(*case thr s T of None* \Rightarrow *True* | $\llbracket (x, ln) \rrbracket \Rightarrow t \neq T \wedge \text{final } x \wedge ln = \text{no-wait-locks} \wedge \text{wset } s T = \text{None}$)
 | *cond-action-ok s t Yield* = *True*

primrec *cond-action-oks* :: ('l,'t,'x,'m,'w) state \Rightarrow 't \Rightarrow 't conditional-action list \Rightarrow bool **where**
cond-action-oks s t [] = *True*
 | *cond-action-oks s t (ct#cts)* = (*cond-action-ok s t ct* \wedge *cond-action-oks s t cts*)

lemma *cond-action-oks-append [simp]*:
cond-action-oks s t (cts @ cts') \longleftrightarrow *cond-action-oks s t cts* \wedge *cond-action-oks s t cts'*
by(*induct cts, auto*)

lemma *cond-action-oks-conv-set*:
cond-action-oks s t cts \longleftrightarrow ($\forall ct \in \text{set } cts. \text{cond-action-ok } s t ct$)
by(*induct cts*) *simp-all*

lemma *cond-action-ok-Join*:
 $\bigwedge ln. \llbracket \text{cond-action-ok } s t (\text{Join } T); \text{thr } s T = \llbracket (x, ln) \rrbracket \rrbracket \Longrightarrow \text{final } x \wedge ln = \text{no-wait-locks} \wedge \text{wset } s T = \text{None}$
by(*auto*)

lemma *cond-action-oks-Join*:
 $\bigwedge ln. \llbracket \text{cond-action-oks } s t \text{ cas}; \text{Join } T \in \text{set } \text{cas}; \text{thr } s T = \llbracket (x, ln) \rrbracket \rrbracket \Longrightarrow \text{final } x \wedge ln = \text{no-wait-locks} \wedge \text{wset } s T = \text{None} \wedge t \neq T$
by(*induct cas*)(*auto*)

lemma *cond-action-oks-upd*:
assumes *tst: thr s t = [xln]*
shows *cond-action-oks (locks s, ((thr s)(t \mapsto xln'), shr s), wset s, interrupts s) t cas = cond-action-oks s t cas*
proof(*induct cas*)
case Nil thus ?case by simp
next
case (Cons ca cas)
from tst have eq: cond-action-ok (locks s, ((thr s)(t \mapsto xln'), shr s), wset s, interrupts s) t ca = cond-action-ok s t ca
by(cases ca) auto
with Cons show ?case by(auto simp del: fun-upd-apply)
qed

lemma *cond-action-ok-shr-change*:
cond-action-ok (ls, (ts, m), ws, is) t ct \Longrightarrow *cond-action-ok (ls, (ts, m'), ws, is) t ct*
by(cases ct) auto

lemma *cond-action-oks-shr-change*:
cond-action-oks (ls, (ts, m), ws, is) t cts \Longrightarrow *cond-action-oks (ls, (ts, m'), ws, is) t cts*
by(auto simp add: cond-action-oks-conv-set intro: cond-action-ok-shr-change)

primrec *cond-action-ok'* :: ('l,'t,'x,'m,'w) state \Rightarrow 't \Rightarrow 't conditional-action \Rightarrow bool
where
cond-action-ok' - - (Join t) = *True*
 | *cond-action-ok' - - Yield* = *True*

primrec *cond-action-oks'* :: ('l,'t,'x,'m,'w) state \Rightarrow 't \Rightarrow 't conditional-action list \Rightarrow bool **where**

$cond\text{-}action\text{-}oks' s t [] = True$
 $| cond\text{-}action\text{-}oks' s t (ct\#cts) = (cond\text{-}action\text{-}ok' s t ct \wedge cond\text{-}action\text{-}oks' s t cts)$

lemma *cond-action-oks'-append* [simp]:

$cond\text{-}action\text{-}oks' s t (cts @ cts') \longleftrightarrow cond\text{-}action\text{-}oks' s t cts \wedge cond\text{-}action\text{-}oks' s t cts'$
by(*induct cts, auto*)

lemma *cond-action-oks'-subset-Join*:

$set\ cts \subseteq insert\ Yield\ (range\ Join) \implies cond\text{-}action\text{-}oks' s t cts$
apply(*induct cts*)
apply(*auto*)
done

end

definition *collect-cond-actions* :: 't conditional-action list \Rightarrow 't set **where**

$collect\text{-}cond\text{-}actions\ cts = \{t. Join\ t \in set\ cts\}$

declare *collect-cond-actions-def* [simp]

lemma *cond-action-ok-final-change*:

$\llbracket final\text{-}thread.cond\text{-}action\text{-}ok\ final1\ s1\ t\ ca;$
 $\bigwedge t. thr\ s1\ t = None \longleftrightarrow thr\ s2\ t = None;$
 $\bigwedge t\ x1. \llbracket thr\ s1\ t = \lfloor(x1, no\text{-}wait\text{-}locks)\rfloor; final1\ x1; wset\ s1\ t = None \rrbracket$
 $\implies \exists x2. thr\ s2\ t = \lfloor(x2, no\text{-}wait\text{-}locks)\rfloor \wedge final2\ x2 \wedge ln2 = no\text{-}wait\text{-}locks \wedge wset\ s2\ t = None \rrbracket$
 $\implies final\text{-}thread.cond\text{-}action\text{-}ok\ final2\ s2\ t\ ca$

apply(*cases ca*)

apply(*fastforce simp add: final-thread.cond-action-ok.simps*)
done

lemma *cond-action-oks-final-change*:

assumes *major*: $final\text{-}thread.cond\text{-}action\text{-}oks\ final1\ s1\ t\ cas$

and *minor*: $\bigwedge t. thr\ s1\ t = None \longleftrightarrow thr\ s2\ t = None$

$\bigwedge t\ x1. \llbracket thr\ s1\ t = \lfloor(x1, no\text{-}wait\text{-}locks)\rfloor; final1\ x1; wset\ s1\ t = None \rrbracket$

$\implies \exists x2. thr\ s2\ t = \lfloor(x2, no\text{-}wait\text{-}locks)\rfloor \wedge final2\ x2 \wedge ln2 = no\text{-}wait\text{-}locks \wedge wset\ s2\ t = None$

shows $final\text{-}thread.cond\text{-}action\text{-}oks\ final2\ s2\ t\ cas$

using *major*

by(*induct cas*)(*auto simp add: final-thread.cond-action-oks.simps intro: cond-action-ok-final-change[OF - minor]*)

end

1.7 Wellformedness conditions for the multithreaded state

theory *FWWellform*

imports

FWLocking

FWThread

FWWait

FWCondAction

begin

Well-formedness property: Locks are held by real threads

definition

lock-thread-ok :: (*l*, *t*) *locks* ⇒ (*l*, *t*, *x*) *thread-info* ⇒ *bool*
where [*code del*]:
lock-thread-ok *ls ts* ≡ ∀ *l t*. *has-lock* (*ls* \$ *l*) *t* → (∃ *xw*. *ts t* = [*xw*])

lemma *lock-thread-ok-code* [*code*]:
lock-thread-ok *ls ts* = *finfun-All* ((λ*l*. *case l of None* ⇒ *True* | [(*t*, *n*)] ⇒ (*ts t* ≠ *None*)) ∘\$ *ls*)
by(*simp add: lock-thread-ok-def finfun-All-All has-lock-has-locks-conv has-locks-iff o-def*)

lemma *lock-thread-okI*:
(∧ *l t*. *has-lock* (*ls* \$ *l*) *t* ⇒ ∃ *xw*. *ts t* = [*xw*]) ⇒ *lock-thread-ok* *ls ts*
by(*auto simp add: lock-thread-ok-def*)

lemma *lock-thread-okD*:
[[*lock-thread-ok* *ls ts*; *has-lock* (*ls* \$ *l*) *t*]] ⇒ ∃ *xw*. *ts t* = [*xw*]
by(*fastforce simp add: lock-thread-ok-def*)

lemma *lock-thread-okD'*:
[[*lock-thread-ok* *ls ts*; *has-locks* (*ls* \$ *l*) *t* = *Suc n*]] ⇒ ∃ *xw*. *ts t* = [*xw*]
by(*auto elim: lock-thread-okD*[**where** *l=l*] *simp del: split-paired-Ex*)

lemma *lock-thread-okE*:
[[*lock-thread-ok* *ls ts*; ∀ *l t*. *has-lock* (*ls* \$ *l*) *t* → (∃ *xw*. *ts t* = [*xw*]) ⇒ *P*]] ⇒ *P*
by(*auto simp add: lock-thread-ok-def simp del: split-paired-Ex*)

lemma *lock-thread-ok-upd*:
lock-thread-ok *ls ts* ⇒ *lock-thread-ok* *ls* (*ts*(*t* ↦ *xw*))
by(*auto intro!: lock-thread-okI dest: lock-thread-okD*)

lemma *lock-thread-ok-has-lockE*:
assumes *lock-thread-ok* *ls ts*
and *has-lock* (*ls* \$ *l*) *t*
obtains *x ln'* **where** *ts t* = [(*x*, *ln'*)]
using *assms*
by(*auto dest!: lock-thread-okD*)

lemma *redT-updLs-preserves-lock-thread-ok*:
assumes *lto: lock-thread-ok* *ls ts*
and *tst: ts t* = [*xw*]
shows *lock-thread-ok* (*redT-updLs* *ls t las*) *ts*
proof(*rule lock-thread-okI*)
fix *L T*
assume *ru: has-lock* (*redT-updLs* *ls t las* \$ *L*) *T*
show ∃ *xw*. *ts T* = [*xw*]
proof(*cases t = T*)
case *True*
thus *?thesis* **using** *tst lto*
by(*auto elim: lock-thread-okE*)
next
case *False*
with *ru* **have** *has-lock* (*ls* \$ *L*) *T*
by(*rule redT-updLs-Some-thread-idD*)
thus *?thesis* **using** *lto*
by(*auto elim!: lock-thread-okE simp del: split-paired-Ex*)
qed

qed

lemma *redT-updTs-preserves-lock-thread-ok:*

assumes *lto: lock-thread-ok ls ts*

shows *lock-thread-ok ls (redT-updTs ts nts)*

proof(*rule lock-thread-okI*)

fix *l t*

assume *has-lock (ls \$ l) t*

with *lto* **have** $\exists xw. ts\ t = \lfloor xw \rfloor$

by(*auto elim!: lock-thread-okE simp del: split-paired-Ex*)

thus $\exists xw. redT-updTs\ ts\ nts\ t = \lfloor xw \rfloor$

by(*auto intro: redT-updTs-Some1 simp del: split-paired-Ex*)

qed

lemma *lock-thread-ok-has-lock:*

assumes *lock-thread-ok ls ts*

and *has-lock (ls \$ l) t*

obtains *xw* **where** *ts t = $\lfloor xw \rfloor$*

using *assms*

by(*auto dest!: lock-thread-okD*)

lemma *lock-thread-ok-None-has-locks-0:*

$\llbracket lock-thread-ok\ ls\ ts; ts\ t = None \rrbracket \implies has-locks\ (ls\ \$\ l)\ t = 0$

by(*rule ccontr*)(*auto dest: lock-thread-okD*)

lemma *redT-upds-preserves-lock-thread-ok:*

$\llbracket lock-thread-ok\ ls\ ts; ts\ t = \lfloor xw \rfloor; thread-oks\ ts\ tas \rrbracket$

$\implies lock-thread-ok\ (redT-updLs\ ls\ t\ las)\ ((redT-updTs\ ts\ tas)(t \mapsto xw')$

apply(*rule lock-thread-okI*)

apply(*clarsimp simp del: split-paired-Ex*)

apply(*drule has-lock-upd-locks-implies-has-lock, simp*)

apply(*drule lock-thread-okD, assumption*)

apply(*erule exE*)

by(*rule redT-updTs-Some1*)

lemma *acquire-all-preserves-lock-thread-ok:*

fixes *ln*

shows $\llbracket lock-thread-ok\ ls\ ts; ts\ t = \lfloor (x, ln) \rfloor \rrbracket \implies lock-thread-ok\ (acquire-all\ ls\ t\ ln)\ (ts(t \mapsto xw))$

by(*rule lock-thread-okI*)(*auto dest!: has-lock-acquire-locks-implies-has-lock dest: lock-thread-okD*)

Well-formedness condition: Wait sets contain only real threads

definition *wset-thread-ok* :: $(w, t)\ wait-sets \Rightarrow (l, t, x)\ thread-info \Rightarrow bool$

where *wset-thread-ok ws ts* $\equiv \forall t. ts\ t = None \longrightarrow ws\ t = None$

lemma *wset-thread-okI:*

$(\bigwedge t. ts\ t = None \implies ws\ t = None) \implies wset-thread-ok\ ws\ ts$

by(*simp add: wset-thread-ok-def*)

lemma *wset-thread-okD:*

$\llbracket wset-thread-ok\ ws\ ts; ts\ t = None \rrbracket \implies ws\ t = None$

by(*simp add: wset-thread-ok-def*)

lemma *wset-thread-ok-conv-dom:*

wset-thread-ok ws ts $\longleftrightarrow dom\ ws \subseteq dom\ ts$

by(*auto simp add: wset-thread-ok-def*)

lemma *wset-thread-ok-upd*:

wset-thread-ok ls ts \implies wset-thread-ok ls (ts(t \mapsto xw))

by(*auto intro!: wset-thread-okI dest: wset-thread-okD split: if-split-asm*)

lemma *wset-thread-ok-upd-None*:

wset-thread-ok ws ts \implies wset-thread-ok (ws(t := None)) (ts(t := None))

by(*auto intro!: wset-thread-okI dest: wset-thread-okD split: if-split-asm*)

lemma *wset-thread-ok-upd-Some*:

wset-thread-ok ws ts \implies wset-thread-ok (ws(t := wo)) (ts(t \mapsto xln))

by(*auto intro!: wset-thread-okI dest: wset-thread-okD split: if-split-asm*)

lemma *wset-thread-ok-upd-ws*:

\llbracket *wset-thread-ok ws ts; ts t = \lfloor xln \rfloor $\rrbracket \implies$ *wset-thread-ok (ws(t := w)) ts**

by(*auto intro!: wset-thread-okI dest: wset-thread-okD*)

lemma *wset-thread-ok-NotifyAllI*:

wset-thread-ok ws ts \implies wset-thread-ok (λ t. if ws t = \lfloor w t \rfloor then \lfloor w' t \rfloor else ws t) ts

by(*simp add: wset-thread-ok-def*)

lemma *redT-updTs-preserves-wset-thread-ok*:

assumes *wto: wset-thread-ok ws ts*

shows *wset-thread-ok ws (redT-updTs ts nts)*

proof(*rule wset-thread-okI*)

fix *t*

assume *redT-updTs ts nts t = None*

hence *ts t = None* **by**(*rule redT-updTs-None*)

with *wto* **show** *ws t = None* **by**(*rule wset-thread-okD*)

qed

lemma *redT-updW-preserve-wset-thread-ok*:

\llbracket *wset-thread-ok ws ts; redT-updW t ws wa ws'; ts t = \lfloor xln \rfloor $\rrbracket \implies$ *wset-thread-ok ws' ts**

by(*fastforce simp add: redT-updW.simps intro: wset-thread-okI wset-thread-ok-NotifyAllI wset-thread-ok-upd-ws dest: wset-thread-okD*)

lemma *redT-updWs-preserve-wset-thread-ok*:

\llbracket *wset-thread-ok ws ts; redT-updWs t ws was ws'; ts t = \lfloor xln \rfloor $\rrbracket \implies$ *wset-thread-ok ws' ts**

unfolding *redT-updWs-def* **apply**(*rotate-tac 1*)

by(*induct rule: rtrancl3p-converse-induct*)(*auto intro: redT-updW-preserve-wset-thread-ok*)

Well-formedness condition: Wait sets contain only non-final threads

context *final-thread* **begin**

definition *wset-final-ok* :: (*'w, 't*) *wait-sets* \Rightarrow (*'l, 't, 'x*) *thread-info* \Rightarrow *bool*

where *wset-final-ok ws ts* \iff ($\forall t \in \text{dom } \text{ws}. \exists x \text{ ln}. \text{ts } t = \lfloor(x, \text{ln})\rfloor \wedge \neg \text{final } x$)

lemma *wset-final-okI*:

$(\bigwedge t \text{ w}. \text{ws } t = \lfloor w \rfloor \implies \exists x \text{ ln}. \text{ts } t = \lfloor(x, \text{ln})\rfloor \wedge \neg \text{final } x) \implies \text{wset-final-ok } \text{ws } \text{ts}$

unfolding *wset-final-ok-def* **by**(*blast*)

lemma *wset-final-okD*:

\llbracket *wset-final-ok ws ts; ws t = \lfloor w \rfloor $\rrbracket \implies \exists x \text{ ln}. \text{ts } t = \lfloor(x, \text{ln})\rfloor \wedge \neg \text{final } x$*

unfolding *wset-final-ok-def* **by**(*blast*)

lemma *wset-final-okE*:

assumes *wset-final-ok ws ts ws t = [w]*
and $\bigwedge x \ln. ts\ t = [(x, \ln)] \implies \neg \text{final } x \implies \text{thesis}$
shows *thesis*

using *assms* **by**(*blast dest: wset-final-okD*)

lemma *wset-final-ok-imp-wset-thread-ok*:

wset-final-ok ws ts \implies wset-thread-ok ws ts

apply(*rule wset-thread-okI*)

apply(*rule ccontr*)

apply(*auto elim: wset-final-okE*)

done

end

end

1.8 Semantics of the thread action ReleaseAcquire for the thread state

theory *FWLockingThread*

imports

FWLocking

begin

fun *upd-threadR* :: *nat* \Rightarrow *'t lock* \Rightarrow *'t* \Rightarrow *lock-action* \Rightarrow *nat*

where

upd-threadR n l t ReleaseAcquire = n + has-locks l t

| *upd-threadR n l t - = n*

primrec *upd-threadRs* :: *nat* \Rightarrow *'t lock* \Rightarrow *'t* \Rightarrow *lock-action list* \Rightarrow *nat*

where

upd-threadRs n l t [] = n

| *upd-threadRs n l t (la # las) = upd-threadRs (upd-threadR n l t la) (upd-lock l t la) t las*

lemma *upd-threadRs-append* [*simp*]:

upd-threadRs n l t (las @ las') = upd-threadRs (upd-threadRs n l t las) (upd-locks l t las) t las'

by(*induct las arbitrary: n l, auto*)

definition *redT-updLns* :: (*'l, 't*) *locks* \Rightarrow *'t* \Rightarrow (*'l* \Rightarrow *f nat*) \Rightarrow *'l lock-actions* \Rightarrow (*'l* \Rightarrow *f nat*)

where $\bigwedge \ln. \text{redT-updLns } ls\ t\ \ln\ las = (\lambda(l, n, la). \text{upd-threadRs } n\ l\ t\ la) \circ \$ (\$ls, (\$ln, las)\$)$

lemma *redT-updLns-iff* [*simp*]:

$\bigwedge \ln. \text{redT-updLns } ls\ t\ \ln\ las\ \$\ l = \text{upd-threadRs } (\ln\ \$\ l) (ls\ \$\ l) t (las\ \$\ l)$

by(*simp add: redT-updLns-def*)

lemma *upd-threadRs-comp-empty* [*simp*]: $(\lambda(l, n, las). \text{upd-threadRs } n\ l\ t\ las) \circ \$ (\$ls, (\$lns, K\$ [])\$)$

$= \text{lns}$

by(*auto intro!: finfun-ext*)

lemma *redT-updLs-empty* [*simp*]: *redT-updLs* *ls t (K\$ []) = ls*

by(*simp add: redT-updLs-def*)

end

1.9 Semantics of the thread actions for interruption

theory *FWInterrupt*

imports

FWState

begin

primrec *redT-updI* :: 't interrupts \Rightarrow 't interrupt-action \Rightarrow 't interrupts

where

redT-updI is (Interrupt t) = insert t is
| *redT-updI is (ClearInterrupt t) = is - {t}*
| *redT-updI is (IsInterrupted t b) = is*

fun *redT-updIs* :: 't interrupts \Rightarrow 't interrupt-action list \Rightarrow 't interrupts

where

redT-updIs is [] = is
| *redT-updIs is (ia # ias) = redT-updIs (redT-updI is ia) ias*

primrec *interrupt-action-ok* :: 't interrupts \Rightarrow 't interrupt-action \Rightarrow bool

where

interrupt-action-ok is (Interrupt t) = True
| *interrupt-action-ok is (ClearInterrupt t) = True*
| *interrupt-action-ok is (IsInterrupted t b) = (b = (t \in is))*

fun *interrupt-actions-ok* :: 't interrupts \Rightarrow 't interrupt-action list \Rightarrow bool

where

interrupt-actions-ok is [] = True
| *interrupt-actions-ok is (ia # ias) \longleftrightarrow interrupt-action-ok is ia \wedge interrupt-actions-ok (redT-updI is ia) ias*

primrec *interrupt-action-ok'* :: 't interrupts \Rightarrow 't interrupt-action \Rightarrow bool

where

interrupt-action-ok' is (Interrupt t) = True
| *interrupt-action-ok' is (ClearInterrupt t) = True*
| *interrupt-action-ok' is (IsInterrupted t b) = (b \vee t \notin is)*

fun *interrupt-actions-ok'* :: 't interrupts \Rightarrow 't interrupt-action list \Rightarrow bool

where

interrupt-actions-ok' is [] = True
| *interrupt-actions-ok' is (ia # ias) \longleftrightarrow interrupt-action-ok' is ia \wedge interrupt-actions-ok' (redT-updI is ia) ias*

fun *collect-interrupt* :: 't interrupt-action \Rightarrow 't set \Rightarrow 't set

where

collect-interrupt (IsInterrupted t True) Ts = insert t Ts
| *collect-interrupt (Interrupt t) Ts = Ts - {t}*
| *collect-interrupt - Ts = Ts*

definition *collect-interrupts* :: 't interrupt-action list \Rightarrow 't set

where $\text{collect-interrupts } ias = \text{foldr collect-interrupt } ias \ \{\}$

lemma *collect-interrupts-interrupted*:

$\llbracket \text{interrupt-actions-ok } is \ ias; t' \in \text{collect-interrupts } ias \rrbracket \implies t' \in is$

unfolding *collect-interrupts-def*

proof(*induct ias arbitrary: is*)

case *Nil* **thus** ?*case* **by** *simp*

next

case (*Cons ia ias*) **thus** ?*case*

by(*cases (ia, foldr collect-interrupt ias {})* *rule: collect-interrupt.cases*) *auto*

qed

lemma *interrupt-actions-ok-append* [*simp*]:

$\text{interrupt-actions-ok } is \ (ias \ @ \ ias') \longleftrightarrow \text{interrupt-actions-ok } is \ ias \ \wedge \ \text{interrupt-actions-ok } (\text{redT-updIs } is \ ias) \ ias'$

by(*induct ias arbitrary: is*) *auto*

lemma *collect-interrupt-subset*: $Ts \subseteq Ts' \implies \text{collect-interrupt } ia \ Ts \subseteq \text{collect-interrupt } ia \ Ts'$

by(*cases (ia, Ts)* *rule: collect-interrupt.cases*) *auto*

lemma *foldr-collect-interrupt-subset*:

$Ts \subseteq Ts' \implies \text{foldr collect-interrupt } ias \ Ts \subseteq \text{foldr collect-interrupt } ias \ Ts'$

by(*induct ias*)(*simp-all add: collect-interrupt-subset*)

lemma *interrupt-actions-ok-all-nthI*:

assumes $\bigwedge n. n < \text{length } ias \implies \text{interrupt-action-ok } (\text{redT-updIs } is \ (\text{take } n \ ias)) \ (ias \ ! \ n)$

shows $\text{interrupt-actions-ok } is \ ias$

using *assms*

proof(*induct ias arbitrary: is*)

case *Nil* **thus** ?*case* **by** *simp*

next

case (*Cons ia ias*)

from *Cons.premis[of 0]* **have** $\text{interrupt-action-ok } is \ ia$ **by** *simp*

moreover

{ **fix** *n*

assume $n < \text{length } ias$

hence $\text{interrupt-action-ok } (\text{redT-updIs } (\text{redT-updI } is \ ia) \ (\text{take } n \ ias)) \ (ias \ ! \ n)$

using *Cons.premis[of Suc n]* **by** *simp* }

hence $\text{interrupt-actions-ok } (\text{redT-updI } is \ ia) \ ias$ **by**(*rule Cons.hyps*)

ultimately show ?*case* **by** *simp*

qed

lemma *interrupt-actions-ok-nthD*:

assumes $\text{interrupt-actions-ok } is \ ias$

and $n < \text{length } ias$

shows $\text{interrupt-action-ok } (\text{redT-updIs } is \ (\text{take } n \ ias)) \ (ias \ ! \ n)$

using *assms*

by(*induct n arbitrary: is ias*)(*case-tac [!]* *ias, auto*)

lemma *interrupt-actions-ok'-all-nthI*:

assumes $\bigwedge n. n < \text{length } ias \implies \text{interrupt-action-ok}' \ (\text{redT-updIs } is \ (\text{take } n \ ias)) \ (ias \ ! \ n)$

shows $\text{interrupt-actions-ok}' \ is \ ias$

using *assms*

proof(*induct ias arbitrary: is*)

case Nil thus ?case by simp
next
case (Cons ia ias)
from Cons.premis[of 0] have interrupt-action-ok' is ia by simp
moreover
{ fix n
assume n < length ias
hence interrupt-action-ok' (redT-updIs (redT-updI is ia) (take n ias)) (ias ! n)
using Cons.premis[of Suc n] by simp }
hence interrupt-actions-ok' (redT-updI is ia) ias by(rule Cons.hyps)
ultimately show ?case by simp
qed

lemma interrupt-actions-ok'-nthD:
assumes interrupt-actions-ok' is ias
and n < length ias
shows interrupt-action-ok' (redT-updIs is (take n ias)) (ias ! n)
using assms
by(induct n arbitrary: is ias)(case-tac [!] ias, auto)

lemma interrupt-action-ok-imp-interrupt-action-ok' [simp]:
interrupt-action-ok is ia \implies interrupt-action-ok' is ia
by(cases ia) simp-all

lemma interrupt-actions-ok-imp-interrupt-actions-ok' [simp]:
interrupt-actions-ok is ias \implies interrupt-actions-ok' is ias
by(induct ias arbitrary: is)(simp-all)

lemma collect-interruptsE:
assumes t' \in collect-interrupts ias'
obtains n' where n' < length ias' ias' ! n' = IsInterrupted t' True
and Interrupt t' \notin set (take n' ias')
proof(atomize-elim)
from assms show $\exists n' < \text{length } ias'. ias' ! n' = \text{IsInterrupted } t' \text{ True} \wedge \text{Interrupt } t' \notin \text{set } (\text{take } n' ias')$
unfolding collect-interrupts-def
proof(induct ias' arbitrary: t')
case Nil thus ?case by simp
next
case (Cons ia ias) thus ?case
by(cases (ia, foldr collect-interrupt ias {})) rule: collect-interrupt.cases) fastforce+
qed
qed

lemma collect-interrupts-prefix:
collect-interrupts ias \subseteq collect-interrupts (ias @ ias')
by (metis Un-empty collect-interrupts-def foldr-append foldr-collect-interrupt-subset inf-sup-ord(1) inf-sup-ord(2) subset-Un-eq)

lemma redT-updI-insert-Interrupt:
 $\llbracket t \in \text{redT-updI is ia}; t \notin \text{is} \rrbracket \implies ia = \text{Interrupt } t$
by(cases ia) simp-all

lemma redT-updIs-insert-Interrupt:

$\llbracket t \in \text{redT-updIs } is \text{ias}; t \notin is \rrbracket \implies \text{Interrupt } t \in \text{set } is$
proof(*induct ias arbitrary: is*)
case Nil thus ?case by simp
next
case (Cons ia ias) thus ?case
by(cases t ∈ redT-updI is ia)(auto dest: redT-updI-insert-Interrupt)
qed

lemma *interrupt-actions-ok-takeI:*

interrupt-actions-ok is ias \implies interrupt-actions-ok is (take n ias)
by(*subst (asm) append-take-drop-id[symmetric, where n=n]*)(*simp del: append-take-drop-id*)

lemma *interrupt-actions-ok'-collect-interrupts-imp-interrupt-actions-ok:*

assumes *int: interrupt-actions-ok' is ias*
and *ci: collect-interrupts ias \subseteq is*
and *int': interrupt-actions-ok is' ias*
shows *interrupt-actions-ok is ias*
proof(*rule interrupt-actions-ok-all-nthI*)
fix *n*
assume *n: n < length ias*
show *interrupt-action-ok (redT-updIs is (take n ias)) (ias ! n)*
proof(*cases $\exists t. ias ! n = \text{IsInterrupted } t \text{ True}$*)
case False
with *interrupt-actions-ok'-nthD[OF int n]* **show** *?thesis by(cases ias ! n) simp-all*
next
case True
then obtain t where ia: ias ! n = IsInterrupted t True ..
from *int' n* **have** *interrupt-action-ok (redT-updIs is' (take n ias)) (ias ! n) by(rule interrupt-actions-ok-nthD)*
with ia **have** *t ∈ redT-updIs is' (take n ias) by simp*
moreover **have** *ias = take (Suc n) ias @ drop (Suc n) ias by simp*
with ci **have** *collect-interrupts (take (Suc n) ias) \subseteq is*
by (*metis collect-interrupts-prefix subset-trans*)
ultimately **have** *t ∈ redT-updIs is (take n ias) using n ia int int'*
proof(*induct n arbitrary: is is' ias*)
case 0 thus ?case by(*clarsimp simp add: neq-Nil-conv collect-interrupts-def*)
next
case (Suc n)
from $\langle \text{Suc } n < \text{length } ias \rangle$ **obtain** *ia ias'*
where *ias [simp]: ias = ia # ias' by(cases ias) auto*
from $\langle \text{interrupt-actions-ok is' ias} \rangle$
have *ia-ok: interrupt-action-ok is' ia by simp*

from $\langle t \in \text{redT-updIs is' (take (Suc } n) \text{ ias)} \rangle$
have *t ∈ redT-updIs (redT-updI is' ia) (take n ias')* **by** *simp*
moreover **from** $\langle \text{collect-interrupts (take (Suc (Suc } n)) \text{ ias)} \subseteq is \rangle$ *ia-ok*
have *collect-interrupts (take (Suc n) ias') \subseteq redT-updI is ia*
proof(*cases (ia, is) rule: collect-interrupt.cases*)
case (*3-2 t' Ts*)
hence [*simp*]: *ia = ClearInterrupt t' Ts = is by simp-all*
have *t' \notin collect-interrupts (take (Suc n) ias')*
proof
assume *t' ∈ collect-interrupts (take (Suc n) ias')*
then obtain n' where *n' < length (take (Suc n) ias') take (Suc n) ias' ! n' = IsInterrupted*

$t' \text{ True}$
 $\text{Interrupt } t' \notin \text{set } (take\ n' (take\ (Suc\ n)\ ias'))$ **by** (rule collect-interruptsE)
hence $n' \leq n$ $ias' ! n' = IsInterrupted\ t' \text{ True}$ $\text{Interrupt } t' \notin \text{set } (take\ n' ias')$
using $\langle Suc\ n < length\ ias \rangle$ **by** (simp-all add: min-def split: if-split-asm)
hence $Suc\ n' < length\ ias$ **using** $\langle Suc\ n < length\ ias \rangle$ **by** (simp add: min-def)
with $\langle interrupt\ actions\ ok\ is'\ ias \rangle$
have $interrupt\ action\ ok\ (redT\ updIs\ is'\ (take\ (Suc\ n')\ ias))\ (ias\ !\ Suc\ n')$
by (rule interrupt-actions-ok-nthD)
with $\langle Suc\ n < length\ ias \rangle$ $\langle ias' ! n' = IsInterrupted\ t' \text{ True} \rangle$
have $t' \in redT\ updIs\ (is' - \{t'\})\ (take\ n'\ ias')$ **by** simp
hence $\text{Interrupt } t' \in \text{set } (take\ n'\ ias')$
by (rule redT-updIs-insert-Interrupt) simp
with $\langle \text{Interrupt } t' \notin \text{set } (take\ n'\ ias') \rangle$ **show** False **by** contradiction
qed
thus ?thesis **using** $\langle collect\ interrupts\ (take\ (Suc\ (Suc\ n))\ ias) \subseteq is \rangle$
by (auto simp add: collect-interrupts-def)
qed (auto simp add: collect-interrupts-def)
moreover from $\langle Suc\ n < length\ ias \rangle$ **have** $n < length\ ias'$ **by** simp
moreover from $\langle ias\ !\ Suc\ n = IsInterrupted\ t\ \text{True} \rangle$ **have** $ias' ! n = IsInterrupted\ t\ \text{True}$ **by**
simp
moreover from $\langle interrupt\ actions\ ok'\ is\ ias \rangle$ **have** $interrupt\ actions\ ok'\ (redT\ updI\ is\ ia)\ ias'$
unfolding ias **by** simp
moreover from $\langle interrupt\ actions\ ok\ is'\ ias \rangle$ **have** $interrupt\ actions\ ok\ (redT\ updI\ is'\ ia)\ ias'$
by simp
ultimately have $t \in redT\ updIs\ (redT\ updI\ is\ ia)\ (take\ n\ ias')$ **by** (rule Suc)
thus ?case **by** simp
qed
thus ?thesis **unfolding** ia **by** simp
qed
qed
end

1.10 The multithreaded semantics

theory FWSemantics

imports

FWWellform

FWLockingThread

FWCondAction

FWInterrupt

begin

inductive $redT\ upd :: ('l, 't, 'x, 'm, 'w)\ state \Rightarrow 't \Rightarrow ('l, 't, 'x, 'm, 'w, 'o)\ thread\ action \Rightarrow 'x \Rightarrow 'm \Rightarrow ('l, 't, 'x, 'm, 'w)\ state \Rightarrow bool$

for $s\ t\ ta\ x'\ m'$

where

$redT\ upd\ Ws\ t\ (wset\ s)\ \{\!|ta\!\}_w\ ws'$
 $\implies redT\ upd\ s\ t\ ta\ x'\ m'\ (redT\ upd\ Ls\ (locks\ s)\ t\ \{\!|ta\!\}_l, ((redT\ upd\ Ts\ (thr\ s)\ \{\!|ta\!\}_t)(t \mapsto (x', redT\ upd\ Lns\ (locks\ s)\ t\ (snd\ (the\ (thr\ s\ t))))\ \{\!|ta\!\}_l), m'), ws', redT\ upd\ Is\ (interrupts\ s)\ \{\!|ta\!\}_i)$

inductive-simps $redT\ upd\ simps\ [simp]:$

$redT\ upd\ s\ t\ ta\ x'\ m'\ s'$

definition $redT-acq :: ('l, 't, 'x, 'm, 'w) state \Rightarrow 't \Rightarrow ('l \Rightarrow f \text{ nat}) \Rightarrow ('l, 't, 'x, 'm, 'w) state$

where

$\bigwedge ln. redT-acq s t ln = (acquire-all (locks s) t ln, ((thr s)(t \mapsto (fst (the (thr s t))), no-wait-locks)), shr s), wset s, interrupts s)$

context *final-thread begin*

inductive $actions-ok :: ('l, 't, 'x, 'm, 'w) state \Rightarrow 't \Rightarrow ('l, 't, 'x, 'm, 'w, 'o) thread-action \Rightarrow bool$

for $s :: ('l, 't, 'x, 'm, 'w) state$ **and** $t :: 't$ **and** $ta :: ('l, 't, 'x, 'm, 'w, 'o) thread-action$

where

$\llbracket lock-ok-las (locks s) t \{ta\}_l; thread-oks (thr s) \{ta\}_t; cond-action-oks s t \{ta\}_c;$
 $wset-actions-ok (wset s) t \{ta\}_w; interrupt-actions-ok (interrupts s) \{ta\}_i \rrbracket$
 $\implies actions-ok s t ta$

declare $actions-ok.intros$ [intro!]

declare $actions-ok.cases$ [elim!]

lemma $actions-ok-iff$ [simp]:

$actions-ok s t ta \iff$
 $lock-ok-las (locks s) t \{ta\}_l \wedge thread-oks (thr s) \{ta\}_t \wedge cond-action-oks s t \{ta\}_c \wedge$
 $wset-actions-ok (wset s) t \{ta\}_w \wedge interrupt-actions-ok (interrupts s) \{ta\}_i$

by(*auto*)

lemma $actions-ok-thread-oksD$:

$actions-ok s t ta \implies thread-oks (thr s) \{ta\}_t$

by(*erule actions-ok.cases*)

inductive $actions-ok' :: ('l, 't, 'x, 'm, 'w) state \Rightarrow 't \Rightarrow ('l, 't, 'x, 'm, 'w, 'o) thread-action \Rightarrow bool$ **where**

$\llbracket lock-ok-las' (locks s) t \{ta\}_l; thread-oks (thr s) \{ta\}_t; cond-action-oks' s t \{ta\}_c;$
 $wset-actions-ok (wset s) t \{ta\}_w; interrupt-actions-ok' (interrupts s) \{ta\}_i \rrbracket$
 $\implies actions-ok' s t ta$

declare $actions-ok'.intros$ [intro!]

declare $actions-ok'.cases$ [elim!]

lemma $actions-ok'-iff$:

$actions-ok' s t ta \iff$
 $lock-ok-las' (locks s) t \{ta\}_l \wedge thread-oks (thr s) \{ta\}_t \wedge cond-action-oks' s t \{ta\}_c \wedge$
 $wset-actions-ok (wset s) t \{ta\}_w \wedge interrupt-actions-ok' (interrupts s) \{ta\}_i$

by *auto*

lemma $actions-ok'-ta-upd-obs$:

$actions-ok' s t (ta-update-obs ta obs) \iff actions-ok' s t ta$

by(*auto simp add: actions-ok'-iff lock-ok-las'-def ta-upd-simps wset-actions-ok-def*)

lemma $actions-ok'-empty$: $actions-ok' s t \varepsilon \iff wset s t = None$

by(*simp add: actions-ok'-iff lock-ok-las'-def*)

lemma $actions-ok'-convert-extTA$:

$actions-ok' s t (convert-extTA f ta) = actions-ok' s t ta$

by(*simp add: actions-ok'-iff*)

inductive $actions-subset :: ('l, 't, 'x, 'm, 'w, 'o) thread-action \Rightarrow ('l, 't, 'x, 'm, 'w, 'o) thread-action \Rightarrow bool$

where

$\llbracket \text{collect-locks}' \{ta'\}_l \subseteq \text{collect-locks} \{ta\}_l;$
 $\text{collect-cond-actions} \{ta'\}_c \subseteq \text{collect-cond-actions} \{ta\}_c;$
 $\text{collect-interrupts} \{ta'\}_i \subseteq \text{collect-interrupts} \{ta\}_i \rrbracket$
 $\implies \text{actions-subset } ta' ta$

declare *actions-subset.intros* [intro!]

declare *actions-subset.cases* [elim!]

lemma *actions-subset-iff*:

actions-subset $ta' ta \iff$
 $\text{collect-locks}' \{ta'\}_l \subseteq \text{collect-locks} \{ta\}_l \wedge$
 $\text{collect-cond-actions} \{ta'\}_c \subseteq \text{collect-cond-actions} \{ta\}_c \wedge$
 $\text{collect-interrupts} \{ta'\}_i \subseteq \text{collect-interrupts} \{ta\}_i$

by *auto*

lemma *actions-subset-refl* [intro]:

actions-subset $ta ta$

by(*auto intro: actions-subset.intros collect-locks'-subset-collect-locks del: subsetI*)

definition *final-thread* :: (l, t, x, m, w) *state* $\Rightarrow t \Rightarrow \text{bool}$ **where**

$\wedge \text{ln. final-thread } s t \equiv (\text{case } \text{thr } s t \text{ of } \text{None} \Rightarrow \text{False} \mid \llbracket (x, \text{ln}) \rrbracket \Rightarrow \text{final } x \wedge \text{ln} = \text{no-wait-locks} \wedge \text{wset } s t = \text{None})$

definition *final-threads* :: (l, t, x, m, w) *state* $\Rightarrow t \text{ set}$

where *final-threads* $s \equiv \{t. \text{final-thread } s t\}$

lemma [iff]: $t \in \text{final-threads } s = \text{final-thread } s t$

by (*simp add: final-threads-def*)

lemma [*pred-set-conv*]: $\text{final-thread } s = (\lambda t. t \in \text{final-threads } s)$

by *simp*

definition *mfinal* :: (l, t, x, m, w) *state* $\Rightarrow \text{bool}$

where *mfinal* $s \iff (\forall t x \text{ln. } \text{thr } s t = \llbracket (x, \text{ln}) \rrbracket \longrightarrow \text{final } x \wedge \text{ln} = \text{no-wait-locks} \wedge \text{wset } s t = \text{None})$

lemma *final-threadI*:

$\llbracket \text{thr } s t = \llbracket (x, \text{no-wait-locks}) \rrbracket; \text{final } x; \text{wset } s t = \text{None} \rrbracket \implies \text{final-thread } s t$

by(*simp add: final-thread-def*)

lemma *final-threadE*:

assumes *final-thread* $s t$

obtains x **where** $\text{thr } s t = \llbracket (x, \text{no-wait-locks}) \rrbracket \text{final } x \text{wset } s t = \text{None}$

using *assms* **by**(*auto simp add: final-thread-def*)

lemma *mfinalI*:

$(\wedge t x \text{ln. } \text{thr } s t = \llbracket (x, \text{ln}) \rrbracket \implies \text{final } x \wedge \text{ln} = \text{no-wait-locks} \wedge \text{wset } s t = \text{None}) \implies \text{mfinal } s$

unfolding *mfinal-def* **by** *blast*

lemma *mfinalD*:

fixes ln

assumes *mfinal* $s \text{thr } s t = \llbracket (x, \text{ln}) \rrbracket$

shows $\text{final } x \text{ln} = \text{no-wait-locks} \text{wset } s t = \text{None}$

using *assms* **unfolding** *mfinal-def* **by** *blast+*

lemma *mfinalE*:

fixes *ln*

assumes *mfinal s thr s t = [(x, ln)]*

obtains *final x ln = no-wait-locks wset s t = None*

using *mfinalD[OF assms]* **by**(*rule that*)

lemma *mfinal-def2*: *mfinal s \longleftrightarrow dom (thr s) \subseteq final-threads s*

by(*fastforce elim: mfinalE final-threadE intro: mfinalI final-threadI*)

end

locale *multithreaded-base = final-thread +*

constrains *final :: 'x \Rightarrow bool*

fixes *r :: ('l, 't, 'x, 'm, 'w, 'o) semantics ($\langle - \vdash - \dashrightarrow - \rangle$ [50, 0, 0, 50] 80)*

and *convert-RA :: 'l released-locks \Rightarrow 'o list*

begin

abbreviation

r-syntax :: 't \Rightarrow 'x \Rightarrow 'm \Rightarrow ('l, 't, 'x, 'm, 'w, 'o) thread-action \Rightarrow 'x \Rightarrow 'm \Rightarrow bool

($\langle - \vdash \langle -, - \rangle \dashrightarrow \langle -, - \rangle$) [50, 0, 0, 0, 0, 0] 80)

where

t $\vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle \equiv t \vdash (x, m) -ta \rightarrow (x', m')$

inductive

redT :: ('l, 't, 'x, 'm, 'w) state \Rightarrow 't \times ('l, 't, 'x, 'm, 'w, 'o) thread-action \Rightarrow ('l, 't, 'x, 'm, 'w) state \Rightarrow bool

and

redT-syntax1 :: ('l, 't, 'x, 'm, 'w) state \Rightarrow 't \Rightarrow ('l, 't, 'x, 'm, 'w, 'o) thread-action \Rightarrow ('l, 't, 'x, 'm, 'w) state \Rightarrow bool ($\langle - \dashrightarrow - \rangle$ [50, 0, 0, 50] 80)

where

s -t>ta \rightarrow s' \equiv redT s (t, ta) s'

| *redT-normal:*

$\llbracket t \vdash \langle x, shr s \rangle -ta \rightarrow \langle x', m' \rangle;$

thr s t = [(x, no-wait-locks)];

actions-ok s t ta;

redT-upd s t ta x' m' s' \rrbracket

$\implies s -t>ta \rightarrow s'$

| *redT-acquire:*

$\bigwedge ln. \llbracket thr s t = [(x, ln)]; \neg waiting (wset s t);$

may-acquire-all (locks s) t ln; ln \$ n > 0;

s' = (acquire-all (locks s) t ln, ((thr s)(t \mapsto (x, no-wait-locks)), shr s), wset s, interrupts s) \rrbracket

$\implies s -t>(K\$ \llbracket \rrbracket, \llbracket \rrbracket, \llbracket \rrbracket, \llbracket \rrbracket, \llbracket \rrbracket, convert-RA ln) \rightarrow s'$

abbreviation

redT-syntax2 :: ('l, 't) locks \Rightarrow ('l, 't, 'x) thread-info \times 'm \Rightarrow ('w, 't) wait-sets \Rightarrow 't interrupts

\Rightarrow 't \Rightarrow ('l, 't, 'x, 'm, 'w, 'o) thread-action

\Rightarrow ('l, 't) locks \Rightarrow ('l, 't, 'x) thread-info \times 'm \Rightarrow ('w, 't) wait-sets \Rightarrow 't interrupts \Rightarrow bool

($\langle \langle -, -, -, - \rangle \dashrightarrow \langle -, -, -, - \rangle$) [0, 0, 0, 0, 0, 0, 0, 0, 0] 80)

where

(ls, tsm, ws, is) -t>ta \rightarrow (ls', tsm', ws', is') \equiv (ls, tsm, ws, is) -t>ta \rightarrow (ls', tsm', ws', is')

lemma *redT-elim* [consumes 1, case-names normal acquire]:

assumes *red*: $s \multimap ta \rightarrow s'$
and *normal*: $\bigwedge x x' m' ws'. \llbracket t \vdash \langle x, shr\ s \rangle \multimap \langle x', m' \rangle;$
 $thr\ s\ t = \llbracket (x, no\ wait\ locks) \rrbracket;$
 $lock\ ok\ las\ (locks\ s)\ t\ \{\{ta\}\}_i;$
 $thread\ oks\ (thr\ s)\ \{\{ta\}\}_i;$
 $cond\ action\ oks\ s\ t\ \{\{ta\}\}_c;$
 $wset\ actions\ ok\ (wset\ s)\ t\ \{\{ta\}\}_w;$
 $interrupt\ actions\ ok\ (interrupts\ s)\ \{\{ta\}\}_i;$
 $redT\ updWs\ t\ (wset\ s)\ \{\{ta\}\}_w\ ws';$
 $s' = (redT\ updLs\ (locks\ s)\ t\ \{\{ta\}\}_l, ((redT\ updTs\ (thr\ s)\ \{\{ta\}\}_t)(t \mapsto (x', redT\ updLns\ (locks\ s)\ t$
 $no\ wait\ locks\ \{\{ta\}\}_l)), m', ws', redT\ updIs\ (interrupts\ s)\ \{\{ta\}\}_i) \rrbracket$
 $\implies thesis$
and *acquire*: $\bigwedge x ln\ n.$
 $\llbracket thr\ s\ t = \llbracket (x, ln) \rrbracket;$
 $ta = (K\$ \llbracket, \llbracket, \llbracket, \llbracket, \llbracket, convert\ RA\ ln) \rrbracket;$
 $\neg waiting\ (wset\ s\ t);$
 $may\ acquire\ all\ (locks\ s)\ t\ ln; 0 < ln \$ n;$
 $s' = (acquire\ all\ (locks\ s)\ t\ ln, ((thr\ s)(t \mapsto (x, no\ wait\ locks)), shr\ s), wset\ s, interrupts\ s) \rrbracket$
 $\implies thesis$
shows *thesis*
using *red*
proof *cases*
case *redT-normal*
thus ?thesis **using** *normal* **by**(cases s')(*auto*)
next
case *redT-acquire*
thus ?thesis **by**-(rule *acquire*, *fastforce*+)
qed

definition

RedT :: (l, t, x, m, w) state $\Rightarrow (t \times (l, t, x, m, w, o)$ thread-action) list $\Rightarrow (l, t, x, m, w)$ state \Rightarrow
bool
 $(\langle \cdot \rangle \multimap \rightarrow^* \rightarrow [50, 0, 50] 80)$

where

$RedT \equiv rtrancl3p\ redT$

lemma *RedTI*:

$rtrancl3p\ redT\ s\ ttas\ s' \implies RedT\ s\ ttas\ s'$
by(*simp* add: *RedT-def*)

lemma *RedTE*:

$\llbracket RedT\ s\ ttas\ s'; rtrancl3p\ redT\ s\ ttas\ s' \implies P \rrbracket \implies P$
by(*auto* *simp* add: *RedT-def*)

lemma *RedTD*:

$RedT\ s\ ttas\ s' \implies rtrancl3p\ redT\ s\ ttas\ s'$
by(*simp* add: *RedT-def*)

lemma *RedT-induct* [consumes 1, case-names refl step]:

$\llbracket s \multimap ttas \rightarrow^* s';$
 $\bigwedge s. P\ s \rrbracket s;$
 $\bigwedge s\ ttas\ s'\ t\ ta\ s''. \llbracket s \multimap ttas \rightarrow^* s'; P\ s\ ttas\ s'; s' \multimap ta \rightarrow s'' \rrbracket \implies P\ s\ (ttas\ @\ [(t, ta)])\ s'' \rrbracket$

$\implies P\ s\ ttas\ s'$
unfolding *RedT-def*
by(*erule rtrancl3p.induct*) *auto*

lemma *RedT-induct'* [*consumes 1, case-names refl step*]:
 $\llbracket s \dashv\rightarrow ttas \rightarrow^* s';$
 $P\ s\ \llbracket s;$
 $\bigwedge ttas\ s'\ t\ ta\ s''. \llbracket s \dashv\rightarrow ttas \rightarrow^* s'; P\ s\ ttas\ s'; s' -t>ta \rightarrow s'' \rrbracket \implies P\ s\ (ttas\ @\ [(t,\ ta)])\ s''\rrbracket$
 $\implies P\ s\ ttas\ s'$
unfolding *RedT-def*
apply(*erule rtrancl3p.induct'*, *blast*)
apply(*case-tac b*, *blast*)
done

lemma *RedT-lift-preserveD*:
assumes *Red*: $s \dashv\rightarrow ttas \rightarrow^* s'$
and *P*: $P\ s$
and *preserve*: $\bigwedge s\ t\ tas\ s'. \llbracket s -t>tas \rightarrow s'; P\ s \rrbracket \implies P\ s'$
shows $P\ s'$
using *Red P*
by(*induct rule: RedT-induct*)(*auto intro: preserve*)

lemma *RedT-refl* [*intro, simp*]:
 $s \dashv\rightarrow \llbracket \rightarrow^* s$
by(*rule RedTI*)(*rule rtrancl3p-refl*)

lemma *redT-has-locks-inv*:
 $\llbracket \langle ls, (ts, m), ws, is \rangle -t>ta \rightarrow \langle ls', (ts', m'), ws', is' \rangle; t \neq t' \rrbracket \implies$
 $has_locks\ (ls\ \$\ l)\ t' = has_locks\ (ls'\ \$\ l)\ t'$
by(*auto elim!:* *redT.cases intro: redT-updLs-has-locks[THEN sym, simplified]* *may-acquire-all-has-locks-acquire-lock*)

lemma *redT-has-lock-inv*:
 $\llbracket \langle ls, (ts, m), ws, is \rangle -t>ta \rightarrow \langle ls', (ts', m'), ws', is' \rangle; t \neq t' \rrbracket$
 $\implies has_lock\ (ls'\ \$\ l)\ t' = has_lock\ (ls\ \$\ l)\ t'$
by(*auto simp add: redT-has-locks-inv*)

lemma *redT-ts-Some-inv*:
 $\llbracket \langle ls, (ts, m), ws, is \rangle -t>ta \rightarrow \langle ls', (ts', m'), ws', is' \rangle; t \neq t'; ts\ t' = [x] \rrbracket \implies ts'\ t' = [x]$
by(*fastforce elim!:* *redT.cases simp: redT-updTs-upd[THEN sym]* *intro: redT-updTs-Some*)

lemma *redT-thread-not-disappear*:
 $\llbracket s -t>ta \rightarrow s'; thr\ s'\ t' = None \rrbracket \implies thr\ s\ t' = None$
apply(*cases t \neq t'*)
apply(*auto elim!:* *redT-elims simp add: redT-updTs-upd[THEN sym]* *intro: redT-updTs-None*)
done

lemma *RedT-thread-not-disappear*:
 $\llbracket s \dashv\rightarrow ttas \rightarrow^* s'; thr\ s'\ t' = None \rrbracket \implies thr\ s\ t' = None$
apply(*erule contrapos-pp[where Q=thr s' t' = None]*)
apply(*drule (1) RedT-lift-preserveD*)
apply(*erule-tac Q=thr sa t' = None in contrapos-nn*)
apply(*erule redT-thread-not-disappear*)
apply(*auto*)
done

lemma *redT-preserves-wset-thread-ok*:

$\llbracket s -t>ta \rightarrow s'; \text{wset-thread-ok } (\text{wset } s) (\text{thr } s) \rrbracket \Longrightarrow \text{wset-thread-ok } (\text{wset } s') (\text{thr } s')$
by(*fastforce elim!*: *redT.cases intro*: *wset-thread-ok-upd redT-updTs-preserves-wset-thread-ok redT-updWs-preserve-wset-thread-ok*)

lemma *RedT-preserves-wset-thread-ok*:

$\llbracket s -\triangleright ttas \rightarrow * s'; \text{wset-thread-ok } (\text{wset } s) (\text{thr } s) \rrbracket \Longrightarrow \text{wset-thread-ok } (\text{wset } s') (\text{thr } s')$
by(*erule (1) RedT-lift-preserveD*)(*erule redT-preserves-wset-thread-ok*)

lemma *redT-new-thread-ts-Some*:

$\llbracket s -t>ta \rightarrow s'; \text{NewThread } t' x m'' \in \text{set } \{\{ta\}_t\}; \text{wset-thread-ok } (\text{wset } s) (\text{thr } s) \rrbracket$
 $\Longrightarrow \text{thr } s' t' = \llbracket (x, \text{no-wait-locks}) \rrbracket$
by(*erule redT-elim*s)(*auto dest*: *thread-oks-new-thread elim*: *redT-updTs-new-thread-ts*)

lemma *RedT-new-thread-ts-not-None*:

$\llbracket s -\triangleright ttas \rightarrow * s'; \text{NewThread } t x m'' \in \text{set } (\text{concat } (\text{map } (\text{thr-a } \circ \text{snd}) \text{ttas})); \text{wset-thread-ok } (\text{wset } s) (\text{thr } s) \rrbracket$
 $\Longrightarrow \text{thr } s' t \neq \text{None}$

proof(*induct rule*: *RedT-induct*)

case *refl* **thus** *?case* **by** *simp*

next

case (*step S TTAS S' T TA S''*)

note *Red* = $\langle S -\triangleright TTAS \rightarrow * S' \rangle$

note *IH* = $\llbracket \text{NewThread } t x m'' \in \text{set } (\text{concat } (\text{map } (\text{thr-a } \circ \text{snd}) \text{TTAS})); \text{wset-thread-ok } (\text{wset } S) (\text{thr } S) \rrbracket \Longrightarrow \text{thr } S' t \neq \text{None}$

note *red* = $\langle S' -T \triangleright TA \rightarrow S'' \rangle$

note *ins* = $\langle \text{NewThread } t x m'' \in \text{set } (\text{concat } (\text{map } (\text{thr-a } \circ \text{snd}) (\text{TTAS } @ \llbracket (T, TA) \rrbracket))) \rangle$

note *wto* = $\langle \text{wset-thread-ok } (\text{wset } S) (\text{thr } S) \rangle$

from *Red* *wto* **have** *wto'*: *wset-thread-ok* (*wset S'*) (*thr S'*) **by**(*auto dest*: *RedT-preserves-wset-thread-ok*)

show *?case*

proof(*cases NewThread t x m'' \in \text{set } \{\{TA\}_t\}*)

case *True* **thus** *?thesis* **using** *red wto'*

by(*auto dest!*: *redT-new-thread-ts-Some*)

next

case *False*

hence *NewThread t x m'' \in \text{set } (\text{concat } (\text{map } (\text{thr-a } \circ \text{snd}) \text{TTAS}))* **using** *ins* **by**(*auto*)

hence *thr S' t \neq \text{None}* **using** *wto* **by**(*rule IH*)

with *red* **show** *?thesis*

by $-(\text{erule } \text{contrapos-nn}, \text{auto } \text{dest}: \text{redT-thread-not-disappear})$

qed

qed

lemma *redT-preserves-lock-thread-ok*:

$\llbracket s -t>ta \rightarrow s'; \text{lock-thread-ok } (\text{locks } s) (\text{thr } s) \rrbracket \Longrightarrow \text{lock-thread-ok } (\text{locks } s') (\text{thr } s')$
by(*auto elim!*: *redT-elim*s *intro*: *redT-upds-preserves-lock-thread-ok acquire-all-preserves-lock-thread-ok*)

lemma *RedT-preserves-lock-thread-ok*:

$\llbracket s -\triangleright ttas \rightarrow * s'; \text{lock-thread-ok } (\text{locks } s) (\text{thr } s) \rrbracket \Longrightarrow \text{lock-thread-ok } (\text{locks } s') (\text{thr } s')$
by(*erule (1) RedT-lift-preserveD*)(*erule redT-preserves-lock-thread-ok*)

lemma *redT-ex-new-thread*:

assumes $s -t>ta \rightarrow s' \text{wset-thread-ok } (\text{wset } s) (\text{thr } s) \text{thr } s' t = \llbracket (x, w) \rrbracket \text{thr } s t = \text{None}$

shows $\exists m. \text{NewThread } t x m \in \text{set } \{\{ta\}_t\} \wedge w = \text{no-wait-locks}$

using *assms*

by *cases* (*fastforce split: if-split-asm dest: wset-thread-okD redT-updTs-new-thread*)⁺

lemma *redT-ex-new-thread'*:

assumes $s -t \triangleright ta \rightarrow s' \text{ thr } s' t = \lfloor (x, w) \rfloor \text{ thr } s t = \text{None}$

shows $\exists m x. \text{NewThread } t x m \in \text{set } \{ta\}_t$

using *assms*

by(*cases*)(*fastforce split: if-split-asm dest!: redT-updTs-new-thread*)⁺

definition *deterministic* :: (l, t, x, m, w) state set \Rightarrow bool

where

deterministic $I \iff$

$(\forall s t x ta' x' m' ta'' x'' m''.$

$s \in I$

$\rightarrow \text{thr } s t = \lfloor (x, \text{no-wait-locks}) \rfloor$

$\rightarrow t \vdash \langle x, \text{shr } s \rangle -ta' \rightarrow \langle x', m' \rangle$

$\rightarrow t \vdash \langle x, \text{shr } s \rangle -ta'' \rightarrow \langle x'', m'' \rangle$

$\rightarrow \text{actions-ok } s t ta' \rightarrow \text{actions-ok } s t ta''$

$\rightarrow ta' = ta'' \wedge x' = x'' \wedge m' = m'' \wedge \text{invariant3p } \text{redT } I$

lemma *deterministicI*:

$\llbracket \bigwedge s t x ta' x' m' ta'' x'' m''.$

$\llbracket s \in I; \text{thr } s t = \lfloor (x, \text{no-wait-locks}) \rfloor;$

$t \vdash \langle x, \text{shr } s \rangle -ta' \rightarrow \langle x', m' \rangle; t \vdash \langle x, \text{shr } s \rangle -ta'' \rightarrow \langle x'', m'' \rangle;$

$\text{actions-ok } s t ta'; \text{actions-ok } s t ta'' \rrbracket$

$\implies ta' = ta'' \wedge x' = x'' \wedge m' = m'';$

$\text{invariant3p } \text{redT } I \rrbracket$

$\implies \text{deterministic } I$

unfolding *deterministic-def* **by** *blast*

lemma *deterministicD*:

$\llbracket \text{deterministic } I;$

$t \vdash \langle x, \text{shr } s \rangle -ta' \rightarrow \langle x', m' \rangle; t \vdash \langle x, \text{shr } s \rangle -ta'' \rightarrow \langle x'', m'' \rangle;$

$\text{thr } s t = \lfloor (x, \text{no-wait-locks}) \rfloor; \text{actions-ok } s t ta'; \text{actions-ok } s t ta''; s \in I \rrbracket$

$\implies ta' = ta'' \wedge x' = x'' \wedge m' = m''$

unfolding *deterministic-def* **by** *blast*

lemma *deterministic-invariant3p*:

$\text{deterministic } I \implies \text{invariant3p } \text{redT } I$

unfolding *deterministic-def* **by** *blast*

lemma *deterministic-THE*:

$\llbracket \text{deterministic } I; \text{thr } s t = \lfloor (x, \text{no-wait-locks}) \rfloor; t \vdash \langle x, \text{shr } s \rangle -ta \rightarrow \langle x', m' \rangle; \text{actions-ok } s t ta; s \in I \rrbracket$

$\implies (\text{THE } (ta, x', m'). t \vdash \langle x, \text{shr } s \rangle -ta \rightarrow \langle x', m' \rangle \wedge \text{actions-ok } s t ta) = (ta, x', m')$

by(*rule the-equality*)(*blast dest: deterministicD*)⁺

end

locale *multithreaded* = *multithreaded-base* +

constrains *final* :: $'x \Rightarrow$ bool

and $r :: (l, t, x, m, w, o)$ semantics

and *convert-RA* :: l released-locks \Rightarrow o list

assumes *new-thread-memory*: $\llbracket t \vdash s -ta \rightarrow s'; \text{NewThread } t' x m \in \text{set } \{ta\}_t \rrbracket \implies m = \text{snd } s'$

and *final-no-red*: $\llbracket t \vdash (x, m) \text{--}ta \rightarrow (x', m'); \text{final } x \rrbracket \implies \text{False}$
begin

lemma *redT-new-thread-common*:

$\llbracket s \text{--}t \triangleright ta \rightarrow s'; \text{NewThread } t' x m'' \in \text{set } \{\{ta\}_t; \{ta\}_w = [] \rrbracket \implies m'' = \text{shr } s'$
by(*auto elim!*: *redT-elim*s *rtrancl3p-cases* *dest*: *new-thread-memory*)

lemma *redT-new-thread*:

assumes $s \text{--}t \triangleright ta \rightarrow s'$ **thr** $s' t = \llbracket (x, w) \rrbracket$ **thr** $s t = \text{None}$ $\{ta\}_w = []$
shows $\text{NewThread } t x (\text{shr } s') \in \text{set } \{\{ta\}_t\} \wedge w = \text{no-wait-locks}$

using *assms*

apply(*cases* *rule*: *redT-elim*s)

apply(*auto split*: *if-split-asm* *del*: *conjI* *elim!*: *rtrancl3p-cases*)

apply(*drule* (2) *redT-updTs-new-thread*)

apply(*auto* *dest*: *new-thread-memory*)

done

lemma *final-no-redT*:

$\llbracket s \text{--}t \triangleright ta \rightarrow s'; \text{thr } s t = \llbracket (x, \text{no-wait-locks}) \rrbracket \rrbracket \implies \neg \text{final } x$
by(*auto elim!*: *redT-elim*s *dest*: *final-no-red*)

lemma *mfinal-no-redT*:

assumes *redT*: $s \text{--}t \triangleright ta \rightarrow s'$ **and** *mfinal*: *mfinal* s
shows *False*

using *redT mfinalD*[*OF mfinal*, *of t*]

by *cases* (*metis final-no-red*, *metis neg-no-wait-locks-conv*)

end

end

1.11 Auxiliary definitions for the progress theorem for the multithreaded semantics

theory *FWProgressAux*

imports

FWSemantics

begin

abbreviation *collect-waits* :: (l, t, x, m, w, o) *thread-action* $\Rightarrow (l + t + x)$ *set*

where *collect-waits* $ta \equiv \text{collect-locks } \{\{ta\}_l\} \langle + \rangle \text{collect-cond-actions } \{\{ta\}_c\} \langle + \rangle \text{collect-interrupts } \{\{ta\}_i\}$

lemma *collect-waits-unfold*:

collect-waits $ta = \{l. \text{Lock} \in \text{set } (\{\{ta\}_l\} \$ l)\} \langle + \rangle \{t. \text{Join } t \in \text{set } \{\{ta\}_c\}\} \langle + \rangle \text{collect-interrupts } \{\{ta\}_i\}$

by(*simp* *add*: *collect-locks-def*)

context *multithreaded-base* **begin**

definition *must-sync* :: $t \Rightarrow x \Rightarrow m \Rightarrow \text{bool}$ ($\text{--} \vdash \langle -, / - \rangle / \text{--} \triangleright [50, 0, 0] 81$) **where**

$t \vdash \langle x, m \rangle \text{--} \langle \longleftrightarrow (\exists ta x' m' s. t \vdash \langle x, m \rangle \text{--}ta \rightarrow \langle x', m' \rangle \wedge \text{shr } s = m \wedge \text{actions-ok } s t ta)$

lemma *must-sync-def2*:

$t \vdash \langle x, m \rangle \wr \iff (\exists ta\ x' m' s. t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle \wedge \text{actions-ok } s\ t\ ta)$

by(*fastforce simp add: must-sync-def intro: cond-action-oks-shr-change*)

lemma *must-syncI*:

$\exists ta\ x' m' s. t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle \wedge \text{actions-ok } s\ t\ ta \implies t \vdash \langle x, m \rangle \wr$

by(*fastforce simp add: must-sync-def2*)

lemma *must-syncE*:

$\llbracket t \vdash \langle x, m \rangle \wr; \bigwedge ta\ x' m' s. \llbracket t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle; \text{actions-ok } s\ t\ ta; m = \text{shr } s \rrbracket \implies \text{thesis} \rrbracket$
 $\implies \text{thesis}$

by(*fastforce simp only: must-sync-def*)

definition *can-sync* :: $'t \Rightarrow 'x \Rightarrow 'm \Rightarrow ('l + 't + 't)\ \text{set} \Rightarrow \text{bool}$ ($\langle \cdot \vdash \langle \cdot, \cdot \rangle / \cdot \rangle / \cdot \rangle$) [50,0,0,0] 81
where

$t \vdash \langle x, m \rangle\ LT \wr \equiv \exists ta\ x' m'. t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle \wedge (LT = \text{collect-waits } ta)$

lemma *can-syncI*:

$\llbracket t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle;$
 $LT = \text{collect-waits } ta \rrbracket$
 $\implies t \vdash \langle x, m \rangle\ LT \wr$

by(*cases ta*)(*fastforce simp add: can-sync-def*)

lemma *can-syncE*:

assumes $t \vdash \langle x, m \rangle\ LT \wr$
obtains $ta\ x' m'$
where $t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle$
and $LT = \text{collect-waits } ta$
using *assms*

by(*clarsimp simp add: can-sync-def*)

inductive-set *active-threads* :: $('l, 't, 'x, 'm, 'w)\ \text{state} \Rightarrow 't\ \text{set}$

for $s :: ('l, 't, 'x, 'm, 'w)\ \text{state}$

where

normal:

$\bigwedge ln. \llbracket \text{thr } s\ t = \text{Some } (x, ln);$
 $ln = \text{no-wait-locks};$
 $t \vdash (x, \text{shr } s) -ta \rightarrow x' m';$
 $\text{actions-ok } s\ t\ ta \rrbracket$
 $\implies t \in \text{active-threads } s$

| *acquire*:

$\bigwedge ln. \llbracket \text{thr } s\ t = \text{Some } (x, ln);$
 $ln \neq \text{no-wait-locks};$
 $\neg \text{waiting } (wset\ s\ t);$
 $\text{may-acquire-all } (locks\ s)\ t\ ln \rrbracket$
 $\implies t \in \text{active-threads } s$

lemma *active-threads-iff*:

active-threads $s =$

$\{t. \exists x\ ln. \text{thr } s\ t = \text{Some } (x, ln) \wedge$
 $(\text{if } ln = \text{no-wait-locks}$
 $\text{then } \exists ta\ x' m'. t \vdash (x, \text{shr } s) -ta \rightarrow (x', m') \wedge \text{actions-ok } s\ t\ ta$
 $\text{else } \neg \text{waiting } (wset\ s\ t) \wedge \text{may-acquire-all } (locks\ s)\ t\ ln)\}$

apply(*auto elim!: active-threads.cases intro: active-threads.intros*)

apply blast
done

lemma *active-thread-ex-red*:
 assumes $t \in \text{active-threads } s$
 shows $\exists ta\ s'.\ s \text{ --t> } ta \rightarrow s'$
 using *assms*
 proof cases
 case (*normal* $x\ ta\ x'm'\ ln$)
 with *redT-updWs-total*[of $t\ wset\ s\ \{ta\}_w$]
 show ?thesis
 by(*cases* $x'm'$)(*fastforce* *intro!*: *redT-normal simp del: split-paired-Ex*)
 next
 case *acquire thus* ?thesis
 by(*fastforce* *intro*: *redT-acquire simp del: split-paired-Ex simp add: neq-no-wait-locks-conv*)
 qed
 end

Well-formedness conditions for final

context *final-thread* begin

inductive *not-final-thread* :: ($'l, 't, 'x, 'm, 'w$) *state* $\Rightarrow 't \Rightarrow \text{bool}$
 for $s :: ('l, 't, 'x, 'm, 'w)$ *state* and $t :: 't$ where
not-final-thread-final: $\bigwedge ln.\ \llbracket \text{thr } s\ t = \lfloor(x, ln)\rfloor; \neg \text{final } x \rrbracket \Longrightarrow \text{not-final-thread } s\ t$
 | *not-final-thread-wait-locks*: $\bigwedge ln.\ \llbracket \text{thr } s\ t = \lfloor(x, ln)\rfloor; ln \neq \text{no-wait-locks} \rrbracket \Longrightarrow \text{not-final-thread } s\ t$
 | *not-final-thread-wait-set*: $\bigwedge ln.\ \llbracket \text{thr } s\ t = \lfloor(x, ln)\rfloor; wset\ s\ t = \lfloor w \rrbracket \rrbracket \Longrightarrow \text{not-final-thread } s\ t$

declare *not-final-thread.cases* [*elim*]

lemmas *not-final-thread-cases* = *not-final-thread.cases* [*consumes 1, case-names final wait-locks wait-set*]

lemma *not-final-thread-cases2* [*consumes 2, case-names final wait-locks wait-set*]:
 $\bigwedge ln.\ \llbracket \text{not-final-thread } s\ t; \text{thr } s\ t = \lfloor(x, ln)\rfloor;$
 $\neg \text{final } x \Longrightarrow \text{thesis}; ln \neq \text{no-wait-locks} \Longrightarrow \text{thesis}; \bigwedge w.\ wset\ s\ t = \lfloor w \rrbracket \Longrightarrow \text{thesis} \rrbracket$
 $\Longrightarrow \text{thesis}$
 by(*auto*)

lemma *not-final-thread-iff*:
 $\text{not-final-thread } s\ t \iff (\exists x\ ln.\ \text{thr } s\ t = \lfloor(x, ln)\rfloor \wedge (\neg \text{final } x \vee ln \neq \text{no-wait-locks} \vee (\exists w.\ wset\ s\ t = \lfloor w \rrbracket)))$
 by(*auto* *intro: not-final-thread.intros*)

lemma *not-final-thread-conv*:
 $\text{not-final-thread } s\ t \iff \text{thr } s\ t \neq \text{None} \wedge \neg \text{final-thread } s\ t$
 by(*auto* *simp add: final-thread-def* *intro: not-final-thread.intros*)

lemma *not-final-thread-existsE*:
 assumes *not-final-thread* $s\ t$
 and $\bigwedge x\ ln.\ \text{thr } s\ t = \lfloor(x, ln)\rfloor \Longrightarrow \text{thesis}$
 shows *thesis*
 using *assms* by *blast*

lemma *not-final-thread-final-thread-conv*:

$thr\ s\ t \neq None \implies \neg final\text{-thread}\ s\ t \iff not\text{-final}\text{-thread}\ s\ t$
by(*simp add: not-final-thread-iff final-thread-def*)

lemma *may-join-cond-action-oks*:

assumes $\bigwedge t'. Join\ t' \in set\ cas \implies \neg not\text{-final}\text{-thread}\ s\ t' \wedge t \neq t'$
shows *cond-action-oks s t cas*

using *assms*

proof (*induct cas*)

case *Nil* **thus** *?case* **by** *clarsimp*

next

case (*Cons ca cas*)

note $IH = \langle \llbracket \bigwedge t'. Join\ t' \in set\ cas \implies \neg not\text{-final}\text{-thread}\ s\ t' \wedge t \neq t' \rrbracket \implies cond\text{-action}\text{-oks}\ s\ t\ cas \rangle$

note $ass = \langle \bigwedge t'. Join\ t' \in set\ (ca\ \# \ cas) \implies \neg not\text{-final}\text{-thread}\ s\ t' \wedge t \neq t' \rangle$

hence $\bigwedge t'. Join\ t' \in set\ cas \implies \neg not\text{-final}\text{-thread}\ s\ t' \wedge t \neq t'$ **by** *simp*

hence *cond-action-oks s t cas* **by**(*rule IH*)

moreover **have** *cond-action-ok s t ca*

proof(*cases ca*)

case (*Join t'*)

with *ass* **have** $\neg not\text{-final}\text{-thread}\ s\ t'\ t \neq t'$ **by** *auto*

thus *?thesis* **using** *Join* **by**(*auto simp add: not-final-thread-iff*)

next

case *Yield* **thus** *?thesis* **by** *simp*

qed

ultimately **show** *?case* **by** *simp*

qed

end

context *multithreaded* **begin**

lemma *red-not-final-thread*:

$s -t \triangleright ta \rightarrow s' \implies not\text{-final}\text{-thread}\ s\ t$

by(*fastforce elim: redT.cases intro: not-final-thread.intros dest: final-no-red*)

lemma *redT-preserves-final-thread*:

$\llbracket s -t \triangleright ta \rightarrow s'; final\text{-thread}\ s\ t \rrbracket \implies final\text{-thread}\ s'\ t$

apply(*erule redT.cases*)

apply(*clarsimp simp add: final-thread-def*)

apply(*auto simp add: final-thread-def dest: redT-updTs-None redT-updTs-Some final-no-red intro: redT-updWs-None*)

done

end

context *multithreaded-base* **begin**

definition *wset-Suspend-ok* :: (l, t, x, m, w) *state set* \Rightarrow (l, t, x, m, w) *state set*

where

wset-Suspend-ok I =

$\{s. s \in I \wedge$

$(\forall t \in dom\ (wset\ s). \exists s0 \in I. \exists s1 \in I. \exists ttas\ x\ x0\ ta\ w'\ ln'\ ln''. s0 -t \triangleright ta \rightarrow s1 \wedge s1 -\triangleright ttas \rightarrow * s \wedge$
 $thr\ s0\ t = \llbracket (x0, no\text{-wait}\text{-locks} \rrbracket) \wedge t \vdash \langle x0, shr\ s0 \rangle -ta \rightarrow \langle x, shr\ s1 \rangle \wedge Suspend\ w' \in set$

$\llbracket ta \rrbracket_w \wedge$

$$\text{actions-ok } s0 \ t \ ta \wedge \text{thr } s1 \ t = \llbracket (x, \text{ln}') \rrbracket \wedge \text{thr } s \ t = \llbracket (x, \text{ln}'') \rrbracket \}$$

lemma *wset-Suspend-okI*:

$\llbracket s \in I;$
 $\bigwedge t \ w. \text{wset } s \ t = \llbracket w \rrbracket \implies \exists s0 \in I. \exists s1 \in I. \exists \text{ttas } x \ x0 \ ta \ w' \ \text{ln}' \ \text{ln}''. \ s0 \ -t \triangleright ta \rightarrow s1 \wedge s1 \ -\triangleright \text{ttas} \rightarrow * s \wedge$
 $\text{thr } s0 \ t = \llbracket (x0, \text{no-wait-locks}) \rrbracket \wedge t \vdash \langle x0, \text{shr } s0 \rangle \ -ta \rightarrow \langle x, \text{shr } s1 \rangle \wedge \text{Suspend } w' \in \text{set}$
 $\{\{ta\}\}_w \wedge$
 $\text{actions-ok } s0 \ t \ ta \wedge \text{thr } s1 \ t = \llbracket (x, \text{ln}') \rrbracket \wedge \text{thr } s \ t = \llbracket (x, \text{ln}'') \rrbracket \rrbracket$
 $\implies s \in \text{wset-Suspend-ok } I$

unfolding *wset-Suspend-ok-def* **by** *blast*

lemma *wset-Suspend-okD1*:

$s \in \text{wset-Suspend-ok } I \implies s \in I$

unfolding *wset-Suspend-ok-def* **by** *blast*

lemma *wset-Suspend-okD2*:

$\llbracket s \in \text{wset-Suspend-ok } I; \text{wset } s \ t = \llbracket w \rrbracket \rrbracket$
 $\implies \exists s0 \in I. \exists s1 \in I. \exists \text{ttas } x \ x0 \ ta \ w' \ \text{ln}' \ \text{ln}''. \ s0 \ -t \triangleright ta \rightarrow s1 \wedge s1 \ -\triangleright \text{ttas} \rightarrow * s \wedge$
 $\text{thr } s0 \ t = \llbracket (x0, \text{no-wait-locks}) \rrbracket \wedge t \vdash \langle x0, \text{shr } s0 \rangle \ -ta \rightarrow \langle x, \text{shr } s1 \rangle \wedge \text{Suspend } w' \in \text{set}$
 $\{\{ta\}\}_w \wedge$
 $\text{actions-ok } s0 \ t \ ta \wedge \text{thr } s1 \ t = \llbracket (x, \text{ln}') \rrbracket \wedge \text{thr } s \ t = \llbracket (x, \text{ln}'') \rrbracket \rrbracket$

unfolding *wset-Suspend-ok-def* **by** *blast*

lemma *wset-Suspend-ok-imp-wset-thread-ok*:

$s \in \text{wset-Suspend-ok } I \implies \text{wset-thread-ok } (\text{wset } s) \ (\text{thr } s)$

apply(*rule wset-thread-okI*)

apply(*rule ccontr*)

apply(*auto dest: wset-Suspend-okD2*)

done

lemma *invariant3p-wset-Suspend-ok*:

assumes *I: invariant3p redT I*

shows *invariant3p redT (wset-Suspend-ok I)*

proof(*rule invariant3pI*)

fix *s tl s'*

assume *wso: s \in wset-Suspend-ok I*

and *redT s tl s'*

moreover obtain *t' ta* **where** *tl: tl = (t', ta)* **by**(*cases tl*)

ultimately have *red: s -t' \triangleright ta \rightarrow s'* **by** *simp*

moreover from *wso* **have** *s \in I* **by**(*rule wset-Suspend-okD1*)

ultimately have *s' \in I* **by**(*rule invariant3pD[OF I]*)

thus *s' \in wset-Suspend-ok I*

proof(*rule wset-Suspend-okI*)

fix *t w*

assume *ws't: wset s' t = \llbracket w \rrbracket*

show $\exists s0 \in I. \exists s1 \in I. \exists \text{ttas } x \ x0 \ ta \ w' \ \text{ln}' \ \text{ln}''. \ s0 \ -t \triangleright ta \rightarrow s1 \wedge s1 \ -\triangleright \text{ttas} \rightarrow * s' \wedge$

$\text{thr } s0 \ t = \llbracket (x0, \text{no-wait-locks}) \rrbracket \wedge t \vdash \langle x0, \text{shr } s0 \rangle \ -ta \rightarrow \langle x, \text{shr } s1 \rangle \wedge$

$\text{Suspend } w' \in \text{set } \{\{ta\}\}_w \wedge \text{actions-ok } s0 \ t \ ta \wedge$

$\text{thr } s1 \ t = \llbracket (x, \text{ln}') \rrbracket \wedge \text{thr } s' \ t = \llbracket (x, \text{ln}'') \rrbracket$

proof(*cases t = t'*)

case *False*

with *red ws't* **obtain** *w'* **where** *wst: wset s t = \llbracket w' \rrbracket*

by *cases(auto 4 4 dest: redT-updWs-Some-otherD split: wait-set-status.split-asm)*

from *wset-Suspend-okD2*[*OF wso this*] **obtain** $s0\ s1\ ttas\ x\ x0\ ta'\ w'\ ln'\ ln''$
where *reuse*: $s0 \in I\ s1 \in I\ s0 \dashv\rightarrow ta' \rightarrow s1\ thr\ s0\ t = \llbracket(x0, no-wait-locks)\rrbracket$
 $t \vdash \langle x0, shr\ s0 \rangle \dashv\rightarrow \langle x, shr\ s1 \rangle\ Suspend\ w' \in set\ \{\{ta'\}_w\}\ actions-ok\ s0\ t\ ta'\ thr\ s1\ t = \llbracket(x,$
 $ln')\rrbracket$
and *step*: $s1 \dashv\rightarrow ttas \rightarrow^* s$ **and** *tst*: $thr\ s\ t = \llbracket(x, ln'')\rrbracket$ **by** *blast*
from *step red* **have** $s1 \dashv\rightarrow ttas @ \llbracket(t', ta)\rrbracket \rightarrow^* s'$ **unfolding** *RedT-def* **by**(*rule rtrancl3p-step*)
moreover **from** *red tst False* **have** $thr\ s'\ t = \llbracket(x, ln'')\rrbracket$
by(*cases*)(*auto intro: redT-updTs-Some*)
ultimately show *?thesis* **using** *reuse* **by** *blast*
next
case *True*
from *red* **show** *?thesis*
proof(*cases*)
case (*redT-normal* $x\ x'\ m$)
note $red' = \langle t' \vdash \langle x, shr\ s \rangle \dashv\rightarrow \langle x', m \rangle \rangle$
and $tst' = \langle thr\ s\ t' = \llbracket(x, no-wait-locks)\rrbracket \rangle$
and $aok = \langle actions-ok\ s\ t'\ ta \rangle$
and $s' = \langle redT-upd\ s\ t'\ ta\ x'\ m\ s' \rangle$
from s' **have** $ws': redT-updWs\ t'\ (wset\ s)\ \{\{ta\}_w\}\ (wset\ s')$
and $m: m = shr\ s'$
and $ts't: thr\ s'\ t' = \llbracket(x', redT-updLns\ (locks\ s)\ t'\ (snd\ (the\ (thr\ s\ t')))\ \{\{ta\}_l\})\rrbracket$ **by** *auto*
from *aok* **have** *nwait*: $\neg\ waiting\ (wset\ s\ t')$
by(*auto simp add: wset-actions-ok-def waiting-def split: if-split-asm*)
have $\exists w'. Suspend\ w' \in set\ \{\{ta\}_w\}$
proof(*cases wset s t*)
case *None*
from *redT-updWs-None-SomeD*[*OF ws', OF ws't None*]
show *?thesis ..*
next
case (*Some w'*)
with *True aok* **have** $Notified \in set\ \{\{ta\}_w\} \vee WokenUp \in set\ \{\{ta\}_w\}$
by(*auto simp add: wset-actions-ok-def split: if-split-asm*)
with ws' **show** *?thesis* **using** $ws't$ **unfolding** *True*
by(*rule redT-updWs-WokenUp-SuspendD*)
qed
with $tst'\ ts't\ aok\ \langle s \in I \rangle\ \langle s' \in I \rangle$ *red red'* **show** *?thesis*
unfolding *True m* **by** *blast*
next
case (*redT-acquire* $x\ n\ ln$)
with $ws't\ True$ **have** $wset\ s\ t = \llbracket w \rrbracket$ **by** *auto*
from *wset-Suspend-okD2*[*OF wso this*] $\langle thr\ s\ t' = \llbracket(x, ln)\rrbracket \rangle$ *True*
obtain $s0\ s1\ ttas\ x0\ ta'\ w'\ ln'\ ln''$
where *reuse*: $s0 \in I\ s1 \in I\ s0 \dashv\rightarrow ta' \rightarrow s1\ thr\ s0\ t = \llbracket(x0, no-wait-locks)\rrbracket$
 $t \vdash \langle x0, shr\ s0 \rangle \dashv\rightarrow \langle x, shr\ s1 \rangle\ Suspend\ w' \in set\ \{\{ta'\}_w\}\ actions-ok\ s0\ t\ ta'\ thr\ s1\ t =$
 $\llbracket(x, ln')\rrbracket$
and *step*: $s1 \dashv\rightarrow ttas \rightarrow^* s$ **by** *fastforce*
from *step red* **have** $s1 \dashv\rightarrow ttas @ \llbracket(t', ta)\rrbracket \rightarrow^* s'$ **unfolding** *RedT-def* **by**(*rule rtrancl3p-step*)
moreover **from** *redT-acquire True* **have** $thr\ s'\ t = \llbracket(x, no-wait-locks)\rrbracket$ **by** *simp*
ultimately show *?thesis* **using** *reuse* **by** *blast*
qed
qed
qed
qed

end

end

1.12 Deadlock formalisation

theory *FWDeadlock*

imports

FWProgressAux

begin

context *final-thread* **begin**

definition *all-final-except* :: (*l, 't, 'x, 'm, 'w*) *state* \Rightarrow *'t set* \Rightarrow *bool* **where**
all-final-except s Ts $\equiv \forall t. \text{not-final-thread } s \ t \longrightarrow t \in Ts$

lemma *all-final-except-mono* [*mono*]:

$(\bigwedge x. x \in A \longrightarrow x \in B) \Longrightarrow \text{all-final-except } ts \ A \longrightarrow \text{all-final-except } ts \ B$
by(*auto simp add: all-final-except-def*)

lemma *all-final-except-mono'*:

$\llbracket \text{all-final-except } ts \ A; \bigwedge x. x \in A \Longrightarrow x \in B \rrbracket \Longrightarrow \text{all-final-except } ts \ B$
by(*blast intro: all-final-except-mono[rule-format]*)

lemma *all-final-exceptI*:

$(\bigwedge t. \text{not-final-thread } s \ t \Longrightarrow t \in Ts) \Longrightarrow \text{all-final-except } s \ Ts$
by(*auto simp add: all-final-except-def*)

lemma *all-final-exceptD*:

$\llbracket \text{all-final-except } s \ Ts; \text{not-final-thread } s \ t \rrbracket \Longrightarrow t \in Ts$
by(*auto simp add: all-final-except-def*)

inductive *must-wait* :: (*l, 't, 'x, 'm, 'w*) *state* \Rightarrow *'t* \Rightarrow (*l + 't + 't*) \Rightarrow *'t set* \Rightarrow *bool*

for *s* :: (*l, 't, 'x, 'm, 'w*) *state* **and** *t* :: *'t* **where**

— *Lock l*

$\llbracket \text{has-lock } (locks \ s \ \$ \ l) \ t'; \ t' \neq t; \ t' \in Ts \rrbracket \Longrightarrow \text{must-wait } s \ t \ (Inl \ l) \ Ts$

| — *Join t'*

$\llbracket \text{not-final-thread } s \ t'; \ t' \in Ts \rrbracket \Longrightarrow \text{must-wait } s \ t \ (Inr \ (Inl \ t')) \ Ts$

| — *IsInterrupted t' True*

$\llbracket \text{all-final-except } s \ Ts; \ t' \notin \text{interrupts } s \rrbracket \Longrightarrow \text{must-wait } s \ t \ (Inr \ (Inr \ t')) \ Ts$

declare *must-wait.cases* [*elim*]

declare *must-wait.intros* [*intro*]

lemma *must-wait-elim*s [*consumes 1, case-names lock join interrupt, cases pred*]:

assumes *must-wait s t lt Ts*

obtains *l t'* **where** *lt = Inl l has-lock (locks s \$ l) t' t' \neq t t' \in Ts*

| *t'* **where** *lt = Inr (Inl t') not-final-thread s t' t' \in Ts*

| *t'* **where** *lt = Inr (Inr t') all-final-except s Ts t' \notin interrupts s*

using *assms*

by(*auto*)

inductive-cases *must-wait-elim2* [*elim!*]:

must-wait s t (Inl l) Ts
must-wait s t (Inr (Inl t')) Ts
must-wait s t (Inr (Inr t')) Ts

lemma *must-wait-iff*:

must-wait s t lt Ts \longleftrightarrow
 (case *lt* of *Inl l* $\Rightarrow \exists t' \in Ts. t \neq t' \wedge \text{has-lock } (\text{locks } s \ \$ l) t'$
 | *Inr (Inl t')* $\Rightarrow \text{not-final-thread } s t' \wedge t' \in Ts$
 | *Inr (Inr t')* $\Rightarrow \text{all-final-except } s Ts \wedge t' \notin \text{interrupts } s$)

by(*auto simp add: must-wait.simps split: sum.splits*)

end

Deadlock as a system-wide property

context *multithreaded-base* **begin**

definition

deadlock :: (*l, 't, 'x, 'm, 'w*) *state* \Rightarrow *bool*

where

deadlock s
 $\equiv (\forall t x. \text{thr } s t = \lfloor (x, \text{no-wait-locks}) \rfloor \wedge \neg \text{final } x \wedge \text{wset } s t = \text{None}$
 $\longrightarrow t \vdash \langle x, \text{shr } s \rangle \wr \wedge (\forall LT. t \vdash \langle x, \text{shr } s \rangle LT \wr \longrightarrow (\exists lt \in LT. \text{must-wait } s t lt (\text{dom } (\text{thr } s))))$
 $\wedge (\forall t x \text{ln}. \text{thr } s t = \lfloor (x, \text{ln}) \rfloor \wedge (\exists l. \text{ln } \$ l > 0) \wedge \neg \text{waiting } (\text{wset } s t)$
 $\longrightarrow (\exists l t'. \text{ln } \$ l > 0 \wedge t \neq t' \wedge \text{thr } s t' \neq \text{None} \wedge \text{has-lock } (\text{locks } s \ \$ l) t')$
 $\wedge (\forall t x w. \text{thr } s t = \lfloor (x, \text{no-wait-locks}) \rfloor \longrightarrow \text{wset } s t \neq \lfloor \text{PostWS } w \rfloor)$

lemma *deadlockI*:

$\llbracket \bigwedge t x. \llbracket \text{thr } s t = \lfloor (x, \text{no-wait-locks}) \rfloor; \neg \text{final } x; \text{wset } s t = \text{None} \rrbracket$
 $\implies t \vdash \langle x, \text{shr } s \rangle \wr \wedge (\forall LT. t \vdash \langle x, \text{shr } s \rangle LT \wr \longrightarrow (\exists lt \in LT. \text{must-wait } s t lt (\text{dom } (\text{thr } s))))$
 $\bigwedge t x \text{ln } l. \llbracket \text{thr } s t = \lfloor (x, \text{ln}) \rfloor; \text{ln } \$ l > 0; \neg \text{waiting } (\text{wset } s t) \rrbracket$
 $\implies \exists l t'. \text{ln } \$ l > 0 \wedge t \neq t' \wedge \text{thr } s t' \neq \text{None} \wedge \text{has-lock } (\text{locks } s \ \$ l) t'$
 $\bigwedge t x w. \text{thr } s t = \lfloor (x, \text{no-wait-locks}) \rfloor \implies \text{wset } s t \neq \lfloor \text{PostWS } w \rfloor$
 $\implies \text{deadlock } s$

by(*auto simp add: deadlock-def*)

lemma *deadlockE*:

assumes *deadlock s*

obtains $\forall t x. \text{thr } s t = \lfloor (x, \text{no-wait-locks}) \rfloor \wedge \neg \text{final } x \wedge \text{wset } s t = \text{None}$

$\longrightarrow t \vdash \langle x, \text{shr } s \rangle \wr \wedge (\forall LT. t \vdash \langle x, \text{shr } s \rangle LT \wr \longrightarrow (\exists lt \in LT. \text{must-wait } s t lt (\text{dom } (\text{thr } s))))$

and $\forall t x \text{ln}. \text{thr } s t = \lfloor (x, \text{ln}) \rfloor \wedge (\exists l. \text{ln } \$ l > 0) \wedge \neg \text{waiting } (\text{wset } s t)$

$\longrightarrow (\exists l t'. \text{ln } \$ l > 0 \wedge t \neq t' \wedge \text{thr } s t' \neq \text{None} \wedge \text{has-lock } (\text{locks } s \ \$ l) t')$

and $\forall t x w. \text{thr } s t = \lfloor (x, \text{no-wait-locks}) \rfloor \longrightarrow \text{wset } s t \neq \lfloor \text{PostWS } w \rfloor$

using *assms unfolding deadlock-def* **by**(*blast*)

lemma *deadlockD1*:

assumes *deadlock s*

and *thr s t =* $\lfloor (x, \text{no-wait-locks}) \rfloor$

and $\neg \text{final } x$

and *wset s t = None*

obtains $t \vdash \langle x, \text{shr } s \rangle \wr$

and $\forall LT. t \vdash \langle x, \text{shr } s \rangle LT \wr \longrightarrow (\exists lt \in LT. \text{must-wait } s t lt (\text{dom } (\text{thr } s)))$

using *assms unfolding deadlock-def* **by**(*blast*)

lemma *deadlockD2*:

fixes ln
assumes *deadlock s*
and $thr\ s\ t = \lfloor(x, ln)\rfloor$
and $ln\ \$\ l > 0$
and $\neg\ waiting\ (wset\ s\ t)$
obtains $l'\ t'$ **where** $ln\ \$\ l' > 0\ t \neq t'\ thr\ s\ t' \neq None\ has\ lock\ (locks\ s\ \$\ l')\ t'$
using *assms* **unfolding** *deadlock-def* **by** *blast*

lemma *deadlockD3*:

assumes *deadlock s*
and $thr\ s\ t = \lfloor(x, no\ wait\ locks)\rfloor$
shows $\forall w. wset\ s\ t \neq \lfloor PostWS\ w\rfloor$
using *assms* **unfolding** *deadlock-def* **by** *blast*

lemma *deadlock-def2*:

$deadlock\ s \iff$
 $(\forall t\ x. thr\ s\ t = \lfloor(x, no\ wait\ locks)\rfloor \wedge \neg\ final\ x \wedge wset\ s\ t = None$
 $\longrightarrow t \vdash \langle x, shr\ s \rangle \wr \wedge (\forall LT. t \vdash \langle x, shr\ s \rangle LT \wr \longrightarrow (\exists lt \in LT. must\ wait\ s\ t\ lt\ (dom\ (thr\ s))))))$
 $\wedge (\forall t\ x\ ln. thr\ s\ t = \lfloor(x, ln)\rfloor \wedge ln \neq no\ wait\ locks \wedge \neg\ waiting\ (wset\ s\ t)$
 $\longrightarrow (\exists l. ln\ \$\ l > 0 \wedge must\ wait\ s\ t\ (Inl\ l)\ (dom\ (thr\ s))))$
 $\wedge (\forall t\ x\ w. thr\ s\ t = \lfloor(x, no\ wait\ locks)\rfloor \longrightarrow wset\ s\ t \neq \lfloor PostWS\ WSNotified\rfloor \wedge wset\ s\ t \neq \lfloor PostWS$
 $WSWokenUp\rfloor)$

unfolding *neq-no-wait-locks-conv*

apply(*rule iffI*)

apply(*intro strip conjI*)

apply(*blast dest: deadlockD1*)

apply(*blast dest: deadlockD1*)

apply(*blast elim: deadlockD2*)

apply(*blast dest: deadlockD3*)

apply(*blast dest: deadlockD3*)

apply(*elim conjE exE*)

apply(*rule deadlockI*)

apply *blast*

apply(*rotate-tac 1*)

apply(*erule allE, rotate-tac -1*)

apply(*erule allE, rotate-tac -1*)

apply(*erule allE, rotate-tac -1*)

apply(*erule impE, blast*)

apply(*elim exE conjE*)

apply(*erule must-wait.cases*)

apply(*clarify*)

apply(*rotate-tac 3*)

apply(*rule exI conjI |erule not-sym |assumption*)**+**

apply *blast*

apply *blast*

apply *blast*

apply *blast*

apply(*case-tac w*)

apply *blast*

apply *blast*

done

lemma *all-waiting-implies-deadlock*:

assumes *lock-thread-ok* (*locks s*) (*thr s*)
and normal: $\bigwedge t x. \llbracket \text{thr } s \ t = \llbracket (x, \text{no-wait-locks}) \rrbracket; \neg \text{final } x; \text{wset } s \ t = \text{None} \rrbracket$
 $\implies t \vdash \langle x, \text{shr } s \rangle \wr \wedge (\forall LT. t \vdash \langle x, \text{shr } s \rangle \text{ LT } \wr \longrightarrow (\exists lt \in LT. \text{must-wait } s \ t \ lt \ (\text{dom } (\text{thr } s))))$
and acquire: $\bigwedge t x \ln l. \llbracket \text{thr } s \ t = \llbracket (x, \ln) \rrbracket; \neg \text{waiting } (\text{wset } s \ t); \ln \ \$ \ l > 0 \rrbracket$
 $\implies \exists l'. \ln \ \$ \ l' > 0 \wedge \neg \text{may-lock } (\text{locks } s \ \$ \ l') \ t$
and wakeup: $\bigwedge t x w. \text{thr } s \ t = \llbracket (x, \text{no-wait-locks}) \rrbracket \implies \text{wset } s \ t \neq \llbracket \text{PostWS } w \rrbracket$
shows *deadlock s*
proof(*rule deadlockI*)
fix *T X*
assume *thr s T* = $\llbracket (X, \text{no-wait-locks}) \rrbracket \neg \text{final } X \text{wset } s \ T = \text{None}$
thus $T \vdash \langle X, \text{shr } s \rangle \wr \wedge (\forall LT. T \vdash \langle X, \text{shr } s \rangle \text{ LT } \wr \longrightarrow (\exists lt \in LT. \text{must-wait } s \ T \ lt \ (\text{dom } (\text{thr } s))))$
by(*rule normal*)
next
fix *T X LN l'*
assume *thr s T* = $\llbracket (X, LN) \rrbracket$
and $0 < LN \ \$ \ l'$
and wset: $\neg \text{waiting } (\text{wset } s \ T)$
from *acquire*[*OF* $\langle \text{thr } s \ T = \llbracket (X, LN) \rrbracket \rangle$ *wset*, *OF* $\langle 0 < LN \ \$ \ l' \rangle$]
obtain *l'* **where** $0 < LN \ \$ \ l' \neg \text{may-lock } (\text{locks } s \ \$ \ l') \ T$ **by** *blast*
then obtain *t'* **where** $T \neq t' \text{ has-lock } (\text{locks } s \ \$ \ l') \ t'$
unfolding *not-may-lock-conv* **by** *fastforce*
moreover with $\langle \text{lock-thread-ok } (\text{locks } s) \ (\text{thr } s) \rangle$
have *thr s t' ≠ None* **by**(*auto dest: lock-thread-okD*)
ultimately show $\exists l t'. 0 < LN \ \$ \ l \wedge T \neq t' \wedge \text{thr } s \ t' \neq \text{None} \wedge \text{has-lock } (\text{locks } s \ \$ \ l) \ t'$
using $\langle 0 < LN \ \$ \ l' \rangle$ **by**(*auto*)
qed(*rule wakeup*)

lemma *mfinal-deadlock*:
 $\text{mfinal } s \implies \text{deadlock } s$
unfolding *mfinal-def2*
by(*rule deadlockI*)(*auto simp add: final-thread-def*)

Now deadlock for single threads

lemma *must-wait-mono*:
 $(\bigwedge x. x \in A \longrightarrow x \in B) \implies \text{must-wait } s \ t \ lt \ A \longrightarrow \text{must-wait } s \ t \ lt \ B$
by(*auto simp add: must-wait-iff split: sum.split elim: all-final-except-mono'*)

lemma *must-wait-mono'*:
 $\llbracket \text{must-wait } s \ t \ lt \ A; A \subseteq B \rrbracket \implies \text{must-wait } s \ t \ lt \ B$
using *must-wait-mono*[*of A B s t lt*]
by *blast*

end

lemma *UN-mono*: $\llbracket x \in A \longrightarrow x \in A'; x \in B \longrightarrow x \in B' \rrbracket \implies x \in A \cup B \longrightarrow x \in A' \cup B'$
by *blast*

lemma *Collect-mono-conv* [*mono*]: $x \in \{x. P \ x\} \longleftrightarrow P \ x$
by *blast*

context *multithreaded-base* **begin**

coinductive-set *deadlocked* :: $(l, t, x, m, w) \text{ state} \implies t \text{ set}$

for $s :: (l, t, x, m, w)$ **state where**

deadlockedLock:

$\llbracket thr\ s\ t = \llbracket(x, no\text{-}wait\text{-}locks)\rrbracket; t \vdash \langle x, shr\ s \rangle \wr; wset\ s\ t = None;$
 $\bigwedge LT. t \vdash \langle x, shr\ s \rangle LT\ \wr \implies \exists lt \in LT. must\text{-}wait\ s\ t\ lt\ (deadlocked\ s \cup final\text{-}threads\ s) \rrbracket$
 $\implies t \in deadlocked\ s$

| *deadlockedWait:*

$\bigwedge ln. \llbracket thr\ s\ t = \llbracket(x, ln)\rrbracket; all\text{-}final\text{-}except\ s\ (deadlocked\ s); waiting\ (wset\ s\ t) \rrbracket \implies t \in deadlocked\ s$

| *deadlockedAcquire:*

$\bigwedge ln. \llbracket thr\ s\ t = \llbracket(x, ln)\rrbracket; \neg waiting\ (wset\ s\ t); ln\ \$\ l > 0; has\text{-}lock\ (locks\ s\ \$\ l)\ t'; t' \neq t;$
 $t' \in deadlocked\ s \vee final\text{-}thread\ s\ t' \rrbracket$
 $\implies t \in deadlocked\ s$

monos *must-wait-mono UN-mono*

lemma *deadlockedAcquire-must-wait:*

$\bigwedge ln. \llbracket thr\ s\ t = \llbracket(x, ln)\rrbracket; \neg waiting\ (wset\ s\ t); ln\ \$\ l > 0; must\text{-}wait\ s\ t\ (Inl\ l)\ (deadlocked\ s \cup final\text{-}threads\ s) \rrbracket$

$\implies t \in deadlocked\ s$

apply(*erule must-wait-elims*)

apply(*erule (2) deadlockedAcquire*)

apply *auto*

done

lemma *deadlocked-elims [consumes 1, case-names lock wait acquire]:*

assumes $t \in deadlocked\ s$

and *lock:* $\bigwedge x. \llbracket thr\ s\ t = \llbracket(x, no\text{-}wait\text{-}locks)\rrbracket; t \vdash \langle x, shr\ s \rangle \wr; wset\ s\ t = None;$

$\bigwedge LT. t \vdash \langle x, shr\ s \rangle LT\ \wr \implies \exists lt \in LT. must\text{-}wait\ s\ t\ lt\ (deadlocked\ s \cup final\text{-}threads\ s) \rrbracket$
 $\implies thesis$

and *wait:* $\bigwedge x\ ln. \llbracket thr\ s\ t = \llbracket(x, ln)\rrbracket; all\text{-}final\text{-}except\ s\ (deadlocked\ s); waiting\ (wset\ s\ t) \rrbracket$

$\implies thesis$

and *acquire:* $\bigwedge x\ ln\ l\ t'.$

$\llbracket thr\ s\ t = \llbracket(x, ln)\rrbracket; \neg waiting\ (wset\ s\ t); 0 < ln\ \$\ l; has\text{-}lock\ (locks\ s\ \$\ l)\ t'; t \neq t';$

$t' \in deadlocked\ s \vee final\text{-}thread\ s\ t' \rrbracket \implies thesis$

shows *thesis*

using *assms by cases blast+*

lemma *deadlocked-coinduct*

[*consumes 1, case-names deadlocked, case-conclusion deadlocked Lock Wait Acquire, coinduct set: deadlocked*]:

assumes *major:* $t \in X$

and *step:*

$\bigwedge t. t \in X \implies$

$(\exists x. thr\ s\ t = \llbracket(x, no\text{-}wait\text{-}locks)\rrbracket) \wedge t \vdash \langle x, shr\ s \rangle \wr \wedge wset\ s\ t = None \wedge$

$(\forall LT. t \vdash \langle x, shr\ s \rangle LT\ \wr \longrightarrow (\exists lt \in LT. must\text{-}wait\ s\ t\ lt\ (X \cup deadlocked\ s \cup final\text{-}threads\ s))))$

\vee

$(\exists x\ ln. thr\ s\ t = \llbracket(x, ln)\rrbracket) \wedge all\text{-}final\text{-}except\ s\ (X \cup deadlocked\ s) \wedge waiting\ (wset\ s\ t) \vee$

$(\exists x\ l\ t'\ ln. thr\ s\ t = \llbracket(x, ln)\rrbracket) \wedge \neg waiting\ (wset\ s\ t) \wedge 0 < ln\ \$\ l \wedge has\text{-}lock\ (locks\ s\ \$\ l)\ t' \wedge$

$t' \neq t \wedge ((t' \in X \vee t' \in deadlocked\ s) \vee final\text{-}thread\ s\ t')$

shows $t \in deadlocked\ s$

using *major*

proof(*coinduct*)

case (*deadlocked t*)

have $X \cup deadlocked\ s \cup final\text{-}threads\ s = \{x. x \in X \vee x \in deadlocked\ s \vee x \in final\text{-}threads\ s\}$

by *auto*
moreover have $X \cup \text{deadlocked } s = \{x. x \in X \vee x \in \text{deadlocked } s\}$ **by** *blast*
ultimately show *?case using step[OF deadlocked] by(elim disjE) simp-all*
qed

definition *deadlocked'* :: (l, t, x, m, w) *state* \Rightarrow *bool* **where**
deadlocked' s $\equiv (\forall t. \text{not-final-thread } s \ t \longrightarrow t \in \text{deadlocked } s)$

lemma *deadlocked'I*:
 $(\bigwedge t. \text{not-final-thread } s \ t \Longrightarrow t \in \text{deadlocked } s) \Longrightarrow \text{deadlocked}' \ s$
by(*auto simp add: deadlocked'-def*)

lemma *deadlocked'D2*:
 $\llbracket \text{deadlocked}' \ s; \text{not-final-thread } s \ t; t \in \text{deadlocked } s \Longrightarrow \text{thesis} \rrbracket \Longrightarrow \text{thesis}$
by(*auto simp add: deadlocked'-def*)

lemma *not-deadlocked'I*:
 $\llbracket \text{not-final-thread } s \ t; t \notin \text{deadlocked } s \rrbracket \Longrightarrow \neg \text{deadlocked}' \ s$
by(*auto dest: deadlocked'D2*)

lemma *deadlocked'-intro*:
 $\llbracket \forall t. \text{not-final-thread } s \ t \longrightarrow t \in \text{deadlocked } s \rrbracket \Longrightarrow \text{deadlocked}' \ s$
by(*rule deadlocked'I*)(*blast*)**+**

lemma *deadlocked-thread-exists*:
assumes $t \in \text{deadlocked } s$
and $\bigwedge x \text{ ln. } \text{thr } s \ t = \llbracket (x, \text{ln}) \rrbracket \Longrightarrow \text{thesis}$
shows *thesis*
using *assms*
by *cases blast***+**

end

context *multithreaded* **begin**

lemma *red-no-deadlock*:
assumes $P: s \text{ -}t\text{>}ta\text{>} s'$
and *dead*: $t \in \text{deadlocked } s$
shows *False*
proof $-$
from P **show** *False*
proof(*cases*)
case (*redT-normal* $x \ x' \ m'$)
note $\text{red} = \langle t \vdash \langle x, \text{shr } s \rangle \text{ -}ta\text{>} \langle x', m' \rangle \rangle$
note $\text{tst} = \langle \text{thr } s \ t = \llbracket (x, \text{no-wait-locks}) \rrbracket \rangle$
note $\text{aok} = \langle \text{actions-ok } s \ t \ ta \rangle$
show *False*
proof(*cases* $\exists w. \text{wset } s \ t = \llbracket \text{InWS } w \rrbracket$)
case *True* **with** aok **show** *?thesis* **by**(*auto simp add: wset-actions-ok-def split: if-split-asm*)
next
case *False*
with $\text{dead } \text{tst}$
have $\text{mle}: t \vdash \langle x, \text{shr } s \rangle \wr$
and $\text{clead}: \forall LT. t \vdash \langle x, \text{shr } s \rangle \text{ LT } \wr \longrightarrow (\exists lt \in LT. \text{must-wait } s \ t \ \text{lt} \ (\text{deadlocked } s \cup \text{final-threads}$

```

s))
  by(cases, auto simp add: waiting-def)+
let ?LT = collect-waits ta
from red have t ⊢ ⟨x, shr s⟩ ?LT ∖ by(auto intro: can-syncI)
then obtain lt where lt: lt ∈ ?LT and mw: must-wait s t lt (deadlocked s ∪ final-threads s)
  by(blast dest: cledead[rule-format])
from mw show False
proof(cases rule: must-wait-elim)
  case (lock l t')
  from ⟨lt = Inl l⟩ lt have l ∈ collect-locks {ta}_l by(auto)
  with aok have may-lock (locks s $ l) t
    by(auto elim!: collect-locksE lock-ok-las-may-lock)
  with ⟨has-lock (locks s $ l) t'⟩ have t' = t
    by(auto dest: has-lock-may-lock-t-eq)
  with ⟨t' ≠ t⟩ show False by contradiction
next
  case (join t')
  from ⟨lt = Inr (Inl t')⟩ lt have Join t' ∈ set {ta}_c by auto
  from ⟨not-final-thread s t'⟩ obtain x'' ln''
    where thr s t' = [(x'', ln'')] by(rule not-final-thread-existsE)
  moreover with ⟨Join t' ∈ set {ta}_c⟩ aok
  have final x'' ln'' = no-wait-locks wset s t' = None
    by(auto dest: cond-action-oks-Join)
  ultimately show False using ⟨not-final-thread s t'⟩ by(auto)
next
  case (interrupt t')
  from aok lt ⟨lt = Inr (Inr t')⟩
  have t' ∈ interrupts s
    by(auto intro: collect-interrupts-interrupted)
  with ⟨t' ∉ interrupts s⟩ show False by contradiction
qed
qed
next
  case (redT-acquire x n ln)
  show False
  proof(cases ∃ w. wset s t = [InWS w])
    case True with ⟨¬ waiting (wset s t)⟩ show ?thesis
      by(auto simp add: not-waiting-iff)
  next
    case False
    with dead ⟨thr s t = [(x, ln)]⟩ ⟨0 < ln $ n⟩
    obtain l t' where 0 < ln $ l t ≠ t'
      and has-lock (locks s $ l) t'
      by(cases)(fastforce simp add: waiting-def)+
    hence ¬ may-acquire-all (locks s) t ln
      by(auto elim: may-acquire-allE dest: has-lock-may-lock-t-eq)
    with ⟨may-acquire-all (locks s) t ln⟩ show ?thesis by contradiction
  qed
qed
qed
lemma deadlocked'-no-red:
  [[ s -t>ta→ s'; deadlocked' s ]] ⇒ False
apply(rule red-no-deadlock)

```

apply(*assumption*)
apply(*erule deadlocked'D2*)
by(*rule red-not-final-thread*)

lemma *not-final-thread-deadlocked-final-thread [iff]*:

$thr\ s\ t = [xln] \implies not_final_thread\ s\ t \vee t \in deadlocked\ s \vee final_thread\ s\ t$
by(*auto simp add: not-final-thread-final-thread-conv[symmetric]*)

lemma *all-waiting-deadlocked*:

assumes *not-final-thread s t*

and *lock-thread-ok (locks s) (thr s)*

and *normal: $\bigwedge t\ x. \llbracket thr\ s\ t = [(x, no_wait_locks)] \rrbracket; \neg final\ x; wset\ s\ t = None$*

$\implies t \vdash \langle x, shr\ s \rangle \wr \wedge (\forall LT. t \vdash \langle x, shr\ s \rangle LT \wr \longrightarrow (\exists lt \in LT. must_wait\ s\ t\ lt\ (final_threads\ s)))$

and *acquire: $\bigwedge t\ x\ ln\ l. \llbracket thr\ s\ t = [(x, ln)] \rrbracket; \neg waiting\ (wset\ s\ t); ln\ \$\ l > 0$*

$\implies \exists l'. ln\ \$\ l' > 0 \wedge \neg may_lock\ (locks\ s\ \$\ l')\ t$

and *wakeup: $\bigwedge t\ x\ w. thr\ s\ t = [(x, no_wait_locks)] \implies wset\ s\ t \neq [PostWS\ w]$*

shows $t \in deadlocked\ s$

proof –

from $\langle not_final_thread\ s\ t \rangle$

have $t \in \{t. not_final_thread\ s\ t\}$ **by** *simp*

thus *?thesis*

proof(*coinduct*)

case (*deadlocked z*)

hence *not-final-thread s z* **by** *simp*

then obtain $x'\ ln'$ **where** $thr\ s\ z = [(x', ln')]$ **by**(*fastforce elim!: not-final-thread-existsE*)

{

assume $wset\ s\ z = None \neg final\ x'$

and [*simp*]: $ln' = no_wait_locks$

with $\langle thr\ s\ z = [(x', ln')] \rangle$

have $z \vdash \langle x', shr\ s \rangle \wr \wedge (\forall LT. z \vdash \langle x', shr\ s \rangle LT \wr \longrightarrow (\exists lt \in LT. must_wait\ s\ z\ lt\ (final_threads\ s)))$

by(*auto dest: normal*)

then obtain $z \vdash \langle x', shr\ s \rangle \wr$

and *clnml: $\bigwedge LT. z \vdash \langle x', shr\ s \rangle LT \wr \implies \exists lt \in LT. must_wait\ s\ z\ lt\ (final_threads\ s)$* **by**(*blast*)

{ **fix** *LT*

assume $z \vdash \langle x', shr\ s \rangle LT \wr$

then obtain *lt* **where** *mw: must-wait s z lt (final-threads s)* **and** *lt: lt ∈ LT*

by(*blast dest: clnml*)

from *mw* **have** $must_wait\ s\ z\ lt\ (\{t. not_final_thread\ s\ t\} \cup deadlocked\ s \cup final_threads\ s)$

by(*blast intro: must-wait-mono'*)

with *lt* **have** $\exists lt \in LT. must_wait\ s\ z\ lt\ (\{t. not_final_thread\ s\ t\} \cup deadlocked\ s \cup final_threads\ s)$

s)

by *blast* }

with $\langle z \vdash \langle x', shr\ s \rangle \wr \rangle \langle thr\ s\ z = [(x', ln')] \rangle \langle wset\ s\ z = None \rangle$ **have** *?case* **by**(*simp*) }

note *c1 = this*

{

assume *wsz: $\neg waiting\ (wset\ s\ z)$*

and $ln' \neq no_wait_locks$

from $\langle ln' \neq no_wait_locks \rangle$ **obtain** *l* **where** $0 < ln'\ \$\ l$

by(*auto simp add: neq-no-wait-locks-conv*)

with *wsz* $\langle thr\ s\ z = [(x', ln')] \rangle$

obtain *l'* **where** $0 < ln'\ \$\ l' \neg may_lock\ (locks\ s\ \$\ l')\ z$

by(*blast dest: acquire*)


```

then obtain  $t''$  where  $t'' \neq z$  has-lock (locks  $s$   $\$$   $l'$ )  $t''$ 
  unfolding not-may-lock-conv by blast
with  $\langle$ lock-thread-ok (locks  $s$ ) (thr  $s$ ) $\rangle$ 
obtain  $x''$   $ln''$  where thr  $s$   $t'' = \lfloor(x'', ln'')\rfloor$ 
  by(auto elim!: lock-thread-ok-has-lockE)
hence (not-final-thread  $s$   $t'' \vee t'' \in$  deadlocked  $s$ )  $\vee$  final-thread  $s$   $t''$ 
  by(clarsimp simp add: not-final-thread-iff final-thread-def)
with  $wsz$   $\langle 0 < ln' \ \$$   $l' \rangle$   $\langle$ thr  $s$   $z = \lfloor(x', ln')\rfloor$  $\rangle$   $\langle t'' \neq z \rangle$  has-lock (locks  $s$   $\$$   $l'$ )  $t''$ 
have ?Acquire by simp blast
hence ?case by simp }
note  $c2 =$  this
{ fix  $w$ 
  assume waiting (wset  $s$   $z$ )
  with  $\langle$ thr  $s$   $z = \lfloor(x', ln')\rfloor$  $\rangle$ 
  have ?Wait by(clarsimp simp add: all-final-except-def)
  hence ?case by simp }
note  $c3 =$  this
from  $\langle$ not-final-thread  $s$   $z \rangle$   $\langle$ thr  $s$   $z = \lfloor(x', ln')\rfloor$  $\rangle$  show ?case
proof(cases rule: not-final-thread-cases2)
  case final show ?thesis
  proof(cases wset  $s$   $z$ )
    case None show ?thesis
    proof(cases  $ln' =$  no-wait-locks)
      case True with None final show ?thesis by(rule  $c1$ )
    next
      case False
      from None have  $\neg$  waiting (wset  $s$   $z$ ) by(simp add: not-waiting-iff)
      thus ?thesis using False by(rule  $c2$ )
    qed
  next
  case (Some  $w$ )
  show ?thesis
  proof(cases  $w$ )
    case (InWS  $w'$ )
    with Some have waiting (wset  $s$   $z$ ) by(simp add: waiting-def)
    thus ?thesis by(rule  $c3$ )
  next
    case (PostWS  $w'$ )
    with Some have  $\neg$  waiting (wset  $s$   $z$ ) by(simp add: not-waiting-iff)
    moreover from PostWS  $\langle$ thr  $s$   $z = \lfloor(x', ln')\rfloor$  $\rangle$  Some
    have  $ln' \neq$  no-wait-locks by(auto dest: wakeup)
    ultimately show ?thesis by(rule  $c2$ )
  qed
  qed
next
  case wait-locks show ?thesis
  proof(cases wset  $s$   $z$ )
    case None
    hence  $\neg$  waiting (wset  $s$   $z$ ) by(simp add: not-waiting-iff)
    thus ?thesis using wait-locks by(rule  $c2$ )
  next
    case (Some  $w$ )
    show ?thesis
    proof(cases  $w$ )

```

```

    case (InWS w')
    with Some have waiting (wset s z) by(simp add: waiting-def)
    thus ?thesis by(rule c3)
  next
    case (PostWS w')
    with Some have  $\neg$  waiting (wset s z) by(simp add: not-waiting-iff)
    moreover from PostWS  $\langle$ thr s z =  $\lfloor$ (x', ln') $\rfloor$  $\rangle$  Some
    have ln'  $\neq$  no-wait-locks by(auto dest: wakeup)
    ultimately show ?thesis by(rule c2)
  qed
qed
next
case (wait-set w)
show ?thesis
proof(cases w)
  case (InWS w')
  with wait-set have waiting (wset s z) by(simp add: waiting-def)
  thus ?thesis by(rule c3)
next
case (PostWS w')
with wait-set have  $\neg$  waiting (wset s z) by(simp add: not-waiting-iff)
moreover from PostWS  $\langle$ thr s z =  $\lfloor$ (x', ln') $\rfloor$  $\rangle$  wait-set
have ln'  $\neq$  no-wait-locks by(auto dest: wakeup[simplified])
ultimately show ?thesis by(rule c2)
qed
qed
qed
qed

```

Equivalence proof for both notions of deadlock

```

lemma deadlock-implies-deadlocked':
  assumes dead: deadlock s
  shows deadlocked' s
proof -
  show ?thesis
proof(rule deadlocked'I)
  fix t
  assume not-final-thread s t
  hence t  $\in$  {t. not-final-thread s t} ..
  thus t  $\in$  deadlocked s
proof(coinduct)
  case (deadlocked t'')
  hence not-final-thread s t'' ..
  then obtain x'' ln'' where tst'': thr s t'' =  $\lfloor$ (x'', ln'') $\rfloor$ 
  by(rule not-final-thread-existsE)
  { assume waiting (wset s t'')
  moreover
  with tst'' have nfine: not-final-thread s t''
  unfolding waiting-def
  by(blast intro: not-final-thread.intros)
  ultimately have ?case using tst''
  by(blast intro: all-final-exceptI not-final-thread-final) }
note c1 = this
{

```

```

assume wst'':  $\neg$  waiting (wset s t'')
and ln''  $\neq$  no-wait-locks
then obtain l where l: ln'' $ l > 0
by(auto simp add: neq-no-wait-locks-conv)
with dead wst'' tst'' obtain l' T
where ln'' $ l' > 0 t''  $\neq$  T
and hl: has-lock (locks s $ l') T
and tsT: thr s T  $\neq$  None
by - (erule deadlockD2)
moreover from  $\langle$ thr s T  $\neq$  None $\rangle$ 
obtain xln where tsT: thr s T = [xln] by auto
then obtain X LN where thr s T = [(X, LN)]
by(cases xln, auto)
moreover hence not-final-thread s T  $\vee$  final-thread s T
by(auto simp add: final-thread-def not-final-thread-iff)
ultimately have ?case using wst'' tst'' by blast }
note c2 = this
{ assume wset s t'' = None
and [simp]: ln'' = no-wait-locks
moreover
with  $\langle$ not-final-thread s t'' $\rangle$  tst''
have  $\neg$  final x'' by(auto)
ultimately obtain t''  $\vdash$   $\langle$ x'', shr s $\rangle$   $\wr$ 
and clnml:  $\bigwedge$ LT. t''  $\vdash$   $\langle$ x'', shr s $\rangle$  LT  $\wr \implies \exists t'$ . thr s t'  $\neq$  None  $\wedge$  ( $\exists$  lt $\in$ LT. must-wait s t'')
lt (dom (thr s)))
using  $\langle$ thr s t'' = [(x'', ln'')] $\rangle$   $\langle$ deadlock s $\rangle$ 
by(blast elim: deadlockD1)
{ fix LT
assume t''  $\vdash$   $\langle$ x'', shr s $\rangle$  LT  $\wr$ 
then obtain lt where lt: lt  $\in$  LT
and mw: must-wait s t'' lt (dom (thr s))
by(blast dest: clnml)
note mw
also have dom (thr s) = {t. not-final-thread s t}  $\cup$  deadlocked s  $\cup$  final-threads s
by(auto simp add: not-final-thread-conv dest: deadlocked-thread-exists elim: final-threadE)
finally have  $\exists$  lt $\in$ LT. must-wait s t'' lt ({t. not-final-thread s t}  $\cup$  deadlocked s  $\cup$  final-threads
s)
using lt by blast }
with  $\langle$ t''  $\vdash$   $\langle$ x'', shr s $\rangle$   $\wr$  $\rangle$  tst''  $\langle$ wset s t'' = None $\rangle$  have ?case by(simp) }
note c3 = this
from  $\langle$ not-final-thread s t'' $\rangle$  tst'' show ?case
proof(cases rule: not-final-thread-cases2)
case final show ?thesis
proof(cases wset s t'')
case None show ?thesis
proof(cases ln'' = no-wait-locks)
case True with None show ?thesis by(rule c3)
next
case False
from None have  $\neg$  waiting (wset s t'') by(simp add: not-waiting-iff)
thus ?thesis using False by(rule c2)
qed
next
case (Some w)

```

```

show ?thesis
proof(cases w)
  case (InWS w')
    with Some have waiting (wset s t'') by(simp add: waiting-def)
    thus ?thesis by(rule c1)
  next
    case (PostWS w')
    hence  $\neg$  waiting (wset s t'') using Some by(simp add: not-waiting-iff)
    moreover from PostWS Some tst''
    have  $ln'' \neq$  no-wait-locks by(auto dest: deadlockD3[OF dead])
    ultimately show ?thesis by(rule c2)
  qed
qed
next
case wait-locks show ?thesis
proof(cases waiting (wset s t''))
  case False
  thus ?thesis using wait-locks by(rule c2)
next
case True thus ?thesis by(rule c1)
qed
next
case (wait-set w)
show ?thesis
proof(cases w)
  case InWS
  with wait-set have waiting (wset s t'') by(simp add: waiting-def)
  thus ?thesis by(rule c1)
next
case (PostWS w')
  hence  $\neg$  waiting (wset s t'') using wait-set
  by(simp add: not-waiting-iff)
  moreover from PostWS wait-set tst''
  have  $ln'' \neq$  no-wait-locks by(auto dest: deadlockD3[OF dead])
  ultimately show ?thesis by(rule c2)
qed
qed
qed
qed
qed

```

lemma *deadlocked'-implies-deadlock*:

assumes *dead: deadlocked' s*

shows *deadlock s*

proof –

have *deadlocked: $\bigwedge t. \text{not-final-thread } s \ t \implies t \in \text{deadlocked } s$*

using *dead* by(rule *deadlocked'D2*)

show ?thesis

proof(rule *deadlockI*)

fix *t' x'*

assume *thr s t' = [(x', no-wait-locks)]*

and \neg *final x'*

and *wset s t' = None*

hence *not-final-thread s t'* by(auto intro: not-final-thread-final)

hence $t' \in \text{deadlocked } s$ **by**(*rule deadlocked*)
 thus $t' \vdash \langle x', \text{shr } s \rangle \wr \wedge (\forall LT. t' \vdash \langle x', \text{shr } s \rangle LT \wr \implies (\exists lt \in LT. \text{must-wait } s \ t' \ lt \ (\text{dom } (\text{thr } s))))$
proof(*cases rule: deadlocked-elim*s)
 case (*lock x''*)
 note $\text{lock} = \langle \wedge LT. t' \vdash \langle x'', \text{shr } s \rangle LT \wr \implies \exists lt \in LT. \text{must-wait } s \ t' \ lt \ (\text{deadlocked } s \cup \text{final-threads } s) \rangle$
 from $\langle \text{thr } s \ t' = [(x'', \text{no-wait-locks})] \rangle \langle \text{thr } s \ t' = [(x', \text{no-wait-locks})] \rangle$
 have [*simp*]: $x' = x''$ **by** *auto*
 { **fix** LT
 assume $t' \vdash \langle x'', \text{shr } s \rangle LT \wr$
 from $\text{lock}[OF \text{ this}]$ **obtain** lt **where** $lt: lt \in LT$
 and $\text{mw}: \text{must-wait } s \ t' \ lt \ (\text{deadlocked } s \cup \text{final-threads } s)$ **by** *blast*
 have $\text{deadlocked } s \cup \text{final-threads } s \subseteq \text{dom } (\text{thr } s)$
 by(*auto elim: final-threadE dest: deadlocked-thread-exists*)
 with mw **have** $\text{must-wait } s \ t' \ lt \ (\text{dom } (\text{thr } s))$ **by**(*rule must-wait-mono'*)
 with lt **have** $\exists lt \in LT. \text{must-wait } s \ t' \ lt \ (\text{dom } (\text{thr } s))$ **by** *blast* }
 with $\langle t' \vdash \langle x'', \text{shr } s \rangle \wr \rangle$ **show** *?thesis* **by**(*auto*)
 next
 case (*wait x'' ln''*)
 from $\langle \text{wset } s \ t' = \text{None} \rangle \langle \text{waiting } (\text{wset } s \ t') \rangle$
 have *False* **by**(*simp add: waiting-def*)
 thus *?thesis* ..
 next
 case (*acquire x'' ln'' l'' T*)
 from $\langle \text{thr } s \ t' = [(x'', \text{ln}'')] \rangle \langle \text{thr } s \ t' = [(x', \text{no-wait-locks})] \rangle \langle 0 < \text{ln}'' \ \$ \ l'' \rangle$
 have *False* **by**(*auto*)
 thus *?thesis* ..
 qed
 next
 fix $t' \ x' \ \text{ln}' \ l$
 assume $\text{thr } s \ t' = [(x', \text{ln}')]$
 and $0 < \text{ln}' \ \$ \ l$
 and $\text{wst}': \neg \text{waiting } (\text{wset } s \ t')$
 hence $\text{not-final-thread } s \ t'$ **by**(*auto intro: not-final-thread-wait-locks*)
 hence $t' \in \text{deadlocked } s$ **by**(*rule deadlocked*)
 thus $\exists l \ T. 0 < \text{ln}' \ \$ \ l \wedge t' \neq T \wedge \text{thr } s \ T \neq \text{None} \wedge \text{has-lock } (\text{locks } s \ \$ \ l) \ T$
 proof(*cases rule: deadlocked-elim*s)
 case (*lock x''*)
 from $\langle \text{thr } s \ t' = [(x', \text{ln}')] \rangle \langle \text{thr } s \ t' = [(x'', \text{no-wait-locks})] \rangle \langle 0 < \text{ln}' \ \$ \ l \rangle$
 have *False* **by** *auto*
 thus *?thesis* ..
 next
 case (*wait x' ln'*)
 from $\text{wst}' \langle \text{waiting } (\text{wset } s \ t') \rangle$
 have *False* **by** *contradiction*
 thus *?thesis* ..
 next
 case (*acquire x'' ln'' l'' t''*)
 from $\langle \text{thr } s \ t' = [(x'', \text{ln}'')] \rangle \langle \text{thr } s \ t' = [(x', \text{ln}')] \rangle$
 have [*simp*]: $x' = x'' \ \text{ln}' = \text{ln}''$ **by** *auto*
 moreover from $\langle t'' \in \text{deadlocked } s \vee \text{final-thread } s \ t'' \rangle$
 have $\text{thr } s \ t'' \neq \text{None}$
 by(*auto elim: deadlocked-thread-exists simp add: final-thread-def*)

```

  with ⟨0 < ln'' $ l''⟩ ⟨has-lock (locks s $ l'') t''⟩ ⟨t' ≠ t''⟩ ⟨thr s t' = [(x'', ln'')]⟩
  show ?thesis by auto
qed
next
fix t x w
assume tst: thr s t = [(x, no-wait-locks)]
show wset s t ≠ [PostWS w]
proof
  assume wset s t = [PostWS w]
  moreover with tst have not-final-thread s t
    by(auto simp add: not-final-thread-iff)
  hence t ∈ deadlocked s by(rule deadlocked)
  ultimately show False using tst
    by(auto elim: deadlocked.cases simp add: waiting-def)
qed
qed
qed

```

lemma *deadlock-eq-deadlocked'*:
 $deadlock = deadlocked'$
 by(rule ext)(auto intro: deadlock-implies-deadlocked' deadlocked'-implies-deadlock)

lemma *deadlock-no-red*:
 $\llbracket s -t>ta \rightarrow s'; deadlock\ s \rrbracket \implies False$
unfolding *deadlock-eq-deadlocked'*
 by(rule deadlocked'-no-red)

lemma *deadlock-no-active-threads*:
 assumes *dead*: $deadlock\ s$
 shows $active-threads\ s = \{\}$
proof(rule equals0I)
 fix t
 assume *active*: $t \in active-threads\ s$
 then obtain $ta\ s'$ where $s -t>ta \rightarrow s'$ by(auto dest: active-thread-ex-red)
 thus False using *dead* by(rule deadlock-no-red)
 qed

end

locale *preserve-deadlocked = multithreaded final r convert-RA*
 for *final* :: ' $x \Rightarrow bool$ '
 and $r :: ('l, 't, 'x, 'm, 'w, 'o)$ semantics ($\langle - \vdash - \dashrightarrow - \rangle \rightarrow [50, 0, 0, 50]$ 80)
 and *convert-RA* :: ' l released-locks \Rightarrow 'o list'
 +
 fixes *wf-state* :: ('l, 't, 'x, 'm, 'w) state set
 assumes *invariant3p-wf-state*: $invariant3p\ redT\ wf-state$
 assumes *can-lock-preserved*:
 $\llbracket s \in wf-state; s -t'>ta' \rightarrow s';$
 $thr\ s\ t = [(x, no-wait-locks)]; t \vdash \langle x, shr\ s \rangle \wr \rrbracket$
 $\implies t \vdash \langle x, shr\ s' \rangle \wr$
 and *can-lock-devreserp*:
 $\llbracket s \in wf-state; s -t'>ta' \rightarrow s';$
 $thr\ s\ t = [(x, no-wait-locks)]; t \vdash \langle x, shr\ s' \rangle L \wr \rrbracket$
 $\implies \exists L' \subseteq L. t \vdash \langle x, shr\ s \rangle L' \wr$

begin

lemma *redT-deadlocked-subset*:

assumes *wfs*: $s \in \text{wf-state}$

and *Red*: $s \dashv\vdash ta \rightarrow s'$

shows $\text{deadlocked } s \subseteq \text{deadlocked } s'$

proof

fix t'

assume $t'\text{dead}$: $t' \in \text{deadlocked } s$

from *Red* have $t\text{dead}$: $t \notin \text{deadlocked } s$

by(*auto dest*: *red-no-deadlock*)

with $t'\text{dead}$ have $t't$: $t' \neq t$ by *auto*

{ fix t'

assume *final-thread* $s\ t'$

then obtain $x'\ ln'$ where tst' : $\text{thr } s\ t' = [(x', ln')]$ by(*auto elim!*: *final-threadE*)

with $\langle \text{final-thread } s\ t' \rangle$ have *final* x'

and $wset\ s\ t' = \text{None}$ and [*simp*]: $ln' = \text{no-wait-locks}$

by(*auto elim*: *final-threadE*)

with *Red* tst' have $t \neq t'$ by *cases*(*auto dest*: *final-no-red*)

with *Red* tst' have $\text{thr } s'\ t' = [(x', ln')]$

by *cases*(*auto intro*: *redT-updTs-Some*)

moreover from *Red* $\langle t \neq t' \rangle \langle wset\ s\ t' = \text{None} \rangle$

have $wset\ s'\ t' = \text{None}$ by *cases*(*auto simp*: *redT-updWs-None-implies-None*)

ultimately have *final-thread* $s'\ t'$ using tst' $\langle \text{final } x' \rangle$

by(*auto simp add*: *final-thread-def*) }

hence *subset*: $\text{deadlocked } s \cup \text{final-threads } s \subseteq \text{deadlocked } s \cup \text{deadlocked } s' \cup \text{final-threads } s'$ by(*auto*)

from *Red* show $t' \in \text{deadlocked } s'$

proof(*cases*)

case (*redT-normal* $x\ x'\ m'$)

note $\text{red} = \langle t \vdash \langle x, \text{shr } s \rangle \dashv\vdash \langle x', m' \rangle \rangle$

and $tst = \langle \text{thr } s\ t = [(x, \text{no-wait-locks})] \rangle$

and $aok = \langle \text{actions-ok } s\ t\ ta \rangle$

and $s' = \langle \text{redT-upd } s\ t\ ta\ x'\ m'\ s' \rangle$

from *red* have $\neg \text{final } x$ by(*auto dest*: *final-no-red*)

with $t\text{dead}$ tst have $nafe$: $\neg \text{all-final-except } s\ (\text{deadlocked } s)$

by(*fastforce simp add*: *all-final-except-def not-final-thread-iff*)

from $t'\text{dead}$ show *?thesis*

proof(*coinduct*)

case (*deadlocked* t'')

note $t''\text{dead} = \text{this}$

with *Red* have $t''t$: $t'' \neq t$

by(*auto dest*: *red-no-deadlock*)

from $t''\text{dead}$ show *?case*

proof(*cases rule*: *deadlocked-elim*s)

case (*lock* X)

hence est'' : $\text{thr } s\ t'' = [(X, \text{no-wait-locks})]$

and msE : $t'' \vdash \langle X, \text{shr } s \rangle \wr$

and $csexdead$: $\bigwedge LT. t'' \vdash \langle X, \text{shr } s \rangle LT \wr \implies \exists lt \in LT. \text{must-wait } s\ t''\ lt\ (\text{deadlocked } s \cup$

final-threads $s)$

by *auto*

from $t''t$ *Red* est''

have $es't''$: $\text{thr } s'\ t'' = [(X, \text{no-wait-locks})]$

by(*cases* s)(*cases* s' , *auto elim!*: *redT-ts-Some-inv*)

```

note  $es't''$  moreover
from  $wfs\ Red\ est''\ msE$  have  $msE': t'' \vdash \langle X, shr\ s \rangle$   $\wr$  by(rule can-lock-preserved)
moreover
{ fix  $LT$ 
  assume  $clL'': t'' \vdash \langle X, shr\ s \rangle\ LT$   $\wr$ 
  with  $est''$  have  $\exists LT' \subseteq LT. t'' \vdash \langle X, shr\ s \rangle\ LT'$   $\wr$ 
    by(rule can-lock-devreserp[OF wfs Red])
  then obtain  $LT'$  where  $clL': t'' \vdash \langle X, shr\ s \rangle\ LT'$   $\wr$ 
    and  $LL': LT' \subseteq LT$  by blast
  with csexdead obtain  $lt$ 
    where  $lt: lt \in LT$  and  $mw: must\ wait\ s\ t''\ lt$  (deadlocked s  $\cup$  final-threads s)
    by blast
  from  $mw$  have must-wait s' t'' lt (deadlocked s  $\cup$  deadlocked s'  $\cup$  final-threads s')
  proof(cases rule: must-wait-elim)
    case (lock l t')
      from  $\langle t' \in deadlocked\ s \cup final\ threads\ s \rangle\ Red$  have  $tt': t \neq t'$ 
        by(auto dest: red-no-deadlock final-no-redT elim: final-threadE)
      from aok have lock-actions-ok (locks s $ l) t (ta l $ l)
        by(auto simp add: lock-ok-las-def)
      with  $tt' \langle has\ lock\ (locks\ s\ \$\ l)\ t' \rangle\ s'$ 
      have  $hl't': has\ lock\ (locks\ s'\ \$\ l)\ t'$  by(auto)
      moreover note  $\langle t' \neq t'' \rangle$ 
      moreover from  $\langle t' \in deadlocked\ s \cup final\ threads\ s \rangle$ 
      have  $t' \in (deadlocked\ s \cup deadlocked\ s' \cup final\ threads\ s')$ 
        using subset by blast
      ultimately show ?thesis unfolding  $\langle lt = Inl\ l \rangle$  ..
    next
      case (join t')
      note  $t'\ dead = \langle t' \in deadlocked\ s \cup final\ threads\ s \rangle$ 
      with  $Red$  have  $tt': t \neq t'$ 
        by(auto dest: red-no-deadlock final-no-redT elim: final-threadE)
      note  $nftt' = \langle not\ final\ thread\ s\ t' \rangle$ 
      from  $t'\ dead\ Red\ aok\ s'\ tt'$  have  $ts't': thr\ s'\ t' = thr\ s\ t'$ 
        by(auto elim!: deadlocked-thread-exists final-threadE intro: redT-updTs-Some)
      from  $nftt'$  have  $thr\ s\ t' \neq None$  by auto
      with  $nftt'\ t'\ dead$  have  $t' \in deadlocked\ s$ 
        by(simp add: not-final-thread-final-thread-conv[symmetric])
      hence not-final-thread s' t'
      proof(cases rule: deadlocked-elim)
        case (lock x'')
          from  $\langle t' \vdash \langle x'', shr\ s \rangle \wr \rangle$  have  $\neg final\ x''$ 
            by(auto elim: must-syncE dest: final-no-red)
          with  $\langle thr\ s\ t' = [(x'', no\ wait\ locks)] \rangle\ ts't'$  show ?thesis
            by(auto intro: not-final-thread.intros)
        next
          case (wait x'' ln'')
          from  $\langle \neg final\ x \rangle\ tst\ \langle all\ final\ except\ s\ (deadlocked\ s) \rangle$ 
          have  $t \in deadlocked\ s$  by(fastforce dest: all-final-exceptD simp add: not-final-thread-iff)
          with  $Red$  have False by(auto dest: red-no-deadlock)
          thus ?thesis ..
        next
          case (acquire x'' ln'' l'' T'')
          from  $\langle thr\ s\ t' = [(x'', ln'')] \rangle\ \langle 0 < ln''\ \$\ l'' \rangle\ ts't'$ 
          show ?thesis by(auto intro: not-final-thread.intros(2))

```



```

qed
moreover from  $t'$  dead subset have  $t' \in \text{deadlocked } s \cup \text{deadlocked } s' \cup \text{final-threads } s' ..$ 
ultimately show ?thesis unfolding  $\langle lt = \text{Inr } (\text{Inl } t') \rangle ..$ 
next
case (interrupt  $t'$ )
from  $tst$  red aok have not-final-thread  $s$   $t$ 
by(auto simp add: wset-actions-ok-def not-final-thread-iff split: if-split-asm dest: final-no-red)
with  $\langle \text{all-final-except } s (\text{deadlocked } s \cup \text{final-threads } s) \rangle$ 
have  $t \in \text{deadlocked } s \cup \text{final-threads } s$  by(rule all-final-exceptD)
moreover have  $t \notin \text{deadlocked } s$  using Red by(blast dest: red-no-deadlock)
moreover have  $\neg \text{final-thread } s$   $t$  using red  $tst$  by(auto simp add: final-thread-def dest:
final-no-red)
ultimately have False by blast
thus ?thesis ..
qed
with  $lt$  have  $\exists lt \in LT. \text{must-wait } s' t'' lt (\text{deadlocked } s \cup \text{deadlocked } s' \cup \text{final-threads } s')$  by
blast }
moreover have  $wset s' t'' = \text{None}$  using  $s' t'' t \langle wset s t'' = \text{None} \rangle$ 
by(auto intro: redT-updWs-None-implies-None)
ultimately show ?thesis by(auto)
next
case (wait  $x$   $ln$ )
from  $\langle \text{all-final-except } s (\text{deadlocked } s) \rangle$  nafe have False by simp
thus ?thesis by simp
next
case (acquire  $X$   $ln$   $l$   $T$ )
from  $t'' t$  Red  $\langle \text{thr } s t'' = \lfloor (X, ln) \rfloor \rangle s'$ 
have  $es' t''$ :  $\text{thr } s' t'' = \lfloor (X, ln) \rfloor$ 
by(cases  $s$ )(auto dest: redT-ts-Some-inv)
moreover
from  $\langle T \in \text{deadlocked } s \vee \text{final-thread } s T \rangle$ 
have  $T \neq t$ 
proof(rule disjE)
assume  $T \in \text{deadlocked } s$ 
with Red show ?thesis by(auto dest: red-no-deadlock)
next
assume final-thread  $s$   $T$ 
with Red show ?thesis
by(auto dest!: final-no-redT simp add: final-thread-def)
qed
with  $s' tst$  Red  $\langle \text{has-lock } (\text{locks } s \ \$ l) T \rangle$  have  $\text{has-lock } (\text{locks } s' \ \$ l) T$ 
by  $\neg(\text{cases } s, \text{auto dest: redT-has-lock-inv}[\text{THEN iffD2}])$ 
moreover
from  $s' \langle T \neq t \rangle$  have  $wset: wset s T = \text{None} \implies wset s' T = \text{None}$ 
by(auto intro: redT-updWs-None-implies-None)
{ fix  $x$ 
assume  $\text{thr } s T = \lfloor (x, \text{no-wait-locks}) \rfloor$ 
with  $\langle T \neq t \rangle$  Red  $s' aok$   $tst$  have  $\text{thr } s' T = \lfloor (x, \text{no-wait-locks}) \rfloor$ 
by(auto intro: redT-updTs-Some) }
moreover
hence final-thread  $s$   $T \implies$  final-thread  $s' T$ 
by(auto simp add: final-thread-def intro: wset)
moreover from  $\langle \neg \text{waiting } (wset s t'') \rangle s' t'' t$ 
have  $\neg \text{waiting } (wset s' t'')$ 

```

```

by(auto simp add: redT-updWs-None-implies-None redT-updWs-PostWS-imp-PostWS not-waiting-iff)
ultimately have ?Acquire
  using ⟨0 < ln $ l⟩ ⟨t'' ≠ T⟩ ⟨T ∈ deadlocked s ∨ final-thread s T⟩ by(auto)
thus ?thesis by simp
qed
qed
next
case (redT-acquire x n ln)
hence [simp]: ta = (K$ [], [], [], [], [], convert-RA ln)
and s': s' = (acquire-all (locks s) t ln, ((thr s)(t ↦ (x, no-wait-locks)), shr s), wset s, interrupts
s)
and tst: thr s t = [(x, ln)]
and wst: ¬ waiting (wset s t) by auto
from t'dead show ?thesis
proof(coinduct)
case (deadlocked t'')
note t''dead = this
with Red have t''t: t'' ≠ t
  by(auto dest: red-no-deadlock)
from t''dead show ?case
proof(cases rule: deadlocked-elim)
case (lock X)
note clnml = ⟨∧LT. t'' ⊢ ⟨X, shr s⟩ LT ⟩ ⇒ ∃lt ∈ LT. must-wait s t'' lt (deadlocked s ∪
final-threads s)
note tst'' = ⟨thr s t'' = [(X, no-wait-locks)]⟩
with s' t''t have ts't'': thr s' t'' = [(X, no-wait-locks)] by simp
moreover
{ fix LT
  assume t'' ⊢ ⟨X, shr s'⟩ LT ⟩
  hence t'' ⊢ ⟨X, shr s⟩ LT ⟩ using s' by simp
  then obtain lt where lt: lt ∈ LT and hlnft: must-wait s t'' lt (deadlocked s ∪ final-threads s)
  by(blast dest: clnml)
  from hlnft have must-wait s' t'' lt (deadlocked s ∪ deadlocked s' ∪ final-threads s')
  proof(cases rule: must-wait-elim)
  case (lock l' T)
  from ⟨has-lock (locks s $ l') T⟩ s'
  have has-lock (locks s' $ l') T
  by(auto intro: has-lock-has-lock-acquire-locks)
  moreover note ⟨T ≠ t''⟩
  moreover from ⟨T ∈ deadlocked s ∪ final-threads s⟩
  have T ∈ deadlocked s ∪ deadlocked s' ∪ final-threads s' using subset by blast
  ultimately show ?thesis unfolding ⟨lt = Inl l'⟩ ..
  }
}
next
case (join T)
from ⟨not-final-thread s T⟩ have thr s T ≠ None
  by(auto simp add: not-final-thread-iff)
moreover
from ⟨T ∈ deadlocked s ∪ final-threads s⟩
have T ≠ t
proof
  assume T ∈ deadlocked s
  with Red show ?thesis by(auto dest: red-no-deadlock)
}
next
assume T ∈ final-threads s

```

```

    with  $\langle 0 < \text{ln } \$ n \rangle$  tst show ?thesis
      by(auto simp add: final-thread-def)
  qed
  ultimately have not-final-thread  $s' T$  using  $\langle \text{not-final-thread } s T \rangle s'$ 
    by(auto simp add: not-final-thread-iff)
  moreover from  $\langle T \in \text{deadlocked } s \cup \text{final-threads } s \rangle$ 
  have  $T \in \text{deadlocked } s \cup \text{deadlocked } s' \cup \text{final-threads } s'$  using subset by blast
  ultimately show ?thesis unfolding  $\langle \text{lt} = \text{Inr } (\text{Inl } T) \rangle$  ..
next
  case (interrupt T)
  from  $\text{tst wst } \langle 0 < \text{ln } \$ n \rangle$  have not-final-thread  $s t$ 
    by(auto simp add: waiting-def not-final-thread-iff)
  with  $\langle \text{all-final-except } s (\text{deadlocked } s \cup \text{final-threads } s) \rangle$ 
  have  $t \in \text{deadlocked } s \cup \text{final-threads } s$  by(rule all-final-exceptD)
  moreover have  $t \notin \text{deadlocked } s$  using Red by(blast dest: red-no-deadlock)
  moreover have  $\neg \text{final-thread } s t$  using  $\text{tst } \langle 0 < \text{ln } \$ n \rangle$  by(auto simp add: final-thread-def)
  ultimately have False by blast
  thus ?thesis ..
qed
with  $\text{lt}$  have  $\exists \text{lt} \in \text{LT}. \text{must-wait } s' t'' \text{ lt } (\text{deadlocked } s \cup \text{deadlocked } s' \cup \text{final-threads } s')$  by
blast }
  moreover from  $\langle \text{wset } s t'' = \text{None} \rangle s'$  have  $\text{wset } s' t'' = \text{None}$  by simp
  ultimately show ?thesis using  $\langle \text{thr } s t'' = \lfloor (X, \text{no-wait-locks}) \rfloor \rangle \langle t'' \vdash \langle X, \text{shr } s \rangle \rangle s'$  by
fastforce
next
  case (wait X LN)
  have all-final-except  $s' (\text{deadlocked } s)$ 
  proof(rule all-final-exceptI)
    fix T
    assume not-final-thread  $s' T$ 
    hence not-final-thread  $s T$  using  $\text{wst } \text{tst } s'$ 
      by(auto simp add: not-final-thread-iff split: if-split-asm)
    with  $\langle \text{all-final-except } s (\text{deadlocked } s) \rangle \langle \text{thr } s t = \lfloor (x, \text{ln}) \rfloor \rangle$ 
    show  $T \in \text{deadlocked } s$  by-(erule all-final-exceptD)
  qed
  hence all-final-except  $s' (\text{deadlocked } s \cup \text{deadlocked } s')$ 
    by(rule all-final-except-mono') blast
  with  $t'' t \langle \text{thr } s t'' = \lfloor (X, \text{LN}) \rfloor \rangle \langle \text{waiting } (\text{wset } s t'') \rangle s'$ 
  have ?Wait by simp
  thus ?thesis by simp
next
  case (acquire X LN l T)
  from  $\langle \text{thr } s t'' = \lfloor (X, \text{LN}) \rfloor \rangle t'' t s'$ 
  have  $\text{thr } s' t'' = \lfloor (X, \text{LN}) \rfloor$  by(simp)
  moreover from  $\langle T \in \text{deadlocked } s \vee \text{final-thread } s T \rangle s' \text{tst}$ 
  have  $T \in \text{deadlocked } s \vee \text{final-thread } s' T$ 
    by(clarsimp simp add: final-thread-def)
  moreover from  $\langle \text{has-lock } (\text{locks } s \$ l) T \rangle s'$ 
  have  $\text{has-lock } (\text{locks } s' \$ l) T$ 
    by(auto intro: has-lock-has-lock-acquire-locks)
  moreover have  $\neg \text{waiting } (\text{wset } s' t'')$  using  $\langle \neg \text{waiting } (\text{wset } s t'') \rangle s'$  by simp
  ultimately show ?thesis using  $\langle 0 < \text{LN } \$ l \rangle \langle t'' \neq T \rangle$  by blast
qed
qed

```

qed
qed

corollary *RedT-deadlocked-subset*:

assumes *wfs*: $s \in \text{wf-state}$

and *Red*: $s \rightarrow^* s'$

shows $\text{deadlocked } s \subseteq \text{deadlocked } s'$

using *Red*

apply(*induct rule: RedT-induct'*)

apply(*unfold RedT-def*)

apply(*blast dest: invariant3p-rtrancl3p[OF invariant3p-wf-state - wfs] redT-deadlocked-subset*)

done

end

end

1.13 Progress theorem for the multithreaded semantics

theory *FWProgress*

imports

FWDeadlock

begin

locale *progress = multithreaded final r convert-RA*

for *final* :: $'x \Rightarrow \text{bool}$

and *r* :: $(\text{'l}, \text{'t}, \text{'x}, \text{'m}, \text{'w}, \text{'o}) \text{ semantics } (\langle - \vdash - \dashrightarrow - \rangle \rightarrow [50, 0, 0, 50] 80)$

and *convert-RA* :: $\text{'l released-locks } \Rightarrow \text{'o list}$

+

fixes *wf-state* :: $(\text{'l}, \text{'t}, \text{'x}, \text{'m}, \text{'w}) \text{ state set}$

assumes *wf-stateD*: $s \in \text{wf-state} \Longrightarrow \text{lock-thread-ok } (\text{locks } s) (\text{thr } s) \wedge \text{wset-final-ok } (\text{wset } s) (\text{thr } s)$

and *wf-red*:

$\llbracket s \in \text{wf-state}; \text{thr } s \text{ } t = \llbracket (x, \text{no-wait-locks}) \rrbracket;$

$t \vdash (x, \text{shr } s) \text{ } -ta \rightarrow (x', m'); \neg \text{waiting } (\text{wset } s \text{ } t) \rrbracket$

$\Longrightarrow \exists ta' x' m'. t \vdash (x, \text{shr } s) \text{ } -ta' \rightarrow (x', m') \wedge (\text{actions-ok } s \text{ } ta' \vee \text{actions-ok}' s \text{ } ta' \wedge \text{actions-subset } ta' \text{ } ta)$

and *red-wait-set-not-final*:

$\llbracket s \in \text{wf-state}; \text{thr } s \text{ } t = \llbracket (x, \text{no-wait-locks}) \rrbracket;$

$t \vdash (x, \text{shr } s) \text{ } -ta \rightarrow (x', m'); \neg \text{waiting } (\text{wset } s \text{ } t); \text{Suspend } w \in \text{set } \{\{ta\}_w \rrbracket$

$\Longrightarrow \neg \text{final } x'$

and *wf-progress*:

$\llbracket s \in \text{wf-state}; \text{thr } s \text{ } t = \llbracket (x, \text{no-wait-locks}) \rrbracket; \neg \text{final } x \rrbracket$

$\Longrightarrow \exists ta' x' m'. t \vdash \langle x, \text{shr } s \rangle \text{ } -ta \rightarrow \langle x', m' \rangle$

and *ta-Wakeup-no-join-no-lock-no-interrupt*:

$\llbracket s \in \text{wf-state}; \text{thr } s \text{ } t = \llbracket (x, \text{no-wait-locks}) \rrbracket; t \vdash xm \text{ } -ta \rightarrow xm'; \text{Notified} \in \text{set } \{\{ta\}_w \vee \text{WokenUp} \in \text{set } \{\{ta\}_w \rrbracket$

$\Longrightarrow \text{collect-waits } ta = \{\}$

and *ta-satisfiable*:

$\llbracket s \in \text{wf-state}; \text{thr } s \text{ } t = \llbracket (x, \text{no-wait-locks}) \rrbracket; t \vdash \langle x, \text{shr } s \rangle \text{ } -ta \rightarrow \langle x', m' \rangle \rrbracket$

$\implies \exists s'. \text{actions-ok } s' t ta$
begin

lemma *wf-redE*:

assumes $s \in \text{wf-state } \text{thr } s t = \lfloor (x, \text{no-wait-locks}) \rfloor$
and $t \vdash \langle x, \text{shr } s \rangle -ta \rightarrow \langle x'', m'' \rangle \neg \text{waiting } (\text{wset } s t)$
obtains $ta' x' m'$
where $t \vdash \langle x, \text{shr } s \rangle -ta' \rightarrow \langle x', m' \rangle \text{actions-ok}' s t ta' \text{actions-subset } ta' ta$
 $| ta' x' m' \text{ where } t \vdash \langle x, \text{shr } s \rangle -ta' \rightarrow \langle x', m' \rangle \text{actions-ok } s t ta'$
using *wf-red[OF assms]* **by** *blast*

lemma *wf-progressE*:

assumes $s \in \text{wf-state}$
and $\text{thr } s t = \lfloor (x, \text{no-wait-locks}) \rfloor \neg \text{final } x$
obtains $ta x' m' \text{ where } t \vdash \langle x, \text{shr } s \rangle -ta \rightarrow \langle x', m' \rangle$
using *assms*
by (*blast dest: wf-progress*)

lemma *wf-progress-satisfiable*:

$\llbracket s \in \text{wf-state}; \text{thr } s t = \lfloor (x, \text{no-wait-locks}) \rfloor; \neg \text{final } x \rrbracket$
 $\implies \exists ta x' m' s'. t \vdash \langle x, \text{shr } s \rangle -ta \rightarrow \langle x', m' \rangle \wedge \text{actions-ok } s' t ta$
apply (*frule (2) wf-progress*)
apply (*blast dest: ta-satisfiable*)
done

theorem *redT-progress*:

assumes *wfs*: $s \in \text{wf-state}$
and *ndead*: $\neg \text{deadlock } s$
shows $\exists t' ta' s'. s -t' \triangleright ta' \rightarrow s'$

proof –

from *wfs* **have** *lok*: *lock-thread-ok* (*locks* s) (*thr* s)
and *wfin*: *wset-final-ok* (*wset* s) (*thr* s)
by (*auto dest: wf-stateD*)

from *ndead*

have $\exists t x ln l. \text{thr } s t = \lfloor (x, ln) \rfloor \wedge$
 $(\text{wset } s t = \text{None} \wedge ln = \text{no-wait-locks} \wedge \neg \text{final } x \wedge (\exists LT. t \vdash \langle x, \text{shr } s \rangle LT \wr \wedge (\forall lt \in LT. \neg \text{must-wait } s t lt (\text{dom } (\text{thr } s)))) \vee$
 $\neg \text{waiting } (\text{wset } s t) \wedge ln \$ l > 0 \wedge (\forall l. ln \$ l > 0 \implies \text{may-lock } (\text{locks } s \$ l) t) \vee$
 $(\exists w. ln = \text{no-wait-locks} \wedge \text{wset } s t = \lfloor \text{PostWS } w \rfloor))$

by (*rule contrapos-np*) (*blast intro!: all-waiting-implies-deadlock[OF lok] intro: must-syncI[OF wf-progress-satisfiable[OF wfs]]*)

then obtain $t x ln l$

where *tst*: $\text{thr } s t = \lfloor (x, ln) \rfloor$

and *a*: $\text{wset } s t = \text{None} \wedge ln = \text{no-wait-locks} \wedge \neg \text{final } x \wedge$
 $(\exists LT. t \vdash \langle x, \text{shr } s \rangle LT \wr \wedge (\forall lt \in LT. \neg \text{must-wait } s t lt (\text{dom } (\text{thr } s)))) \vee$
 $\neg \text{waiting } (\text{wset } s t) \wedge ln \$ l > 0 \wedge (\forall l. ln \$ l > 0 \implies \text{may-lock } (\text{locks } s \$ l) t) \vee$
 $(\exists w. ln = \text{no-wait-locks} \wedge \text{wset } s t = \lfloor \text{PostWS } w \rfloor)$

by *blast*

from *a* **have** *cases*[*case-names normal acquire wakeup*]:

$\wedge \text{thesis.}$

$\llbracket \wedge LT. \llbracket \text{wset } s t = \text{None}; ln = \text{no-wait-locks}; \neg \text{final } x; t \vdash \langle x, \text{shr } s \rangle LT \wr; \llbracket \forall lt. lt \in LT \implies \neg \text{must-wait } s t lt (\text{dom } (\text{thr } s)) \rrbracket \implies \text{thesis};$

$\llbracket \neg \text{waiting } (\text{wset } s t); ln \$ l > 0; \wedge l. ln \$ l > 0 \implies \text{may-lock } (\text{locks } s \$ l) t \rrbracket \implies \text{thesis};$
 $\llbracket \wedge w. \llbracket ln = \text{no-wait-locks}; \text{wset } s t = \lfloor \text{PostWS } w \rfloor \rrbracket \implies \text{thesis} \rrbracket \implies \text{thesis}$

```

  by auto
show ?thesis
proof(cases rule: cases)
  case (normal LT)
  note [simp] = ⟨ln = no-wait-locks⟩
  and nfine' = ⟨¬ final x⟩
  and cl' = ⟨t ⊢ ⟨x, shr s⟩ LT ⟩
  and mw = ⟨∧ lt. lt ∈ LT ⇒ ¬ must-wait s t lt (dom (thr s))⟩
  from tst nfine' obtain x'' m'' ta'
  where red: t ⊢ ⟨x, shr s⟩ -ta' → ⟨x'', m''⟩
  by(auto intro: wf-progressE[OF wfs])
  from cl'
  have ∃ ta''' x''' m'''. t ⊢ ⟨x, shr s⟩ -ta''' → ⟨x''', m'''⟩ ∧
    LT = collect-waits ta'''
  by (fastforce elim!: can-syncE)
  then obtain ta''' x''' m'''
  where red'': t ⊢ ⟨x, shr s⟩ -ta''' → ⟨x''', m'''⟩
  and L: LT = collect-waits ta'''
  by blast
  from ⟨wset s t = None⟩ have ¬ waiting (wset s t) by(simp add: not-waiting-iff)
  with tst obtain ta'' x'' m''
  where red': t ⊢ ⟨x, shr s⟩ -ta'' → ⟨x'', m''⟩
  and aok': actions-ok s t ta'' ∨ actions-ok' s t ta'' ∧ actions-subset ta'' ta'''
  by -(rule wf-redE[OF wfs - red'], auto)
  from aok' have actions-ok s t ta''
proof
  assume actions-ok' s t ta'' ∧ actions-subset ta'' ta'''
  hence aok': actions-ok' s t ta'' and aos: actions-subset ta'' ta''' by simp-all

  { fix l
  assume Inl l ∈ LT
  { fix t'
  assume t ≠ t'
  have ¬ has-lock (locks s $ l) t'
  proof
    assume has-lock (locks s $ l) t'
    moreover with lok have thr s t' ≠ None by(auto dest: lock-thread-okD)
    ultimately have must-wait s t (Inl l) (dom (thr s)) using ⟨t ≠ t'⟩ by(auto)
    moreover from ⟨Inl l ∈ LT⟩ have ¬ must-wait s t (Inl l) (dom (thr s)) by(rule mw)
    ultimately show False by contradiction
  qed }
  hence may-lock (locks s $ l) t
  by-(rule classical, auto simp add: not-may-lock-conv) }
note mayl = this
{ fix t'
  assume t'LT: Inr (Inl t') ∈ LT
  hence ¬ not-final-thread s t' ∧ t' ≠ t
  proof(cases t' = t)
    case False with t'LT mw L show ?thesis by(fastforce)
  next
    case True with tst mw[OF t'LT] nfine' L have False
    by(auto intro!: must-wait.intros simp add: not-final-thread-iff)
  thus ?thesis ..
  qed }

```

note $mayj = this$
{ fix t'
assume $t': Inr (Inr t') \in LT$
from t' **have** $\neg must-wait\ s\ t\ (Inr\ (Inr\ t'))\ (dom\ (thr\ s))$ **by** $(rule\ mw)$
hence $t' \in interrupts\ s$
by $(rule\ contrapos-np)(fastforce\ intro:\ all-final-exceptI\ simp\ add:\ not-final-thread-iff)$ **}**
note $interrupt = this$
from $aos\ L\ mayl$
have $\bigwedge l. l \in collect-locks'\ \{ta''\}_l \implies may-lock\ (locks\ s\ \$\ l)\ t$ **by** $auto$
with aok' **have** $lock-ok-las\ (locks\ s)\ t\ \{ta''\}_l$ **by** $(auto\ intro:\ lock-ok-las'-into-lock-on-las)$
moreover
from $mayj\ aos\ L$
have $cond-action-oks\ s\ t\ \{ta''\}_c$
by $(fastforce\ intro:\ may-join-cond-action-oks)$
moreover
from $ta-satisfiable[OF\ wfs\ tst[simplified]\ red^{\wedge}]$
obtain is' **where** $interrupt-actions-ok\ is'\ \{ta''\}_i$ **by** $auto$
with $interrupt\ aos\ aok'\ L$ **have** $interrupt-actions-ok\ (interrupts\ s)\ \{ta''\}_i$
by $(auto\ 5\ 2\ intro:\ interrupt-actions-ok'-collect-interrupts-imp-interrupt-actions-ok)$
ultimately show $actions-ok\ s\ t\ ta''$ **using** aok' **by** $auto$
qed
moreover obtain ws'' **where** $redT-updWs\ t\ (wset\ s)\ \{ta''\}_w\ ws''$
using $redT-updWs-total[of\ t\ wset\ s\ \{ta''\}_w]$ **..**
then obtain s' **where** $redT-upd\ s\ t\ ta''\ x''\ m''\ s'$ **by** $fastforce$
ultimately have $s -t>ta'' \rightarrow s'$
using $red'\ tst\ \langle wset\ s\ t = None \rangle$ **by** $(auto\ intro:\ redT-normal)$
thus $?thesis$ **by** $blast$
next
case $acquire$
hence $may-acquire-all\ (locks\ s)\ t\ ln$ **by** $(auto)$
with $tst\ \langle \neg\ waiting\ (wset\ s\ t) \rangle\ \langle 0 < ln\ \$\ l \rangle$
show $?thesis$ **by** $(fastforce\ intro:\ redT-acquire)$
next
case $(wakeup\ w)$
from $\langle wset\ s\ t = \lfloor PostWS\ w \rfloor \rangle$
have $\neg\ waiting\ (wset\ s\ t)$ **by** $(simp\ add:\ not-waiting-iff)$
from $tst\ wakeup$ **have** $tst:\ thr\ s\ t = \lfloor (x,\ no-wait-locks) \rfloor$ **by** $simp$
from $wakeup\ tst\ wfin$ **have** $\neg\ final\ x$ **by** $(auto\ dest:\ wset-final-okD)$
from $wf-progress[OF\ wfs\ tst\ this]$
obtain $ta\ x'\ m'$ **where** $red:\ t \vdash \langle x,\ shr\ s \rangle -ta \rightarrow \langle x',\ m' \rangle$ **by** $auto$
from $wf-red[OF\ wfs\ tst\ red\ \langle \neg\ waiting\ (wset\ s\ t) \rangle]$
obtain $ta'\ x''\ m''$
where $red': t \vdash \langle x,\ shr\ s \rangle -ta' \rightarrow \langle x'',\ m'' \rangle$
and $aok': actions-ok\ s\ t\ ta' \vee actions-ok'\ s\ t\ ta' \wedge actions-subset\ ta'\ ta$ **by** $blast$
from aok' **have** $actions-ok\ s\ t\ ta'$
proof
assume $actions-ok'\ s\ t\ ta' \wedge actions-subset\ ta'\ ta$
hence $aok': actions-ok'\ s\ t\ ta'$
and $subset:\ actions-subset\ ta'\ ta$ **by** $simp-all$
from $wakeup\ aok'$ **have** $Notified \in set\ \{ta''\}_w \vee WokenUp \in set\ \{ta''\}_w$
by $(auto\ simp\ add:\ wset-actions-ok-def\ split:\ if-split-asm)$
from $ta-Wakeup-no-join-no-lock-no-interrupt[OF\ wfs\ tst\ red'\ this]$
have $no-join:\ collect-cond-actions\ \{ta''\}_c = \{\}$
and $no-lock:\ collect-locks\ \{ta''\}_l = \{\}$

```

    and no-interrupt: collect-interrupts  $\{ta'\}_i = \{\}$  by auto
  from no-lock have no-lock': collect-locks'  $\{ta'\}_l = \{\}$ 
    using collect-locks'-subset-collect-locks[of  $\{ta'\}_l$ ] by auto
  from aok' have lock-ok-las' (locks s) t  $\{ta'\}_l$  by auto
  hence lock-ok-las (locks s) t  $\{ta'\}_l$ 
    by(rule lock-ok-las'-into-lock-on-las)(simp add: no-lock')
  moreover from subset aok' no-join have cond-action-oks s t  $\{ta'\}_c$ 
    by(auto intro: may-join-cond-action-oks)
  moreover from ta-satisfiable[OF wfs tst[simplified] red']
  obtain is' where interrupt-actions-ok is'  $\{ta'\}_i$  by auto
  with aok' no-interrupt have interrupt-actions-ok (interrupts s)  $\{ta'\}_i$ 
    by(auto intro: interrupt-actions-ok'-collect-interrupts-imp-interrupt-actions-ok)
  ultimately show actions-ok s t ta' using aok' by auto
qed
moreover obtain ws'' where redT-updWs t (wset s)  $\{ta'\}_w$  ws''
  using redT-updWs-total[of t wset s  $\{ta'\}_w$ ] ..
then obtain s' where redT-upd s t ta' x'' m'' s' by fastforce
ultimately have s -t>ta'→ s' using tst red' wakeup
  by(auto intro: redT-normal)
thus ?thesis by blast
qed
qed
end
end
end

```

1.14 Lifting of thread-local properties to the multithreaded case

```

theory FWLifting
imports
  FWWellform
begin

```

Lifting for properties that only involve thread-local state information and the shared memory.

definition

$ts\text{-ok} :: ('t \Rightarrow 'x \Rightarrow 'm \Rightarrow \text{bool}) \Rightarrow ('l, 't, 'x) \text{ thread-info} \Rightarrow 'm \Rightarrow \text{bool}$

where

$\bigwedge ln. ts\text{-ok} P ts m \equiv \forall t. \text{case} (ts t) \text{ of } None \Rightarrow \text{True} \mid [(x, ln)] \Rightarrow P t x m$

lemma ts-okI:

$\llbracket \bigwedge t x ln. ts t = [(x, ln)] \implies P t x m \rrbracket \implies ts\text{-ok} P ts m$

by(auto simp add: ts-ok-def)

lemma ts-okE:

$\llbracket ts\text{-ok} P ts m; \llbracket \bigwedge t x ln. ts t = [(x, ln)] \implies P t x m \rrbracket \implies Q \rrbracket \implies Q$

by(auto simp add: ts-ok-def)

lemma ts-okD:

$\bigwedge ln. \llbracket ts\text{-ok} P ts m; ts t = [(x, ln)] \rrbracket \implies P t x m$

by(auto simp add: ts-ok-def)

lemma *ts-ok-True* [*simp*]:
 $ts\text{-ok } (\lambda t m x. True) ts\ m$
by(*auto intro: ts-okI*)

lemma *ts-ok-conj*:
 $ts\text{-ok } (\lambda t x m. P\ t\ x\ m \wedge Q\ t\ x\ m) = (\lambda ts\ m. ts\text{-ok } P\ ts\ m \wedge ts\text{-ok } Q\ ts\ m)$
by(*auto intro: ts-okI intro!: ext dest: ts-okD*)

lemma *ts-ok-mono*:
 $\llbracket ts\text{-ok } P\ ts\ m; \bigwedge t x. P\ t\ x\ m \implies Q\ t\ x\ m \rrbracket \implies ts\text{-ok } Q\ ts\ m$
by(*auto intro!: ts-okI dest: ts-okD*)

Lifting for perperites, that also require additional data that does not change during execution

definition

$ts\text{-inv} :: ('i \Rightarrow 't \Rightarrow 'x \Rightarrow 'm \Rightarrow bool) \Rightarrow ('t \rightarrow 'i) \Rightarrow ('l, 't, 'x)\ \text{thread-info} \Rightarrow 'm \Rightarrow bool$

where

$\bigwedge ln. ts\text{-inv } P\ I\ ts\ m \equiv \forall t. \text{case } (ts\ t) \text{ of } None \Rightarrow True \mid [(x, ln)] \Rightarrow \exists i. I\ t = [i] \wedge P\ i\ t\ x\ m$

lemma *ts-invI*:
 $\llbracket \bigwedge t x ln. ts\ t = [(x, ln)] \rrbracket \implies \exists i. I\ t = [i] \wedge P\ i\ t\ x\ m \rrbracket \implies ts\text{-inv } P\ I\ ts\ m$
by(*simp add: ts-inv-def*)

lemma *ts-invE*:
 $\llbracket ts\text{-inv } P\ I\ ts\ m; \forall t x ln. ts\ t = [(x, ln)] \longrightarrow (\exists i. I\ t = [i] \wedge P\ i\ t\ x\ m) \rrbracket \implies R \rrbracket \implies R$
by(*auto simp add: ts-inv-def*)

lemma *ts-invD*:
 $\bigwedge ln. \llbracket ts\text{-inv } P\ I\ ts\ m; ts\ t = [(x, ln)] \rrbracket \implies \exists i. I\ t = [i] \wedge P\ i\ t\ x\ m$
by(*auto simp add: ts-inv-def*)

Wellformedness properties for lifting

definition

$ts\text{-inv-ok} :: ('l, 't, 'x)\ \text{thread-info} \Rightarrow ('t \rightarrow 'i) \Rightarrow bool$

where

$ts\text{-inv-ok } ts\ I \equiv \forall t. ts\ t = None \longleftrightarrow I\ t = None$

lemma *ts-inv-okI*:
 $(\bigwedge t. ts\ t = None \longleftrightarrow I\ t = None) \implies ts\text{-inv-ok } ts\ I$
by(*clarsimp simp add: ts-inv-ok-def*)

lemma *ts-inv-okI2*:
 $(\bigwedge t. (\exists v. ts\ t = [v]) \longleftrightarrow (\exists v. I\ t = [v])) \implies ts\text{-inv-ok } ts\ I$
by(*force simp add: ts-inv-ok-def*)

lemma *ts-inv-okE*:
 $\llbracket ts\text{-inv-ok } ts\ I; \forall t. ts\ t = None \longleftrightarrow I\ t = None \rrbracket \implies P \rrbracket \implies P$
by(*force simp add: ts-inv-ok-def*)

lemma *ts-inv-okE2*:
 $\llbracket ts\text{-inv-ok } ts\ I; \forall t. (\exists v. ts\ t = [v]) \longleftrightarrow (\exists v. I\ t = [v]) \rrbracket \implies P \rrbracket \implies P$
by(*force simp add: ts-inv-ok-def*)

lemma *ts-inv-okD*:

$ts\text{-inv-ok } ts\ I \implies (ts\ t = None) \longleftrightarrow (I\ t = None)$

by(*erule ts-inv-okE, blast*)

lemma *ts-inv-okD2*:

$ts\text{-inv-ok } ts\ I \implies (\exists v. ts\ t = [v]) \longleftrightarrow (\exists v. I\ t = [v])$

by(*erule ts-inv-okE2, blast*)

lemma *ts-inv-ok-conv-dom-eq*:

$ts\text{-inv-ok } ts\ I \longleftrightarrow (dom\ ts = dom\ I)$

proof –

have $ts\text{-inv-ok } ts\ I \longleftrightarrow (\forall t. ts\ t = None \longleftrightarrow I\ t = None)$

unfolding *ts-inv-ok-def* **by** *blast*

also have $\dots \longleftrightarrow (\forall t. t \in -\ dom\ ts \longleftrightarrow t \in -\ dom\ I)$ **by**(*force*)

also have $\dots \longleftrightarrow dom\ ts = dom\ I$ **by** *auto*

finally show *?thesis* .

qed

lemma *ts-inv-ok-upd-ts*:

$\llbracket ts\ t = [x]; ts\text{-inv-ok } ts\ I \rrbracket \implies ts\text{-inv-ok } (ts(t \mapsto x'))\ I$

by(*auto dest!: ts-inv-okD intro!: ts-inv-okI split: if-splits*)

lemma *ts-inv-upd-map-option*:

assumes $ts\text{-inv } P\ I\ ts\ m$

and $\bigwedge x\ ln. ts\ t = [x, ln] \implies P\ (the\ (I\ t))\ t\ (fst\ (f\ (x, ln)))\ m$

shows $ts\text{-inv } P\ I\ (ts(t := (map\ option\ f\ (ts\ t))))\ m$

using *assms*

by(*fastforce intro!: ts-invI split: if-split-asm dest: ts-invD*)

fun *upd-inv* :: $('t \rightarrow 'i) \Rightarrow ('i \Rightarrow 't \Rightarrow 'x \Rightarrow 'm \Rightarrow bool) \Rightarrow ('t, 'x, 'm)\ new\ thread\ action \Rightarrow ('t \rightarrow 'i)$

where

$upd\text{-inv } I\ P\ (NewThread\ t\ x\ m) = I(t \mapsto SOME\ i. P\ i\ t\ x\ m)$

| $upd\text{-inv } I\ P\ - = I$

fun *upd-invs* :: $('t \rightarrow 'i) \Rightarrow ('i \Rightarrow 't \Rightarrow 'x \Rightarrow 'm \Rightarrow bool) \Rightarrow ('t, 'x, 'm)\ new\ thread\ action\ list \Rightarrow ('t \rightarrow 'i)$

where

$upd\text{-invs } I\ P\ [] = I$

| $upd\text{-invs } I\ P\ (ta\#\!tas) = upd\text{-invs } (upd\text{-inv } I\ P\ ta)\ P\ tas$

lemma *upd-invs-append* [*simp*]:

$upd\text{-invs } I\ P\ (xs\ @\ ys) = upd\text{-invs } (upd\text{-invs } I\ P\ xs)\ P\ ys$

by(*induct xs arbitrary: I*)(*auto*)

lemma *ts-inv-ok-upd-inv'*:

$ts\text{-inv-ok } ts\ I \implies ts\text{-inv-ok } (redT\text{-upd}T'\ ts\ ta)\ (upd\text{-inv } I\ P\ ta)$

by(*cases ta*)(*auto intro!: ts-inv-okI elim: ts-inv-okD del: iffI*)

lemma *ts-inv-ok-upd-invs'*:

$ts\text{-inv-ok } ts\ I \implies ts\text{-inv-ok } (redT\text{-upd}Ts'\ ts\ tas)\ (upd\text{-invs } I\ P\ tas)$

proof(*induct tas arbitrary: ts I*)

case *Nil* **thus** *?case* **by** *simp*

next

case (*Cons TA TAS TS I*)

note $IH = \langle \bigwedge ts I. ts\text{-inv-ok } ts I \implies ts\text{-inv-ok } (redT\text{-updTs}' ts TAS) (upd\text{-invs } I P TAS) \rangle$
note $esok = \langle ts\text{-inv-ok } TS I \rangle$
from $esok$ **have** $ts\text{-inv-ok } (redT\text{-updT}' TS TA) (upd\text{-inv } I P TA)$
by $-(rule\ ts\text{-inv-ok-upd-inv}')$
hence $ts\text{-inv-ok } (redT\text{-updTs}' (redT\text{-updT}' TS TA) TAS) (upd\text{-invs } (upd\text{-inv } I P TA) P TAS)$
by $(rule\ IH)$
thus $?case$ **by** $simp$
qed

lemma $ts\text{-inv-ok-upd-inv}$:
 $ts\text{-inv-ok } ts I \implies ts\text{-inv-ok } (redT\text{-updT } ts ta) (upd\text{-inv } I P ta)$
apply $(cases\ ta)$
apply $(auto\ intro!: ts\text{-inv-ok}I\ elim: ts\text{-inv-ok}D\ del: iffI)$
done

lemma $ts\text{-inv-ok-upd-invs}$:
 $ts\text{-inv-ok } ts I \implies ts\text{-inv-ok } (redT\text{-updTs } ts tas) (upd\text{-invs } I P tas)$
proof $(induct\ tas\ arbitrary: ts\ I)$
case Nil **thus** $?case$ **by** $simp$
next
case $(Cons\ TA\ TAS\ TS\ I)$
note $IH = \langle \bigwedge ts I. ts\text{-inv-ok } ts I \implies ts\text{-inv-ok } (redT\text{-updTs } ts TAS) (upd\text{-invs } I P TAS) \rangle$
note $esok = \langle ts\text{-inv-ok } TS I \rangle$
from $esok$ **have** $ts\text{-inv-ok } (redT\text{-updT } TS TA) (upd\text{-inv } I P TA)$
by $-(rule\ ts\text{-inv-ok-upd-inv})$
hence $ts\text{-inv-ok } (redT\text{-updTs } (redT\text{-updT } TS TA) TAS) (upd\text{-invs } (upd\text{-inv } I P TA) P TAS)$
by $(rule\ IH)$
thus $?case$ **by** $simp$
qed

lemma $ts\text{-inv-ok-inv-ext-upd-inv}$:
 $\llbracket ts\text{-inv-ok } ts I; thread\text{-ok } ts ta \rrbracket \implies I \subseteq_m upd\text{-inv } I P ta$
by $(cases\ ta)(auto\ intro!: map\text{-le-same-upd}\ dest: ts\text{-inv-ok}D)$

lemma $ts\text{-inv-ok-inv-ext-upd-invs}$:
 $\llbracket ts\text{-inv-ok } ts I; thread\text{-oks } ts tas \rrbracket \implies I \subseteq_m upd\text{-invs } I P tas$
proof $(induct\ tas\ arbitrary: ts\ I)$
case Nil **thus** $?case$ **by** $simp$
next
case $(Cons\ TA\ TAS\ TS\ I)$
note $IH = \langle \bigwedge ts I. \llbracket ts\text{-inv-ok } ts I; thread\text{-oks } ts TAS \rrbracket \implies I \subseteq_m upd\text{-invs } I P TAS \rangle$
note $esinv = \langle ts\text{-inv-ok } TS I \rangle$
note $cct = \langle thread\text{-oks } TS (TA \# TAS) \rangle$
from $esinv\ cct$ **have** $I \subseteq_m upd\text{-inv } I P TA$
by $(auto\ intro: ts\text{-inv-ok-inv-ext-upd-inv})$
also from $esinv\ cct$ **have** $ts\text{-inv-ok } (redT\text{-updT}' TS TA) (upd\text{-inv } I P TA)$
by $(auto\ intro: ts\text{-inv-ok-upd-inv}')$
with cct **have** $upd\text{-inv } I P TA \subseteq_m upd\text{-invs } (upd\text{-inv } I P TA) P TAS$
by $(auto\ intro: IH)$
finally show $?case$ **by** $simp$
qed

lemma $upd\text{-invs-Some}$:

$\llbracket \text{thread-oks } ts \text{ tas}; I t = [i]; ts t = [x] \rrbracket \implies \text{upd-invs } I Q \text{ tas } t = [i]$
proof(*induct tas arbitrary: ts I*)
case Nil thus ?case by simp
next
case (Cons TA TAS TS I)
note $IH = \langle \bigwedge ts I. \llbracket \text{thread-oks } ts \text{ TAS}; I t = [i]; ts t = [x] \rrbracket \implies \text{upd-invs } I Q \text{ TAS } t = [i] \rangle$
note $cct = \langle \text{thread-oks } TS (TA \# TAS) \rangle$
note $it = \langle I t = [i] \rangle$
note $est = \langle TS t = [x] \rangle$
from cct have cctta: thread-ok TS TA
and ccttas: thread-oks (redT-updT' TS TA) TAS by auto
from cctta it est have upd-inv I Q TA t = [i]
by(cases TA, auto)
moreover
have redT-updT' TS TA t = [x] using cctta est
by - (rule redT-updT'-Some)
ultimately have upd-invs (upd-inv I Q TA) Q TAS t = [i] using ccttas
by -(erule IH)
thus ?case by simp
qed

lemma upd-inv-Some-eq:

$\llbracket \text{thread-ok } ts \text{ ta}; ts t = [x] \rrbracket \implies \text{upd-inv } I Q \text{ ta } t = I t$
by(cases ta, auto)

lemma upd-invs-Some-eq: $\llbracket \text{thread-oks } ts \text{ tas}; ts t = [x] \rrbracket \implies \text{upd-invs } I Q \text{ tas } t = I t$

proof(*induct tas arbitrary: ts I*)

case Nil thus ?case by simp

next

case (Cons TA TAS TS I)

note $IH = \langle \bigwedge ts I. \llbracket \text{thread-oks } ts \text{ TAS}; ts t = [x] \rrbracket \implies \text{upd-invs } I Q \text{ TAS } t = I t \rangle$

note $cct = \langle \text{thread-oks } TS (TA \# TAS) \rangle$

note $est = \langle TS t = [x] \rangle$

from cct est have upd-invs (upd-inv I Q TA) Q TAS t = upd-inv I Q TA t

apply(clarsimp)

apply(erule IH)

by(rule redT-updT'-Some)

also from cct est have ... = I t

by(auto elim: upd-inv-Some-eq)

finally show ?case by simp

qed

lemma SOME-new-thread-upd-invs:

assumes $Q_{\text{some}}: Q (\text{SOME } i. Q i t x m) t x m$

and $nt: \text{NewThread } t x m \in \text{set } tas$

and $cct: \text{thread-oks } ts \text{ tas}$

shows $\exists i. \text{upd-invs } I Q \text{ tas } t = [i] \wedge Q i t x m$

proof(*rule exI[where x=SOME i. Q i t x m]*)

from nt cct have upd-invs I Q tas t = [SOME i. Q i t x m]

proof(*induct tas arbitrary: ts I*)

case Nil thus ?case by simp

next

case (Cons TA TAS TS I)

note $IH = \langle \bigwedge ts I. \llbracket \text{NewThread } t x m \in \text{set } TAS; \text{thread-oks } ts \text{ TAS} \rrbracket \implies \text{upd-invs } I Q \text{ TAS } t =$

```

[SOME i. Q i t x m]
  note nt = ⟨NewThread t x m ∈ set (TA # TAS)⟩
  note cct = ⟨thread-oks TS (TA # TAS)⟩
  { assume nt': NewThread t x m ∈ set TAS
    from cct have ?case
      apply(clarsimp)
      by(rule IH[OF nt']) }
  moreover
  { assume ta: TA = NewThread t x m
    with cct have rup: redT-updT' TS TA t = [(undefined, no-wait-locks)]
      by(simp)
    from cct have cctta: thread-oks (redT-updT' TS TA) TAS by simp
    from ta have upd-inv I Q TA t = [SOME i. Q i t x m]
      by(simp)
    hence ?case
      by(clarsimp simp add: upd-invs-Some-eq[OF cctta, OF rup]) }
  ultimately show ?case using nt by auto
qed
with Qsome show upd-invs I Q tas t = [SOME i. Q i t x m] ∧ Q (SOME i. Q i t x m) t x m
  by(simp)
qed

```

lemma *ts-ok-into-ts-inv-const*:

assumes *ts-ok P ts m*

obtains *I* where *ts-inv* ($\lambda\cdot. P$) *I ts m*

proof –

from *assms* have *ts-inv* ($\lambda\cdot. P$) ($\lambda t. \text{if } t \in \text{dom } ts \text{ then Some undefined else None}$) *ts m*

by(*auto intro!*: *ts-invI dest: ts-okD*)

thus *thesis* by(*rule that*)

qed

lemma *ts-inv-const-into-ts-ok*:

ts-inv ($\lambda\cdot. P$) *I ts m* \implies *ts-ok P ts m*

by(*auto intro!*: *ts-okI dest: ts-invD*)

lemma *ts-inv-into-ts-ok-Ex*:

ts-inv Q I ts m \implies *ts-ok* ($\lambda t x m. \exists i. Q i t x m$) *ts m*

by(*rule ts-okI*)(*blast dest: ts-invD*)

lemma *ts-ok-Ex-into-ts-inv*:

ts-ok ($\lambda t x m. \exists i. Q i t x m$) *ts m* $\implies \exists I. \text{ts-inv } Q I \text{ ts m}$

by(*rule exI*[**where** $x=\lambda t. [SOME i. Q i t (fst (the (ts t))) m]$])(*auto 4 4 dest: ts-okD intro: someI intro: ts-invI*)

lemma *Ex-ts-inv-conv-ts-ok*:

$(\exists I. \text{ts-inv } Q I \text{ ts m}) \longleftrightarrow (\text{ts-ok } (\lambda t x m. \exists i. Q i t x m) \text{ ts m})$

by(*auto dest: ts-inv-into-ts-ok-Ex ts-ok-Ex-into-ts-inv*)

end

1.15 Labelled transition systems

theory *LTS*

imports

../Basic/Auxiliary
Coinductive.TLList

begin

unbundle *no floor-ceiling-syntax*

lemma *rel-option-mono*:

$\llbracket \text{rel-option } R \ x \ y; \bigwedge x \ y. R \ x \ y \implies R' \ x \ y \rrbracket \implies \text{rel-option } R' \ x \ y$
by(*cases* *x*)(*case-tac* [!] *y*, *auto*)

lemma *nth-concat-conv*:

$n < \text{length } (\text{concat } xss)$
 $\implies \exists m \ n'. \text{concat } xss \ ! \ n = (xss \ ! \ m) \ ! \ n' \wedge n' < \text{length } (xss \ ! \ m) \wedge$
 $m < \text{length } xss \wedge n = (\sum_{i < m. \text{length } (xss \ ! \ i)} + n')$

using *lnth-lconcat-conv*[*of* *n* *llist-of* (*map* *llist-of* *xss*)]

sum-comp-morphism[**where** *h* = *enat* **and** *g* = $\lambda i. \text{length } (xss \ ! \ i)$]

by(*clarsimp* *simp* *add*: *lconcat-llist-of* *zero-enat-def*[*symmetric*]) *blast*

definition *flip* :: (*'a* \Rightarrow *'b* \Rightarrow *'c*) \Rightarrow *'b* \Rightarrow *'a* \Rightarrow *'c*

where *flip* *f* = ($\lambda b \ a. f \ a \ b$)

Create a dynamic list *flip-simps* of theorems for *flip*

ML \langle

structure *FlipSimpRules* = *Named-Thms*

(

val *name* = @{*binding* *flip-simps*}

val *description* = *Simplification rules for flip in bisimulations*

)

\rangle

setup \langle *FlipSimpRules*.*setup* \rangle

lemma *flip-conv* [*flip-simps*]: *flip* *f* *b* *a* = *f* *a* *b*

by(*simp* *add*: *flip-def*)

lemma *flip-flip* [*flip-simps*, *simp*]: *flip* (*flip* *f*) = *f*

by(*simp* *add*: *flip-def*)

lemma *list-all2-flip* [*flip-simps*]: *list-all2* (*flip* *P*) *xs* *ys* = *list-all2* *P* *ys* *xs*

unfolding *flip-def* *list-all2-conv-all-nth* **by** *auto*

lemma *llist-all2-flip* [*flip-simps*]: *llist-all2* (*flip* *P*) *xs* *ys* = *llist-all2* *P* *ys* *xs*

unfolding *flip-def* *llist-all2-conv-all-lnth* **by** *auto*

lemma *rtranclp-flipD*:

assumes (*flip* *r*)^{***} *x* *y*

shows *r*^{***} *y* *x*

using *assms*

by(*induct* *rule*: *rtranclp-induct*)(*auto* *intro*: *rtranclp.rtrancl-into-rtrancl* *simp* *add*: *flip-conv*)

lemma *rtranclp-flip* [*flip-simps*]:

(*flip* *r*)^{***} = *flip* *r*^{***}

by(*auto* *intro*!: *ext* *simp* *add*: *flip-conv* *intro*: *rtranclp-flipD*)

lemma *rel-prod-flip* [*flip-simps*]:
 $rel\text{-}prod (flip\ R) (flip\ S) = flip (rel\text{-}prod\ R\ S)$
by(*auto intro!*: *ext simp add: flip-def*)

lemma *rel-option-flip* [*flip-simps*]:
 $rel\text{-}option (flip\ R) = flip (rel\text{-}option\ R)$
by(*simp add: fun-eq-iff rel-option-iff flip-def*)

lemma *tllist-all2-flip* [*flip-simps*]:
 $tllist\text{-}all2 (flip\ P) (flip\ Q) xs\ ys \longleftrightarrow tllist\text{-}all2\ P\ Q\ ys\ xs$

proof

assume $tllist\text{-}all2 (flip\ P) (flip\ Q) xs\ ys$
thus $tllist\text{-}all2\ P\ Q\ ys\ xs$
by(*coinduct rule: tllist-all2-coinduct*)(*auto dest: tllist-all2-is-TNilD tllist-all2-tfinite2-terminalD tllist-all2-thdD intro: tllist-all2-ttlI simp add: flip-def*)

next

assume $tllist\text{-}all2\ P\ Q\ ys\ xs$
thus $tllist\text{-}all2 (flip\ P) (flip\ Q) xs\ ys$
by(*coinduct rule: tllist-all2-coinduct*)(*auto dest: tllist-all2-is-TNilD tllist-all2-tfinite2-terminalD tllist-all2-thdD intro: tllist-all2-ttlI simp add: flip-def*)

qed

1.15.1 Labelled transition systems

type-synonym (*'a, 'b*) *trsys* = *'a* \Rightarrow *'b* \Rightarrow *'a* \Rightarrow *bool*

locale *trsys* =
fixes *trsys* :: (*'s, 'tl*) *trsys* ($\langle - / \dashrightarrow / \rightarrow$ [50, 0, 50] 60)
begin

abbreviation *Trsys* :: (*'s, 'tl list*) *trsys* ($\langle - / \dashrightarrow * / \rightarrow$ [50,0,50] 60)
where $\bigwedge tl. s \text{-}tl \rightarrow * s' \equiv rtrancl3p\ trsys\ s\ tl\ s'$

coinductive *inf-step* :: *'s* \Rightarrow *'tl llist* \Rightarrow *bool* ($\langle - \dashrightarrow * \infty \rangle$ [50, 0] 80)
where *inf-stepI*: $\llbracket trsys\ a\ b\ a'; a' \text{-}bs \rightarrow * \infty \rrbracket \Longrightarrow a \text{-}LCons\ b\ bs \rightarrow * \infty$

coinductive *inf-step-table* :: *'s* \Rightarrow (*'s* \times *'tl* \times *'s*) *llist* \Rightarrow *bool* ($\langle - \dashrightarrow * t \infty \rangle$ [50, 0] 80)
where

inf-step-tableI:
 $\bigwedge tl. \llbracket trsys\ s\ tl\ s'; s' \text{-}stls \rightarrow * t \infty \rrbracket$
 $\Longrightarrow s \text{-}LCons (s, tl, s') stls \rightarrow * t \infty$

definition *inf-step2inf-step-table* :: *'s* \Rightarrow *'tl llist* \Rightarrow (*'s* \times *'tl* \times *'s*) *llist*
where

inf-step2inf-step-table *s* *tls* =
unfold-llist
 $(\lambda (s, tls). lnull\ tls)$
 $(\lambda (s, tls). (s, lhd\ tls, SOME\ s'. trsys\ s\ (lhd\ tls)\ s' \wedge s' \text{-}ltl\ tls \rightarrow * \infty))$
 $(\lambda (s, tls). (SOME\ s'. trsys\ s\ (lhd\ tls)\ s' \wedge s' \text{-}ltl\ tls \rightarrow * \infty, ltl\ tls))$
 (s, tls)

coinductive *Rtrancl3p* :: *'s* \Rightarrow (*'tl, 's*) *tllist* \Rightarrow *bool*
where

$Rtrancl3p\text{-stop}: (\bigwedge tl\ s'. \neg s -tl \rightarrow s') \Longrightarrow Rtrancl3p\ s\ (TNil\ s)$
 $| Rtrancl3p\text{-into-}Rtrancl3p: \bigwedge tl. \llbracket s -tl \rightarrow s'; Rtrancl3p\ s'\ tss \rrbracket \Longrightarrow Rtrancl3p\ s\ (TCons\ tl\ tss)$

inductive-simps $Rtrancl3p\text{-simps}$:

$Rtrancl3p\ s\ (TNil\ s')$
 $Rtrancl3p\ s\ (TCons\ tl'\ tss)$

inductive-cases $Rtrancl3p\text{-cases}$:

$Rtrancl3p\ s\ (TNil\ s')$
 $Rtrancl3p\ s\ (TCons\ tl'\ tss)$

coinductive $Runs :: 's \Rightarrow 'tl\ llist \Rightarrow bool$

where

$Stuck: (\bigwedge tl\ s'. \neg s -tl \rightarrow s') \Longrightarrow Runs\ s\ LNil$
 $| Step: \bigwedge tl. \llbracket s -tl \rightarrow s'; Runs\ s'\ tss \rrbracket \Longrightarrow Runs\ s\ (LCons\ tl\ tss)$

coinductive $Runs\text{-table} :: 's \Rightarrow ('s \times 'tl \times 's)\ llist \Rightarrow bool$

where

$Stuck: (\bigwedge tl\ s'. \neg s -tl \rightarrow s') \Longrightarrow Runs\text{-table}\ s\ LNil$
 $| Step: \bigwedge tl. \llbracket s -tl \rightarrow s'; Runs\text{-table}\ s'\ stss \rrbracket \Longrightarrow Runs\text{-table}\ s\ (LCons\ (s, tl, s')\ stss)$

inductive-simps $Runs\text{-table-simps}$:

$Runs\text{-table}\ s\ LNil$
 $Runs\text{-table}\ s\ (LCons\ stls\ stss)$

lemma $inf\text{-step-not-finite-llist}$:

assumes $r: s -bs \rightarrow^* \infty$

shows $\neg lfinite\ bs$

proof

assume $lfinite\ bs$ **thus** $False$ **using** r

by($induct\ arbitrary: s$ $rule: lfinite.induct$)($auto\ elim: inf\text{-step.cases}$)

qed

lemma $inf\text{-step2}inf\text{-step-table-LNil}$ [$simp$]: $inf\text{-step2}inf\text{-step-table}\ s\ LNil = LNil$

by($simp\ add: inf\text{-step2}inf\text{-step-table-def}$)

lemma $inf\text{-step2}inf\text{-step-table-LCons}$ [$simp$]:

fixes tl **shows**

$inf\text{-step2}inf\text{-step-table}\ s\ (LCons\ tl\ tss) =$
 $LCons\ (s, tl, SOME\ s'. trsys\ s\ tl\ s' \wedge s' -tss \rightarrow^* \infty)$
 $(inf\text{-step2}inf\text{-step-table}\ (SOME\ s'. trsys\ s\ tl\ s' \wedge s' -tss \rightarrow^* \infty)\ tss)$

by($simp\ add: inf\text{-step2}inf\text{-step-table-def}$)

lemma $lnull\text{-}inf\text{-step2}inf\text{-step-table}$ [$simp$]:

$lnull\ (inf\text{-step2}inf\text{-step-table}\ s\ tss) \longleftrightarrow lnull\ tss$

by($simp\ add: inf\text{-step2}inf\text{-step-table-def}$)

lemma $inf\text{-step2}inf\text{-step-table-eq-LNil}$:

$inf\text{-step2}inf\text{-step-table}\ s\ tss = LNil \longleftrightarrow tss = LNil$

using $lnull\text{-}inf\text{-step2}inf\text{-step-table}$ **unfolding** $lnull\text{-def}$.

lemma $lhd\text{-}inf\text{-step2}inf\text{-step-table}$ [$simp$]:

$\neg lnull\ tss$

$\Longrightarrow lhd\ (inf\text{-step2}inf\text{-step-table}\ s\ tss) =$

$(s, \text{lhs } tls, \text{SOME } s'. \text{trsys } s (\text{lhs } tls) s' \wedge s' - \text{lhs } tls \rightarrow^* \infty)$
by(*simp add: inf-step2inf-step-table-def*)

lemma *ltl-inf-step2inf-step-table* [*simp*]:
 $\text{ltl } (\text{inf-step2inf-step-table } s \ tls) =$
 $\text{inf-step2inf-step-table } (\text{SOME } s'. \text{trsys } s (\text{lhs } tls) s' \wedge s' - \text{lhs } tls \rightarrow^* \infty) (\text{ltl } tls)$
by(*cases tls*) *simp-all*

lemma *lmap-inf-step2inf-step-table*: $\text{lmap } (\text{fst } \circ \text{snd}) (\text{inf-step2inf-step-table } s \ tls) = tls$
by(*coinduction arbitrary: s tls*) *auto*

lemma *inf-step-imp-inf-step-table*:
assumes $s - \text{lhs } tls \rightarrow^* \infty$
shows $\exists \text{stls}. s - \text{stls} \rightarrow^* t \ \infty \wedge tls = \text{lmap } (\text{fst } \circ \text{snd}) \ \text{stls}$

proof –

from *assms* **have** $s - \text{inf-step2inf-step-table } s \ tls \rightarrow^* t \ \infty$

proof(*coinduction arbitrary: s tls*)

case (*inf-step-table s tls*)

thus *?case*

proof *cases*

case (*inf-stepI tl s' tls'*)

let $?s' = \text{SOME } s'. \text{trsys } s \ tl \ s' \wedge s' - \text{lhs } tls' \rightarrow^* \infty$

have $\text{trsys } s \ tl \ ?s' \wedge ?s' - \text{lhs } tls' \rightarrow^* \infty$ **by**(*rule someI*)(*blast intro: inf-stepI*)

thus *?thesis* **using** $\langle tls = \text{LCons } tl \ tls' \rangle$ **by** *auto*

qed

qed

moreover **have** $tls = \text{lmap } (\text{fst } \circ \text{snd}) (\text{inf-step2inf-step-table } s \ tls)$

by(*simp only: lmap-inf-step2inf-step-table*)

ultimately show *?thesis* **by** *blast*

qed

lemma *inf-step-table-imp-inf-step*:
 $s - \text{stls} \rightarrow^* t \ \infty \implies s - \text{lmap } (\text{fst } \circ \text{snd}) \ \text{stls} \rightarrow^* \infty$
proof(*coinduction arbitrary: s stls rule: inf-step.coinduct*)

case (*inf-step s tls*)

thus *?case* **by** *cases auto*

qed

lemma *Runs-table-into-Runs*:
 $\text{Runs-table } s \ \text{stlss} \implies \text{Runs } s \ (\text{lmap } (\lambda(s, tl, s'). \ tl) \ \text{stlss})$

proof(*coinduction arbitrary: s stlss*)

case (*Runs s tls*)

thus *?case* **by** (*cases*)*auto*

qed

lemma *Runs-into-Runs-table*:
assumes $\text{Runs } s \ tls$
obtains stlss
where $tls = \text{lmap } (\lambda(s, tl, s'). \ tl) \ \text{stlss}$
and $\text{Runs-table } s \ \text{stlss}$

proof –

define stlss **where** $\text{stlss } s \ tls = \text{unfold-llist}$

$(\lambda(s, tls). \ \text{lnull } tls)$

$(\lambda(s, tls). \ (s, \text{lhs } tls, \text{SOME } s'. \ s - \text{lhs } tls \rightarrow s' \wedge \text{Runs } s' \ (\text{ltl } tls)))$

$(\lambda(s, tls). (SOME\ s'.\ s -lhd\ tls \rightarrow s' \wedge Runs\ s' (ltl\ tls), ltl\ tls))$
 (s, tls)
for $s\ tls$
have [*simp*]:
 $\bigwedge s. stlss\ s\ LNil = LNil$
 $\bigwedge s\ tl\ tls. stlss\ s\ (LCons\ tl\ tls) = LCons\ (s, tl, SOME\ s'.\ s -tl \rightarrow s' \wedge Runs\ s' tls) (stlss\ (SOME\ s'.\ s -tl \rightarrow s' \wedge Runs\ s' tls)\ tls)$
 $\bigwedge s\ tls. lnull\ (stlss\ s\ tls) \longleftrightarrow lnull\ tls$
 $\bigwedge s\ tls. \neg\ lnull\ tls \implies lhd\ (stlss\ s\ tls) = (s, lhd\ tls, SOME\ s'.\ s -lhd\ tls \rightarrow s' \wedge Runs\ s' (ltl\ tls))$
 $\bigwedge s\ tls. \neg\ lnull\ tls \implies ltl\ (stlss\ s\ tls) = stlss\ (SOME\ s'.\ s -lhd\ tls \rightarrow s' \wedge Runs\ s' (ltl\ tls)) (ltl\ tls)$
by(*simp-all add: stlss-def*)

from *assms* **have** $tls = lmap\ (\lambda(s, tl, s').\ tl)\ (stlss\ s\ tls)$
proof(*coinduction arbitrary: s tls*)
case *Eq-list*
thus *?case* **by** *cases(auto 4 3 intro: someI2)*
qed
moreover
from *assms* **have** *Runs-table s (stlss s tls)*
proof(*coinduction arbitrary: s tls*)
case (*Runs-table s stlss'*)
thus *?case*
proof(*cases*)
case (*Step s' tls' tl*)
let $?P = \lambda s'.\ s -tl \rightarrow s' \wedge Runs\ s' tls'$
from $\langle s -tl \rightarrow s' \rangle \langle Runs\ s' tls' \rangle$ **have** $?P\ s' ..$
hence $?P\ (Eps\ ?P)$ **by**(*rule someI*)
with *Step* **have** *?Step* **by** *auto*
thus *?thesis ..*
qed *simp*
qed
ultimately show *?thesis* **by**(*rule that*)
qed

lemma *Runs-lappendE*:
assumes *Runs* σ (*lappend tls tls'*)
and *lfinite tls*
obtains σ' **where** $\sigma -list-of\ tls \rightarrow^* \sigma'$
and *Runs* σ' *tls'*
proof(*atomize-elim*)
from $\langle lfinite\ tls \rangle \langle Runs\ \sigma\ (lappend\ tls\ tls') \rangle$
show $\exists\ \sigma'.\ \sigma -list-of\ tls \rightarrow^* \sigma' \wedge Runs\ \sigma'\ tls'$
proof(*induct arbitrary: sigma*)
case *lfinite-LNil* **thus** *?case* **by**(*auto*)
next
case (*lfinite-LConsI tls tl*)
from $\langle Runs\ \sigma\ (lappend\ (LCons\ tl\ tls)\ tls') \rangle$
show *?case* **unfolding** *lappend-code*
proof(*cases*)
case (*Step sigma'*)
from $\langle Runs\ \sigma'\ (lappend\ tls\ tls') \implies \exists\ \sigma''.\ \sigma' -list-of\ tls \rightarrow^* \sigma'' \wedge Runs\ \sigma''\ tls' \rangle \langle Runs\ \sigma'\ (lappend\ tls\ tls') \rangle$
obtain σ'' **where** $\sigma' -list-of\ tls \rightarrow^* \sigma''$ *Runs* $\sigma''\ tls'$ **by** *blast*
from $\langle \sigma -tl \rightarrow \sigma' \rangle \langle \sigma' -list-of\ tls \rightarrow^* \sigma'' \rangle$

```

  have  $\sigma -tl \# \text{list-of } tls \rightarrow^* \sigma''$  by(rule rtrancl3p-step-converse)
  with  $\langle \text{finite } tls \rangle$  have  $\sigma -\text{list-of } (LCons \text{ } tl \ tls) \rightarrow^* \sigma''$  by(simp)
  with  $\langle \text{Runs } \sigma'' \ tls' \rangle$  show ?thesis by blast
qed
qed
qed

lemma Trsys-into-Runs:
  assumes  $s -tls \rightarrow^* s'$ 
  and  $\text{Runs } s' \ tls'$ 
  shows  $\text{Runs } s \ (\text{lappend } (\text{list-of } tls) \ tls')$ 
using assms
by(induct rule: rtrancl3p-converse-induct)(auto intro: Runs.Step)

lemma rtrancl3p-into-Rtrancl3p:
   $\llbracket \text{rtrancl3p } trsys \ a \ bs \ a'; \bigwedge b \ a''. \neg a' -b \rightarrow a'' \rrbracket \implies \text{Rtrancl3p } a \ (\text{tllist-of-llist } a' \ (\text{lappend } bs))$ 
  by(induct rule: rtrancl3p-converse-induct)(auto intro: Rtrancl3p.intros)

lemma Rtrancl3p-into-Runs:
   $\text{Rtrancl3p } s \ tss \implies \text{Runs } s \ (\text{tllist-of-tllist } tss)$ 
by(coinduction arbitrary: s tss rule: Runs.coinduct)(auto elim: Rtrancl3p.cases)

lemma Runs-into-Rtrancl3p:
  assumes  $\text{Runs } s \ tss$ 
  obtains  $tss$  where  $tss = \text{tllist-of-tllist } tss \ \text{Rtrancl3p } s \ tss$ 
proof
  let  $?Q = \lambda s \ tss \ s'. \ s -\text{lhs } tss \rightarrow s' \wedge \text{Runs } s' \ (\text{tllist } tss)$ 
  define  $tss$  where  $tss = \text{corec-tllist}$ 
     $(\lambda(s, tss). \text{lappend } tss) \ (\lambda(s, tss). \ s)$ 
     $(\lambda(s, tss). \ \text{lhs } tss)$ 
     $(\lambda-. \ \text{False}) \ \text{undefined} \ (\lambda(s, tss). \ (\text{SOME } s'. \ ?Q \ s \ tss \ s', \ \text{tllist } tss))$ 
  have [simp]:
     $tss \ (s, \ \text{LNil}) = \text{TNil } s$ 
     $tss \ (s, \ \text{LCons } tl \ tss) = \text{TCons } tl \ (tss \ (\text{SOME } s'. \ ?Q \ s \ (\text{LCons } tl \ tss) \ s', \ tss))$ 
  for  $s \ tl \ tss$  by(auto simp add: tss-def intro: tllist.expand)

  show  $tss = \text{tllist-of-tllist } (tss \ (s, \ tss))$  using assms
  by(coinduction arbitrary: s tss)(erule Runs.cases; fastforce intro: someI2)

  show  $\text{Rtrancl3p } s \ (tss \ (s, \ tss))$  using assms
  by(coinduction arbitrary: s tss)(erule Runs.cases; simp; iprover intro: someI2[where  $Q = trsys \ - \ -$ ]
someI2[where  $Q = \lambda s'. \ \text{Runs } s' \ -$ ])
qed

lemma fixes  $tl$ 
  assumes  $\text{Rtrancl3p } s \ tss \ \text{finite } tss$ 
  shows  $\text{Rtrancl3p-into-Trsys: } \text{Trsys } s \ (\text{list-of } (\text{tllist-of-tllist } tss)) \ (\text{terminal } tss)$ 
  and  $\text{terminal-Rtrancl3p-final: } \neg \text{terminal } tss \ -tl \rightarrow s'$ 
using assms(2,1) by(induction arbitrary: s rule: finite-induct)(auto simp add: Rtrancl3p-simps intro:
rtrancl3p-step-converse)

end

```

1.15.2 Labelled transition systems with internal actions

locale $\tau trsys = trsys +$
constrains $trsys :: ('s, 'tl) trsys$
fixes $\tau move :: ('s, 'tl) trsys$
begin

inductive $silent-move :: 's \Rightarrow 's \Rightarrow bool$ ($\langle \cdot -\tau \rightarrow \cdot \rangle [50, 50] 60$)
where [*intro*]: $!!tl. \llbracket trsys\ s\ tl\ s'; \tau move\ s\ tl\ s' \rrbracket \Longrightarrow s -\tau \rightarrow s'$

declare $silent-move.cases$ [*elim*]

lemma $silent-move-iff: silent-move = (\lambda s\ s'. (\exists tl. trsys\ s\ tl\ s' \wedge \tau move\ s\ tl\ s'))$
by(*auto simp add: fun-eq-iff*)

abbreviation $silent-moves :: 's \Rightarrow 's \Rightarrow bool$ ($\langle \cdot -\tau \rightarrow * \cdot \rangle [50, 50] 60$)
where $silent-moves == silent-move \widehat{**}$

abbreviation $silent-movet :: 's \Rightarrow 's \Rightarrow bool$ ($\langle \cdot -\tau \rightarrow + \cdot \rangle [50, 50] 60$)
where $silent-movet == silent-move \widehat{++}$

coinductive $\tau diverge :: 's \Rightarrow bool$ ($\langle \cdot -\tau \rightarrow \infty \rangle [50] 60$)
where
 $\tau diverge I: \llbracket s -\tau \rightarrow s'; s' -\tau \rightarrow \infty \rrbracket \Longrightarrow s -\tau \rightarrow \infty$

coinductive $\tau inf-step :: 's \Rightarrow 'tl\ llist \Rightarrow bool$ ($\langle \cdot -\tau \dashrightarrow * \infty \rangle [50, 0] 60$)
where

$\tau inf-step-Cons: \bigwedge tl. \llbracket s -\tau \rightarrow * s'; s' -tl \rightarrow s''; \neg \tau move\ s'\ tl\ s''; s'' -\tau -tls \rightarrow * \infty \rrbracket \Longrightarrow s -\tau -LCons\ tl\ tls \rightarrow * \infty$
 $\tau inf-step-Nil: s -\tau \rightarrow \infty \Longrightarrow s -\tau -LNil \rightarrow * \infty$

coinductive $\tau inf-step-table :: 's \Rightarrow ('s \times 's \times 'tl \times 's)\ llist \Rightarrow bool$ ($\langle \cdot -\tau \dashrightarrow * t \infty \rangle [50, 0] 80$)
where

$\tau inf-step-table-Cons: \bigwedge tl. \llbracket s -\tau \rightarrow * s'; s' -tl \rightarrow s''; \neg \tau move\ s'\ tl\ s''; s'' -\tau -tls \rightarrow * t \infty \rrbracket \Longrightarrow s -\tau -LCons\ (s, s', tl, s'')\ tls \rightarrow * t \infty$

$\tau inf-step-table-Nil: s -\tau \rightarrow \infty \Longrightarrow s -\tau -LNil \rightarrow * t \infty$

definition $\tau inf-step2\tau inf-step-table :: 's \Rightarrow 'tl\ llist \Rightarrow ('s \times 's \times 'tl \times 's)\ llist$
where

$\tau inf-step2\tau inf-step-table\ s\ tils =$
 $unfold-llist$
 $(\lambda (s, tils). lnull\ tils)$
 $(\lambda (s, tils). let (s', s'') = SOME (s', s''). s -\tau \rightarrow * s' \wedge s' -lhd\ tils \rightarrow s'' \wedge \neg \tau move\ s'\ (lhd\ tils)\ s'' \wedge s'' -\tau -ltl\ tils \rightarrow * \infty$
 $in (s, s', lhd\ tils, s''))$
 $(\lambda (s, tils). let (s', s'') = SOME (s', s''). s -\tau \rightarrow * s' \wedge s' -lhd\ tils \rightarrow s'' \wedge \neg \tau move\ s'\ (lhd\ tils)\ s'' \wedge s'' -\tau -ltl\ tils \rightarrow * \infty$
 $in (s'', ltl\ tils))$
 $(s, tils)$

definition $silent-move-from :: 's \Rightarrow 's \Rightarrow 's \Rightarrow bool$

where $\text{silent-move-from } s0\ s1\ s2 \longleftrightarrow \text{silent-moves } s0\ s1 \wedge \text{silent-move } s1\ s2$

inductive $\tau\text{rtrancl3p} :: 's \Rightarrow 'tl\ list \Rightarrow 's \Rightarrow \text{bool} (\langle \cdot \ -\tau\text{---} \rangle * \rightarrow [50, 0, 50] 60)$

where

$\tau\text{rtrancl3p-refl}: \tau\text{rtrancl3p } s \ []\ s$
 $|\ \tau\text{rtrancl3p-step}: \bigwedge tl. \llbracket s \text{---} tl \rightarrow s'; \neg \tau\text{move } s\ tl\ s'; \tau\text{rtrancl3p } s'\ tl\ s'' \rrbracket \Longrightarrow \tau\text{rtrancl3p } s\ (tl \#\ tls)\ s''$
 $|\ \tau\text{rtrancl3p-}\tau\text{step}: \bigwedge tl. \llbracket s \text{---} tl \rightarrow s'; \tau\text{move } s\ tl\ s'; \tau\text{rtrancl3p } s'\ tl\ s'' \rrbracket \Longrightarrow \tau\text{rtrancl3p } s\ tls\ s''$

coinductive $\tau\text{Runs} :: 's \Rightarrow ('tl, 's\ \text{option})\ tlist \Rightarrow \text{bool} (\langle \cdot \ \Downarrow \ \cdot \rangle [50, 50] 51)$

where

$\text{Terminate}: \llbracket s \text{---}\tau \rightarrow * s'; \bigwedge tl\ s''. \neg s' \text{---} tl \rightarrow s'' \rrbracket \Longrightarrow s \Downarrow\ \text{TNil } [s']$
 $|\ \text{Diverge}: s \text{---}\tau \rightarrow \infty \Longrightarrow s \Downarrow\ \text{TNil } \text{None}$
 $|\ \text{Proceed}: \bigwedge tl. \llbracket s \text{---}\tau \rightarrow * s'; s' \text{---} tl \rightarrow s''; \neg \tau\text{move } s'\ tl\ s''; s'' \Downarrow\ tls \rrbracket \Longrightarrow s \Downarrow\ \text{TCons } tl\ tls$

inductive-simps $\tau\text{Runs-simps}$:

$s \Downarrow\ \text{TNil } (\text{Some } s')$
 $s \Downarrow\ \text{TNil } \text{None}$
 $s \Downarrow\ \text{TCons } tl'\ tls$

coinductive $\tau\text{Runs-table} :: 's \Rightarrow ('tl \times 's, 's\ \text{option})\ tlist \Rightarrow \text{bool}$

where

$\text{Terminate}: \llbracket s \text{---}\tau \rightarrow * s'; \bigwedge tl\ s''. \neg s' \text{---} tl \rightarrow s'' \rrbracket \Longrightarrow \tau\text{Runs-table } s\ (\text{TNil } [s'])$
 $|\ \text{Diverge}: s \text{---}\tau \rightarrow \infty \Longrightarrow \tau\text{Runs-table } s\ (\text{TNil } \text{None})$
 $|\ \text{Proceed}: \bigwedge tl. \llbracket s \text{---}\tau \rightarrow * s'; s' \text{---} tl \rightarrow s''; \neg \tau\text{move } s'\ tl\ s''; \tau\text{Runs-table } s''\ tls \rrbracket \Longrightarrow \tau\text{Runs-table } s\ (\text{TCons } (tl, s'')\ tls)$

definition $\text{silent-move2} :: 's \Rightarrow 'tl \Rightarrow 's \Rightarrow \text{bool}$

where $\bigwedge tl. \text{silent-move2 } s\ tl\ s' \longleftrightarrow s \text{---} tl \rightarrow s' \wedge \tau\text{move } s\ tl\ s'$

abbreviation $\text{silent-moves2} :: 's \Rightarrow 'tl\ list \Rightarrow 's \Rightarrow \text{bool}$

where $\text{silent-moves2} \equiv \text{rtrancl3p } \text{silent-move2}$

coinductive $\tau\text{Runs-table2} :: 's \Rightarrow ('tl\ list \times 's \times 'tl \times 's, ('tl\ list \times 's) + 'tl\ llist)\ tlist \Rightarrow \text{bool}$

where

$\text{Terminate}: \llbracket \text{silent-moves2 } s\ tls\ s'; \bigwedge tl\ s''. \neg s' \text{---} tl \rightarrow s'' \rrbracket \Longrightarrow \tau\text{Runs-table2 } s\ (\text{TNil } (\text{Inl } (tls, s')))$
 $|\ \text{Diverge}: \text{trsys.inf-step } \text{silent-move2 } s\ tls \Longrightarrow \tau\text{Runs-table2 } s\ (\text{TNil } (\text{Inr } tls))$
 $|\ \text{Proceed}: \bigwedge tl. \llbracket \text{silent-moves2 } s\ tls\ s'; s' \text{---} tl \rightarrow s''; \neg \tau\text{move } s'\ tl\ s''; \tau\text{Runs-table2 } s''\ tlstlss \rrbracket \Longrightarrow \tau\text{Runs-table2 } s\ (\text{TCons } (tls, s', tl, s'')\ tlstlss)$

inductive-simps $\tau\text{Runs-table2-simps}$:

$\tau\text{Runs-table2 } s\ (\text{TNil } tlls)$
 $\tau\text{Runs-table2 } s\ (\text{TCons } tlstls\ tllstlss)$

lemma $\text{inf-step-table-all-}\tau\text{-into-}\tau\text{diverge}$:

$\llbracket s \text{---} stls \rightarrow * t \infty; \forall (s, tl, s') \in \text{lset } stls. \tau\text{move } s\ tl\ s' \rrbracket \Longrightarrow s \text{---}\tau \rightarrow \infty$

proof(*coinduction arbitrary: s stls*)

case ($\tau\text{diverge } s$)

thus ?*case by cases* (*auto simp add: silent-move-iff, blast*)

qed

lemma $\text{inf-step-table-lappend-llist-ofD}$:

$s \text{---} \text{lappend } (llist\ \text{of } stls)\ (\text{LCons } (x, tl', x')\ xs) \rightarrow * t \infty$

$\implies (s \text{ -map } (fst \circ snd) \text{ stls} \rightarrow * x) \wedge (x \text{ -LCons } (x, tl', x') \text{ xs} \rightarrow * t \infty)$
proof(*induct stls arbitrary: s*)
case Nil thus ?*case* **by**(*auto elim: inf-step-table.cases intro: inf-step-table.intros rtrancl3p-refl*)
next
case (Cons st stls)
note $IH = \langle \bigwedge s. s \text{ -lappend } (l\text{list-of stls}) (L\text{Cons } (x, tl', x') \text{ xs}) \rightarrow * t \infty \implies$
 $s \text{ -map } (fst \circ snd) \text{ stls} \rightarrow * x \wedge x \text{ -LCons } (x, tl', x') \text{ xs} \rightarrow * t \infty \rangle$
from $\langle s \text{ -lappend } (l\text{list-of } (st \# stls)) (L\text{Cons } (x, tl', x') \text{ xs}) \rightarrow * t \infty \rangle$
show ?*case*
proof *cases*
case (*inf-step-tableI s' stls' tl*)
hence [*simp*]: $st = (s, tl, s') \text{ stls}' = \text{lappend } (l\text{list-of stls}) (L\text{Cons } (x, tl', x') \text{ xs})$
and $s \text{ -tl} \rightarrow s'$ $s' \text{ -lappend } (l\text{list-of stls}) (L\text{Cons } (x, tl', x') \text{ xs}) \rightarrow * t \infty$ **by** *simp-all*
from $IH[OF \langle s' \text{ -lappend } (l\text{list-of stls}) (L\text{Cons } (x, tl', x') \text{ xs}) \rightarrow * t \infty \rangle]$
have $s' \text{ -map } (fst \circ snd) \text{ stls} \rightarrow * x \text{ -LCons } (x, tl', x') \text{ xs} \rightarrow * t \infty$ **by** *auto*
with $\langle s \text{ -tl} \rightarrow s' \rangle$ **show** ?*thesis* **by**(*auto simp add: o-def intro: rtrancl3p-step-converse*)
qed
qed

lemma *inf-step-table-lappend-llist-of- τ -into- τ moves*:
assumes *lfinite stls*
shows $\llbracket s \text{ -lappend stls } (L\text{Cons } (x, tl' x') \text{ xs}) \rightarrow * t \infty; \forall (s, tl, s') \in \text{lset stls}. \tau \text{ move } s \text{ tl } s' \rrbracket \implies s$
 $\text{-}\tau \rightarrow * x$
using *assms*
proof(*induct arbitrary: s rule: lfinite.induct*)
case lfinite-LNil thus ?*case* **by**(*auto elim: inf-step-table.cases*)
next
case (*lfinite-LConsI stls st*)
note $IH = \langle \bigwedge s. \llbracket s \text{ -lappend stls } (L\text{Cons } (x, tl' x') \text{ xs}) \rightarrow * t \infty; \forall (s, tl, s') \in \text{lset stls}. \tau \text{ move } s \text{ tl } s' \rrbracket \implies s$
 $\text{-}\tau \rightarrow * x \rangle$
obtain $s1 \text{ tl1 } s1'$ **where** [*simp*]: $st = (s1, tl1, s1')$ **by**(*cases st*)
from $\langle s \text{ -lappend } (L\text{Cons } st \text{ stls}) (L\text{Cons } (x, tl' x') \text{ xs}) \rightarrow * t \infty \rangle$
show ?*case*
proof *cases*
case (*inf-step-tableI X' STLS TL*)
hence [*simp*]: $s1 = s \text{ TL} = \text{tl1 } X' = s1' \text{ STLS} = \text{lappend stls } (L\text{Cons } (x, tl' x') \text{ xs})$
and $s \text{ -tl1} \rightarrow s1'$ **and** $s1' \text{ -lappend stls } (L\text{Cons } (x, tl' x') \text{ xs}) \rightarrow * t \infty$ **by** *simp-all*
from $\langle \forall (s, tl, s') \in \text{lset } (L\text{Cons } st \text{ stls}). \tau \text{ move } s \text{ tl } s' \rangle$ **have** $\tau \text{ move } s \text{ tl1 } s1'$ **by** *simp*
moreover
from $IH[OF \langle s1' \text{ -lappend stls } (L\text{Cons } (x, tl' x') \text{ xs}) \rightarrow * t \infty \rangle] \langle \forall (s, tl, s') \in \text{lset } (L\text{Cons } st \text{ stls}).$
 $\tau \text{ move } s \text{ tl } s' \rangle$
have $s1' \text{ -}\tau \rightarrow * x$ **by** *simp*
ultimately show ?*thesis* **using** $\langle s \text{ -tl1} \rightarrow s1' \rangle$ **by**(*auto intro: converse-rtranclp-into-rtranclp*)
qed
qed

lemma *inf-step-table-into- τ inf-step*:

$s \text{ -stls} \rightarrow * t \infty \implies s \text{ -}\tau \text{ -lmap } (fst \circ snd) (l\text{filter } (\lambda(s, tl, s'). \neg \tau \text{ move } s \text{ tl } s') \text{ stls}) \rightarrow * t \infty$
proof(*coinduction arbitrary: s stls*)
case ($\tau \text{ inf-step } s \text{ stls}$)
let ? $P = \lambda(s, tl, s'). \neg \tau \text{ move } s \text{ tl } s'$
show ?*case*
proof(*cases lfilter ?P stls*)

case *LNil*
with τ *inf-step* **have** $?\tau$ *inf-step-Nil*
by(*auto intro: inf-step-table-all- τ -into- τ diverge simp add: lfilter-eq-LNil*)
thus $?thesis$..
next
case (*LCons stls' xs*)
obtain x tl x' **where** $stls' = (x, tl, x')$ **by**(*cases stls'*)
with *LCons* **have** $stls: lfilter ?P stls = LCons (x, tl, x') xs$ **by** *simp*
from *lfilter-eq-LConsD[OF this]* **obtain** $stls1$ $stls2$
where $stls1: stls = lappend stls1 (LCons (x, tl, x') stls2)$
and *lfinite stls1*
and $\tau s: \forall (s, tl, s') \in lset stls1. \tau move s tl s'$
and $n\tau: \neg \tau move x tl x'$ **and** $xs: xs = lfilter ?P stls2$ **by** *blast*
from $\langle lfinite stls1 \rangle \tau$ *inf-step* τs **have** $s -\tau \rightarrow^* x$ **unfolding** $stls1$
by(*rule inf-step-table-lappend-llist-of- τ -into- τ moves*)
moreover from $\langle lfinite stls1 \rangle$ **have** l *list-of* ($list$ -of $stls1$) = $stls1$ **by**(*simp add: llist-of-list-of*)
with τ *inf-step stls1* **have** $s -lappend (l$ *list-of* ($list$ -of $stls1$)) ($LCons (x, tl, x') stls2$) $\rightarrow^* t \infty$ **by**
simp
from *inf-step-table-lappend-llist-ofD[OF this]*
have $x -LCons (x, tl, x') stls2 \rightarrow^* t \infty$..
hence $x -tl \rightarrow x' x' -stls2 \rightarrow^* t \infty$ **by**(*auto elim: inf-step-table.cases*)
ultimately have $? \tau$ *inf-step-Cons* **using** xs $n\tau$ **by**(*auto simp add: stls o-def*)
thus $?thesis$..
qed
qed

lemma *inf-step-into- τ inf-step*:

assumes $s -tls \rightarrow^* \infty$

shows $\exists A. s -\tau -lnths\ tl\ A \rightarrow^* \infty$

proof –

from *inf-step-imp-inf-step-table[OF assms]*

obtain $stls$ **where** $s -stls \rightarrow^* t \infty$ **and** $tls: tls = lmap (fst \circ snd) stls$ **by** *blast*

from $\langle s -stls \rightarrow^* t \infty \rangle$ **have** $s -\tau -lmap (fst \circ snd) (lfilter (\lambda(s, tl, s'). \neg \tau move s tl s') stls) \rightarrow^* \infty$

by(*rule inf-step-table-into- τ inf-step*)

hence $s -\tau -lnths\ tl\ \{n. enat\ n < llength\ stls \wedge (\lambda(s, tl, s'). \neg \tau move s tl s') (lnth\ stls\ n)\} \rightarrow^* \infty$

unfolding *lfilter-conv-lnths tl* **by** *simp*

thus $?thesis$ **by** *blast*

qed

lemma *silent-moves-into- τ rtrancl3p*:

$s -\tau \rightarrow^* s' \implies s -\tau -[] \rightarrow^* s'$

by(*induct rule: converse-rtranclp-induct*)(*blast intro: τ rtrancl3p.intros*)+

lemma *τ rtrancl3p-into-silent-moves*:

$s -\tau -[] \rightarrow^* s' \implies s -\tau \rightarrow^* s'$

apply(*induct s tls $\equiv []$:: 'tl list s' rule: τ rtrancl3p.induct*)

apply(*auto intro: converse-rtranclp-into-rtranclp*)

done

lemma *τ rtrancl3p-Nil-eq- τ moves*:

$s -\tau -[] \rightarrow^* s' \iff s -\tau \rightarrow^* s'$

by(*blast intro: silent-moves-into- τ rtrancl3p τ rtrancl3p-into-silent-moves*)

lemma *τ rtrancl3p-trans* [*trans*]:

moreover from $\text{tranclpD}[OF \langle \text{silent-move}^{\hat{++}} s' s'' \rangle]$ **obtain** s''
where $\text{silent-move} s' s'' \text{ silent-move}^{\hat{**}} s'' s'''$ **by** *blast*
ultimately show $?thesis$ **using** $\langle \text{silent-move}^{\hat{**}} s'' s''' \rangle \langle X s''' \vee s''' -\tau \rightarrow \infty \rangle$
by $(\text{auto intro: } \tau \text{diverge-rtranclp-silent-move})$
next
case $(\text{step } S)$
moreover from $\langle \text{silent-move}^{\hat{**}} S s' \rangle \langle \text{silent-move}^{\hat{++}} s' s'' \rangle$
have $\text{silent-move}^{\hat{**}} S s''$ **by** $(\text{rule rtranclp-trans}[OF - \text{tranclp-into-rtranclp}])$
ultimately show $?thesis$ **using** $\langle X s'' \vee s'' -\tau \rightarrow \infty \rangle$ **by** $(\text{auto intro: } \tau \text{diverge-rtranclp-silent-move})$
qed
qed
qed

lemma $\tau \text{diverge-trancl-measure-coinduct}$ [*consumes 2, case-names $\tau \text{diverge}$*]:

assumes *major*: $X s t \text{ wfp } \mu$

and *step*: $\bigwedge s t. X s t \implies \exists s' t'. (\mu t' t \wedge s' = s \vee \text{silent-move}^{\hat{++}} s s') \wedge (X s' t' \vee s' -\tau \rightarrow \infty)$

shows $s -\tau \rightarrow \infty$

proof –

{ **fix** $s t$

assume $X s t$

with $\langle \text{wfp } \mu \rangle$ **have** $\exists s' t'. \text{silent-move}^{\hat{++}} s s' \wedge (X s' t' \vee s' -\tau \rightarrow \infty)$

proof (*induct arbitrary: s rule: wfp-induct[consumes 1]*)

case $(1 t)$

hence *IH*: $\bigwedge s' t'. \llbracket \mu t' t; X s' t' \rrbracket \implies$

$\exists s'' t''. \text{silent-move}^{\hat{++}} s' s'' \wedge (X s'' t'' \vee s'' -\tau \rightarrow \infty)$ **by** *blast*

from $\text{step}[OF \langle X s t \rangle]$ **obtain** $s' t'$

where $\mu t' t \wedge s' = s \vee \text{silent-move}^{\hat{++}} s s' X s' t' \vee s' -\tau \rightarrow \infty$ **by** *blast*

from $\langle \mu t' t \wedge s' = s \vee \text{silent-move}^{\hat{++}} s s' \rangle$ **show** $?case$

proof

assume $\mu t' t \wedge s' = s$

hence $\mu t' t$ **and** [*simp*]: $s' = s$ **by** *simp-all*

from $\langle X s' t' \vee s' -\tau \rightarrow \infty \rangle$ **show** $?thesis$

proof

assume $X s' t'$

from *IH*[$OF \langle \mu t' t \text{ this} \rangle$] **show** $?thesis$ **by** *simp*

next

assume $s' -\tau \rightarrow \infty$ **thus** $?thesis$

by $\text{cases}(\text{auto simp add: silent-move-iff})$

qed

next

assume $\text{silent-move}^{\hat{++}} s s'$

thus $?thesis$ **using** $\langle X s' t' \vee s' -\tau \rightarrow \infty \rangle$ **by** *blast*

qed

qed }

note $X = \text{this}$

from $\langle X s t \rangle$ **have** $\exists t. X s t ..$

thus $?thesis$

proof (*coinduct rule: $\tau \text{diverge-trancl-coinduct}$*)

case $(\tau \text{diverge } s)$

then obtain t **where** $X s t ..$

from $X[OF \text{ this}]$ **show** $?case$ **by** *blast*

qed

qed

lemma $\tau\text{inf-step2}\tau\text{inf-step-table-LNil}$ [simp]: $\tau\text{inf-step2}\tau\text{inf-step-table } s \text{ LNil} = \text{LNil}$
by(simp add: $\tau\text{inf-step2}\tau\text{inf-step-table-def}$)

lemma $\tau\text{inf-step2}\tau\text{inf-step-table-LCons}$ [simp]:

fixes $s \text{ tl } ss \text{ tls}$

defines $ss \equiv \text{SOME } (s', s''). s -\tau \rightarrow^* s' \wedge s' -\text{tl} \rightarrow s'' \wedge \neg \tau\text{move } s' \text{ tl } s'' \wedge s'' -\tau -\text{tls} \rightarrow^* \infty$

shows

$\tau\text{inf-step2}\tau\text{inf-step-table } s \text{ (LCons tl tls)} =$

$\text{LCons } (s, \text{fst } ss, \text{tl}, \text{snd } ss) (\tau\text{inf-step2}\tau\text{inf-step-table } (\text{snd } ss) \text{ tls})$

by(simp add: $ss\text{-def } \tau\text{inf-step2}\tau\text{inf-step-table-def split-beta}$)

lemma $\text{lnull-}\tau\text{inf-step2}\tau\text{inf-step-table}$ [simp]:

$\text{lnull } (\tau\text{inf-step2}\tau\text{inf-step-table } s \text{ tls}) \longleftrightarrow \text{lnull } \text{tls}$

by(simp add: $\tau\text{inf-step2}\tau\text{inf-step-table-def}$)

lemma $\text{lhd-}\tau\text{inf-step2}\tau\text{inf-step-table}$ [simp]:

$\neg \text{lnull } \text{tls} \implies \text{lhd } (\tau\text{inf-step2}\tau\text{inf-step-table } s \text{ tls}) =$

$(\text{let } (s', s'') = \text{SOME } (s', s''). s -\tau \rightarrow^* s' \wedge s' -\text{lhd } \text{tls} \rightarrow s'' \wedge \neg \tau\text{move } s' \text{ (lhd } \text{tls}) } s'' \wedge s'' -\tau -\text{tl}$
 $\text{tls} \rightarrow^* \infty$

$\text{in } (s, s', \text{lhd } \text{tls}, s'')$

unfolding $\tau\text{inf-step2}\tau\text{inf-step-table-def Let-def}$ **by** simp

lemma $\text{ttl-}\tau\text{inf-step2}\tau\text{inf-step-table}$ [simp]:

$\neg \text{lnull } \text{tls} \implies \text{ttl } (\tau\text{inf-step2}\tau\text{inf-step-table } s \text{ tls}) =$

$(\text{let } (s', s'') = \text{SOME } (s', s''). s -\tau \rightarrow^* s' \wedge s' -\text{lhd } \text{tls} \rightarrow s'' \wedge \neg \tau\text{move } s' \text{ (lhd } \text{tls}) } s'' \wedge s'' -\tau -\text{ttl}$
 $\text{tls} \rightarrow^* \infty$

$\text{in } \tau\text{inf-step2}\tau\text{inf-step-table } s'' \text{ (ttl } \text{tls})$

unfolding $\tau\text{inf-step2}\tau\text{inf-step-table-def Let-def}$

by(simp add: $split\text{-beta}$)

lemma $\text{lmap-}\tau\text{inf-step2}\tau\text{inf-step-table}$: $\text{lmap } (\text{fst} \circ \text{snd} \circ \text{snd}) (\tau\text{inf-step2}\tau\text{inf-step-table } s \text{ tls}) = \text{tls}$

by(coinduction arbitrary: $s \text{ tls}$)(auto simp add: $split\text{-beta}$)

lemma $\tau\text{inf-step-into-}\tau\text{inf-step-table}$:

$s -\tau -\text{tls} \rightarrow^* \infty \implies s -\tau -\tau\text{inf-step2}\tau\text{inf-step-table } s \text{ tls} \rightarrow^* t \infty$

proof(coinduction arbitrary: $s \text{ tls}$)

case $(\tau\text{inf-step-table } s \text{ tls})$

thus ?case

proof(cases)

case $(\tau\text{inf-step-Cons } s' s'' \text{ tls}' \text{ tl})$

let ?ss = $\text{SOME } (s', s''). s -\tau \rightarrow^* s' \wedge s' -\text{tl} \rightarrow s'' \wedge \neg \tau\text{move } s' \text{ tl } s'' \wedge s'' -\tau -\text{tls}' \rightarrow^* \infty$

from $\tau\text{inf-step-Cons}$ **have** $\text{tls} = \text{LCons } \text{tl } \text{tls}'$ **and** $s -\tau \rightarrow^* s' s' -\text{tl} \rightarrow s''$

$\neg \tau\text{move } s' \text{ tl } s'' s'' -\tau -\text{tls}' \rightarrow^* \infty$ **by** simp-all

hence $(\lambda(s', s''). s -\tau \rightarrow^* s' \wedge s' -\text{tl} \rightarrow s'' \wedge \neg \tau\text{move } s' \text{ tl } s'' \wedge s'' -\tau -\text{tls}' \rightarrow^* \infty) (s', s'')$ **by**

simp

hence $(\lambda(s', s''). s -\tau \rightarrow^* s' \wedge s' -\text{tl} \rightarrow s'' \wedge \neg \tau\text{move } s' \text{ tl } s'' \wedge s'' -\tau -\text{tls}' \rightarrow^* \infty)$?ss **by**(rule

someI)

with tls **have** ? $\tau\text{inf-step-table-Cons}$ **by** auto

thus ?thesis ..

next

case $\tau\text{inf-step-Nil}$

then **have** ? $\tau\text{inf-step-table-Nil}$ **by** simp

thus ?thesis ..

qed

qed

lemma τ inf-step-imp- τ inf-step-table:

assumes $s \text{ -}\tau\text{-}t \text{ls} \rightarrow^* \infty$

shows $\exists \text{sstls}. s \text{ -}\tau\text{-}\text{sstls} \rightarrow^* t \infty \wedge \text{tls} = \text{lmap} (\text{fst} \circ \text{snd} \circ \text{snd}) \text{sstls}$

using τ inf-step-into- τ inf-step-table[OF assms]

by(*auto simp only: lmap- τ inf-step2 τ inf-step-table*)

lemma τ inf-step-table-into- τ inf-step:

$s \text{ -}\tau\text{-}\text{sstls} \rightarrow^* t \infty \implies s \text{ -}\tau\text{-}\text{lmap} (\text{fst} \circ \text{snd} \circ \text{snd}) \text{sstls} \rightarrow^* \infty$

proof(*coinduction arbitrary: s sstls*)

case (τ inf-step $s \text{tls}$)

thus ?*case* **by** *cases(auto simp add: o-def)*

qed

lemma *silent-move-fromI* [*intro*]:

$\llbracket \text{silent-moves } s0 \ s1; \text{silent-move } s1 \ s2 \rrbracket \implies \text{silent-move-from } s0 \ s1 \ s2$

by(*simp add: silent-move-from-def*)

lemma *silent-move-fromE* [*elim*]:

assumes *silent-move-from* $s0 \ s1 \ s2$

obtains *silent-moves* $s0 \ s1$ *silent-move* $s1 \ s2$

using *assms* **by**(*auto simp add: silent-move-from-def*)

lemma *rtranclp-silent-move-from-imp-silent-moves*:

assumes $s'x: \text{silent-move}^{**} \ s' \ x$

shows $(\text{silent-move-from } s')^{\wedge **} \ x \ z \implies \text{silent-moves } s' \ z$

by(*induct rule: rtranclp-induct*)(*auto intro: s'x*)

lemma τ diverge-not-wfP-silent-move-from:

assumes $s \text{ -}\tau \rightarrow \infty$

shows $\neg \text{wfP} (\text{flip} (\text{silent-move-from } s))$

proof

assume $\text{wfP} (\text{flip} (\text{silent-move-from } s))$

moreover **define** Q **where** $Q = \{s'. \text{silent-moves } s \ s' \wedge s' \text{ -}\tau \rightarrow \infty\}$

hence $s \in Q$ **using** $\langle s \text{ -}\tau \rightarrow \infty \rangle$ **by**(*auto*)

ultimately **have** $\exists z \in Q. \forall y. \text{silent-move-from } s \ z \ y \longrightarrow y \notin Q$

unfolding *wfp-eq-minimal flip-simps* **by** *blast*

then **obtain** z **where** $z \in Q$

and *min*: $\bigwedge y. \text{silent-move-from } s \ z \ y \implies y \notin Q$ **by** *blast*

from $\langle z \in Q \rangle$ **have** $\text{silent-moves } s \ z \ z \text{ -}\tau \rightarrow \infty$ **unfolding** *Q-def* **by** *auto*

from $\langle z \text{ -}\tau \rightarrow \infty \rangle$ **obtain** y **where** $\text{silent-move } z \ y \ y \text{ -}\tau \rightarrow \infty$ **by** *cases auto*

from $\langle \text{silent-moves } s \ z \rangle$ $\langle \text{silent-move } z \ y \rangle$ **have** $\text{silent-move-from } s \ z \ y \ ..$

hence $y \notin Q$ **by**(*rule min*)

moreover **from** $\langle \text{silent-moves } s \ z \rangle$ $\langle \text{silent-move } z \ y \rangle$ $\langle y \text{ -}\tau \rightarrow \infty \rangle$

have $y \in Q$ **unfolding** *Q-def* **by** *auto*

ultimately **show** *False* **by** *contradiction*

qed

lemma *wfP-silent-move-from-unroll*:

assumes $\text{wfPs}': \bigwedge s'. s \text{ -}\tau \rightarrow s' \implies \text{wfP} (\text{flip} (\text{silent-move-from } s'))$

shows $\text{wfP} (\text{flip} (\text{silent-move-from } s))$

unfolding *wfp-eq-minimal flip-conv*

proof(*intro allI impI*)

fix Q **and** $x :: 's$
assume $x \in Q$
show $\exists z \in Q. \forall y. \text{silent-move-from } s \ z \ y \longrightarrow y \notin Q$
proof (*cases* $\exists s'. s \text{--}\tau \longrightarrow s' \wedge (\exists x'. \text{silent-moves } s' \ x' \wedge x' \in Q)$)
 case *False*
 hence $\forall y. \text{silent-move-from } s \ x \ y \longrightarrow \neg y \in Q$
 by (*cases* $x=s$) (*auto*, *blast elim: converse-rtranclpE intro: rtranclp.rtrancl-into-rtrancl*)
 with $\langle x \in Q \rangle$ **show** *?thesis* **by** *blast*
next
 case *True*
 then obtain $s' \ x'$ **where** $s \text{--}\tau \longrightarrow s'$ **and** *silent-moves* $s' \ x'$ **and** $x' \in Q$
 by *auto*
 from $\langle s \text{--}\tau \longrightarrow s' \rangle$ **have** *wfP* (*flip* (*silent-move-from* s')) **by** (*rule* *wfPs'*)
 from *this* $\langle x' \in Q \rangle$ **obtain** z **where** $z \in Q$ **and** *min*: $\bigwedge y. \text{silent-move-from } s' \ z \ y \Longrightarrow \neg y \in Q$
 and (*silent-move-from* s')^{**} $x' \ z$
 by (*rule* *wfP-minimalE*) (*unfold flip-simps*, *blast*)
 { **fix** y
 assume *silent-move-from* $s \ z \ y$
 with $\langle (\text{silent-move-from } s')^{\wedge**} x' \ z \rangle \langle \text{silent-move}^{\wedge**} s' \ x' \rangle$
 have *silent-move-from* $s' \ z \ y$
 by (*blast intro: rtranclp-silent-move-from-imp-silent-moves*)
 hence $\neg y \in Q$ **by** (*rule* *min*) }
 with $\langle z \in Q \rangle$ **show** *?thesis* **by** (*auto simp add: intro!: bexI*)
 qed
qed

lemma *not-wfP-silent-move-from- τ diverge*:
 assumes $\neg \text{wfP} (\text{flip} (\text{silent-move-from } s))$
 shows $s \text{--}\tau \longrightarrow \infty$
using *assms*
proof (*coinduct*)
 case (*τ diverge* s)
 { **assume** *wfPs'*: $\bigwedge s'. s \text{--}\tau \longrightarrow s' \Longrightarrow \text{wfP} (\text{flip} (\text{silent-move-from } s'))$
 hence $\text{wfP} (\text{flip} (\text{silent-move-from } s))$ **by** (*rule* *wfP-silent-move-from-unroll*) }
 with *τ diverge* **have** $\exists s'. s \text{--}\tau \longrightarrow s' \wedge \neg \text{wfP} (\text{flip} (\text{silent-move-from } s'))$ **by** *auto*
 thus *?case* **by** *blast*
qed

lemma *τ diverge-neq-wfP-silent-move-from*:
 $s \text{--}\tau \longrightarrow \infty \neq \text{wfP} (\text{flip} (\text{silent-move-from } s))$
by (*auto intro: not-wfP-silent-move-from- τ diverge dest: τ diverge-not-wfP-silent-move-from*)

lemma *not- τ diverge-to-no- τ move*:
 assumes $\neg s \text{--}\tau \longrightarrow \infty$
 shows $\exists s'. s \text{--}\tau \longrightarrow* s' \wedge (\forall s''. \neg s' \text{--}\tau \longrightarrow s'')$
proof –
 define S **where** $S = s$
 from $\langle \neg \tau \text{ diverge } s \rangle$ **have** *wfP* (*flip* (*silent-move-from* S)) **unfolding** *S-def*
 using *τ diverge-neq-wfP-silent-move-from*[*of* s] **by** *simp*
 moreover **have** *silent-moves* $S \ s$ **unfolding** *S-def* ..
 ultimately show *?thesis*
 proof (*induct rule: wfp-induct'*)
 case (*wfP* s)
 note $IH = \langle \bigwedge y. \llbracket \text{flip} (\text{silent-move-from } S) \ y \ s; S \text{--}\tau \longrightarrow* y \rrbracket$

$\implies \exists s'. y \text{ } \tau \rightarrow^* s' \wedge (\forall s''. \neg s' \text{ } \tau \rightarrow s'')$
show *?case*
proof(*cases* $\exists s'. \text{silent-move } s \ s'$)
 case *False* **thus** *?thesis* **by** *auto*
next
 case *True*
 then obtain s' **where** $s \text{ } \tau \rightarrow s'$..
 with $\langle S \text{ } \tau \rightarrow^* s \rangle$ **have** *flip (silent-move-from S) s' s*
 unfolding *flip-conv* **by**(*rule silent-move-fromI*)
 moreover from $\langle S \text{ } \tau \rightarrow^* s \rangle \langle s \text{ } \tau \rightarrow s' \rangle$ **have** $S \text{ } \tau \rightarrow^* s' ..$
 ultimately have $\exists s''. s' \text{ } \tau \rightarrow^* s'' \wedge (\forall s'''. \neg s'' \text{ } \tau \rightarrow s''')$ **by**(*rule IH*)
 then obtain s'' **where** $s' \text{ } \tau \rightarrow^* s'' \forall s'''. \neg s'' \text{ } \tau \rightarrow s'''$ **by** *blast*
 from $\langle s \text{ } \tau \rightarrow s' \rangle \langle s' \text{ } \tau \rightarrow^* s'' \rangle$ **have** $s \text{ } \tau \rightarrow^* s''$ **by**(*rule converse-rtranclp-into-rtranclp*)
 with $\langle \forall s'''. \neg s'' \text{ } \tau \rightarrow s''' \rangle$ **show** *?thesis* **by** *blast*
qed
qed
qed

lemma *τ diverge-conv- τ Runs*:
 $s \text{ } \tau \rightarrow \infty \iff s \Downarrow \text{TNil None}$
by(*auto intro: τ Runs.Diverge elim: τ Runs.cases*)

lemma *τ inf-step-into- τ Runs*:
 $s \text{ } \tau \text{ } \text{tls} \rightarrow^* \infty \implies s \Downarrow \text{tlist-of-llist None } \text{tls}$
proof(*coinduction arbitrary: s tls*)
 case (*τ Runs s tls'*)
 thus *?case* **by** *cases(auto simp add: τ diverge-conv- τ Runs)*
qed

lemma *τ -into- τ Runs*:
 $\llbracket s \text{ } \tau \rightarrow s'; s' \Downarrow \text{tls} \rrbracket \implies s \Downarrow \text{tls}$
by(*blast elim: τ Runs.cases intro: τ Runs.intros τ diverge.intros converse-rtranclp-into-rtranclp*)

lemma *τ rtrancl3p-into- τ Runs*:
 assumes $s \text{ } \tau \text{ } \text{tls} \rightarrow^* s'$
 and $s' \Downarrow \text{tls}'$
 shows $s \Downarrow \text{lappendt (llist-of } \text{tls) } \text{tls}'$
using *assms*
by *induct(auto intro: τ Runs.Proceed τ -into- τ Runs)*

lemma *τ Runs-table-into- τ Runs*:
 $\tau\text{Runs-table } s \ \text{stlsss} \implies s \Downarrow \text{tmap fst id stlsss}$
proof(*coinduction arbitrary: s stlsss*)
 case (*τ Runs s tls*)
 thus *?case* **by** *cases(auto simp add: o-def id-def)*
qed

definition *τ Runs2 τ Runs-table* :: $'s \Rightarrow ('tl, 's \text{ option}) \text{tlist} \Rightarrow ('tl \times 's, 's \text{ option}) \text{tlist}$
where
 $\tau\text{Runs2}\tau\text{Runs-table } s \ \text{tls} = \text{unfold-tlist}$
 ($\lambda(s, \text{tls}). \text{is-TNil } \text{tls}$)
 ($\lambda(s, \text{tls}). \text{terminal } \text{tls}$)
 ($\lambda(s, \text{tls}). (\text{thd } \text{tls}, \text{SOME } s''). \exists s'. s \text{ } \tau \rightarrow^* s' \wedge s' \text{ } \text{thd } \text{tls} \rightarrow s'' \wedge \neg \tau\text{move } s' (\text{thd } \text{tls}) s'' \wedge s'' \Downarrow \text{ttl } \text{tls}$)

$(\lambda(s, tls). (SOME s''. \exists s'. s -\tau \rightarrow * s' \wedge s' -thd\ tls \rightarrow s'' \wedge \neg \tau move\ s' (thd\ tls)\ s'' \wedge s'' \Downarrow\ ttl\ tls, ttl\ tls))$
 (s, tls)

lemma *is-TNil- τ Runs2 τ Runs-table* [simp]:
is-TNil (τ Runs2 τ Runs-table $s\ tls$) \longleftrightarrow *is-TNil* tls

thm *unfold-tllist.disc*

by(*simp add: τ Runs2 τ Runs-table-def*)

lemma *thd- τ Runs2 τ Runs-table* [simp]:

\neg *is-TNil* $tls \implies$

thd (τ Runs2 τ Runs-table $s\ tls$) =

$(thd\ tls, SOME\ s''. \exists s'. s -\tau \rightarrow * s' \wedge s' -thd\ tls \rightarrow s'' \wedge \neg \tau move\ s' (thd\ tls)\ s'' \wedge s'' \Downarrow\ ttl\ tls)$

by(*simp add: τ Runs2 τ Runs-table-def*)

lemma *ttl- τ Runs2 τ Runs-table* [simp]:

\neg *is-TNil* $tls \implies$

ttl (τ Runs2 τ Runs-table $s\ tls$) =

τ Runs2 τ Runs-table ($SOME\ s''. \exists s'. s -\tau \rightarrow * s' \wedge s' -thd\ tls \rightarrow s'' \wedge \neg \tau move\ s' (thd\ tls)\ s'' \wedge s''$

$\Downarrow\ ttl\ tls$) (*ttl* tls)

by(*simp add: τ Runs2 τ Runs-table-def*)

lemma *terminal- τ Runs2 τ Runs-table* [simp]:

is-TNil $tls \implies$ *terminal* (τ Runs2 τ Runs-table $s\ tls$) = *terminal* tls

by(*simp add: τ Runs2 τ Runs-table-def*)

lemma *τ Runs2 τ Runs-table-simps* [simp, nitpick-simp]:

τ Runs2 τ Runs-table s (*TNil* so) = *TNil* so

\bigwedge *tl.*

τ Runs2 τ Runs-table s (*TCons* $tl\ tls$) =

$(let\ s'' = SOME\ s''. \exists s'. s -\tau \rightarrow * s' \wedge s' -tl \rightarrow s'' \wedge \neg \tau move\ s' tl\ s'' \wedge s'' \Downarrow\ tls$
in *TCons* (tl, s'') (τ Runs2 τ Runs-table $s''\ tls$))

apply(*simp add: τ Runs2 τ Runs-table-def*)

apply(*rule tllist.expand*)

apply(*simp-all*)

done

lemma *τ Runs2 τ Runs-table-inverse*:

tmap fst id (τ Runs2 τ Runs-table $s\ tls$) = tls

by(*coinduction arbitrary: s tls*) *auto*

lemma *τ Runs-into- τ Runs-table*:

assumes $s \Downarrow\ tls$

shows $\exists stlsss. tls = tmap\ fst\ id\ stlsss \wedge \tau$ Runs-table $s\ stlsss$

proof(*intro exI conjI*)

from *assms* **show** τ Runs-table s (τ Runs2 τ Runs-table $s\ tls$)

proof(*coinduction arbitrary: s tls*)

case (τ Runs-table $s\ tls$)

thus ?*case*

proof *cases*

case (*Terminate* s')

hence ?*Terminate* **by** *simp*

thus ?*thesis* ..

next

```

case Diverge
hence ?Diverge by simp
thus ?thesis by simp
next
case (Proceed s' s'' tls' tl)
let  $?P = \lambda s''. \exists s'. s -\tau \rightarrow^* s' \wedge s' -tl \rightarrow s'' \wedge \neg \tau \text{move } s' \text{ tl } s'' \wedge s'' \Downarrow tls'$ 
from Proceed have  $?P \ s''$  by auto
hence  $?P \ (Eps \ ?P)$  by (rule someI)
hence ?Proceed using  $\langle tls = TCons \ tl \ tls' \rangle$ 
  by (auto simp add: split-beta)
thus ?thesis by simp
qed
qed
qed (simp add:  $\tau$ Runs2 $\tau$ Runs-table-inverse)

lemma  $\tau$ Runs-lappendE:
assumes  $\sigma \Downarrow \text{lappendt } tls \ tls'$ 
and lfinite tls
obtains  $\sigma'$  where  $\sigma -\tau \text{-list-of } tls \rightarrow^* \sigma'$ 
and  $\sigma' \Downarrow tls'$ 
proof (atomize-elim)
from  $\langle \text{lfinite } tls \rangle \langle \sigma \Downarrow \text{lappendt } tls \ tls' \rangle$ 
show  $\exists \sigma'. \sigma -\tau \text{-list-of } tls \rightarrow^* \sigma' \wedge \sigma' \Downarrow tls'$ 
proof (induct arbitrary:  $\sigma$ )
  case lfinite-LNil thus ?case by (auto intro:  $\tau$ rtrancl3p-refl)
next
case (lfinite-LConsI tls tl)
from  $\langle \sigma \Downarrow \text{lappendt } (LCons \ tl \ tls) \ tls' \rangle$ 
show ?case unfolding lappendt-LCons
proof (cases)
  case (Proceed  $\sigma' \sigma''$ )
from  $\langle \sigma'' \Downarrow \text{lappendt } tls \ tls' \implies \exists \sigma'''. \sigma'' -\tau \text{-list-of } tls \rightarrow^* \sigma''' \wedge \sigma''' \Downarrow tls' \rangle \langle \sigma'' \Downarrow \text{lappendt } tls \ tls' \rangle$ 
obtain  $\sigma'''$  where  $\sigma'' -\tau \text{-list-of } tls \rightarrow^* \sigma''' \wedge \sigma''' \Downarrow tls'$  by blast
from  $\langle \sigma' -tl \rightarrow \sigma'' \rangle \langle \neg \tau \text{move } \sigma' \text{ tl } \sigma'' \rangle \langle \sigma'' -\tau \text{-list-of } tls \rightarrow^* \sigma''' \rangle$ 
have  $\sigma' -\tau \text{-tl} \# \text{list-of } tls \rightarrow^* \sigma'''$  by (rule  $\tau$ rtrancl3p-step)
with  $\langle \sigma -\tau \rightarrow^* \sigma' \rangle$  have  $\sigma -\tau \text{-} [] @ (tl \# \text{list-of } tls) \rightarrow^* \sigma'''$ 
  unfolding  $\tau$ rtrancl3p-Nil-eq- $\tau$ moves[symmetric] by (rule  $\tau$ rtrancl3p-trans)
with  $\langle \text{lfinite } tls \rangle$  have  $\sigma -\tau \text{-list-of } (LCons \ tl \ tls) \rightarrow^* \sigma'''$  by (simp add: list-of-LCons)
with  $\langle \sigma''' \Downarrow tls' \rangle$  show ?thesis by blast
qed
qed
qed

lemma  $\tau$ Runs-total:
   $\exists tls. \sigma \Downarrow tls$ 
proof
let  $?\tau \text{halt} = \lambda \sigma \sigma'. \sigma -\tau \rightarrow^* \sigma' \wedge (\forall tl \sigma''. \neg \sigma' -tl \rightarrow \sigma'')$ 
let  $?\tau \text{diverge} = \lambda \sigma. \sigma -\tau \rightarrow \infty$ 
let  $?proceed = \lambda \sigma (tl, \sigma''). \exists \sigma'. \sigma -\tau \rightarrow^* \sigma' \wedge \sigma' -tl \rightarrow \sigma'' \wedge \neg \tau \text{move } \sigma' \text{ tl } \sigma''$ 

define tls where  $tls = \text{unfold-tllist}$ 
  ( $\lambda \sigma. (\exists \sigma'. ?\tau \text{halt } \sigma \ \sigma') \vee ?\tau \text{diverge } \sigma$ )
  ( $\lambda \sigma. \text{if } \exists \sigma'. ?\tau \text{halt } \sigma \ \sigma' \text{ then Some (SOME } \sigma'. ?\tau \text{halt } \sigma \ \sigma') \text{ else None}$ )

```

```

  (λσ. fst (SOME tlσ'. ?proceed σ tlσ'))
  (λσ. snd (SOME tlσ'. ?proceed σ tlσ')) σ
then show σ ↓ tls
proof(coinduct σ tls rule: τRuns.coinduct)
  case (τRuns σ tls)
  show ?case
  proof(cases ∃σ'. ?τhalt σ σ')
    case True
    hence ?τhalt σ (SOME σ'. ?τhalt σ σ') by(rule someI-ex)
    hence ?Terminate using True unfolding τRuns by simp
    thus ?thesis ..
  next
  case False
  note τhalt = this
  show ?thesis
  proof(cases ?τdiverge σ)
    case True
    hence ?Diverge using False unfolding τRuns by simp
    thus ?thesis by simp
  next
  case False
  from not-τdiverge-to-no-τmove[OF this]
  obtain σ' where σ-σ': σ -τ→* σ'
    and no-τ: ∧σ''. ¬σ' -τ→σ'' by blast
  from σ-σ' τhalt obtain tl σ'' where σ' -tl→σ'' by auto
  moreover with no-τ[of σ''] have ¬τmove σ' tl σ'' by auto
  ultimately have ?proceed σ (tl, σ'') using σ-σ' by auto
  hence ?proceed σ (SOME tlσ. ?proceed σ tlσ) by(rule someI)
  hence ?Proceed using False τhalt unfolding τRuns
    by(subst unfold-tllist.code) fastforce
  thus ?thesis by simp
  qed
  qed
  qed
  qed

```

```

lemma silent-move2-into-silent-move:
  fixes tl
  assumes silent-move2 s tl s'
  shows s -τ→s'
using assms by(auto simp add: silent-move2-def)

```

```

lemma silent-move-into-silent-move2:
  assumes s -τ→s'
  shows ∃tl. silent-move2 s tl s'
using assms by(auto simp add: silent-move2-def)

```

```

lemma silent-moves2-into-silent-moves:
  assumes silent-moves2 s tls s'
  shows s -τ→*s'
using assms
by(induct)(blast intro: silent-move2-into-silent-move rtranclp.rtrancl-into-rtrancl)+

```

```

lemma silent-moves-into-silent-moves2:

```


assumes $s \rightarrow^* s'$
shows $\exists tls. \text{silent-moves2 } s \ tls \ s'$
using *assms*
by(*induct*)(*blast dest: silent-move-into-silent-move2 intro: rtrancl3p-step*)+

lemma *inf-step-silent-move2-into- τ diverge*:

trsys.inf-step silent-move2 s tls $\implies s \rightarrow \infty$

proof(*coinduction arbitrary: s tls*)

case (τ *diverge s*)

thus *?case*

by(*cases rule: trsys.inf-step.cases[consumes 1]*)(*auto intro: silent-move2-into-silent-move*)

qed

lemma *τ diverge-into-inf-step-silent-move2*:

assumes $s \rightarrow \infty$

obtains *tls where trsys.inf-step silent-move2 s tls*

proof –

define *tls where tls = unfold-llist*

(λ -. *False*)

(λ s. *fst (SOME (tl, s'). silent-move2 s tl s' \wedge s' $\rightarrow \infty$)*)

(λ s. *snd (SOME (tl, s'). silent-move2 s tl s' \wedge s' $\rightarrow \infty$)*)

s (is - = ?tls s)

with *assms have s $\rightarrow \infty \wedge tls = ?tls s$ by simp*

hence *trsys.inf-step silent-move2 s tls*

proof(*coinduct rule: trsys.inf-step.coinduct[consumes 1, case-names inf-step, case-conclusion inf-step step]*)

case (*inf-step s tls*)

let *?P = $\lambda(tl, s'). \text{silent-move2 } s \ tl \ s' \ \wedge \ s' \ \rightarrow \ \infty$*

from *inf-step obtain s $\rightarrow \infty$ and tls: tls = ?tls s ..*

from $\langle s \rightarrow \infty \rangle$ **obtain** *s' where s $\rightarrow s' \ s' \rightarrow \infty$ by cases*

from $\langle s \rightarrow s' \rangle$ **obtain** *tl where silent-move2 s tl s'*

by(*blast dest: silent-move-into-silent-move2*)

with $\langle s' \rightarrow \infty \rangle$ **have** *?P (tl, s')* **by** *simp*

hence *?P (Eps ?P) by(rule someI)*

thus *?case using tls*

by(*subst (asm) unfold-llist.code*)(*auto*)

qed

thus *thesis by(rule that)*

qed

lemma *τ Runs-into- τ rtrancl3p*:

assumes *runs: s \Downarrow tlss*

and *fin: tfinite tlss*

and *terminal: terminal tlss = Some s'*

shows τ *rtrancl3p s (list-of (llist-of-tlss)) s'*

using *fin runs terminal*

proof(*induct arbitrary: s rule: tfinite-induct*)

case *TNil thus ?case by cases(auto intro: silent-moves-into- τ rtrancl3p)*

next

case (*TCons tl tlss*)

from $\langle s \Downarrow TCons \ tl \ tlss \rangle$ **obtain** *s'' s'''*

where *step: s $\rightarrow^* s''$*

and *step2: s'' \rightarrow tl \rightarrow s''' \rightarrow τ move s'' tl s'''*

and $s''' \Downarrow \text{tlss}$ **by cases**
from $\langle \text{terminal } (TCons \text{ tl } \text{tlss}) = [s'] \rangle$ **have** $\text{terminal } \text{tlss} = [s']$ **by simp**
with $\langle s''' \Downarrow \text{tlss} \rangle$ **have** $s''' -\tau - \text{list-of } (\text{llist-of-tlss } \text{tlss}) \rightarrow^* s'$ **by** (rule $TCons$)
with step2 **have** $s'' -\tau - \text{tl} \# \text{list-of } (\text{llist-of-tlss } \text{tlss}) \rightarrow^* s'$ **by** (rule $\tau\text{rtrancl3p-step}$)
with step **have** $s -\tau - [] @ \text{tl} \# \text{list-of } (\text{llist-of-tlss } \text{tlss}) \rightarrow^* s'$
by (rule $\tau\text{rtrancl3p-trans}[OF \text{ silent-moves-into-}\tau\text{rtrancl3p}]$)
thus $?case$ **using** $\langle \text{tfinite } \text{tlss} \rangle$ **by simp**
qed

lemma $\tau\text{Runs-terminal-stuck}$:

assumes $\text{Runs}: s \Downarrow \text{tlss}$
and $\text{fin}: \text{tfinite } \text{tlss}$
and $\text{terminal}: \text{terminal } \text{tlss} = \text{Some } s'$
and $\text{proceed}: s' -\text{tls} \rightarrow s''$
shows False

using fin Runs terminal

proof (induct arbitrary: s rule: tfinite-induct)

case $TNil$ **thus** $?case$ **using** proceed **by cases auto**

next

case $TCons$ **thus** $?case$ **by** (fastforce elim: $\tau\text{Runs.cases}$)

qed

lemma $\text{Runs-table-silent-diverge}$:

$[\text{Runs-table } s \text{ stlss}; \forall (s, \text{tl}, s') \in \text{lset } \text{stlss}. \tau\text{move } s \text{ tl } s'; \neg \text{lfinite } \text{stlss}]$
 $\implies s -\tau \rightarrow \infty$

proof (coinduction arbitrary: $s \text{ stlss}$)

case $(\tau\text{diverge } s)$

thus $?case$ **by cases** (auto 5 2)

qed

lemma $\text{Runs-table-silent-rtrancl}$:

assumes $\text{lfinite } \text{stlss}$
and $\text{Runs-table } s \text{ stlss}$
and $\forall (s, \text{tl}, s') \in \text{lset } \text{stlss}. \tau\text{move } s \text{ tl } s'$
shows $s -\tau \rightarrow^* \text{llast } (LCons \ s \ (lmap \ (\lambda(s, \text{tl}, s'). \ s') \ \text{stlss}))$ (**is** $?thesis1$)
and $\text{llast } (LCons \ s \ (lmap \ (\lambda(s, \text{tl}, s'). \ s') \ \text{stlss})) -\text{tl}' \rightarrow s'' \implies \text{False}$ (**is** $\text{PROP } ?thesis2$)

proof –

from assms **have** $?thesis1 \wedge (\text{llast } (LCons \ s \ (lmap \ (\lambda(s, \text{tl}, s'). \ s') \ \text{stlss})) -\text{tl}' \rightarrow s'' \implies \text{False})$

proof (induct arbitrary: s)

case lfinite-LNil **thus** $?case$ **by** (auto elim: Runs-table.cases)

next

case $(\text{lfinite-LConsI } \text{stlss } \text{stls})$

from $\langle \text{Runs-table } s \ (LCons \ \text{stls } \ \text{stlss}) \rangle$

obtain $\text{tl } s'$ **where** $[\text{simp}]: \text{stls} = (s, \text{tl}, s')$

and $s -\text{tl} \rightarrow s'$ **and** $\text{Run}': \text{Runs-table } s' \ \text{stlss}$ **by cases**

from $\langle \forall (s, \text{tl}, s') \in \text{lset } (LCons \ \text{stls } \ \text{stlss}). \tau\text{move } s \ \text{tl } s' \rangle$

have $\tau\text{move } s \ \text{tl } s'$ **and** $\text{silent}': \forall (s, \text{tl}, s') \in \text{lset } \text{stlss}. \tau\text{move } s \ \text{tl } s'$ **by simp-all**

from $\langle s -\text{tl} \rightarrow s' \rangle \langle \tau\text{move } s \ \text{tl } s' \rangle$ **have** $s -\tau \rightarrow s'$ **by auto**

moreover **from** $\text{Run}' \ \text{silent}'$

have $s' -\tau \rightarrow^* \text{llast } (LCons \ s' \ (lmap \ (\lambda(s, \text{tl}, s'). \ s') \ \text{stlss})) \wedge$

$(\text{llast } (LCons \ s' \ (lmap \ (\lambda(s, \text{tl}, s'). \ s') \ \text{stlss})) -\text{tl}' \rightarrow s'' \implies \text{False})$

by (rule lfinite-LConsI)

ultimately show $?case$ **by** (auto)

qed

thus *?thesis1 PROP ?thesis2* **by** *blast+*
qed

lemma *Runs-table-silent-lappendD*:

fixes *s stlss*
defines $s' \equiv \text{llast } (LCons\ s\ (\text{lmap } (\lambda(s, tl, s').\ s')\ stlss))$
assumes *Runs: Runs-table s (lappend stlss stlss')*
and *fin: lfinite stlss*
and *silent: $\forall (s, tl, s') \in \text{lset } stlss. \tau\text{move } s\ tl\ s'$*
shows $s -\tau \rightarrow^* s'$ (**is** *?thesis1*)
and *Runs-table s' stlss' (is ?thesis2)*
and $stlss' \neq LNil \implies s' = \text{fst } (\text{lhs } stlss')$ (**is** *PROP ?thesis3*)

proof –

from *fin Runs silent*
have *?thesis1 \wedge ?thesis2 \wedge (stlss' \neq LNil \longrightarrow s' = fst (lhs stlss'))*
unfolding *s'-def*
proof(*induct arbitrary: s*)
case *lfinite-LNil thus ?case*
by(*auto simp add: neq-LNil-conv Runs-table-simps*)
next
case *lfinite-LConsI thus ?case*
by(*clarsimp simp add: neq-LNil-conv Runs-table-simps*)(*blast intro: converse-rtranclp-into-rtranclp*)
qed
thus *?thesis1 ?thesis2 PROP ?thesis3* **by** *simp-all*
qed

lemma *Runs-table-into- τ Runs*:

fixes *s stlss*
defines $tls \equiv \text{tmap } (\lambda(s, tl, s').\ tl)\ id\ (\text{tfilter } None\ (\lambda(s, tl, s').\ \neg \tau\text{move } s\ tl\ s'))\ (\text{tlist-of-llist } (\text{Some } (\text{llast } (LCons\ s\ (\text{lmap } (\lambda(s, tl, s').\ s')\ stlss))))\ stlss)$
(is $- \equiv ?conv\ s\ stlss$)
assumes *Runs-table s stlss*
shows $\tau\text{Runs } s\ tls$

using *assms*

proof(*coinduction arbitrary: s tls stlss*)

case ($\tau\text{Runs } s\ tls\ stlss$)
note $tls = \langle tls = ?conv\ s\ stlss \rangle$
and $Run = \langle \text{Runs-table } s\ stlss \rangle$
show *?case*
proof(*cases tls*)
case [*simp*]: (*TNil so*)
from *tls*
have *silent: $\forall (s, tl, s') \in \text{lset } stlss. \tau\text{move } s\ tl\ s'$*
by(*auto simp add: TNil-eq-tmap-conv tfilter-empty-conv*)
show *?thesis*
proof(*cases lfinite stlss*)
case *False*
with *Run silent* **have** $s -\tau \rightarrow \infty$ **by**(*rule Runs-table-silent-diverge*)
hence *?Diverge using False tls* **by**(*simp add: TNil-eq-tmap-conv tfilter-empty-conv*)
thus *?thesis* **by** *simp*
next
case *True*
with *Runs-table-silent-rtrancl*[*OF this Run silent*]
have *?Terminate using tls*

```

    by(auto simp add: TNil-eq-tmap-conv tfilter-empty-conv terminal-tl-list-of-llist split-def)
  thus ?thesis by simp
qed
next
case [simp]: (TCons tl tls')
from tls obtain s' s'' stlss'
  where tl': tfilter None ( $\lambda(s, tl, s'). \neg \tau \text{move } s \text{ tl } s'$ ) (tl-list-of-llist [llast (LCons s (lmap ( $\lambda(s, tl, s'). s')$  stlss))]) stlss) = TCons (s', tl, s'') stlss'
  and tls': tls' = tmap ( $\lambda(s, tl, s'). tl$ ) id stlss'
  by(simp add: TCons-eq-tmap-conv split-def id-def split-paired-Ex) blast
from tfilter-eq-TConsD[OF tl']
obtain stlstr rest
  where stlss-eq: tl-list-of-llist [llast (LCons s (lmap ( $\lambda(s, tl, s'). s')$  stlss))]) stlss = lappendt stlstr
  (TCons (s', tl, s'') rest)
  and fin: lfinite stlstr
  and silent:  $\forall (s, tl, s') \in \text{lset } stlstr. \tau \text{move } s \text{ tl } s'$ 
  and  $\neg \tau \text{move } s' \text{ tl } s''$ 
  and stlss': stlss' = tfilter None ( $\lambda(s, tl, s'). \neg \tau \text{move } s \text{ tl } s'$ ) rest
  by(auto simp add: split-def)
from stlss-eq fin obtain rest'
  where stlss: stlss = lappendt stlstr rest'
  and rest': tl-list-of-llist [llast (LCons s (lmap ( $\lambda(s, tl, s'). s')$  stlss))]) rest' = TCons (s', tl, s'')
rest
  unfolding tl-list-of-llist-eq-lappendt-conv by auto
hence rest'  $\neq$  LNil by clarsimp
from Run[unfolded stlss] fin silent
have s  $-\tau \rightarrow^*$  llast (LCons s (lmap ( $\lambda(s, tl, s'). s')$  stlstr))
  and Runs-table (llast (LCons s (lmap ( $\lambda(s, tl, s'). s')$  stlstr))) rest'
  and llast (LCons s (lmap ( $\lambda(s, tl, s'). s')$  stlstr)) = fst (lhd rest')
  by(rule Runs-table-silent-lappendD)+(simp add:  $\langle \text{rest}' \neq \text{LNil} \rangle$ )
moreover with rest'  $\langle \text{rest}' \neq \text{LNil} \rangle$  stlss fin obtain rest''
  where rest': rest' = LCons (s', tl, s'') rest''
  and rest: rest = tl-list-of-llist [llast (LCons s'' (lmap ( $\lambda(s, tl, s'). s')$  rest''))]) rest''
  by(clarsimp simp add: neq-LNil-conv llast-LCons lmap-lappend-distrib)
ultimately have s  $-\tau \rightarrow^*$  s' s' -tl $\rightarrow$  s'' Runs-table s'' rest''
  by(simp-all add: Runs-table-simps)
hence ?Proceed using  $\langle \neg \tau \text{move } s' \text{ tl } s'' \rangle$  tls' stlss' rest
  by(auto simp add: id-def)
thus ?thesis by simp
qed
qed

```

lemma τ Runs-table2-into- τ Runs:

```

 $\tau$ Runs-table2 s tlstlss
 $\implies s \Downarrow \text{tmap } (\lambda(tls, s', tl, s''). tl) (\lambda x. \text{case } x \text{ of } \text{Inl } (tls, s') \implies \text{Some } s' \mid \text{Inr } - \implies \text{None}) \text{tlstlss}$ 

```

proof(coinduction arbitrary: s tlstlss)

```

  case ( $\tau$ Runs s tlstlss)
  thus ?case by cases(auto intro: silent-moves2-into-silent-moves inf-step-silent-move2-into- $\tau$ diverge)

```

qed

lemma τ Runs-into- τ Runs-table2:

```

  assumes s  $\Downarrow$  tls
  obtains tlstlss
  where  $\tau$ Runs-table2 s tlstlss

```

and $tls = tmap (\lambda(tls, s', tl, s''). tl) (\lambda x. case x of Inl (tls, s') \Rightarrow Some s' | Inr - \Rightarrow None) tlstlss$
proof –
let $?terminal = \lambda s tls. case terminal\ tl\ of$
 $None \Rightarrow Inr (SOME\ tls'. trsys.inf-step\ silent-move2\ s\ tls')$
 $| Some\ s' \Rightarrow let\ tls' = SOME\ tls'. silent-moves2\ s\ tls'\ s' in Inl (tls', s')$
let $?P = \lambda s tls (tls'', s', s''). silent-moves2\ s\ tls''\ s' \wedge s' - thd\ tls \rightarrow s'' \wedge \neg \tau move\ s' (thd\ tls)\ s'' \wedge$
 $s'' \Downarrow ttl\ tls$
define $tlstlss$ **where** $tlstlss\ s\ tls = unfold-tl\ list$
 $(\lambda(s, tls). is-TNil\ tls)$
 $(\lambda(s, tls). ?terminal\ s\ tls)$
 $(\lambda(s, tls). let (tls'', s', s'') = Eps (?P\ s\ tls) in (tls'', s', thd\ tls, s''))$
 $(\lambda(s, tls). let (tls'', s', s'') = Eps (?P\ s\ tls) in (s'', ttl\ tls))$
 (s, tls)
for $s\ tls$

have $[simp]:$
 $\bigwedge s\ tls. is-TNil (tlstlss\ s\ tls) \longleftrightarrow is-TNil\ tls$
 $\bigwedge s\ tls. is-TNil\ tls \Longrightarrow terminal (tlstlss\ s\ tls) = ?terminal\ s\ tls$
 $\bigwedge s\ tls. \neg is-TNil\ tls \Longrightarrow thd (tlstlss\ s\ tls) = (let (tls'', s', s'') = Eps (?P\ s\ tls) in (tls'', s', thd\ tls,$
 $s''))$
 $\bigwedge s\ tls. \neg is-TNil\ tls \Longrightarrow ttl (tlstlss\ s\ tls) = (let (tls'', s', s'') = Eps (?P\ s\ tls) in tlstlss\ s'' (ttl\ tls))$
by $(simp-all\ add: tlstlss-def\ split-beta)$

have $[simp]:$
 $\bigwedge s. tlstlss\ s (TNil\ None) = TNil (Inr (SOME\ tls'. trsys.inf-step\ silent-move2\ s\ tls'))$
 $\bigwedge s\ s'. tlstlss\ s (TNil (Some\ s')) = TNil (Inl (SOME\ tls'. silent-moves2\ s\ tls'\ s', s'))$
unfolding $tlstlss-def$ **by** $simp-all$

let $?conv = tmap (\lambda(tls, s', tl, s''). tl) (\lambda x. case x of Inl (tls, s') \Rightarrow Some s' | Inr - \Rightarrow None)$
from $assms$ **have** $\tauRuns-table2\ s (tlstlss\ s\ tls)$
proof $(coinduction\ arbitrary: s\ tls)$
case $(\tauRuns-table2\ s\ tls)$
thus $?case$
proof $(cases)$
case $(Terminate\ s')$
let $?P = \lambda tls'. silent-moves2\ s\ tls'\ s'$
from $\langle s -\tau \rightarrow s' \rangle$ **obtain** tls' **where** $?P\ tls'$ **by** $(blast\ dest: silent-moves-into-silent-moves2)$
hence $?P (Eps\ ?P)$ **by** $(rule\ someI)$
with $Terminate$ **have** $?Terminate$ **by** $auto$
thus $?thesis$ **by** $simp$
next
case $Diverge$
let $?P = \lambda tls'. trsys.inf-step\ silent-move2\ s\ tls'$
from $\langle s -\tau \rightarrow \infty \rangle$ **obtain** tls' **where** $?P\ tls'$ **by** $(rule\ \tau\ diverge-into-inf-step-silent-move2)$
hence $?P (Eps\ ?P)$ **by** $(rule\ someI)$
hence $?Diverge$ **using** $\langle tls = TNil\ None \rangle$ **by** $simp$
thus $?thesis$ **by** $simp$
next
case $(Proceed\ s'\ s''\ tls'\ tl)$
from $\langle s -\tau \rightarrow s' \rangle$ **obtain** tls'' **where** $silent-moves2\ s\ tls''\ s'$
by $(blast\ dest: silent-moves-into-silent-moves2)$
with $Proceed$ **have** $?P\ s\ tls (tls'', s', s'')$ **by** $simp$
hence $?P\ s\ tls (Eps\ (?P\ s\ tls))$ **by** $(rule\ someI)$
hence $?Proceed$ **using** $Proceed$ **unfolding** $tlstlss-def$

```

      by(subst unfold-tllist.code)(auto simp add: split-def)
    thus ?thesis by simp
  qed
qed
moreover
from assms have  $tls = ?conv (tlsstls\ s\ tls)$ 
proof(coinduction arbitrary:  $s\ tls$ )
  case (Eq-tllist  $s\ tls$ )
  thus ?case
  proof(cases)
    case (Proceed  $s'\ s''\ tls'\ tl$ )
    from  $\langle s -\tau \rightarrow^* s' \rangle$  obtain  $tls''$  where silent-moves2  $s\ tls''\ s'$ 
      by(blast dest: silent-moves-into-silent-moves2)
    with Proceed have  $?P\ s\ tls\ (tls'',\ s',\ s')$  by simp
    hence  $?P\ s\ tls\ (Eps\ (?P\ s\ tls))$  by(rule someI)
    thus ?thesis using  $\langle tls = TCons\ tl\ tls' \rangle$  by auto
  qed auto
qed
ultimately show thesis by(rule that)
qed

lemma  $\tau$ Runs-table2-into-Runs:
  assumes  $\tau$ Runs-table2  $s\ tlsstls$ 
  shows Runs  $s\ (lconcat\ (lappend\ (lmap\ (\lambda(tls,\ s,\ tl,\ s').\ llist-of\ (tls\ @\ [tl]))\ (llist-of-tllist\ tlsstls))\ (LCons\ (case\ terminal\ tlsstls\ of\ Inl\ (tls,\ s')\ \Rightarrow\ llist-of\ tls\ | \ Inr\ tls\ \Rightarrow\ tls)\ LNil)))$ 
  (is Runs - ( $?conv\ tlsstls$ ))
using assms
proof(coinduction arbitrary:  $s\ tlsstls$ )
  case (Runs  $s\ tlsstls$ )
  thus ?case
  proof(cases)
    case (Terminate  $tls'\ s'$ )
    from  $\langle silent-moves2\ s\ tls'\ s' \rangle$  show ?thesis
  proof(cases rule: rtrancl3p-converseE)
    case refl
    hence ?Stuck using Terminate by simp
    thus ?thesis ..
  next
    case (step  $tls''\ tl\ s''$ )
    from  $\langle silent-moves2\ s''\ tls''\ s' \rangle$   $\langle \wedge tl\ s''.\ \neg\ s' -tl \rightarrow s'' \rangle$ 
    have  $\tau$ Runs-table2  $s''\ (TNil\ (Inl\ (tls'',\ s''))$ ) ..
    with  $\langle tls' = tl\ \# \ tls'' \rangle$   $\langle silent-move2\ s\ tl\ s'' \rangle$   $\langle tlsstls = TNil\ (Inl\ (tls',\ s')) \rangle$ 
    have ?Step by(auto simp add: silent-move2-def intro!: exI)
    thus ?thesis ..
  qed
qed
next
  case (Diverge  $tls'$ )
  from  $\langle trsys.inf-step\ silent-move2\ s\ tls' \rangle$ 
  obtain  $tl\ tls''\ s'$  where silent-move2  $s\ tl\ s'$ 
    and  $tls' = LCons\ tl\ tls''\ trsys.inf-step\ silent-move2\ s'\ tls''$ 
  by(cases rule: trsys.inf-step.cases[consumes 1]) auto
  from  $\langle trsys.inf-step\ silent-move2\ s'\ tls'' \rangle$ 
  have  $\tau$ Runs-table2  $s'\ (TNil\ (Inr\ tls''))$  ..
  hence ?Step using  $\langle tlsstls = TNil\ (Inr\ tls') \rangle$   $\langle tls' = LCons\ tl\ tls'' \rangle$   $\langle silent-move2\ s\ tl\ s' \rangle$ 

```

```

  by(auto simp add: silent-move2-def intro!: exI)
  thus ?thesis ..
next
  case (Proceed tls' s' s'' tlstlss' tl)
  from ⟨silent-moves2 s tls' s'⟩ have ?Step
  proof(cases rule: rtrancl3p-converseE)
    case refl with Proceed show ?thesis by auto
  next
    case (step tls'' tl' s''')
    from ⟨silent-moves2 s''' tls'' s'⟩ ⟨s' -tl→ s''⟩ ⟨¬ τmove s' tl s''⟩ ⟨τRuns-table2 s'' tlstlss'⟩
    have τRuns-table2 s''' (TCons (tls'', s', tl, s'') tlstlss') ..
    with ⟨tls' = tl' # tls''⟩ ⟨silent-move2 s tl' s'''⟩ ⟨tlstlss = TCons (tls', s', tl, s'') tlstlss'⟩
    show ?thesis by(auto simp add: silent-move2-def intro!: exI)
  qed
  thus ?thesis ..
qed
qed

```

```

lemma τRuns-table2-silentsD:
  fixes tl
  assumes Runs: τRuns-table2 s tlstlss
  and tset: (tls, s', tl', s'') ∈ tset tlstlss
  and set: tl ∈ set tls
  shows ∃ s''' s'''. silent-move2 s''' tl s''''
using tset Runs
proof(induct arbitrary: s rule: tset-induct)
  case (find tlstlss')
  from ⟨τRuns-table2 s (TCons (tls, s', tl', s'') tlstlss')⟩
  have silent-moves2 s tls s' by cases
  thus ?case using set by induct auto
next
  case step thus ?case by(auto simp add: τRuns-table2-simps)
qed

```

```

lemma τRuns-table2-terminal-silentsD:
  assumes Runs: τRuns-table2 s tlstlss
  and fin: lfinite (llist-of-tl tlstlss)
  and terminal: terminal tlstlss = Inl (tls, s'')
  shows ∃ s'. silent-moves2 s' tls s''
using fin Runs terminal
proof(induct llist-of-tl tlstlss arbitrary: tlstlss s)
  case lfinite-LNil thus ?case
  by(cases tlstlss)(auto simp add: τRuns-table2-simps)
next
  case (lfinite-LConsI xs tlstlss)
  thus ?case by(cases tlstlss)(auto simp add: τRuns-table2-simps)
qed

```

```

lemma τRuns-table2-terminal-inf-stepD:
  assumes Runs: τRuns-table2 s tlstlss
  and fin: lfinite (llist-of-tl tlstlss)
  and terminal: terminal tlstlss = Inr tls
  shows ∃ s'. trsys.inf-step silent-move2 s' tls
using fin Runs terminal

```

```

proof(induct llist-of-tlist tlstlss arbitrary: s tlstlss)
  case lfinite-LNil thus ?case
    by(cases tlstlss)(auto simp add:  $\tau$ Runs-table2-simps)
next
  case (lfinite-LConsI xs tlstls)
  thus ?case by(cases tlstlss)(auto simp add:  $\tau$ Runs-table2-simps)
qed

```

```

lemma  $\tau$ Runs-table2-lappendtD:
  assumes Runs:  $\tau$ Runs-table2 s (lappendt tlstlss tlstlss')
  and fin: lfinite tlstlss
  shows  $\exists s'. \tau$ Runs-table2 s' tlstlss'
using fin Runs
by(induct arbitrary: s)(auto simp add:  $\tau$ Runs-table2-simps)

end

```

```

lemma  $\tau$ moves-False:  $\tau$ trsys.silent-move r ( $\lambda s ta s'. False$ ) = ( $\lambda s s'. False$ )
by(auto simp add:  $\tau$ trsys.silent-move-iff)

```

```

lemma  $\tau$ rtrancl3p-False-eq-rtrancl3p:  $\tau$ trsys. $\tau$ rtrancl3p r ( $\lambda s tl s'. False$ ) = rtrancl3p r
proof(intro ext iffI)
  fix s tls s'
  assume  $\tau$ trsys. $\tau$ rtrancl3p r ( $\lambda s tl s'. False$ ) s tls s'
  thus rtrancl3p r s tls s' by(rule  $\tau$ trsys. $\tau$ rtrancl3p.induct)(blast intro: rtrancl3p-step-converse)+
next
  fix s tls s'
  assume rtrancl3p r s tls s'
  thus  $\tau$ trsys. $\tau$ rtrancl3p r ( $\lambda s tl s'. False$ ) s tls s'
    by(induct rule: rtrancl3p-converse-induct)(auto intro:  $\tau$ trsys. $\tau$ rtrancl3p.intros)
qed

```

```

lemma  $\tau$ diverge-empty- $\tau$ move:
   $\tau$ trsys. $\tau$ diverge r ( $\lambda s ta s'. False$ ) = ( $\lambda s. False$ )
by(auto intro!: ext elim:  $\tau$ trsys. $\tau$ diverge.cases  $\tau$ trsys.silent-move.cases)

end

```

1.16 The multithreaded semantics as a labelled transition system

```

theory FWLTS
imports
  FWProgressAux
  FWLifting
  LTS
begin

```

```

sublocale multithreaded-base < trsys r t for t .
sublocale multithreaded-base < mthr: trsys redT .

```

— Move to FWSemantics?

```

definition redT-upd- $\varepsilon$  :: (l, 't, 'x, 'm, 'w) state  $\Rightarrow$  't  $\Rightarrow$  'x  $\Rightarrow$  'm  $\Rightarrow$  (l, 't, 'x, 'm, 'w) state

```


where $[simp]$: $redT\text{-upd-}\varepsilon\ s\ t\ x'\ m' = (locks\ s,\ ((thr\ s)(t\ \mapsto\ (x',\ snd\ (the\ (thr\ s\ t))))),\ m'),\ wset\ s,\ interrupts\ s)$

lemma $redT\text{-upd-}\varepsilon\text{-}redT\text{-upd}$:

$redT\text{-upd}\ s\ t\ \varepsilon\ x'\ m'\ (redT\text{-upd-}\varepsilon\ s\ t\ x'\ m')$

by($auto\ simp\ add$: $redT\text{-updLns-def}\ redT\text{-updWs-def}$)

context $multithreaded$ **begin**

sublocale $trsys\ r\ t$ **for** t .

sublocale $mthr$: $trsys\ redT$.

end

1.16.1 The multithreaded semantics with internal actions

type-synonym

$(l, t, x, m, w, o)\ \tau moves =$

$'x \times 'm \Rightarrow (l, t, x, m, w, o)\ thread\text{-}action \Rightarrow 'x \times 'm \Rightarrow bool$

pretty printing for $\tau moves$

print-translation \langle

```

let
  fun tr' [(Const (@{type-syntax prod}, -) $ x1 $ m1),
            (Const (@{type-syntax fun}, -) $
              (Const (@{type-syntax prod}, -) $
                (Const (@{type-syntax finfun}, -) $ l $
                  (Const (@{type-syntax list}, -) $ Const (@{type-syntax lock-action}, -))) $
                  (Const (@{type-syntax prod}, -) $
                    (Const (@{type-syntax list}, -) $ (Const (@{type-syntax new-thread-action}, -) $ t1 $
                      x2 $ m2)) $
                    (Const (@{type-syntax prod}, -) $
                      (Const (@{type-syntax list}, -) $ (Const (@{type-syntax conditional-action}, -) $ t2))
                    $
                    (Const (@{type-syntax prod}, -) $
                      (Const (@{type-syntax list}, -) $ (Const (@{type-syntax wait-set-action}, -) $ t3 $
                        w)) $
                    (Const (@{type-syntax prod}, -) $
                      (Const (@{type-syntax list}, -) $ (Const (@{type-syntax interrupt-action}, -) $
                        t4)) $
                    (Const (@{type-syntax list}, -) $ o1)))]))] $
            (Const (@{type-syntax fun}, -) $
              (Const (@{type-syntax prod}, -) $ x3 $ m3) $
              (Const (@{type-syntax bool}, -)))] =
  if x1 = x2 andalso x1 = x3 andalso m1 = m2 andalso m1 = m3 andalso t1 = t2 andalso t2 =
  t3 andalso t3 = t4
  then Syntax.const (@{type-syntax \tau moves}) $ l $ t1 $ x1 $ m1 $ w $ o1
  else raise Match;
in [(@{type-syntax fun}, K tr')]
end

```

typ $(l, t, x, m, w, o)\ \tau moves$

locale $\tau\text{multithreaded}$ = *multithreaded-base* +
constrains *final* :: 'x \Rightarrow *bool*
and *r* :: ('l,'t,'x,'m,'w,'o) *semantics*
and *convert-RA* :: 'l *released-locks* \Rightarrow 'o *list*
fixes τmove :: ('l,'t,'x,'m,'w,'o) τmoves

sublocale $\tau\text{multithreaded} < \tau\text{trsys } r \ t \ \tau\text{move}$ **for** *t* .

context $\tau\text{multithreaded}$ **begin**

inductive $m\tau\text{move}$:: (('l,'t,'x,'m,'w) *state*, 't \times ('l,'t,'x,'m,'w,'o) *thread-action*) *trsys*

where

$\llbracket \text{thr } s \ t = \llbracket (x, \text{no-wait-locks}) \rrbracket; \text{thr } s' \ t = \llbracket (x', \text{ln}') \rrbracket; \tau\text{move } (x, \text{shr } s) \ \text{ta } (x', \text{shr } s') \rrbracket$
 $\Longrightarrow m\tau\text{move } s \ (t, \text{ta}) \ s'$

end

sublocale $\tau\text{multithreaded} < \text{mthr}: \tau\text{trsys } \text{redT} \ m\tau\text{move}$.

context $\tau\text{multithreaded}$ **begin**

abbreviation τmredT :: ('l,'t,'x,'m,'w) *state* \Rightarrow ('l,'t,'x,'m,'w) *state* \Rightarrow *bool*

where $\tau\text{mredT} == \text{mthr.silent-move}$

end

lemma (**in** *multithreaded-base*) $\tau\text{rtrancl3p-redT-thread-not-disappear}$:

assumes $\tau\text{trsys}.\tau\text{rtrancl3p } \text{redT} \ \tau\text{move } s \ \text{ttas } s' \ \text{thr } s \ t \neq \text{None}$

shows $\text{thr } s' \ t \neq \text{None}$

proof –

interpret *T*: $\tau\text{trsys } \text{redT} \ \tau\text{move}$.

show *?thesis*

proof

assume $\text{thr } s' \ t = \text{None}$

with $\langle \tau\text{trsys}.\tau\text{rtrancl3p } \text{redT} \ \tau\text{move } s \ \text{ttas } s' \rangle$ **have** $\text{thr } s \ t = \text{None}$

by (*induct rule*: *T*. $\tau\text{rtrancl3p.induct}$) (*auto simp add*: *split-paired-all dest*: *redT-thread-not-disappear*)

with $\langle \text{thr } s \ t \neq \text{None} \rangle$ **show** *False* **by** *contradiction*

qed

qed

lemma *m $\tau\text{move-False}$* : $\tau\text{multithreaded}.\text{m}\tau\text{move} \ (\lambda s \ \text{ta} \ s'. \ \text{False}) = (\lambda s \ \text{ta} \ s'. \ \text{False})$

by (*auto intro!*: *ext elim*: $\tau\text{multithreaded}.\text{m}\tau\text{move}.\text{cases}$)

declare *split-paired-Ex* [*simp del*]

locale $\tau\text{multithreaded-wf}$ =

$\tau\text{multithreaded}$ - - - τmove +

multithreaded final r convert-RA

for τmove :: ('l,'t,'x,'m,'w,'o) τmoves +

assumes $\tau\text{move-heap}$: $\llbracket t \vdash (x, m) \text{--ta--} (x', m') \rrbracket; \tau\text{move } (x, m) \ \text{ta } (x', m') \rrbracket \Longrightarrow m = m'$

assumes *silent-tl*: $\tau\text{move } s \ \text{ta} \ s' \Longrightarrow \text{ta} = \varepsilon$

begin

lemma *m $\tau\text{move-silentD}$* : $\text{m}\tau\text{move } s \ (t, \text{ta}) \ s' \Longrightarrow \text{ta} = (K\$ \ \square, \ \square, \ \square, \ \square, \ \square, \ \square)$

by(auto elim!: m τ move.cases dest: silent-tl)

lemma m τ move-heap:

assumes redT: redT s (t, ta) s'

and m τ move: m τ move s (t, ta) s'

shows shr s' = shr s

using m τ move redT

by cases(auto dest: τ move-heap elim!: redT.cases)

lemma τ mredT-thread-preserved:

τ mredT s s' \implies thr s t = None \longleftrightarrow thr s' t = None

by(auto simp add: mthr.silent-move-iff elim!: redT.cases dest!: m τ move-silentD split: if-split-asm)

lemma τ mRedT-thread-preserved:

τ mredT^{**} s s' \implies thr s t = None \longleftrightarrow thr s' t = None

by(induct rule: rtranclp.induct)(auto dest: τ mredT-thread-preserved[where t=t])

lemma τ mtRedT-thread-preserved:

τ mredT⁺⁺ s s' \implies thr s t = None \longleftrightarrow thr s' t = None

by(induct rule: tranclp.induct)(auto dest: τ mredT-thread-preserved[where t=t])

lemma τ mredT-add-thread-inv:

assumes τ red: τ mredT s s' and tst: thr s t = None

shows τ mredT (locks s, ((thr s)(t \mapsto xln), shr s), wset s, interrupts s) (locks s', ((thr s')(t \mapsto xln), shr s'), wset s', interrupts s')

proof –

obtain ls ts m ws is **where** s: s = (ls, (ts, m), ws, is) **by**(cases s) fastforce

obtain ls' ts' m' ws' is' **where** s': s' = (ls', (ts', m'), ws', is') **by**(cases s') fastforce

from τ red s s' **obtain** t' **where** red: (ls, (ts, m), ws, is) $-t' \vdash \varepsilon \rightarrow$ (ls', (ts', m'), ws', is')

and τ : m τ move (ls, (ts, m), ws, is) (t', ε) (ls', (ts', m'), ws', is')

by(auto simp add: mthr.silent-move-iff dest: m τ move-silentD)

from red **have** (ls, (ts(t \mapsto xln), m), ws, is) $-t' \vdash \varepsilon \rightarrow$ (ls', (ts'(t \mapsto xln), m'), ws', is')

proof(cases rule: redT-elims)

case (normal x x' m') **with** tst s **show** ?thesis

by–(rule redT-normal, auto simp add: fun-upd-twist elim!: rtrancl3p-cases)

next

case (acquire x ln n)

with tst s **show** ?thesis

unfolding $\langle \varepsilon = (K\$ \square, \square, \square, \square, \square, \text{convert-RA } ln) \rangle$

by –(rule redT-acquire, auto intro: fun-upd-twist)

qed

moreover from red tst s **have** tt': t \neq t' **by**(cases) auto

have $(\lambda t''. (ts(t \mapsto xln)) t'' \neq \text{None} \wedge (ts(t \mapsto xln)) t'' \neq (ts'(t \mapsto xln)) t'') =$

$(\lambda t''. ts t'' \neq \text{None} \wedge ts t'' \neq ts' t'')$ **using** tst s **by**(auto simp add: fun-eq-iff)

with τ tst tt' **have** m τ move (ls, (ts(t \mapsto xln), m), ws, is) (t', ε) (ls', (ts'(t \mapsto xln), m'), ws', is')

by cases(rule m τ move.intros, auto)

ultimately show ?thesis **unfolding** s s' **by** auto

qed

lemma τ mRedT-add-thread-inv:

$\llbracket \tau$ mredT^{**} s s'; thr s t = None \rrbracket

$\implies \tau$ mredT^{**} (locks s, ((thr s)(t \mapsto xln), shr s), wset s, interrupts s) (locks s', ((thr s')(t \mapsto xln), shr s'), wset s', interrupts s')

apply(induct rule: rtranclp-induct)

apply(blast dest: $\tau mRedT$ -thread-preserved[**where** $t=t$] $\tau mredT$ -add-thread-inv[**where** $xln=xln$] intro: rtranclp.rtrancl-into-rtrancl)+
done

lemma $\tau mtRed$ -add-thread-inv:

$\llbracket \tau mredT^{\wedge}++ s s'; thr s t = None \rrbracket$
 $\implies \tau mredT^{\wedge}++ (locks s, ((thr s)(t \mapsto xln), shr s), wset s, interrupts s) (locks s', ((thr s')(t \mapsto xln), shr s'), wset s', interrupts s')$

apply(induct rule: tranclp-induct)

apply(blast dest: $\tau mtRedT$ -thread-preserved[**where** $t=t$] $\tau mredT$ -add-thread-inv[**where** $xln=xln$] intro: tranclp.trancl-into-trancl)+
done

lemma *silent-move-into-RedT- τ -inv*:

assumes *move*: *silent-move* $t (x, shr s) (x', m')$
and *state*: $thr s t = \llbracket (x, no-wait-locks) \rrbracket$ $wset s t = None$
shows $\tau mredT s (redT\text{-upd-}\varepsilon s t x' m')$

proof –

from *move* **obtain** $red: t \vdash (x, shr s) -\varepsilon \rightarrow (x', m')$ **and** $\tau: \tau move (x, shr s) \varepsilon (x', m')$

by(*auto simp add: silent-move-iff dest: silent-tl*)

from *red state* **have** $s -t \triangleright \varepsilon \rightarrow redT\text{-upd-}\varepsilon s t x' m'$

by –(*rule redT-normal, auto simp add: redT-updLns-def o-def finfun-Diag-const2 redT-updWs-def*)

moreover from τ *red state* **have** $m\tau move s (t, \varepsilon) (redT\text{-upd-}\varepsilon s t x' m')$

by –(*rule $m\tau move.intros, auto dest: \tau move\text{-heap simp add: redT-updLns-def}$*)

ultimately show *?thesis* **by** *auto*

qed

lemma *silent-moves-into-RedT- τ -inv*:

assumes *major*: *silent-moves* $t (x, shr s) (x', m')$
and *state*: $thr s t = \llbracket (x, no-wait-locks) \rrbracket$ $wset s t = None$
shows $\tau mredT^{\wedge}** s (redT\text{-upd-}\varepsilon s t x' m')$

using *major*

proof(induct rule: rtranclp-induct2)

case *refl* **with** *state* **show** *?case* **by**(*cases s*)(*auto simp add: fun-upd-idem*)

next

case (*step* $x' m' x'' m''$)

from $\langle silent-move t (x', m') (x'', m'') \rangle$ *state*

have $\tau mredT (redT\text{-upd-}\varepsilon s t x' m') (redT\text{-upd-}\varepsilon (redT\text{-upd-}\varepsilon s t x' m') t x'' m'')$

by –(*rule silent-move-into-RedT- τ -inv, auto*)

hence $\tau mredT (redT\text{-upd-}\varepsilon s t x' m') (redT\text{-upd-}\varepsilon s t x'' m'')$ **by**(*simp*)

with $\langle \tau mredT^{\wedge}** s (redT\text{-upd-}\varepsilon s t x' m') \rangle$ **show** *?case* ..

qed

lemma *red-rtrancl- τ -heapD-inv*:

$\llbracket silent-moves t s s'; wfs t s \rrbracket \implies snd s' = snd s$

proof(induct rule: rtranclp-induct)

case *base* **show** *?case* ..

next

case (*step* $s' s''$)

thus *?case* **by**(*cases s, cases s', cases s''*)(*auto dest: $\tau move\text{-heap}$*)

qed

lemma *red-trancl- τ -heapD-inv*:

$\llbracket silent-movet t s s'; wfs t s \rrbracket \implies snd s' = snd s$

proof(*induct rule: tranclp-induct*)
case (*base s'*) **thus** ?*case* **by**(*cases s'*)(*cases s, auto simp add: silent-move-iff dest: τ move-heap*)
next
case (*step s' s''*)
thus ?*case* **by**(*cases s, cases s', cases s''*)(*auto simp add: silent-move-iff dest: τ move-heap*)
qed

lemma *red-trancl- τ -into-RedT- τ -inv*:

assumes *major: silent-move t t (x , shr s) (x' , m')*
and *state: thr s t = $\lfloor(x, no-wait-locks)\rfloor$ wset s t = None*
shows $\tau mredT^{\hat{+}+} s$ (*redT-upd- ε s t x' m'*)

using *major*

proof(*induct rule: tranclp-induct2*)

case (*base x' m'*)
thus ?*case* **using** *state*
by $-(rule\ tranclp.r-into-trancl, rule\ silent-move-into-RedT- τ -inv, auto)$

next

case (*step x' m' x'' m''*)
hence $\tau mredT^{\hat{+}+} s$ (*redT-upd- ε s t x' m'*) **by** *blast*
moreover from $\langle silent-move\ t\ (x' , m')\ (x'' , m'') \rangle$ *state*
have $\tau mredT$ (*redT-upd- ε s t x' m'*) (*redT-upd- ε ($redT-upd- ε s t x' m') t x'' $m''$$*)
by $-(rule\ silent-move-into-RedT- τ -inv, auto\ simp\ add: redT-updLns-def)$
hence $\tau mredT$ (*redT-upd- ε s t x' m'*) (*redT-upd- ε s t x'' m''*)
by(*simp add: redT-updLns-def*)
ultimately show ?*case* ..

qed

lemma *τ diverge-into- $\tau mredT$* :

assumes τ *diverge* t (x , shr s)
and *thr s t = $\lfloor(x, no-wait-locks)\rfloor$ wset s t = None*
shows *mthr. τ diverge* s

using *assms*

proof(*coinduction arbitrary: s x*)

case (τ *diverge* s x)
note *tst = $\langle thr\ s\ t = \lfloor(x, no-wait-locks)\rfloor \rangle$*
from $\langle \tau$ *diverge* t (x , shr s) \rangle **obtain** x' m' **where** *silent-move* t (x , shr s) (x' , m')
and τ *diverge* t (x' , m') **by** *cases auto*
from $\langle silent-move\ t\ (x , shr s)\ (x' , m') \rangle$ *tst* $\langle wset\ s\ t = None \rangle$
have $\tau mredT$ s (*redT-upd- ε s t x' m'*) **by**(*rule silent-move-into-RedT- τ -inv*)
moreover have *thr* (*redT-upd- ε s t x' m'*) $t = \lfloor(x', no-wait-locks)\rfloor$
using *tst* **by**(*auto simp add: redT-updLns-def*)
moreover have *wset* (*redT-upd- ε s t x' m'*) $t = None$ **using** $\langle wset\ s\ t = None \rangle$ **by** *simp*
moreover from $\langle \tau$ *diverge* t (x' , m') \rangle **have** τ *diverge* t (x' , shr (*redT-upd- ε s t x' m'*)) **by** *simp*
ultimately show ?*case* **using** $\langle \tau$ *diverge* t (x' , m') \rangle **by** *blast*

qed

lemma *τ diverge- $\tau mredTD$* :

assumes *div: mthr. τ diverge* s
and *fin: finite* (*dom* (*thr* s))
shows $\exists t\ x. thr\ s\ t = \lfloor(x, no-wait-locks)\rfloor \wedge wset\ s\ t = None \wedge \tau$ *diverge* t (x , shr s)

using *fin div*

proof(*induct $A \equiv dom$ (*thr* s) arbitrary: s rule: finite-induct*)

case *empty*

from $\langle mthr.\tau$ *diverge* $s \rangle$ **obtain** s' **where** $\tau mredT$ s s' **by** *cases auto*

with $\langle \{ \} = \text{dom } (\text{thr } s) \rangle [\text{symmetric}]$ **have** *False* **by** (*auto elim!*: *mthr.silent-move.cases redT.cases*)
thus *?case ..*
next
case (*insert t A*)
note $IH = \langle \bigwedge s. \llbracket A = \text{dom } (\text{thr } s); \text{mthr}.\tau \text{diverge } s \rrbracket \implies \exists t x. \text{thr } s t = \llbracket (x, \text{no-wait-locks}) \rrbracket \wedge \text{wset } s t = \text{None} \wedge \tau \text{diverge } t (x, \text{shr } s) \rangle$
from $\langle \text{insert } t A = \text{dom } (\text{thr } s) \rangle$
obtain $x \text{ ln}$ **where** $\text{tst}: \text{thr } s t = \llbracket (x, \text{ln}) \rrbracket$ **by** (*fastforce simp add: dom-def*)
define s' **where** $s' = (\text{locks } s, ((\text{thr } s)(t := \text{None}), \text{shr } s), \text{wset } s, \text{interrupts } s)$
show *?case*
proof (*cases ln = no-wait-locks \wedge $\tau \text{diverge } t (x, \text{shr } s) \wedge \text{wset } s t = \text{None}$*)
 case *True*
 with tst **show** *?thesis* **by** *blast*
next
 case *False*
 define xm **where** $xm = (x, \text{shr } s)$
 define xm' **where** $xm' = (x, \text{shr } s)$
 have $A = \text{dom } (\text{thr } s')$ **using** $\langle t \notin A \rangle \langle \text{insert } t A = \text{dom } (\text{thr } s) \rangle$
 unfolding *s'-def* **by** *auto*
 moreover {
 from *xm'-def* tst $\langle \text{mthr}.\tau \text{diverge } s \rangle$ *False*
 have $\exists s x. \text{thr } s t = \llbracket (x, \text{ln}) \rrbracket \wedge (\text{ln} \neq \text{no-wait-locks} \vee \text{wset } s t \neq \text{None} \vee \neg \tau \text{diverge } t xm') \wedge$
 $s' = (\text{locks } s, ((\text{thr } s)(t := \text{None}), \text{shr } s), \text{wset } s, \text{interrupts } s) \wedge xm = (x, \text{shr } s) \wedge$
 $\text{mthr}.\tau \text{diverge } s \wedge \text{silent-moves } t xm' xm$
 unfolding *s'-def xm-def* **by** *blast*
 moreover
 from *False* **have** *wfP* (*if* $\tau \text{diverge } t xm'$ *then* $(\lambda s s'. \text{False})$ *else* *flip* (*silent-move-from* $t xm'$))
 using $\tau \text{diverge-neq-wfP-silent-move-from}$ [*of* $t (x, \text{shr } s)$] **unfolding** *xm'-def* **by** (*auto*)
 ultimately **have** $\text{mthr}.\tau \text{diverge } s'$
 proof (*coinduct s' xm rule: mthr.τdiverge-trancl-measure-coinduct*)
 case ($\tau \text{diverge } s' xm$)
 then **obtain** $s x$ **where** $\text{tst}: \text{thr } s t = \llbracket (x, \text{ln}) \rrbracket$
 and $\text{blocked}: \text{ln} \neq \text{no-wait-locks} \vee \text{wset } s t \neq \text{None} \vee \neg \tau \text{diverge } t xm'$
 and *s'-def*: $s' = (\text{locks } s, ((\text{thr } s)(t := \text{None}), \text{shr } s), \text{wset } s, \text{interrupts } s)$
 and *xm-def*: $xm = (x, \text{shr } s)$
 and *xm:xm'*: $\text{silent-moves } t xm' (x, \text{shr } s)$
 and $\text{mthr}.\tau \text{diverge } s$ **by** *blast*
 from $\langle \text{mthr}.\tau \text{diverge } s \rangle$ **obtain** s'' **where** $\tau \text{mredT } s s'' \text{mthr}.\tau \text{diverge } s''$ **by** *cases auto*
 from $\langle \tau \text{mredT } s s'' \rangle$ **obtain** $t' ta$ **where** $s - t' \triangleright ta \rightarrow s''$ **and** $\text{m}\tau \text{move } s (t', ta) s''$ **by** *auto*
 then **obtain** $x' x'' m''$ **where** $\text{red}: t' \vdash \langle x', \text{shr } s \rangle - ta \rightarrow \langle x'', m'' \rangle$
 and $\text{tst}': \text{thr } s t' = \llbracket (x', \text{no-wait-locks}) \rrbracket$
 and *aoe*: $\text{actions-ok } s t' ta$
 and $s'': \text{redT-upd } s t' ta x'' m'' s''$
 by *cases* (*fastforce elim: mτmove.cases*) +
 from $\langle \text{m}\tau \text{move } s (t', ta) s'' \rangle$ **have** [*simp*]: $ta = \varepsilon$
 by (*auto elim!*: *mτmove.cases dest!*: *silent-tl*)
 hence $\text{wst}': \text{wset } s t' = \text{None}$ **using** *aoe* **by** *auto*
 from $\langle \text{m}\tau \text{move } s (t', ta) s'' \rangle$ $\text{tst}' s''$
 have $\tau \text{move } (x', \text{shr } s) \varepsilon (x'', m'')$ **by** (*auto elim: mτmove.cases*)
 show *?case*
 proof (*cases t' = t*)
 case *False*
 with $\text{tst}' \text{wst}'$ **have** $\text{thr } s' t' = \llbracket (x', \text{no-wait-locks}) \rrbracket$
 $\text{wset } s' t' = \text{None}$ $\text{shr } s' = \text{shr } s$ **unfolding** *s'-def* **by** *auto*

with red **have** $s' - t \triangleright \varepsilon \rightarrow redT\text{-upd-}\varepsilon$ $s' t' x'' m''$
by $-(rule\ redT\text{-normal}, auto\ simp\ add: redT\text{-updLns-def}\ o\text{-def}\ finfun\text{-Diag-const2}\ redT\text{-updWs-def})$
moreover from $\langle \tau move (x', shr\ s) \varepsilon (x'', m'') \rangle \langle thr\ s' t' = [(x', no\text{-wait-locks})] \rangle \langle shr\ s' =$
shr s
have $m\tau move\ s' (t', ta) (redT\text{-upd-}\varepsilon\ s' t' x'' m'')$
by $-(rule\ m\tau move.intros, auto)$
ultimately have $\tau mredT\ s' (redT\text{-upd-}\varepsilon\ s' t' x'' m'')$
unfolding $\langle ta = \varepsilon \rangle$ **by** $(rule\ mthr.silent-move.intros)$
hence $\tau mredT^{++}\ s' (redT\text{-upd-}\varepsilon\ s' t' x'' m'')$..
moreover have $thr\ s'' t = [(x, ln)]$
using $tst\ \langle t' \neq t \rangle\ s''$ **by** $auto$
moreover from $\langle \tau move (x', shr\ s) \varepsilon (x'', m'') \rangle red$
have $[simp]: m'' = shr\ s$ **by** $(auto\ dest: \tau move\text{-heap})$
hence $shr\ s = shr\ s''$ **using** s'' **by** $(auto)$
have $ln \neq no\text{-wait-locks} \vee wset\ s'' t \neq None \vee \neg \tau\ diverge\ t\ xm'$
using $blocked\ s''$ **by** $(auto\ simp\ add: redT\text{-updWs-def}\ elim!: rtrancl3p\text{-cases})$
moreover have $redT\text{-upd-}\varepsilon\ s' t' x'' m'' = (locks\ s'', ((thr\ s'')(t := None), shr\ s''), wset\ s'',$
interrupts\ s'')
unfolding $s'\text{-def}$ **using** $tst\ s''\ \langle t' \neq t \rangle$
by $(auto\ intro: ext\ elim!: rtrancl3p\text{-cases}\ simp\ add: redT\text{-updLns-def}\ redT\text{-updWs-def})$
ultimately show $?thesis$ **using** $\langle mthr.\tau\ diverge\ s'' \rangle\ xm\ xm'$
unfolding $\langle shr\ s = shr\ s'' \rangle$ **by** $blast$
next
case $True$
with $tst\ tst'\ wst'\ blocked$ **have** $\neg \tau\ diverge\ t\ xm'$
and $[simp]: x' = x$ **by** $auto$
moreover from $red\ \langle \tau move (x', shr\ s) \varepsilon (x'', m'') \rangle True$
have $silent\text{-move}\ t\ (x, shr\ s)\ (x'', m'')$ **by** $auto$
with $xm\ xm'$ **have** $silent\text{-move-from}\ t\ xm'\ (x, shr\ s)\ (x'', m'')$
by $(rule\ silent\text{-move-fromI})$
ultimately have $(if\ \tau\ diverge\ t\ xm'\ then\ \lambda s\ s'. False\ else\ flip\ (silent\text{-move-from}\ t\ xm'))\ (x'',$
m'')\ xm
by $(auto\ simp\ add: flip\text{-conv}\ xm\text{-def})$
moreover have $thr\ s'' t = [(x'', ln)]$ **using** $tst\ True\ s''$
by $(auto\ simp\ add: redT\text{-updLns-def})$
moreover from $\langle \tau move (x', shr\ s) \varepsilon (x'', m'') \rangle red$
have $[simp]: m'' = shr\ s$ **by** $(auto\ dest: \tau move\text{-heap})$
hence $shr\ s = shr\ s''$ **using** s'' **by** $auto$
have $s' = (locks\ s'', ((thr\ s'')(t := None), shr\ s''), wset\ s'', interrupts\ s'')$
using $True\ s''$ **unfolding** $s'\text{-def}$
by $(auto\ intro: ext\ elim!: rtrancl3p\text{-cases}\ simp\ add: redT\text{-updLns-def}\ redT\text{-updWs-def})$
moreover have $(x'', m'') = (x'', shr\ s'')$ **using** s'' **by** $auto$
moreover from $xm\ xm'\ \langle silent\text{-move}\ t\ (x, shr\ s)\ (x'', m'') \rangle$
have $silent\text{-moves}\ t\ xm'\ (x'', shr\ s'')$
unfolding $\langle m'' = shr\ s \rangle\ \langle shr\ s = shr\ s'' \rangle$ **by** $auto$
ultimately show $?thesis$ **using** $\langle \neg \tau\ diverge\ t\ xm' \rangle\ \langle mthr.\tau\ diverge\ s'' \rangle$ **by** $blast$
qed
qed }
ultimately have $\exists t\ x. thr\ s' t = [(x, no\text{-wait-locks})] \wedge wset\ s' t = None \wedge \tau\ diverge\ t\ (x, shr\ s')$
by $(rule\ IH)$
then obtain $t' x'$ **where** $thr\ s' t' = [(x', no\text{-wait-locks})]$
and $wset\ s' t' = None$ **and** $\tau\ diverge\ t' (x', shr\ s')$ **by** $blast$
moreover with $False$ **have** $t' \neq t$ **by** $(auto\ simp\ add: s'\text{-def})$
ultimately have $thr\ s\ t' = [(x', no\text{-wait-locks})]$ $wset\ s\ t' = None$ $\tau\ diverge\ t' (x', shr\ s)$

unfolding s' -def by auto
thus ?thesis by blast
qed
qed

lemma $\tau mredT$ -preserves-final-thread:

$\llbracket \tau mredT\ s\ s';\ final\text{-thread}\ s\ t \rrbracket \implies final\text{-thread}\ s'\ t$
by(auto elim: mthr.silent-move.cases intro: redT-preserves-final-thread)

lemma $\tau mRedT$ -preserves-final-thread:

$\llbracket \tau mRedT^{**}\ s\ s';\ final\text{-thread}\ s\ t \rrbracket \implies final\text{-thread}\ s'\ t$
by(induct rule: rtranclp.induct)(blast intro: $\tau mredT$ -preserves-final-thread)+

lemma silent-moves2-silentD:

assumes rtrancl3p mthr.silent-move2 s ttas s'
and $(t, ta) \in set\ ttas$
shows $ta = \varepsilon$
using assms
by(induct)(auto simp add: mthr.silent-move2-def dest: $m\tau move$ -silentD)

lemma inf-step-silentD:

assumes step: trsys.inf-step mthr.silent-move2 s ttas
and lset: $(t, ta) \in lset\ ttas$
shows $ta = \varepsilon$
using lset step
by(induct arbitrary: s rule: lset-induct)(fastforce elim: trsys.inf-step.cases simp add: mthr.silent-move2-def dest: $m\tau move$ -silentD)+

end

1.16.2 The multithreaded semantics with a well-founded relation on states

locale multithreaded-base-measure = multithreaded-base +

constrains final :: 'x \Rightarrow bool
and r :: ('l, 't, 'x, 'm, 'w, 'o) semantics
and convert-RA :: 'l released-locks \Rightarrow 'o list
fixes $\mu :: ('x \times 'm) \Rightarrow ('x \times 'm) \Rightarrow bool$
begin

inductive $m\mu t :: 'm \Rightarrow ('l, 't, 'x)\ thread\text{-info} \Rightarrow ('l, 't, 'x)\ thread\text{-info} \Rightarrow bool$

for m **and** ts **and** ts'

where

$m\mu tI:$
 $\bigwedge ln. \llbracket finite\ (dom\ ts);\ ts\ t = \lfloor (x, ln) \rfloor; ts'\ t = \lfloor (x', ln') \rfloor; \mu\ (x, m)\ (x', m); \bigwedge t'. t' \neq t \implies ts\ t' = ts'\ t' \rrbracket$
 $\implies m\mu t\ m\ ts\ ts'$

definition $m\mu :: ('l, 't, 'x, 'm, 'w)\ state \Rightarrow ('l, 't, 'x, 'm, 'w)\ state \Rightarrow bool$

where $m\mu\ s\ s' \iff shr\ s = shr\ s' \wedge m\mu t\ (shr\ s)\ (thr\ s)\ (thr\ s')$

lemma $m\mu t$ -thr-dom-eq: $m\mu t\ m\ ts\ ts' \implies dom\ ts = dom\ ts'$

apply(erule $m\mu t$.cases)

apply(rule equalityI)

apply(rule subsetI)


```

apply(case-tac xa = t)
  apply(auto)[2]
apply(rule subsetI)
apply(case-tac xa = t)
apply auto
done

```

```

lemma mμ-finite-thrD:
  assumes mμt m ts ts'
  shows finite (dom ts) finite (dom ts')
using assms
by(simp-all add: mμt-thr-dom-eq[symmetric])(auto elim: mμt.cases)

```

end

```

locale multithreaded-base-measure-wf = multithreaded-base-measure +
  constrains final :: 'x ⇒ bool
  and r :: ('l,'t,'x,'m,'w,'o) semantics
  and convert-RA :: 'l released-locks ⇒ 'o list
  and μ :: ('x × 'm) ⇒ ('x × 'm) ⇒ bool
  assumes wf-μ: wfP μ
begin

```

```

lemma wf-mμt: wfP (mμt m)
unfolding wfp-eq-minimal
proof(intro strip)
  fix Q :: ('l,'t,'x) thread-info set and ts
  assume ts ∈ Q
  show  $\exists z \in Q. \forall y. m\mu t\ m\ y\ z \longrightarrow y \notin Q$ 
  proof(cases finite (dom ts))
    case False
    hence  $\forall y. m\mu t\ m\ y\ ts \longrightarrow y \notin Q$  by(auto dest: mμ-finite-thrD)
    thus ?thesis using <ts ∈ Q> by blast
  next
  case True
  thus ?thesis using <ts ∈ Q>
  proof(induct A ≡ dom ts arbitrary: ts Q rule: finite-induct)
    case empty
    hence dom ts = {} by simp
    with <ts ∈ Q> show ?case by (auto elim: mμt.cases)
  next
  case (insert t A)
  note IH = <∧ ts Q. [A = dom ts; ts ∈ Q] ⇒ ∃ z ∈ Q. ∀ y. mμt m y z ⇒ y ∉ Q>
  define Q' where Q' = {ts. ts t = None ∧ (∃ xln. ts(t ↦ xln) ∈ Q)}
  let ?ts' = ts(t := None)
  from <insert t A = dom ts> <t ∉ A> have A = dom ?ts' by auto
  moreover from <insert t A = dom ts> obtain xln where ts t = [xln] by (cases ts t) auto
  hence ts(t ↦ xln) = ts by (auto simp add: fun-eq-iff)
  with <ts ∈ Q> have ts(t ↦ xln) ∈ Q by (auto)
  hence ?ts' ∈ Q' unfolding Q'-def by (auto simp del: split-paired-Ex)
  ultimately have  $\exists z \in Q'. \forall y. m\mu t\ m\ y\ z \longrightarrow y \notin Q'$  by(rule IH)
  then obtain ts' where ts' ∈ Q'
    and min: ∧ ts''. mμt m ts'' ts' ⇒ ts'' ∉ Q' by blast
  from <ts' ∈ Q'> obtain x' ln' where ts' t = None ts'(t ↦ (x', ln')) ∈ Q

```

unfolding Q' -def by auto
 define Q'' where $Q'' = \{(x, m) \mid x. \exists ln. ts'(t \mapsto (x, ln)) \in Q\}$
 from $\langle ts'(t \mapsto (x', ln')) \in Q \rangle$ have $(x', m) \in Q''$ unfolding Q'' -def by blast
 hence $\exists xm'' \in Q'' . \forall xm''' . \mu xm''' xm'' \longrightarrow xm''' \notin Q''$ by (rule wf- μ [unfolded wfp-eq-minimal, rule-format])
 then obtain xm'' where $xm'' \in Q''$ and min' : $\bigwedge xm''' . \mu xm''' xm'' \Longrightarrow xm''' \notin Q''$ by blast
 from $\langle xm'' \in Q'' \rangle$ obtain $x'' ln''$ where $xm'' = (x'', m)$ $ts'(t \mapsto (x'', ln'')) \in Q$ unfolding Q'' -def by blast
 moreover {
 fix ts''
 assume $m\mu t m ts'' (ts'(t \mapsto (x'', ln'')))$
 then obtain $T X'' LN'' X' LN'$
 where $finite (dom ts'') ts'' T = \lfloor (X'', LN'') \rfloor$
 and $(ts'(t \mapsto (x'', ln''))) T = \lfloor (X', LN') \rfloor \mu (X'', m) (X', m)$
 and $eq: \bigwedge t'. t' \neq T \Longrightarrow ts'' t' = (ts'(t \mapsto (x'', ln''))) t'$ by (cases) blast
 have $ts'' \notin Q$
 proof (cases $T = t$)
 case True
 from True $\langle (ts'(t \mapsto (x'', ln''))) T = \lfloor (X', LN') \rfloor \rangle$ have $X' = x''$ by simp
 with $\langle \mu (X'', m) (X', m) \rangle$ have $(X'', m) \notin Q''$ by (auto dest: min' [unfolded $\langle xm'' = (x'', m) \rangle$])
 hence $\forall ln. ts'(t \mapsto (X'', ln)) \notin Q$ by (simp add: Q'' -def)
 moreover from $\langle ts' t = None \rangle eq True$
 have $ts''(t := None) = ts'$ by (auto simp add: fun-eq-iff)
 with $\langle ts'' T = \lfloor (X'', LN'') \rfloor \rangle True$
 have $ts'': ts'' = ts'(t \mapsto (X'', LN''))$ by (auto intro!: ext)
 ultimately show ?thesis by blast
 case False
 from $\langle finite (dom ts'') \rangle$ have $finite (dom (ts''(t := None)))$ by simp
 moreover from $\langle ts'' T = \lfloor (X'', LN'') \rfloor \rangle False$
 have $(ts''(t := None)) T = \lfloor (X'', LN'') \rfloor$ by simp
 moreover from $\langle (ts'(t \mapsto (x'', ln''))) T = \lfloor (X', LN') \rfloor \rangle False$
 have $ts' T = \lfloor (X', LN') \rfloor$ by simp
 ultimately have $m\mu t m (ts''(t := None)) ts'$ using $\langle \mu (X'', m) (X', m) \rangle$
 proof (rule $m\mu t I$)
 fix t'
 assume $t' \neq T$
 with $eq[OF False[symmetric]] eq[OF this] \langle ts' t = None \rangle$
 show $(ts''(t := None)) t' = ts' t'$ by auto
 qed
 hence $ts''(t := None) \notin Q'$ by (rule min)
 thus ?thesis
 proof (rule contrapos-nn)
 assume $ts'' \in Q$
 from $eq[OF False[symmetric]]$ have $ts'' t = \lfloor (x'', ln'') \rfloor$ by simp
 hence $ts'': ts''(t \mapsto (x'', ln'')) = ts''$ by (auto simp add: fun-eq-iff)
 from $\langle ts'' \in Q \rangle$ have $ts''(t \mapsto (x'', ln'')) \in Q$ unfolding ts'' .
 thus $ts''(t := None) \in Q'$ unfolding Q' -def by auto
 qed
 qed
 }

ultimately show ?case by blast

qed

qed
qed

lemma *wf-m μ* : *wfP m μ*

proof –

have *wf* (*inv-image* (*same-fst* ($\lambda m. True$) ($\lambda m. \{(ts, ts'). m\mu t m ts ts'\}$)) ($\lambda s. (shr s, thr s)$))
 by(*rule wf-inv-image*)(*rule wf-same-fst*, *rule wf-m μ* [*unfolded wfp-def*])
 also have *inv-image* (*same-fst* ($\lambda m. True$) ($\lambda m. \{(ts, ts'). m\mu t m ts ts'\}$)) ($\lambda s. (shr s, thr s)$) = $\{(s, s'). m\mu s s'\}$
 by(*auto simp add: m μ -def same-fst-def*)
 finally show ?*thesis* by(*simp add: wfp-def*)
 qed

end

end

1.17 Various notions of bisimulation

theory *Bisimulation*

imports

LTS

begin

type-synonym (*'a*, *'b*) *bisim* = *'a* \Rightarrow *'b* \Rightarrow *bool*

1.17.1 Strong bisimulation

locale *bisimulation-base* = *r1*: *trsys trsys1* + *r2*: *trsys trsys2*
 for *trsys1* :: (*'s1*, *'tl1*) *trsys* ($\langle - / -1 \dashrightarrow / - \rangle [50, 0, 50] 60$)
 and *trsys2* :: (*'s2*, *'tl2*) *trsys* ($\langle - / -2 \dashrightarrow / - \rangle [50, 0, 50] 60$) +
 fixes *bisim* :: (*'s1*, *'s2*) *bisim* ($\langle - / \approx \rangle [50, 50] 60$)
 and *tlsim* :: (*'tl1*, *'tl2*) *bisim* ($\langle - / \sim \rangle [50, 50] 60$)
 begin

notation

r1.*Trsys* ($\langle - / -1 \dashrightarrow * / - \rangle [50, 0, 50] 60$) and
r2.*Trsys* ($\langle - / -2 \dashrightarrow * / - \rangle [50, 0, 50] 60$)

notation

r1.*inf-step* ($\langle - -1 \dashrightarrow * \infty \rangle [50, 0] 80$) and
r2.*inf-step* ($\langle - -2 \dashrightarrow * \infty \rangle [50, 0] 80$)

notation

r1.*inf-step-table* ($\langle - -1 \dashrightarrow * t \infty \rangle [50, 0] 80$) and
r2.*inf-step-table* ($\langle - -2 \dashrightarrow * t \infty \rangle [50, 0] 80$)

abbreviation *Tlsim* :: (*'tl1 list*, *'tl2 list*) *bisim* ($\langle - / [\sim] \rangle [50, 50] 60$)

where *Tlsim tl1 tl2* \equiv *list-all2 tlim tl1 tl2*

abbreviation *Tlsiml* :: (*'tl1 llist*, *'tl2 llist*) *bisim* ($\langle - / [[\sim]] \rangle [50, 50] 60$)

where *Tlsiml tl1 tl2* \equiv *llist-all2 tlim tl1 tl2*

end

```

locale bisimulation = bisimulation-base +
  constrains trsys1 :: ('s1, 'tl1) trsys
  and trsys2 :: ('s2, 'tl2) trsys
  and bisim :: ('s1, 's2) bisim
  and tlsim :: ('tl1, 'tl2) bisim
  assumes simulation1:  $\llbracket s1 \approx s2; s1 -1-tl1 \rightarrow s1' \rrbracket \implies \exists s2' tl2. s2 -2-tl2 \rightarrow s2' \wedge s1' \approx s2' \wedge$ 
   $tl1 \sim tl2$ 
  and simulation2:  $\llbracket s1 \approx s2; s2 -2-tl2 \rightarrow s2' \rrbracket \implies \exists s1' tl1. s1 -1-tl1 \rightarrow s1' \wedge s1' \approx s2' \wedge tl1$ 
   $\sim tl2$ 
begin

```

lemma bisimulation-flip:

```

  bisimulation trsys2 trsys1 (flip bisim) (flip tlsim)
by(unfold-locales)(unfold flip-simps,(blast intro: simulation1 simulation2)+)

```

end

lemma bisimulation-flip-simps [flip-simps]:

```

  bisimulation trsys2 trsys1 (flip bisim) (flip tlsim) = bisimulation trsys1 trsys2 bisim tlsim
by(auto dest: bisimulation.bisimulation-flip simp only: flip-flip)

```

context bisimulation **begin**

lemma simulation1-rtrancl:

```

 $\llbracket s1 -1-tls1 \rightarrow^* s1'; s1 \approx s2 \rrbracket$ 
 $\implies \exists s2' tls2. s2 -2-tls2 \rightarrow^* s2' \wedge s1' \approx s2' \wedge tls1 [\sim] tls2$ 

```

proof(induct rule: rtrancl3p.induct)

```

  case rtrancl3p-refl thus ?case by(auto intro: rtrancl3p.rtrancl3p-refl)

```

next

```

  case (rtrancl3p-step s1 tls1 s1' tl1 s1'')

```

```

  from  $\langle s1 \approx s2 \implies \exists s2' tls2. s2 -2-tls2 \rightarrow^* s2' \wedge s1' \approx s2' \wedge tls1 [\sim] tls2 \rangle$   $\langle s1 \approx s2 \rangle$ 

```

```

  obtain s2' tls2 where  $s2 -2-tls2 \rightarrow^* s2' \wedge s1' \approx s2' \wedge tls1 [\sim] tls2$  by blast

```

```

  moreover from  $\langle s1' -1-tl1 \rightarrow s1'' \rangle$   $\langle s1' \approx s2' \rangle$ 

```

```

  obtain s2'' tl2 where  $s2' -2-tl2 \rightarrow s2'' \wedge s1'' \approx s2'' \wedge tl1 \sim tl2$  by(auto dest: simulation1)

```

```

  ultimately have  $s2 -2-tls2 @ [tl2] \rightarrow^* s2'' \wedge tls1 @ [tl1] [\sim] tls2 @ [tl2]$ 

```

```

  by(auto intro: rtrancl3p.rtrancl3p-step list-all2-appendI)

```

```

  with  $\langle s1'' \approx s2'' \rangle$  show ?case by(blast)

```

qed

lemma simulation2-rtrancl:

```

 $\llbracket s2 -2-tls2 \rightarrow^* s2'; s1 \approx s2 \rrbracket$ 
 $\implies \exists s1' tls1. s1 -1-tls1 \rightarrow^* s1' \wedge s1' \approx s2' \wedge tls1 [\sim] tls2$ 

```

using bisimulation.simulation1-rtrancl[OF bisimulation-flip]

unfolding flip-simps .

lemma simulation1-inf-step:

```

  assumes red1:  $s1 -1-tls1 \rightarrow^* \infty$  and bisim:  $s1 \approx s2$ 

```

```

  shows  $\exists tls2. s2 -2-tls2 \rightarrow^* \infty \wedge tls1 [[\sim]] tls2$ 

```

proof -

```

  from r1.inf-step-imp-inf-step-table[OF red1]

```

```

  obtain stls1 where red1':  $s1 -1-stls1 \rightarrow^* \infty$ 

```

```

  and tls1:  $stls1 = lmap (fst \circ snd) stls1$  by blast

```

```

  define tl1-to-tl2 where tl1-to-tl2 s2 stls1 = unfold-llist

```

```

( $\lambda(s2, stls1). \text{lnull } stls1$ )
( $\lambda(s2, stls1). \text{let } (s1, tl1, s1') = \text{lhs } stls1;$ 
  ( $tl2, s2'$ ) =  $SOME (tl2, s2')$ .  $\text{trsys2 } s2 \text{ } tl2 \text{ } s2' \wedge s1' \approx s2' \wedge tl1 \sim tl2$ 
  in ( $s2, tl2, s2'$ ))
( $\lambda(s2, stls1). \text{let } (s1, tl1, s1') = \text{lhs } stls1;$ 
  ( $tl2, s2'$ ) =  $SOME (tl2, s2')$ .  $\text{trsys2 } s2 \text{ } tl2 \text{ } s2' \wedge s1' \approx s2' \wedge tl1 \sim tl2$ 
  in ( $s2', \text{lhs } stls1$ ))
( $s2, stls1$ )
for  $s2 :: 's2$  and  $stls1 :: ('s1 \times 'tl1 \times 's1)$  llist

have tl1-to-tl2-simps [simp]:
 $\bigwedge s2 \text{ } stls1. \text{lnull } (tl1\text{-to-tl2 } s2 \text{ } stls1) \longleftrightarrow \text{lnull } stls1$ 
 $\bigwedge s2 \text{ } stls1. \neg \text{lnull } stls1 \implies \text{lhs } (tl1\text{-to-tl2 } s2 \text{ } stls1) =$ 
( $\text{let } (s1, tl1, s1') = \text{lhs } stls1;$ 
  ( $tl2, s2'$ ) =  $SOME (tl2, s2')$ .  $\text{trsys2 } s2 \text{ } tl2 \text{ } s2' \wedge s1' \approx s2' \wedge tl1 \sim tl2$ 
  in ( $s2, tl2, s2'$ ))
 $\bigwedge s2 \text{ } stls1. \neg \text{lnull } stls1 \implies \text{lhs } (tl1\text{-to-tl2 } s2 \text{ } stls1) =$ 
( $\text{let } (s1, tl1, s1') = \text{lhs } stls1;$ 
  ( $tl2, s2'$ ) =  $SOME (tl2, s2')$ .  $\text{trsys2 } s2 \text{ } tl2 \text{ } s2' \wedge s1' \approx s2' \wedge tl1 \sim tl2$ 
  in  $tl1\text{-to-tl2 } s2' \text{ } (\text{lhs } stls1)$ )
 $\bigwedge s2. \text{tl1-to-tl2 } s2 \text{ } LNil = LNil$ 
 $\bigwedge s2 \text{ } s1 \text{ } tl1 \text{ } s1' \text{ } stls1'. \text{tl1-to-tl2 } s2 \text{ } (LCons (s1, tl1, s1') \text{ } stls1') =$ 
   $LCons (s2, SOME (tl2, s2'). \text{trsys2 } s2 \text{ } tl2 \text{ } s2' \wedge s1' \approx s2' \wedge tl1 \sim tl2)$ 
  ( $\text{tl1-to-tl2 } (\text{snd } (SOME (tl2, s2'). \text{trsys2 } s2 \text{ } tl2 \text{ } s2' \wedge s1' \approx s2' \wedge tl1 \sim tl2)) \text{ } stls1'$ )
by(simp-all add: tl1-to-tl2-def split-beta)

have [simp]:  $\text{length } (tl1\text{-to-tl2 } s2 \text{ } stls1) = \text{length } stls1$ 
by(coinduction arbitrary: s2 stls1 rule: enat-coinduct)(auto simp add: epred-length split-beta)

from red1' bisim have  $s2 \text{ } -2\text{-tl1-to-tl2 } s2 \text{ } stls1 \rightarrow^* t \infty$ 
proof(coinduction arbitrary: s2 s1 stls1)
  case (inf-step-table s2 s1 stls1)
  note  $\text{red1}' = \langle s1 \text{ } -1\text{-stls1} \rightarrow^* t \infty \rangle$  and  $\text{bisim} = \langle s1 \approx s2 \rangle$ 
  from  $\text{red1}'$  show ?case
  proof(cases)
    case (inf-step-tableI s1' stls1' tl1)
    hence  $stls1: stls1 = LCons (s1, tl1, s1') \text{ } stls1'$ 
    and  $r: s1 \text{ } -1\text{-tl1} \rightarrow s1'$  and  $\text{reds1}: s1' \text{ } -1\text{-stls1}' \rightarrow^* t \infty$  by simp-all
    let ? $tl2s2' = SOME (tl2, s2'). s2 \text{ } -2\text{-tl2} \rightarrow s2' \wedge s1' \approx s2' \wedge tl1 \sim tl2$ 
    let ? $tl2 = \text{fst } ?tl2s2' \text{ let } ?s2' = \text{snd } ?tl2s2'$ 
    from simulation1[OF bisim r] obtain  $s2' \text{ } tl2$ 
    where  $s2 \text{ } -2\text{-tl2} \rightarrow s2' \text{ } s1' \approx s2' \text{ } tl1 \sim tl2$  by blast
    hence ( $\lambda(tl2, s2'). s2 \text{ } -2\text{-tl2} \rightarrow s2' \wedge s1' \approx s2' \wedge tl1 \sim tl2$ ) ( $tl2, s2'$ ) by simp
    hence ( $\lambda(tl2, s2'). s2 \text{ } -2\text{-tl2} \rightarrow s2' \wedge s1' \approx s2' \wedge tl1 \sim tl2$ ) ? $tl2s2'$  by(rule someI)
    hence  $s2 \text{ } -2\text{-?tl2} \rightarrow ?s2' \text{ } s1' \approx ?s2' \text{ } tl1 \sim ?tl2$  by(simp-all add: split-beta)
    then show ?thesis using  $\text{reds1 } stls1$  by(fastforce intro: prod-eqI)
  qed
qed
hence  $s2 \text{ } -2\text{-lmap } (\text{fst } \circ \text{snd}) \text{ } (tl1\text{-to-tl2 } s2 \text{ } stls1) \rightarrow^* \infty$ 
by(rule r2.inf-step-table-imp-inf-step)
moreover have  $stls1 \text{ } [[\sim]] \text{ lmap } (\text{fst } \circ \text{snd}) \text{ } (tl1\text{-to-tl2 } s2 \text{ } stls1)$ 
proof(rule llist-all2-all-lnthI)
  show  $\text{length } stls1 = \text{length } (\text{lmap } (\text{fst } \circ \text{snd}) \text{ } (tl1\text{-to-tl2 } s2 \text{ } stls1))$ 
  using  $stls1$  by simp

```

```

next
  fix n
  assume enat n < llength tls1
  thus lnth tls1 n ~ lnth (lmap (fst o snd) (tl1-to-tl2 s2 stls1)) n
    using red1' bisim unfolding tls1
  proof (induct n arbitrary: s1 s2 stls1 rule: nat-less-induct)
  case (1 n)
  hence IH:  $\bigwedge m s1 s2 stls1. \llbracket m < n; \text{enat } m < \text{llength } (lmap (fst \circ snd) stls1);$ 
              $s1 -1 -stls1 \rightarrow *t \infty; s1 \approx s2 \rrbracket$ 
              $\implies \text{lnth } (lmap (fst \circ snd) stls1) m \sim \text{lnth } (lmap (fst \circ snd) (tl1\text{-to-tl2 } s2 stls1)) m$ 
    by blast
  from  $\langle s1 -1 -stls1 \rightarrow *t \infty \rangle$  show ?case
  proof cases
  case (inf-step-tableI s1' stls1' tl1)
  hence stls1:  $stls1 = LCons (s1, tl1, s1') stls1'$ 
    and r:  $s1 -1 -tl1 \rightarrow s1'$  and reds:  $s1' -1 -stls1' \rightarrow *t \infty$  by simp-all
  let ?tl2s2' = SOME (tl2, s2').  $s2 -2 -tl2 \rightarrow s2' \wedge s1' \approx s2' \wedge tl1 \sim tl2$ 
  let ?tl2 = fst ?tl2s2' let ?s2' = snd ?tl2s2'
  from simulation1[OF  $\langle s1 \approx s2 \rangle$  r] obtain s2' tl2
    where  $s2 -2 -tl2 \rightarrow s2' \wedge s1' \approx s2' \wedge tl1 \sim tl2$  by blast
  hence  $(\lambda (tl2, s2'). s2 -2 -tl2 \rightarrow s2' \wedge s1' \approx s2' \wedge tl1 \sim tl2) (tl2, s2')$  by simp
  hence  $(\lambda (tl2, s2'). s2 -2 -tl2 \rightarrow s2' \wedge s1' \approx s2' \wedge tl1 \sim tl2) ?tl2s2'$  by (rule someI)
  hence bisim':  $s1' \approx ?s2'$  and tlsim:  $tl1 \sim ?tl2$  by (simp-all add: split-beta)
  show ?thesis
  proof (cases n)
  case 0
  with stls1 tlsim show ?thesis by simp
  next
  case (Suc m)
  hence  $m < n$  by simp
  moreover have  $\text{enat } m < \text{llength } (lmap (fst \circ snd) stls1')$ 
    using stls1  $\langle \text{enat } n < \text{llength } (lmap (fst \circ snd) stls1) \rangle$  Suc by (simp add: Suc-ile-eq)
  ultimately have  $\text{lnth } (lmap (fst \circ snd) stls1') m \sim \text{lnth } (lmap (fst \circ snd) (tl1\text{-to-tl2 } ?s2'$ 
     $stls1')) m$ 
    using reds bisim' by (rule IH)
  with Suc stls1 show ?thesis by (simp del: o-apply)
  qed
  qed
  qed
  qed
  ultimately show ?thesis by blast
  qed

```

lemma simulation2-inf-step:

```

 $\llbracket s2 -2 -tls2 \rightarrow * \infty; s1 \approx s2 \rrbracket \implies \exists stls1. s1 -1 -stls1 \rightarrow * \infty \wedge stls1 \llbracket [\sim] \rrbracket tls2$ 
using bisimulation.simulation1-inf-step[OF bisimulation-flip]
unfolding flip-simps .

```

end

locale bisimulation-final-base =

```

  bisimulation-base +
  constrains trsys1 :: ('s1, 'tl1) trsys
  and trsys2 :: ('s2, 'tl2) trsys

```

```

and bisim :: ('s1, 's2) bisim
and tlsim :: ('tl1, 'tl2) bisim
fixes final1 :: 's1  $\Rightarrow$  bool
and final2 :: 's2  $\Rightarrow$  bool

locale bisimulation-final = bisimulation-final-base + bisimulation +
  constrains trsys1 :: ('s1, 'tl1) trsys
  and trsys2 :: ('s2, 'tl2) trsys
  and bisim :: ('s1, 's2) bisim
  and tlsim :: ('tl1, 'tl2) bisim
  and final1 :: 's1  $\Rightarrow$  bool
  and final2 :: 's2  $\Rightarrow$  bool
  assumes bisim-final:  $s1 \approx s2 \implies final1\ s1 \longleftrightarrow final2\ s2$ 

begin

lemma bisimulation-final-flip:
  bisimulation-final trsys2 trsys1 (flip bisim) (flip tlsim) final2 final1
apply(intro-locales)
apply(rule bisimulation-flip)
apply(unfold-locales)
by(unfold flip-simps, rule bisim-final[symmetric])

end

lemma bisimulation-final-flip-simps [flip-simps]:
  bisimulation-final trsys2 trsys1 (flip bisim) (flip tlsim) final2 final1 =
  bisimulation-final trsys1 trsys2 bisim tlsim final1 final2
by(auto dest: bisimulation-final.bisimulation-final-flip simp only: flip-flip)

context bisimulation-final begin

lemma final-simulation1:
   $\llbracket s1 \approx s2; s1 -1-tls1 \rightarrow^* s1'; final1\ s1' \rrbracket$ 
   $\implies \exists s2'\ tls2. s2 -2-tls2 \rightarrow^* s2' \wedge s1' \approx s2' \wedge final2\ s2' \wedge tls1\ [\sim]\ tls2$ 
by(auto dest: bisim-final dest!: simulation1-rtrancl)

lemma final-simulation2:
   $\llbracket s1 \approx s2; s2 -2-tls2 \rightarrow^* s2'; final2\ s2' \rrbracket$ 
   $\implies \exists s1'\ tls1. s1 -1-tls1 \rightarrow^* s1' \wedge s1' \approx s2' \wedge final1\ s1' \wedge tls1\ [\sim]\ tls2$ 
by(auto dest: bisim-final dest!: simulation2-rtrancl)

end

```

1.17.2 Delay bisimulation

```

locale delay-bisimulation-base =
  bisimulation-base +
  trsys1?:  $\tau trsys\ trsys1\ \tau move1$  +
  trsys2?:  $\tau trsys\ trsys2\ \tau move2$ 
  for  $\tau move1\ \tau move2$  +
  constrains trsys1 :: ('s1, 'tl1) trsys
  and trsys2 :: ('s2, 'tl2) trsys
  and bisim :: ('s1, 's2) bisim

```

```

and tlsim :: ('tl1, 'tl2) bisim
and τmove1 :: ('s1, 'tl1) trsys
and τmove2 :: ('s2, 'tl2) trsys
begin

```

notation

```

trsys1.silent-move (⟦-/ -τ1→ -> [50, 50] 60) and
trsys2.silent-move (⟦-/ -τ2→ -> [50, 50] 60)

```

notation

```

trsys1.silent-moves (⟦-/ -τ1→* -> [50, 50] 60) and
trsys2.silent-moves (⟦-/ -τ2→* -> [50, 50] 60)

```

notation

```

trsys1.silent-movet (⟦-/ -τ1→+ -> [50, 50] 60) and
trsys2.silent-movet (⟦-/ -τ2→+ -> [50, 50] 60)

```

notation

```

trsys1.τrtrancl3p (⟦-/ -τ1--->* -> [50, 0, 50] 60) and
trsys2.τrtrancl3p (⟦-/ -τ2--->* -> [50, 0, 50] 60)

```

notation

```

trsys1.τinf-step (⟦-/ -τ1--->* ∞> [50, 0] 80) and
trsys2.τinf-step (⟦-/ -τ2--->* ∞> [50, 0] 80)

```

notation

```

trsys1.τdiverge (⟦-/ -τ1→ ∞> [50] 80) and
trsys2.τdiverge (⟦-/ -τ2→ ∞> [50] 80)

```

notation

```

trsys1.τinf-step-table (⟦-/ -τ1--->*t ∞> [50, 0] 80) and
trsys2.τinf-step-table (⟦-/ -τ2--->*t ∞> [50, 0] 80)

```

notation

```

trsys1.τRuns (⟦- ↓1 -> [50, 50] 51) and
trsys2.τRuns (⟦- ↓2 -> [50, 50] 51)

```

lemma *simulation-silent1I'*:

```

assumes  $\exists s2'. (if \mu1 s1' s1 \text{ then } trsys2.silent-moves \text{ else } trsys2.silent-movet) s2 s2' \wedge s1' \approx s2'$ 
shows  $s1' \approx s2 \wedge \mu1^{++} s1' s1 \vee (\exists s2'. s2 -\tau2 \rightarrow + s2' \wedge s1' \approx s2')$ 

```

proof –

```

from assms obtain s2' where red: (if  $\mu1 s1' s1$  then trsys2.silent-moves else trsys2.silent-movet)
s2 s2'

```

```

and bisim:  $s1' \approx s2'$  by blast

```

```

show ?thesis

```

```

proof(cases  $\mu1 s1' s1$ )

```

```

case True

```

```

with red have  $s2 -\tau2 \rightarrow * s2'$  by simp

```

```

thus ?thesis using bisim True by cases(blast intro: rtranclp-into-tranclp1)+

```

```

next

```

```

case False

```

```

with red bisim show ?thesis by auto

```

```

qed

```

```

qed

```



```

lemma simulation-silent2I':
  assumes  $\exists s1'. (if \mu2\ s2'\ s2\ then\ trsys1.\ silent\ moves\ else\ trsys1.\ silent\ movet)\ s1\ s1' \wedge s1' \approx s2'$ 
  shows  $s1 \approx s2' \wedge \mu2\ \hat{\ }_{++}\ s2'\ s2 \vee (\exists s1'. s1\ -\tau1 \rightarrow\ +\ s1' \wedge s1' \approx s2')$ 
using assms
by(rule delay-bisimulation-base.simulation-silent1I')

end

locale delay-bisimulation-obs = delay-bisimulation-base - - -  $\tau move1\ \tau move2$ 
  for  $\tau move1 :: 's1 \Rightarrow 'tl1 \Rightarrow 's1 \Rightarrow bool$ 
  and  $\tau move2 :: 's2 \Rightarrow 'tl2 \Rightarrow 's2 \Rightarrow bool +$ 
  assumes simulation1:
     $\llbracket s1 \approx s2; s1\ -1\ -tl1 \rightarrow s1'; \neg \tau move1\ s1\ tl1\ s1' \rrbracket$ 
     $\implies \exists s2'\ s2''\ tl2. s2\ -\tau2 \rightarrow * s2' \wedge s2'\ -2\ -tl2 \rightarrow s2'' \wedge \neg \tau move2\ s2'\ tl2\ s2'' \wedge s1' \approx s2'' \wedge tl1$ 
     $\sim tl2$ 
  and simulation2:
     $\llbracket s1 \approx s2; s2\ -2\ -tl2 \rightarrow s2'; \neg \tau move2\ s2\ tl2\ s2' \rrbracket$ 
     $\implies \exists s1'\ s1''\ tl1. s1\ -\tau1 \rightarrow * s1' \wedge s1'\ -1\ -tl1 \rightarrow s1'' \wedge \neg \tau move1\ s1'\ tl1\ s1'' \wedge s1'' \approx s2' \wedge tl1$ 
     $\sim tl2$ 
begin

lemma delay-bisimulation-obs-flip: delay-bisimulation-obs trsys2 trsys1 (flip bisim) (flip tlsim)  $\tau move2$ 
 $\tau move1$ 
apply(unfold-locales)
apply(unfold flip-simps)
by(blast intro: simulation1 simulation2)+

end

lemma delay-bisimulation-obs-flip-simps [flip-simps]:
  delay-bisimulation-obs trsys2 trsys1 (flip bisim) (flip tlsim)  $\tau move2\ \tau move1 =$ 
  delay-bisimulation-obs trsys1 trsys2 bisim tlsim  $\tau move1\ \tau move2$ 
by(auto dest: delay-bisimulation-obs.delay-bisimulation-obs-flip simp only: flip-flip)

locale delay-bisimulation-diverge = delay-bisimulation-obs - - -  $\tau move1\ \tau move2$ 
  for  $\tau move1 :: 's1 \Rightarrow 'tl1 \Rightarrow 's1 \Rightarrow bool$ 
  and  $\tau move2 :: 's2 \Rightarrow 'tl2 \Rightarrow 's2 \Rightarrow bool +$ 
  assumes simulation-silent1:
     $\llbracket s1 \approx s2; s1\ -\tau1 \rightarrow s1' \rrbracket \implies \exists s2'. s2\ -\tau2 \rightarrow * s2' \wedge s1' \approx s2'$ 
  and simulation-silent2:
     $\llbracket s1 \approx s2; s2\ -\tau2 \rightarrow s2' \rrbracket \implies \exists s1'. s1\ -\tau1 \rightarrow * s1' \wedge s1' \approx s2'$ 
  and  $\tau diverge\ bisim\ inv: s1 \approx s2 \implies s1\ -\tau1 \rightarrow \infty \longleftrightarrow s2\ -\tau2 \rightarrow \infty$ 
begin

lemma delay-bisimulation-diverge-flip: delay-bisimulation-diverge trsys2 trsys1 (flip bisim) (flip tlsim)
 $\tau move2\ \tau move1$ 
apply(rule delay-bisimulation-diverge.intro)
apply(rule delay-bisimulation-obs-flip)
apply(unfold-locales)
apply(unfold flip-simps)
by(blast intro: simulation-silent1 simulation-silent2  $\tau diverge\ bisim\ inv$ [symmetric] del: iffI)+

end

```

lemma *delay-bisimulation-diverge-flip-simps* [*flip-simps*]:
delay-bisimulation-diverge trsys2 trsys1 (flip bisim) (flip tlsim) τ move2 τ move1 =
delay-bisimulation-diverge trsys1 trsys2 bisim tlsim τ move1 τ move2
by(*auto dest: delay-bisimulation-diverge.delay-bisimulation-diverge-flip simp only: flip-flip*)

context *delay-bisimulation-diverge begin*

lemma *simulation-silents1*:
assumes *bisim: $s1 \approx s2$ and moves: $s1 \rightarrow^* s1'$*
shows $\exists s2'. s2 \rightarrow^* s2' \wedge s1' \approx s2'$
using *moves bisim*
proof *induct*
case *base thus ?case by(blast)*
next
case (*step $s1' s1''$*)
from $\langle s1 \approx s2 \implies \exists s2'. s2 \rightarrow^* s2' \wedge s1' \approx s2' \rangle \langle s1 \approx s2 \rangle$
obtain *$s2'$ where $s2 \rightarrow^* s2' s1' \approx s2'$ by blast*
from *simulation-silent1[OF $\langle s1' \approx s2' \rangle \langle s1' \rightarrow s1'' \rangle$]*
obtain *$s2''$ where $s2' \rightarrow^* s2'' s1'' \approx s2''$ by blast*
from $\langle s2 \rightarrow^* s2' \rangle \langle s2' \rightarrow^* s2'' \rangle$ **have** $s2 \rightarrow^* s2''$ **by**(*rule rtranclp-trans*)
with $\langle s1'' \approx s2'' \rangle$ **show** *?case by blast*
qed

lemma *simulation-silents2*:
 $\llbracket s1 \approx s2; s2 \rightarrow^* s2' \rrbracket \implies \exists s1'. s1 \rightarrow^* s1' \wedge s1' \approx s2'$
using *delay-bisimulation-diverge.simulation-silents1[OF delay-bisimulation-diverge-flip]*
unfolding *flip-simps* .

lemma *simulation1- τ rtrancl3p*:
 $\llbracket s1 \rightarrow^* s1'; s1 \approx s2 \rrbracket \implies \exists t1s2 s2'. s2 \rightarrow^* s2' \wedge s1' \approx s2' \wedge t1s1 [\sim] t1s2$
proof(*induct arbitrary: $s2$ rule: trsys1. τ rtrancl3p.induct*)
case (*τ rtrancl3p-refl s*)
thus *?case by(auto intro: τ trsys. τ rtrancl3p.intros)*
next
case (*τ rtrancl3p-step $s1 s1' t1s1 s1'' t1$*)
from *simulation1[OF $\langle s1 \approx s2 \rangle \langle s1 \rightarrow^* s1' \rangle \langle \neg \tau$ move1 $s1 t1 s1' \rangle$]*
obtain *$s2' s2'' t1s2$ where τ red: $s2 \rightarrow^* s2'$*
and *red: $s2' \rightarrow^* s2''$ and $n\tau: \neg \tau$ move2 $s2' t1s2 s2''$*
and *bisim': $s1' \approx s2''$ and t1sim: $t1s1 \sim t1s2$ by blast*
from *bisim' $\langle s1' \approx s2'' \rangle \implies \exists t1s2 s2'. s2'' \rightarrow^* s2' \wedge s1'' \approx s2' \wedge t1s1 [\sim] t1s2$*
obtain *$t1s2 s2'''$ where IH: $s2'' \rightarrow^* s2''' s1'' \approx s2''' t1s1 [\sim] t1s2$ by blast*
from τ red **have** $s2 \rightarrow^* s2'$ **by**(*rule trsys2.silent-moves-into- τ rtrancl3p*)
also from *red $n\tau$ IH(1) have $s2' \rightarrow^* s2'''$ by(rule τ rtrancl3p. τ rtrancl3p-step)*
finally show *?case using IH t1sim by fastforce*
next
case (*τ rtrancl3p- τ step $s1 s1' t1s1 s1'' t1$*)
from $\langle s1 \rightarrow^* s1' \rangle \langle \tau$ move1 $s1 t1 s1' \rangle$ **have** $s1 \rightarrow^* s1' ..$
from *simulation-silent1[OF $\langle s1 \approx s2 \rangle$ this]*
obtain *$s2'$ where τ red: $s2 \rightarrow^* s2'$ and bisim': $s1' \approx s2'$ by blast*
from τ red **have** $s2 \rightarrow^* s2'$ **by**(*rule trsys2.silent-moves-into- τ rtrancl3p*)
also from *bisim' $\langle s1' \approx s2' \rangle \implies \exists t1s2 s2''. s2' \rightarrow^* s2'' \wedge s1'' \approx s2'' \wedge t1s1 [\sim] t1s2$*

obtain $tls2\ s2''$ **where** $IH: s2' -\tau2-tls2 \rightarrow^* s2''\ s1'' \approx s2''\ t1s1\ [\sim]\ t1s2$ **by** *blast*
note $\langle s2' -\tau2-tls2 \rightarrow^* s2'' \rangle$
finally show $?case$ **using** IH **by** *auto*
qed

lemma *simulation2- τ rtrancl3p*:

$\llbracket s2 -\tau2-tls2 \rightarrow^* s2'; s1 \approx s2 \rrbracket$
 $\implies \exists t1s1\ s1'. s1 -\tau1-t1s1 \rightarrow^* s1' \wedge s1' \approx s2' \wedge t1s1\ [\sim]\ t1s2$

using *delay-bisimulation-diverge.simulation1- τ rtrancl3p*[*OF delay-bisimulation-diverge-flip*]

unfolding *flip-simps* .

lemma *simulation1- τ inf-step*:

assumes $\tau inf1: s1 -\tau1-t1s1 \rightarrow^* \infty$ **and** *bisim*: $s1 \approx s2$

shows $\exists t1s2. s2 -\tau2-t1s2 \rightarrow^* \infty \wedge t1s1\ [[\sim]]\ t1s2$

proof –

from *trsys1. τ inf-step-imp- τ inf-step-table*[*OF $\tau inf1$*]

obtain *sstls1* **where** $\tau inf1': s1 -\tau1-sstls1 \rightarrow^* t \infty$

and *t1s1*: $t1s1 = lmap\ (fst \circ snd \circ snd)\ sstls1$ **by** *blast*

define *tl1-to-tl2* **where** $tl1-to-tl2\ s2\ sstls1 = unfold_l1st$

$(\lambda(s2, sstls1). lnull\ sstls1)$

$(\lambda(s2, sstls1).$

$let\ (s1, s1', t11, s1'') = lhd\ sstls1;$

$(s2', t12, s2'') = SOME\ (s2', t12, s2'').\ s2 -\tau2 \rightarrow^* s2' \wedge trsys2\ s2'\ t12\ s2'' \wedge$
 $\neg\ \tau move2\ s2'\ t12\ s2'' \wedge s1'' \approx s2'' \wedge t11 \sim t12$

$in\ (s2, s2', t12, s2''))$

$(\lambda(s2, sstls1).$

$let\ (s1, s1', t11, s1'') = lhd\ sstls1;$

$(s2', t12, s2'') = SOME\ (s2', t12, s2'').\ s2 -\tau2 \rightarrow^* s2' \wedge trsys2\ s2'\ t12\ s2'' \wedge$
 $\neg\ \tau move2\ s2'\ t12\ s2'' \wedge s1'' \approx s2'' \wedge t11 \sim t12$

$in\ (s2'', ltl\ sstls1))$

$(s2, sstls1)$

for $s2 :: 's2$ **and** $sstls1 :: ('s1 \times 's1 \times 't11 \times 's1)\ l1st$

have [*simp*]:

$\bigwedge s2\ sstls1. lnull\ (tl1-to-tl2\ s2\ sstls1) \longleftrightarrow lnull\ sstls1$

$\bigwedge s2\ sstls1. \neg\ lnull\ sstls1 \implies lhd\ (tl1-to-tl2\ s2\ sstls1) =$

$(let\ (s1, s1', t11, s1'') = lhd\ sstls1;$

$(s2', t12, s2'') = SOME\ (s2', t12, s2'').\ s2 -\tau2 \rightarrow^* s2' \wedge trsys2\ s2'\ t12\ s2'' \wedge$
 $\neg\ \tau move2\ s2'\ t12\ s2'' \wedge s1'' \approx s2'' \wedge t11 \sim t12$

$in\ (s2, s2', t12, s2''))$

$\bigwedge s2\ sstls1. \neg\ lnull\ sstls1 \implies ltl\ (tl1-to-tl2\ s2\ sstls1) =$

$(let\ (s1, s1', t11, s1'') = lhd\ sstls1;$

$(s2', t12, s2'') = SOME\ (s2', t12, s2'').\ s2 -\tau2 \rightarrow^* s2' \wedge trsys2\ s2'\ t12\ s2'' \wedge$
 $\neg\ \tau move2\ s2'\ t12\ s2'' \wedge s1'' \approx s2'' \wedge t11 \sim t12$

$in\ tl1-to-tl2\ s2''\ (ltl\ sstls1))$

$\bigwedge s2. tl1-to-tl2\ s2\ LNil = LNil$

$\bigwedge s2\ s1\ s1'\ t11\ s1''\ stls1'. tl1-to-tl2\ s2\ (LCons\ (s1, s1', t11, s1'')\ stls1') =$

$LCons\ (s2, SOME\ (s2', t12, s2'').\ s2 -\tau2 \rightarrow^* s2' \wedge trsys2\ s2'\ t12\ s2'' \wedge$

$\neg\ \tau move2\ s2'\ t12\ s2'' \wedge s1'' \approx s2'' \wedge t11 \sim t12)$

$(tl1-to-tl2\ (snd\ (snd\ (SOME\ (s2', t12, s2'').\ s2 -\tau2 \rightarrow^* s2' \wedge trsys2\ s2'\ t12\ s2'' \wedge$

$\neg\ \tau move2\ s2'\ t12\ s2'' \wedge s1'' \approx s2'' \wedge t11 \sim t12)))$

$stls1')$

by (*simp-all add: tl1-to-tl2-def split-beta*)

have [*simp*]: $l1ength\ (tl1-to-tl2\ s2\ sstls1) = l1ength\ sstls1$

by(*coinduction arbitrary: s2 sstls1 rule: enat-coinduct*)(*auto simp add: epred-llength split-beta*)

define *sstls2* **where** *sstls2* = *tl1-to-tl2 s2 sstls1*
with $\tau inf1'$ *bisim* **have** $\exists s1$ *sstls1*. $s1 \rightarrow_{\tau 1} sstls1 \rightarrow^* \infty \wedge sstls2 = tl1-to-tl2 s2 sstls1 \wedge s1 \approx s2$
by *blast*

from $\tau inf1'$ *bisim* **have** $s2 \rightarrow_{\tau 2} tl1-to-tl2 s2 sstls1 \rightarrow^* \infty$
proof(*coinduction arbitrary: s2 s1 sstls1*)
case ($\tau inf-step-table s2 s1 sstls1$)
note $\tau inf' = \langle s1 \rightarrow_{\tau 1} sstls1 \rightarrow^* \infty \rangle$ **and** *bisim* = $\langle s1 \approx s2 \rangle$
from $\tau inf'$ **show** *?case*
proof(*cases*)
case ($\tau inf-step-table-Cons s1' s1'' sstls1' tl1$)
hence *sstls1: sstls1* = *LCons (s1, s1', tl1, s1'')* *sstls1'*
and τs : $s1 \rightarrow_{\tau 1} s1'$ **and** r : $s1' \rightarrow_{\tau 1} s1''$ **and** $n\tau$: $\neg \tau move1 s1' tl1 s1''$
and *reds1*: $s1'' \rightarrow_{\tau 1} sstls1' \rightarrow^* \infty$ **by** *simp-all*
let $?P = \lambda(s2', tl2, s2'')$. $s2 \rightarrow_{\tau 2} s2' \wedge trsys2 s2' tl2 s2'' \wedge \neg \tau move2 s2' tl2 s2'' \wedge s1'' \approx s2'' \wedge tl1 \sim tl2$
let $?s2tl2s2' = Eps ?P$
let $?s2'' = snd (snd ?s2tl2s2')$
from *simulation-silents1*[*OF* $\langle s1 \approx s2 \rangle \tau s$]
obtain $s2'$ **where** $s2 \rightarrow_{\tau 2} s2' s1' \approx s2'$ **by** *blast*
from *simulation1*[*OF* $\langle s1' \approx s2' \rangle r n\tau$] **obtain** $s2'' s2''' tl2$
where $s2' \rightarrow_{\tau 2} s2''$
and *rest*: $s2'' \rightarrow_{\tau 2} s2''' \wedge \neg \tau move2 s2'' tl2 s2''' s1'' \approx s2''' tl1 \sim tl2$ **by** *blast*
from $\langle s2 \rightarrow_{\tau 2} s2' \rangle \langle s2' \rightarrow_{\tau 2} s2'' \rangle$ **have** $s2 \rightarrow_{\tau 2} s2''$ **by**(*rule rtranclp-trans*)
with *rest* **have** $?P (s2'', tl2, s2''')$ **by** *simp*
hence $?P ?s2tl2s2'$ **by**(*rule someI*)
then **show** *?thesis* **using** *reds1 sstls1* **by** *fastforce*
next
case $\tau inf-step-table-Nil$
hence [*simp*]: *sstls1* = *LNil* **and** $s1 \rightarrow_{\tau 1} \infty$ **by** *simp-all*
from $\langle s1 \rightarrow_{\tau 1} \infty \rangle \langle s1 \approx s2 \rangle$ **have** $s2 \rightarrow_{\tau 2} \infty$ **by**(*simp add: τ diverge-bisim-inv*)
thus *?thesis* **using** *sstls2-def* **by** *simp*
qed
qed
hence $s2 \rightarrow_{\tau 2} lmap (fst \circ snd \circ snd) (tl1-to-tl2 s2 sstls1) \rightarrow^* \infty$
by(*rule trsys2. $\tau inf-step-table-into-\tau inf-step$*)
moreover **have** *tls1* [[\sim]] *lmap (fst \circ snd \circ snd) (tl1-to-tl2 s2 sstls1)*
proof(*rule llist-all2-all-lnthI*)
show *llength* *tls1* = *llength (lmap (fst \circ snd \circ snd) (tl1-to-tl2 s2 sstls1))*
using *tls1* **by** *simp*
next
fix *n*
assume *enat* $n < llength$ *tls1*
thus *lnth* *tls1* *n* $\sim lnth (lmap (fst \circ snd \circ snd) (tl1-to-tl2 s2 sstls1)) n$
using $\tau inf1'$ *bisim* **unfolding** *tls1*
proof(*induct n arbitrary: s1 s2 sstls1 rule: less-induct*)
case (*less n*)
note $IH = \langle \bigwedge m s1 s2 sstls1. \llbracket m < n; enat m < llength (lmap (fst \circ snd \circ snd) sstls1); s1 \rightarrow_{\tau 1} sstls1 \rightarrow^* \infty; s1 \approx s2 \rrbracket \implies lnth (lmap (fst \circ snd \circ snd) sstls1) m \sim lnth (lmap (fst \circ snd \circ snd) (tl1-to-tl2 s2 sstls1)) m \rangle$
from $\langle s1 \rightarrow_{\tau 1} sstls1 \rightarrow^* \infty \rangle$ **show** *?case*

proof cases

case (τ inf-step-table-Cons $s1' s1'' sstls1' tl1$)
hence $sstls1: sstls1 = LCons (s1, s1', tl1, s1'') sstls1'$
and $\tau s: s1 -\tau 1 \rightarrow * s1'$ **and** $r: s1' -1 -tl1 \rightarrow s1''$
and $n\tau: \neg \tau move1 s1' tl1 s1''$ **and** $reds: s1'' -\tau 1 -sstls1' \rightarrow * t \infty$ **by** *simp-all*
let $?P = \lambda (s2', tl2, s2''). s2 -\tau 2 \rightarrow * s2' \wedge trsys2 s2' tl2 s2'' \wedge \neg \tau move2 s2' tl2 s2'' \wedge s1''$
 $\approx s2'' \wedge tl1 \sim tl2$
let $?s2tl2s2' = Eps ?P$ **let** $?tl2 = fst (snd ?s2tl2s2')$ **let** $?s2'' = snd (snd ?s2tl2s2')$
from *simulation-silents1* [*OF* $\langle s1 \approx s2 \rangle \tau s$] **obtain** $s2'$
where $s2 -\tau 2 \rightarrow * s2' s1' \approx s2'$ **by** *blast*
from *simulation1* [*OF* $\langle s1' \approx s2' \rangle r n\tau$] **obtain** $s2'' s2''' tl2$
where $s2' -\tau 2 \rightarrow * s2''$
and $rest: s2'' -2 -tl2 \rightarrow s2''' \neg \tau move2 s2'' tl2 s2''' s1'' \approx s2''' tl1 \sim tl2$ **by** *blast*
from $\langle s2 -\tau 2 \rightarrow * s2' \rangle \langle s2' -\tau 2 \rightarrow * s2'' \rangle$ **have** $s2 -\tau 2 \rightarrow * s2''$ **by** (*rule rtranclp-trans*)
with $rest$ **have** $?P (s2'', tl2, s2''')$ **by** *auto*
hence $?P ?s2tl2s2'$ **by** (*rule someI*)
hence $s1'' \approx ?s2'' tl1 \sim ?tl2$ **by** (*simp-all add: split-beta*)
show *?thesis*
proof (*cases n*)
case 0
with $sstls1 \langle tl1 \sim ?tl2 \rangle$ **show** *?thesis* **by** *simp*
next
case (*Suc m*)
hence $m < n$ **by** *simp*
moreover **have** $enat m < llength (lmap (fst \circ snd \circ snd) sstls1')$
using $sstls1 \langle enat n < llength (lmap (fst \circ snd \circ snd) sstls1) \rangle$ *Suc* **by** (*simp add: Suc-ile-eq*)
ultimately **have** $lnth (lmap (fst \circ snd \circ snd) sstls1') m \sim lnth (lmap (fst \circ snd \circ snd)$
 $(tl1-to-tl2 ?s2'' sstls1')) m$
using $reds \langle s1'' \approx ?s2'' \rangle$ **by** (*rule IH*)
with *Suc sstls1* **show** *?thesis* **by** (*simp del: o-apply*)
qed
next
case τ inf-step-table-*Nil*
with $\langle enat n < llength (lmap (fst \circ snd \circ snd) sstls1) \rangle$ **have** *False* **by** *simp*
thus *?thesis ..*
qed
qed
qed
ultimately **show** *?thesis* **by** *blast*
qed

lemma *simulation2- τ inf-step*:

$\llbracket s2 -\tau 2 -tls2 \rightarrow * \infty; s1 \approx s2 \rrbracket \implies \exists t1s1. s1 -\tau 1 -t1s1 \rightarrow * \infty \wedge t1s1 \llbracket [\sim] \rrbracket t1s2$
using *delay-bisimulation-diverge.simulation1- τ inf-step* [*OF* *delay-bisimulation-diverge-flip*]
unfolding *flip-simps* .

lemma *no- τ move1- τ s-to-no- τ move2*:

assumes $s1 \approx s2$
and *no- τ moves1*: $\bigwedge s1'. \neg s1 -\tau 1 \rightarrow s1'$
shows $\exists s2'. s2 -\tau 2 \rightarrow * s2' \wedge (\forall s2''. \neg s2' -\tau 2 \rightarrow s2'') \wedge s1 \approx s2'$

proof –

have $\neg s1 -\tau 1 \rightarrow \infty$

proof

assume $s1 -\tau 1 \rightarrow \infty$

then obtain $s1'$ where $s1 \rightarrow_{\tau} s1'$ by cases
 with $no\text{-}\tau\text{-moves1}$ [of $s1'$] show *False* by contradiction
 qed
 with $\langle s1 \approx s2 \rangle$ have $\neg s2 \rightarrow_{\tau} \infty$ by (*simp add: τ diverge-bisim-inv*)
 from $trsys2.not\text{-}\tau\text{-diverge-to-no-}\tau\text{-move}$ [OF *this*]
 obtain $s2'$ where $s2 \rightarrow_{\tau}^* s2'$ and $\bigwedge s2''. \neg s2' \rightarrow_{\tau} s2''$ by blast
 moreover from $simulation\text{-}silents2$ [OF $\langle s1 \approx s2 \rangle \langle s2 \rightarrow_{\tau}^* s2' \rangle$]
 obtain $s1'$ where $s1 \rightarrow_{\tau}^* s1'$ and $s1' \approx s2'$ by blast
 from $\langle s1 \rightarrow_{\tau}^* s1' \rangle no\text{-}\tau\text{-moves1}$ have $s1' = s1$
 by (*auto elim: converse-rtranclpE*)
 ultimately show *?thesis* using $\langle s1' \approx s2' \rangle$ by blast
 qed

lemma *no- τ move2- τ s-to-no- τ move1*:

$\llbracket s1 \approx s2; \bigwedge s2'. \neg s2 \rightarrow_{\tau} s2' \rrbracket \implies \exists s1'. s1 \rightarrow_{\tau}^* s1' \wedge (\forall s1''. \neg s1' \rightarrow_{\tau} s1'') \wedge s1' \approx s2$

using *delay-bisimulation-diverge.no- τ move1- τ s-to-no- τ move2* [OF *delay-bisimulation-diverge-flip*]
 unfolding *flip-simps* .

lemma *no-move1-to-no-move2*:

assumes $s1 \approx s2$

and $no\text{-}moves1: \bigwedge tl1 s1'. \neg s1 \rightarrow_{tl1} s1'$

shows $\exists s2'. s2 \rightarrow_{\tau}^* s2' \wedge (\forall tl2 s2''. \neg s2' \rightarrow_{tl2} s2'') \wedge s1 \approx s2'$

proof –

from $no\text{-}moves1$ have $\bigwedge s1'. \neg s1 \rightarrow_{\tau} s1'$ by (*auto*)

from $no\text{-}\tau\text{-move1-}\tau\text{-s-to-no-}\tau\text{-move2}$ [OF $\langle s1 \approx s2 \rangle$ *this*]

obtain $s2'$ where $s2 \rightarrow_{\tau}^* s2'$ and $s1 \approx s2'$

and $no\text{-}\tau\text{-moves2}: \bigwedge s2''. \neg s2' \rightarrow_{\tau} s2''$ by blast

moreover

have $\bigwedge tl2 s2''. \neg s2' \rightarrow_{tl2} s2''$

proof

fix $tl2 s2''$

assume $s2' \rightarrow_{tl2} s2''$

with $no\text{-}\tau\text{-moves2}$ [of $s2''$] have $\neg \tau\text{-move2 } s2' \rightarrow_{tl2} s2''$ by (*auto*)

from $simulation2$ [OF $\langle s1 \approx s2' \rangle \langle s2' \rightarrow_{tl2} s2'' \rangle$ *this*]

obtain $s1' s1'' tl1$ where $s1 \rightarrow_{\tau}^* s1'$ and $s1' \rightarrow_{tl1} s1''$ by blast

with $no\text{-}moves1$ show *False* by (*fastforce elim: converse-rtranclpE*)

qed

ultimately show *?thesis* by blast

qed

lemma *no-move2-to-no-move1*:

$\llbracket s1 \approx s2; \bigwedge tl2 s2'. \neg s2 \rightarrow_{tl2} s2' \rrbracket$

$\implies \exists s1'. s1 \rightarrow_{\tau}^* s1' \wedge (\forall tl1 s1''. \neg s1' \rightarrow_{tl1} s1'') \wedge s1' \approx s2$

using *delay-bisimulation-diverge.no-move1-to-no-move2* [OF *delay-bisimulation-diverge-flip*]

unfolding *flip-simps* .

lemma *simulation- τ Runs-table1*:

assumes $bisim: s1 \approx s2$

and $run1: trsys1.\tau\text{Runs-table } s1 \text{ stlsss1}$

shows $\exists stlsss2. trsys2.\tau\text{Runs-table } s2 \text{ stlsss2} \wedge tllist\text{-all2 } (\lambda(tl1, s1'') (tl2, s2''). tl1 \sim tl2 \wedge s1'' \approx s2'')$ (*rel-option bisim*) $stlsss1 \text{ stlsss2}$

proof (*intro exI conjI*)

let $?P = \lambda(s2 :: 's2) (stlsss1 :: ('tl1 \times 's1, 's1 \text{ option}) tllist) (tl2, s2'')$.

$\exists s2'. s2 \text{ } \neg\tau2 \rightarrow * s2' \wedge s2' \text{ } \neg2\text{-tl2} \rightarrow s2'' \wedge \neg \tau\text{move2 } s2' \text{ } tl2 \text{ } s2'' \wedge \text{snd } (thd \text{ } stlsss1) \approx s2'' \wedge \text{fst } (thd \text{ } stlsss1) \sim tl2$

define *tls1-to-tls2* **where** *tls1-to-tls2* *s2 stlsss1* = *unfold-tllist*
 $(\lambda(s2, stlsss1). \text{is-TNil } stlsss1)$
 $(\lambda(s2, stlsss1). \text{map-option } (\lambda s1'. \text{SOME } s2'. s2 \text{ } \neg\tau2 \rightarrow * s2' \wedge (\forall tl \text{ } s2''. \neg s2' \text{ } \neg2\text{-tl} \rightarrow s2'') \wedge s1' \approx s2') (\text{terminal } stlsss1))$
 $(\lambda(s2, stlsss1). \text{let } (tl2, s2'') = \text{Eps } (?P \text{ } s2 \text{ } stlsss1) \text{ in } (tl2, s2''))$
 $(\lambda(s2, stlsss1). \text{let } (tl2, s2'') = \text{Eps } (?P \text{ } s2 \text{ } stlsss1) \text{ in } (s2'', \text{ttl } stlsss1))$
 $(s2, stlsss1)$
for *s2 stlsss1*
have [*simp*]:
 $\bigwedge s2 \text{ } stlsss1. \text{is-TNil } (tls1\text{-to-tls2 } s2 \text{ } stlsss1) \iff \text{is-TNil } stlsss1$
 $\bigwedge s2 \text{ } stlsss1. \text{is-TNil } stlsss1 \implies \text{terminal } (tls1\text{-to-tls2 } s2 \text{ } stlsss1) = \text{map-option } (\lambda s1'. \text{SOME } s2'. s2 \text{ } \neg\tau2 \rightarrow * s2' \wedge (\forall tl \text{ } s2''. \neg s2' \text{ } \neg2\text{-tl} \rightarrow s2'') \wedge s1' \approx s2') (\text{terminal } stlsss1)$
 $\bigwedge s2 \text{ } stlsss1. \neg \text{is-TNil } stlsss1 \implies \text{thd } (tls1\text{-to-tls2 } s2 \text{ } stlsss1) = (\text{let } (tl2, s2'') = \text{Eps } (?P \text{ } s2 \text{ } stlsss1) \text{ in } (tl2, s2''))$
 $\bigwedge s2 \text{ } stlsss1. \neg \text{is-TNil } stlsss1 \implies \text{ttl } (tls1\text{-to-tls2 } s2 \text{ } stlsss1) = (\text{let } (tl2, s2'') = \text{Eps } (?P \text{ } s2 \text{ } stlsss1) \text{ in } \text{tls1-to-tls2 } s2'' (\text{ttl } stlsss1))$
 $\bigwedge s2 \text{ } os1. \text{tls1-to-tls2 } s2 \text{ } (\text{TNil } os1) = \text{TNil } (\text{map-option } (\lambda s1'. \text{SOME } s2'. s2 \text{ } \neg\tau2 \rightarrow * s2' \wedge (\forall tl \text{ } s2''. \neg s2' \text{ } \neg2\text{-tl} \rightarrow s2'') \wedge s1' \approx s2') os1)$
by(*simp-all add: tls1-to-tls2-def split-beta*)
have [*simp*]:
 $\bigwedge s2 \text{ } s1 \text{ } s1' \text{ } tl1 \text{ } s1'' \text{ } stlsss1. \text{tls1-to-tls2 } s2 \text{ } (\text{TCons } (tl1, s1'') \text{ } stlsss1) = (\text{let } (tl2, s2'') = \text{SOME } (tl2, s2''). \exists s2'. s2 \text{ } \neg\tau2 \rightarrow * s2' \wedge s2' \text{ } \neg2\text{-tl2} \rightarrow s2'' \wedge \neg \tau\text{move2 } s2' \text{ } tl2 \text{ } s2'' \wedge s1'' \approx s2'' \wedge tl1 \sim tl2 \text{ in } \text{TCons } (tl2, s2'') (tls1\text{-to-tls2 } s2'' \text{ } stlsss1))$
by(*rule tllist.expand*)(*simp-all add: split-beta*)

from *bisim run1*

show *trsys2.τRuns-table s2 (tls1-to-tls2 s2 stlsss1)*

proof(*coinduction arbitrary: s2 s1 stlsss1*)

case (*τRuns-table s2 s1 stlsss1*)

note *bisim* = $\langle s1 \approx s2 \rangle$

and *run1* = $\langle \text{trsys1.}\tau\text{Runs-table } s1 \text{ } stlsss1 \rangle$

from *run1 show ?case*

proof cases

case (*Terminate s1'*)

let $?P = \lambda s2'. s2 \text{ } \neg\tau2 \rightarrow * s2' \wedge (\forall tl2 \text{ } s2''. \neg s2' \text{ } \neg2\text{-tl2} \rightarrow s2'') \wedge s1' \approx s2'$

from *simulation-silents1*[*OF bisim* $\langle s1 \text{ } \neg\tau1 \rightarrow * s1' \rangle$]

obtain *s2'* **where** $s2 \text{ } \neg\tau2 \rightarrow * s2'$ **and** $s1' \approx s2'$ **by** *blast*

moreover from *no-move1-to-no-move2*[*OF* $\langle s1' \approx s2' \rangle \langle \bigwedge tl1 \text{ } s1''. \neg s1' \text{ } \neg1\text{-tl1} \rightarrow s1'' \rangle$]

obtain *s2''* **where** $s2' \text{ } \neg\tau2 \rightarrow * s2''$ **and** $s1' \approx s2''$

and $\bigwedge tl2 \text{ } s2'''. \neg s2'' \text{ } \neg2\text{-tl2} \rightarrow s2'''$ **by** *blast*

ultimately have $?P \text{ } s2''$ **by**(*blast intro: rtranclp-trans*)

hence $?P \text{ } (\text{Eps } ?P)$ **by**(*rule someI*)

hence $?Terminate$ **using** $\langle stlsss1 = \text{TNil } [s1'] \rangle$ **by** *simp*

thus *?thesis ..*

next

case *Diverge*

with $\tau\text{diverge-bisim-inv}$ [*OF bisim*]

have $?Diverge$ **by** *simp*

thus *?thesis by simp*

next
case (*Proceed* $s1' s1'' stlsss1' tl1$)
let $?P = \lambda(tl2, s2''). \exists s2'. s2 \rightarrow^* s2' \wedge s2' \rightarrow^* s2'' \wedge \neg \tau move2 s2' tl2 s2'' \wedge s1''$
 $\approx s2'' \wedge tl1 \sim tl2$
from *simulation-silents1*[*OF* *bisim* $\langle s1 \rightarrow^* s1' \rangle$]
obtain $s2'$ **where** $s2 \rightarrow^* s2'$ **and** $s1' \approx s2'$ **by** *blast*
moreover from *simulation1*[*OF* $\langle s1' \approx s2' \rangle \langle s1' \rightarrow^* s1'' \rangle \langle \neg \tau move1 s1' tl1 s1'' \rangle$]
obtain $s2'' s2''' tl2$ **where** $s2' \rightarrow^* s2''$
and $s2'' \rightarrow^* s2'''$ **and** $\neg \tau move2 s2'' tl2 s2'''$
and $s1'' \approx s2'''$ **and** $tl1 \sim tl2$ **by** *blast*
ultimately have $?P (tl2, s2'')$ **by**(*blast intro: rtranclp-trans*)
hence $?P (Eps ?P)$ **by**(*rule someI*)
hence *?Proceed*
using $\langle stlsss1 = TCons (tl1, s1'') stlsss1' \rangle \langle trsys1.\tau Runs-table s1'' stlsss1' \rangle$
by *auto blast*
thus *?thesis* **by** *simp*
qed
qed

let $?Tlsim = \lambda(tl1, s1'') (tl2, s2''). tl1 \sim tl2 \wedge s1'' \approx s2''$
let $?Bisim = rel-option bisim$
from *run1 bisim*
show *tl1-list-all2 ?Tlsim ?Bisim stlsss1 (tl1-to-tl2 s2 stlsss1)*
proof(*coinduction arbitrary: s1 s2 stlsss1*)
case (*tl1-list-all2 s1 s2 stlsss1*)
note $Runs = \langle trsys1.\tau Runs-table s1 stlsss1 \rangle$ **and** $bisim = \langle s1 \approx s2 \rangle$
from $Runs$ **show** *?case*
proof *cases*
case (*Terminate s1'*)
let $?P = \lambda s2'. s2 \rightarrow^* s2' \wedge (\forall tl2 s2''. \neg s2' \rightarrow^* s2'' \wedge s1' \approx s2')$
from *simulation-silents1*[*OF* *bisim* $\langle s1 \rightarrow^* s1' \rangle$]
obtain $s2'$ **where** $s2 \rightarrow^* s2'$ **and** $s1' \approx s2'$ **by** *blast*
moreover
from *no-move1-to-no-move2*[*OF* $\langle s1' \approx s2' \rangle \langle \bigwedge tl1 s1''. \neg s1' \rightarrow^* s1'' \rangle$]
obtain $s2''$ **where** $s2' \rightarrow^* s2''$ **and** $s1' \approx s2''$
and $\bigwedge tl2 s2'''. \neg s2'' \rightarrow^* s2'''$ **by** *blast*
ultimately have $?P s2''$ **by**(*blast intro: rtranclp-trans*)
hence $?P (Eps ?P)$ **by**(*rule someI*)
thus *?thesis* **using** $\langle stlsss1 = TNil [s1'] \rangle bisim$ **by**(*simp*)
next
case (*Proceed* $s1' s1'' stlsss1' tl1$)
from *simulation-silents1*[*OF* *bisim* $\langle s1 \rightarrow^* s1' \rangle$]
obtain $s2'$ **where** $s2 \rightarrow^* s2'$ **and** $s1' \approx s2'$ **by** *blast*
moreover from *simulation1*[*OF* $\langle s1' \approx s2' \rangle \langle s1' \rightarrow^* s1'' \rangle \langle \neg \tau move1 s1' tl1 s1'' \rangle$]
obtain $s2'' s2''' tl2$ **where** $s2' \rightarrow^* s2''$
and $s2'' \rightarrow^* s2'''$ **and** $\neg \tau move2 s2'' tl2 s2'''$
and $s1'' \approx s2'''$ **and** $tl1 \sim tl2$ **by** *blast*
ultimately have $?P s2 stlsss1 (tl2, s2''')$
using $\langle stlsss1 = TCons (tl1, s1'') stlsss1' \rangle$ **by**(*auto intro: rtranclp-trans*)
hence $?P s2 stlsss1 (Eps (?P s2 stlsss1))$ **by**(*rule someI*)
thus *?thesis* **using** $\langle stlsss1 = TCons (tl1, s1'') stlsss1' \rangle \langle trsys1.\tau Runs-table s1'' stlsss1' \rangle bisim$
by *auto blast*
qed *simp*
qed

qed

lemma *simulation- τ Runs-table2*:

assumes $s1 \approx s2$

and $trsys2.\tauRuns\text{-}table\ s2\ stlsss2$

shows $\exists stlsss1. trsys1.\tauRuns\text{-}table\ s1\ stlsss1 \wedge tllist\text{-}all2\ (\lambda(tl1, s1'') (tl2, s2''). tl1 \sim tl2 \wedge s1'' \approx s2'')$ (*rel-option bisim*) $stlsss1\ stlsss2$

using *delay-bisimulation-diverge.simulation- τ Runs-table1* [*OF* *delay-bisimulation-diverge-flip*, *unfolded flip-simps*, *OF* *assms*]

by (*subst tllist-all2-flip*[*symmetric*])(*simp only: flip-def split-def*)

lemma *simulation- τ Runs1*:

assumes *bisim*: $s1 \approx s2$

and $run1: s1 \Downarrow_1\ tls1$

shows $\exists tls2. s2 \Downarrow_2\ tls2 \wedge tllist\text{-}all2\ tlsim\ (rel\text{-}option\ bisim)\ tls1\ tls2$

proof –

from $trsys1.\tauRuns\text{-}into\text{-}\tauRuns\text{-}table$ [*OF* $run1$]

obtain $stlsss1$ **where** $tls1: tls1 = tmap\ fst\ id\ stlsss1$

and $\tauRuns1: trsys1.\tauRuns\text{-}table\ s1\ stlsss1$ **by** *blast*

from $simulation\text{-}\tauRuns\text{-}table1$ [*OF* *bisim* $\tauRuns1$]

obtain $stlsss2$ **where** $\tauRuns2: trsys2.\tauRuns\text{-}table\ s2\ stlsss2$

and $tlsim: tllist\text{-}all2\ (\lambda(tl1, s1'') (tl2, s2''). tl1 \sim tl2 \wedge s1'' \approx s2'')$
(*rel-option bisim*) $stlsss1\ stlsss2$ **by** *blast*

from $\tauRuns2$ **have** $s2 \Downarrow_2\ tmap\ fst\ id\ stlsss2$

by (*rule* $\tauRuns\text{-}table\text{-}into\text{-}\tauRuns$)

moreover **have** $tllist\text{-}all2\ tlsim\ (rel\text{-}option\ bisim)\ tls1\ (tmap\ fst\ id\ stlsss2)$

using *tlsim unfolding* $tls1$

by (*fastforce simp add: tllist-all2-tmap1 tllist-all2-tmap2 elim: tllist-all2-mono rel-option-mono*)

ultimately show *?thesis* **by** *blast*

qed

lemma *simulation- τ Runs2*:

$\llbracket s1 \approx s2; s2 \Downarrow_2\ tls2 \rrbracket$

$\implies \exists tls1. s1 \Downarrow_1\ tls1 \wedge tllist\text{-}all2\ tlsim\ (rel\text{-}option\ bisim)\ tls1\ tls2$

using *delay-bisimulation-diverge.simulation- τ Runs1* [*OF* *delay-bisimulation-diverge-flip*]

unfolding *flip-simps* .

end

locale *delay-bisimulation-final-base* =

delay-bisimulation-base - - - $\tau move1\ \tau move2$ +

bisimulation-final-base - - - $final1\ final2$

for $\tau move1 :: ('s1, 'tl1)\ trsys$

and $\tau move2 :: ('s2, 'tl2)\ trsys$

and $final1 :: 's1 \Rightarrow bool$

and $final2 :: 's2 \Rightarrow bool$ +

assumes *final1-simulation*: $\llbracket s1 \approx s2; final1\ s1 \rrbracket \implies \exists s2'. s2 \text{-}\tau 2 \text{-}\rightarrow^* s2' \wedge s1 \approx s2' \wedge final2\ s2'$

and *final2-simulation*: $\llbracket s1 \approx s2; final2\ s2 \rrbracket \implies \exists s1'. s1 \text{-}\tau 1 \text{-}\rightarrow^* s1' \wedge s1' \approx s2 \wedge final1\ s1'$

begin

lemma *delay-bisimulation-final-base-flip*:

delay-bisimulation-final-base $trsys2\ trsys1$ (*flip bisim*) $\tau move2\ \tau move1\ final2\ final1$

apply (*unfold-locales*)

apply (*unfold flip-simps*)

by(*blast intro: final1-simulation final2-simulation*)+

end

lemma *delay-bisimulation-final-base-flip-simps* [*flip-simps*]:

delay-bisimulation-final-base trsys2 trsys1 (flip bisim) τ move2 τ move1 final2 final1 =

delay-bisimulation-final-base trsys1 trsys2 bisim τ move1 τ move2 final1 final2

by(*auto dest: delay-bisimulation-final-base.delay-bisimulation-final-base-flip simp only: flip-flip*)

context *delay-bisimulation-final-base* **begin**

lemma *τ Runs-terminate-final1*:

assumes *s1 \Downarrow 1 t1s1*

and *s2 \Downarrow 2 t1s2*

and *t1list-all2 t1sim (rel-option bisim) t1s1 t1s2*

and *tfinite t1s1*

and *terminal t1s1 = Some s1'*

and *final1 s1'*

shows $\exists s2'. tfinite t1s2 \wedge terminal t1s2 = Some s2' \wedge final2 s2'$

using *assms(4) assms(1-3,5-)*

apply(*induct arbitrary: t1s2 s1 s2 rule: tfinite-induct*)

apply(*auto 4 4 simp add: t1list-all2-TCons1 t1list-all2-TNil1 option-rel-Some1 trsys1. τ Runs-simps*

trsys2. τ Runs-simps dest: final1-simulation elim: converse-rtranclpE)

done

lemma *τ Runs-terminate-final2*:

$\llbracket s1 \Downarrow 1 t1s1; s2 \Downarrow 2 t1s2; t1list-all2 t1sim (rel-option bisim) t1s1 t1s2;$

tfinite t1s2; terminal t1s2 = Some s2'; final2 s2' \rrbracket

$\implies \exists s1'. tfinite t1s1 \wedge terminal t1s1 = Some s1' \wedge final1 s1'$

using *delay-bisimulation-final-base. τ Runs-terminate-final1* [**where** *t1sim = flip t1sim, OF delay-bisimulation-final-b*

unfolding *flip-simps* **by** –

end

locale *delay-bisimulation-diverge-final =*

delay-bisimulation-diverge +

delay-bisimulation-final-base +

constrains *trsys1 :: ('s1, 't1) trsys*

and *trsys2 :: ('s2, 't2) trsys*

and *bisim :: ('s1, 's2) bisim*

and *t1sim :: ('t1, 't2) bisim*

and *τ move1 :: ('s1, 't1) trsys*

and *τ move2 :: ('s2, 't2) trsys*

and *final1 :: 's1 \Rightarrow bool*

and *final2 :: 's2 \Rightarrow bool*

begin

lemma *delay-bisimulation-diverge-final-flip*:

delay-bisimulation-diverge-final trsys2 trsys1 (flip bisim) (flip t1sim) τ move2 τ move1 final2 final1

apply(*rule delay-bisimulation-diverge-final.intro*)

apply(*rule delay-bisimulation-diverge-flip*)

apply(*unfold-locales, unfold flip-simps*)

apply(*blast intro: final1-simulation final2-simulation*)+

done

end

lemma *delay-bisimulation-diverge-final-flip-simps* [flip-simps]:

delay-bisimulation-diverge-final trsys2 trsys1 (flip bisim) (flip tlsim) τ move2 τ move1 final2 final1 =
delay-bisimulation-diverge-final trsys1 trsys2 bisim tlsim τ move1 τ move2 final1 final2

by(*auto dest: delay-bisimulation-diverge-final.delay-bisimulation-diverge-final-flip simp only: flip-flip*)

context *delay-bisimulation-diverge-final* **begin**

lemma *delay-bisimulation-diverge*:

delay-bisimulation-diverge trsys1 trsys2 bisim tlsim τ move1 τ move2

by(*unfold-locales*)

lemma *delay-bisimulation-final-base*:

delay-bisimulation-final-base trsys1 trsys2 bisim τ move1 τ move2 final1 final2

by(*unfold-locales*)

lemma *final-simulation1*:

$\llbracket s1 \approx s2; s1 \text{ } \neg\tau1 \text{ } \neg t1s1 \rightarrow^* s1'; \text{ final1 } s1' \rrbracket$
 $\implies \exists s2' t1s2. s2 \text{ } \neg\tau2 \text{ } \neg t1s2 \rightarrow^* s2' \wedge s1' \approx s2' \wedge \text{ final2 } s2' \wedge t1s1 \text{ } [\sim] \text{ } t1s2$

by(*blast dest: simulation1- τ rtrancl3p final1-simulation intro: τ rtrancl3p-trans[OF - silent-moves-into- τ rtrancl3p, simplified]*)

lemma *final-simulation2*:

$\llbracket s1 \approx s2; s2 \text{ } \neg\tau2 \text{ } \neg t1s2 \rightarrow^* s2'; \text{ final2 } s2' \rrbracket$
 $\implies \exists s1' t1s1. s1 \text{ } \neg\tau1 \text{ } \neg t1s1 \rightarrow^* s1' \wedge s1' \approx s2' \wedge \text{ final1 } s1' \wedge t1s1 \text{ } [\sim] \text{ } t1s2$

by(*rule delay-bisimulation-diverge-final.final-simulation1[OF delay-bisimulation-diverge-final-flip, unfolded flip-simps]*)

end

locale *delay-bisimulation-measure-base* =

delay-bisimulation-base +

constrains *trsys1* :: 's1 \Rightarrow 't1 \Rightarrow 's1 \Rightarrow bool

and *trsys2* :: 's2 \Rightarrow 't2 \Rightarrow 's2 \Rightarrow bool

and *bisim* :: 's1 \Rightarrow 's2 \Rightarrow bool

and *tlsim* :: 't1 \Rightarrow 't2 \Rightarrow bool

and *τ move1* :: 's1 \Rightarrow 't1 \Rightarrow 's1 \Rightarrow bool

and *τ move2* :: 's2 \Rightarrow 't2 \Rightarrow 's2 \Rightarrow bool

fixes *μ 1* :: 's1 \Rightarrow 's1 \Rightarrow bool

and *μ 2* :: 's2 \Rightarrow 's2 \Rightarrow bool

locale *delay-bisimulation-measure* =

delay-bisimulation-measure-base - - - *τ move1 τ move2 μ 1 μ 2* +

delay-bisimulation-obs trsys1 trsys2 bisim t1sim τ move1 τ move2

for *τ move1* :: 's1 \Rightarrow 't1 \Rightarrow 's1 \Rightarrow bool

and *τ move2* :: 's2 \Rightarrow 't2 \Rightarrow 's2 \Rightarrow bool

and *μ 1* :: 's1 \Rightarrow 's1 \Rightarrow bool

and *μ 2* :: 's2 \Rightarrow 's2 \Rightarrow bool +

assumes *simulation-silent1*:

$\llbracket s1 \approx s2; s1 \text{ } \neg\tau1 \rightarrow s1' \rrbracket \implies s1' \approx s2 \wedge \mu1 \hat{\text{ }} ++ s1' s1 \vee (\exists s2'. s2 \text{ } \neg\tau2 \rightarrow ++ s2' \wedge s1' \approx s2')$

and *simulation-silent2*:

$\llbracket s1 \approx s2; s2 \text{ } \neg\tau2 \rightarrow s2' \rrbracket \implies s1 \approx s2' \wedge \mu2 \hat{\text{ }} ++ s2' s2 \vee (\exists s1'. s1 \text{ } \neg\tau1 \rightarrow ++ s1' \wedge s1' \approx s2')$

and $wf\text{-}\mu 1$: $wfP \ \mu 1$
and $wf\text{-}\mu 2$: $wfP \ \mu 2$
begin

lemma *delay-bisimulation-measure-flip*:

$delay\text{-bisimulation-measure} \ trsys2 \ trsys1 \ (flip \ bisim) \ (flip \ tlsim) \ \tau move2 \ \tau move1 \ \mu 2 \ \mu 1$
apply(*rule delay-bisimulation-measure.intro*)
apply(*rule delay-bisimulation-obs-flip*)
apply(*unfold-locales*)
apply(*unfold flip-simps*)
apply(*rule simulation-silent1 simulation-silent2 wf- $\mu 1$ wf- $\mu 2$ |assumption*)
done

end

lemma *delay-bisimulation-measure-flip-simps* [*flip-simps*]:

$delay\text{-bisimulation-measure} \ trsys2 \ trsys1 \ (flip \ bisim) \ (flip \ tlsim) \ \tau move2 \ \tau move1 \ \mu 2 \ \mu 1 =$
 $delay\text{-bisimulation-measure} \ trsys1 \ trsys2 \ bisim \ tlsim \ \tau move1 \ \tau move2 \ \mu 1 \ \mu 2$
by(*auto dest: delay-bisimulation-measure.delay-bisimulation-measure-flip simp only: flip-simps*)

context *delay-bisimulation-measure* **begin**

lemma *simulation-silentst1*:

assumes $bisim$: $s1 \approx s2$ **and** $moves$: $s1 \rightarrow\tau s1'$
shows $s1' \approx s2 \wedge \mu 1^{\hat{+}} s1' s1 \vee (\exists s2'. s2 \rightarrow\tau s2' \wedge s1' \approx s2')$
using $moves \ bisim$
proof *induct*
case (*base* $s1'$) **thus** *?case* **by**(*auto dest: simulation-silent1*)
next
case (*step* $s1' \ s1''$)
hence $s1' \approx s2 \wedge \mu 1^{++} s1' s1 \vee (\exists s2'. s2 \rightarrow\tau s2' \wedge s1' \approx s2')$ **by** *blast*
thus *?case*
proof
assume $s1' \approx s2 \wedge \mu 1^{++} s1' s1$
hence $s1' \approx s2 \ \mu 1^{++} \ s1' \ s1$ **by** *simp-all*
with *simulation-silent1*[*OF* $\langle s1' \approx s2 \rangle \langle s1' \rightarrow\tau s1'' \rangle$]
show *?thesis* **by**(*auto*)
next
assume $\exists s2'. trsys2.silent-move^{++} \ s2 \ s2' \wedge s1' \approx s2'$
then obtain $s2'$ **where** $s2 \rightarrow\tau s2' \wedge s1' \approx s2'$ **by** *blast*
with *simulation-silent1*[*OF* $\langle s1' \approx s2' \rangle \langle s1' \rightarrow\tau s1'' \rangle$]
show *?thesis* **by**(*auto intro: tranclp-trans*)
qed
qed

lemma *simulation-silentst2*:

$\llbracket s1 \approx s2; s2 \rightarrow\tau s2' \rrbracket \implies s1 \approx s2' \wedge \mu 2^{\hat{+}} s2' s2 \vee (\exists s1'. s1 \rightarrow\tau s1' \wedge s1' \approx s2')$
using *delay-bisimulation-measure.simulation-silentst1*[*OF* *delay-bisimulation-measure-flip*]
unfolding *flip-simps* .

lemma *τ diverge-simulation1*:

assumes *diverge1*: $s1 \rightarrow\tau \infty$
and $bisim$: $s1 \approx s2$
shows $s2 \rightarrow\tau \infty$

proof –

from *assms* have $s1 \text{ } \neg\tau1 \rightarrow \infty \wedge s1 \approx s2$ by *blast*

thus *?thesis* using *wfp-tranclp*[*OF wf-μ1*]

proof(*coinduct rule: trsys2.τdiverge-trancl-measure-coinduct*)

case (*τdiverge s2 s1*)

hence $s1 \text{ } \neg\tau1 \rightarrow \infty \wedge s1 \approx s2$ by *simp-all*

then obtain $s1'$ where *trsys1.silent-move s1 s1' s1' -τ1 → ∞*

by(*fastforce elim: trsys1.τdiverge.cases*)

from *simulation-silent1*[*OF <s1 ≈ s2> <trsys1.silent-move s1 s1'>*] $\langle s1' \text{ } \neg\tau1 \rightarrow \infty \rangle$

show *?case* by *auto*

qed

qed

lemma *τdiverge-simulation2*:

$\llbracket s2 \text{ } \neg\tau2 \rightarrow \infty; s1 \approx s2 \rrbracket \implies s1 \text{ } \neg\tau1 \rightarrow \infty$

using *delay-bisimulation-measure.τdiverge-simulation1*[*OF delay-bisimulation-measure-flip*]

unfolding *flip-simps* .

lemma *τdiverge-bisim-inv*:

$s1 \approx s2 \implies s1 \text{ } \neg\tau1 \rightarrow \infty \longleftrightarrow s2 \text{ } \neg\tau2 \rightarrow \infty$

by(*blast intro: τdiverge-simulation1 τdiverge-simulation2*)

end

sublocale *delay-bisimulation-measure* < *delay-bisimulation-diverge*

proof

fix $s1 s2 s1'$

assume $s1 \approx s2 \wedge s1 \text{ } \neg\tau1 \rightarrow s1'$

from *simulation-silent1*[*OF this*]

show $\exists s2'. s2 \text{ } \neg\tau2 \rightarrow * s2' \wedge s1' \approx s2'$ by(*auto intro: tranclp-into-rtranclp*)

next

fix $s1 s2 s2'$

assume $s1 \approx s2 \wedge s2 \text{ } \neg\tau2 \rightarrow s2'$

from *simulation-silent2*[*OF this*]

show $\exists s1'. s1 \text{ } \neg\tau1 \rightarrow * s1' \wedge s1' \approx s2'$ by(*auto intro: tranclp-into-rtranclp*)

next

fix $s1 s2$

assume $s1 \approx s2$

thus $s1 \text{ } \neg\tau1 \rightarrow \infty \longleftrightarrow s2 \text{ } \neg\tau2 \rightarrow \infty$ by(*rule τdiverge-bisim-inv*)

qed

Counter example for *delay-bisimulation-diverge trsys1 trsys2 bisim tlim τmove1 τmove2*
 $\implies \exists \mu1 \mu2. \text{delay-bisimulation-measure trsys1 trsys2 bisim tlim } \tau\text{move1 } \tau\text{move2 } \mu1 \mu2$
 (only τ moves):

```
--|
| v
--a ~ x
  |   |
  |   |
  v   v
--b ~ y--
| ^   ^ |
--|   |--
```

```

locale delay-bisimulation-measure-final =
  delay-bisimulation-measure +
  delay-bisimulation-final-base +
  constrains trsys1 :: ('s1, 'tl1) trsys
  and trsys2 :: ('s2, 'tl2) trsys
  and bisim :: ('s1, 's2) bisim
  and tlsim :: ('tl1, 'tl2) bisim
  and  $\tau$ move1 :: ('s1, 'tl1) trsys
  and  $\tau$ move2 :: ('s2, 'tl2) trsys
  and  $\mu$ 1 :: 's1  $\Rightarrow$  's1  $\Rightarrow$  bool
  and  $\mu$ 2 :: 's2  $\Rightarrow$  's2  $\Rightarrow$  bool
  and final1 :: 's1  $\Rightarrow$  bool
  and final2 :: 's2  $\Rightarrow$  bool

```

```

sublocale delay-bisimulation-measure-final < delay-bisimulation-diverge-final
by unfold-locales

```

```

locale  $\tau$ inv = delay-bisimulation-base +
  constrains trsys1 :: ('s1, 'tl1) trsys
  and trsys2 :: ('s2, 'tl2) trsys
  and bisim :: ('s1, 's2) bisim
  and tlsim :: ('tl1, 'tl2) bisim
  and  $\tau$ move1 :: ('s1, 'tl1) trsys
  and  $\tau$ move2 :: ('s2, 'tl2) trsys
  and  $\tau$ moves1 :: 's1  $\Rightarrow$  's1  $\Rightarrow$  bool
  and  $\tau$ moves2 :: 's2  $\Rightarrow$  's2  $\Rightarrow$  bool
  assumes  $\tau$ inv: [  $s1 \approx s2$ ;  $s1 \text{ --1--tl1} \rightarrow s1'$ ;  $s2 \text{ --2--tl2} \rightarrow s2'$ ;  $s1' \approx s2'$ ;  $tl1 \sim tl2$  ]
   $\Rightarrow \tau$ move1 s1 tl1 s1'  $\longleftrightarrow \tau$ move2 s2 tl2 s2'

```

```

begin

```

```

lemma  $\tau$ inv-flip:
   $\tau$ inv trsys2 trsys1 (flip bisim) (flip tlsim)  $\tau$ move2  $\tau$ move1
by(unfold-locales)(unfold flip-simps, rule  $\tau$ inv[symmetric])

```

```

end

```

```

lemma  $\tau$ inv-flip-simps [flip-simps]:
   $\tau$ inv trsys2 trsys1 (flip bisim) (flip tlsim)  $\tau$ move2  $\tau$ move1 =  $\tau$ inv trsys1 trsys2 bisim tlsim  $\tau$ move1
 $\tau$ move2
by(auto dest: \tau inv.\tau inv-flip simp only: flip-simps)

```

```

locale bisimulation-into-delay =
  bisimulation +  $\tau$ inv +
  constrains trsys1 :: ('s1, 'tl1) trsys
  and trsys2 :: ('s2, 'tl2) trsys
  and bisim :: ('s1, 's2) bisim
  and tlsim :: ('tl1, 'tl2) bisim
  and  $\tau$ move1 :: ('s1, 'tl1) trsys
  and  $\tau$ move2 :: ('s2, 'tl2) trsys
begin

```

```

lemma bisimulation-into-delay-flip:
  bisimulation-into-delay trsys2 trsys1 (flip bisim) (flip tlsim)  $\tau$ move2  $\tau$ move1
by(intro-locales)(intro bisimulation-flip \tau inv-flip)+

```

end

lemma *bisimulation-into-delay-flip-simps* [*flip-simps*]:

bisimulation-into-delay trsys2 trsys1 (flip bisim) (flip tlim) τ move2 τ move1 =
bisimulation-into-delay trsys1 trsys2 bisim tlim τ move1 τ move2

by(*auto dest: bisimulation-into-delay.bisimulation-into-delay-flip simp only: flip-simps*)

context *bisimulation-into-delay* **begin**

lemma *simulation-silent1-aux*:

assumes *bisim*: $s1 \approx s2$ **and** $s1 \rightarrow \tau 1 s1'$

shows $s1' \approx s2 \wedge \mu 1^{++} s1' s1 \vee (\exists s2'. s2 \rightarrow \tau 2 s2' \wedge s1' \approx s2')$

proof –

from *assms* **obtain** *tl1* **where** $tr1: s1 \rightarrow \tau 1 tl1 s1'$

and $\tau 1: \tau move1 s1 tl1 s1'$ **by**(*auto*)

from *simulation1*[*OF bisim tr1*]

obtain $s2' tl2$ **where** $tr2: s2 \rightarrow \tau 2 tl2 s2'$

and *bisim'*: $s1' \approx s2'$ **and** *tlim*: $tl1 \sim tl2$ **by** *blast*

from τinv [*OF bisim tr1 tr2 bisim' tlim*] $\tau 1$ **have** $\tau 2: \tau move2 s2 tl2 s2'$ **by** *simp*

from $tr2 \tau 2$ **have** $s2 \rightarrow \tau 2 s2'$ **by**(*auto*)

with *bisim'* **show** *?thesis* **by** *blast*

qed

lemma *simulation-silent2-aux*:

$\llbracket s1 \approx s2; s2 \rightarrow \tau 2 s2' \rrbracket \implies s1 \approx s2' \wedge \mu 2^{++} s2' s2 \vee (\exists s1'. s1 \rightarrow \tau 1 s1' \wedge s1' \approx s2')$

using *bisimulation-into-delay.simulation-silent1-aux*[*OF bisimulation-into-delay-flip*]

unfolding *flip-simps* .

lemma *simulation1-aux*:

assumes *bisim*: $s1 \approx s2$ **and** $tr1: s1 \rightarrow \tau 1 tl1 s1'$ **and** $\tau 1: \neg \tau move1 s1 tl1 s1'$

shows $\exists s2' s2'' tl2. s2 \rightarrow \tau 2 s2' \wedge s2' \rightarrow \tau 2 s2'' \wedge \neg \tau move2 s2' tl2 s2'' \wedge s1' \approx s2'' \wedge tl1 \sim tl2$

proof –

from *simulation1*[*OF bisim tr1*]

obtain $s2' tl2$ **where** $tr2: s2 \rightarrow \tau 2 tl2 s2'$

and *bisim'*: $s1' \approx s2'$ **and** *tlim*: $tl1 \sim tl2$ **by** *blast*

from τinv [*OF bisim tr1 tr2 bisim' tlim*] $\tau 1$ **have** $\tau 2: \neg \tau move2 s2 tl2 s2'$ **by** *simp*

with *bisim'* $tr2$ *tlim* **show** *?thesis* **by** *blast*

qed

lemma *simulation2-aux*:

$\llbracket s1 \approx s2; s2 \rightarrow \tau 2 s2'; \neg \tau move2 s2 tl2 s2' \rrbracket$

$\implies \exists s1' s1'' tl1. s1 \rightarrow \tau 1 s1' \wedge s1' \rightarrow \tau 1 s1'' \wedge \neg \tau move1 s1' tl1 s1'' \wedge s1'' \approx s2' \wedge tl1 \sim tl2$

using *bisimulation-into-delay.simulation1-aux*[*OF bisimulation-into-delay-flip*]

unfolding *flip-simps* .

lemma *delay-bisimulation-measure*:

assumes *wf- $\mu 1$* : *wfP* $\mu 1$

and *wf- $\mu 2$* : *wfP* $\mu 2$

shows *delay-bisimulation-measure trsys1 trsys2 bisim tlim τ move1 τ move2 $\mu 1 \mu 2$*

apply(*unfold-locales*)

apply(*rule simulation-silent1-aux simulation-silent2-aux simulation1-aux simulation2-aux wf- $\mu 1$ wf- $\mu 2$ |assumption*)+

done

lemma *delay-bisimulation*:

delay-bisimulation-diverge trsys1 trsys2 bisim tlim τ move1 τ move2

proof –

interpret *delay-bisimulation-measure trsys1 trsys2 bisim tlim τ move1 τ move2 $\lambda s s'. False \lambda s s'. False$*

by(blast intro: *delay-bisimulation-measure wfp-empty*)

show *?thesis ..*

qed

end

sublocale *bisimulation-into-delay* < *delay-bisimulation-diverge*

by(rule *delay-bisimulation*)

lemma *delay-bisimulation-conv-bisimulation*:

delay-bisimulation-diverge trsys1 trsys2 bisim tlim ($\lambda s tl s'. False$) ($\lambda s tl s'. False$) =

bisimulation trsys1 trsys2 bisim tlim

(**is** *?lhs = ?rhs*)

proof

assume *?lhs*

then interpret *delay-bisimulation-diverge trsys1 trsys2 bisim tlim $\lambda s tl s'. False \lambda s tl s'. False$* .

show *?rhs* **by**(*unfold-locales*)(*fastforce simp add: τ moves-False dest: simulation1 simulation2*)+

next

assume *?rhs*

then interpret *bisimulation trsys1 trsys2 bisim tlim* .

interpret *bisimulation-into-delay trsys1 trsys2 bisim tlim $\lambda s tl s'. False \lambda s tl s'. False$*

by(*unfold-locales*)(rule *refl*)

show *?lhs* **by** *unfold-locales*

qed

context *bisimulation-final* **begin**

lemma *delay-bisimulation-final-base*:

delay-bisimulation-final-base trsys1 trsys2 bisim τ move1 τ move2 final1 final2

by(*unfold-locales*)(*auto simp add: bisim-final*)

end

sublocale *bisimulation-final* < *delay-bisimulation-final-base*

by(rule *delay-bisimulation-final-base*)

1.17.3 Transitivity for bisimulations

definition *bisim-compose* :: (*'s1, 's2*) *bisim* \Rightarrow (*'s2, 's3*) *bisim* \Rightarrow (*'s1, 's3*) *bisim* (**infixr** \circ_B 60)

where (*bisim1* \circ_B *bisim2*) *s1 s3* $\equiv \exists s2. bisim1 s1 s2 \wedge bisim2 s2 s3$

lemma *bisim-composeI* [*intro*]:

$\llbracket bisim12 s1 s2; bisim23 s2 s3 \rrbracket \Longrightarrow (bisim12 \circ_B bisim23) s1 s3$

by(*auto simp add: bisim-compose-def*)

lemma *bisim-composeE* [*elim!*]:

assumes *bisim*: (*bisim12* \circ_B *bisim23*) *s1 s3*

obtains $s2$ **where** $bisim12\ s1\ s2\ bisim23\ s2\ s3$
by(*atomize-elim*)(*rule bisim[unfolded bisim-compose-def]*)

lemma *bisim-compose-assoc* [*simp*]:
 $(bisim12 \circ_B bisim23) \circ_B bisim34 = bisim12 \circ_B bisim23 \circ_B bisim34$
by(*auto simp add: fun-eq-iff*)

lemma *bisim-compose-conv-relcomp*:
 $case\text{-}prod\ (bisim\text{-}compose\ bisim12\ bisim23) = (\lambda x. x \in relcomp\ (Collect\ (case\text{-}prod\ bisim12))\ (Collect\ (case\text{-}prod\ bisim23)))$
by(*auto simp add: relcomp-unfold*)

lemma *list-all2-bisim-composeI*:
 $\llbracket list\text{-}all2\ A\ xs\ ys; list\text{-}all2\ B\ ys\ zs \rrbracket$
 $\implies list\text{-}all2\ (A \circ_B B)\ xs\ zs$
by(*rule list-all2-trans*) *auto*+

lemma *delay-bisimulation-diverge-compose*:
assumes $wbisim12: delay\text{-}bisimulation\text{-}diverge\ trsys1\ trsys2\ bisim12\ tlim12\ \tau move1\ \tau move2$
and $wbisim23: delay\text{-}bisimulation\text{-}diverge\ trsys2\ trsys3\ bisim23\ tlim23\ \tau move2\ \tau move3$
shows $delay\text{-}bisimulation\text{-}diverge\ trsys1\ trsys3\ (bisim12 \circ_B bisim23)\ (tlim12 \circ_B tlim23)\ \tau move1\ \tau move3$

proof –

interpret $trsys1: \tau trsys\ trsys1\ \tau move1$.
interpret $trsys2: \tau trsys\ trsys2\ \tau move2$.
interpret $trsys3: \tau trsys\ trsys3\ \tau move3$.
interpret $wb12: delay\text{-}bisimulation\text{-}diverge\ trsys1\ trsys2\ bisim12\ tlim12\ \tau move1\ \tau move2$ **by**(*auto intro: wbisim12*)

interpret $wb23: delay\text{-}bisimulation\text{-}diverge\ trsys2\ trsys3\ bisim23\ tlim23\ \tau move2\ \tau move3$ **by**(*auto intro: wbisim23*)

show *?thesis*

proof

fix $s1\ s3\ s1'$

assume $bisim: (bisim12 \circ_B bisim23)\ s1\ s3$ **and** $tr1: trsys1.silent\text{-}move\ s1\ s1'$

from $bisim$ **obtain** $s2$ **where** $bisim1: bisim12\ s1\ s2$ **and** $bisim2: bisim23\ s2\ s3$ **by** *blast*

from $wb12.simulation\text{-}silent1[OF\ bisim1\ tr1]$ **obtain** $s2'$

where $tr2: trsys2.silent\text{-}moves\ s2\ s2'$ **and** $bisim1': bisim12\ s1'\ s2'$ **by** *blast*

from $wb23.simulation\text{-}silents1[OF\ bisim2\ tr2]$ **obtain** $s3'$

where $trsys3.silent\text{-}moves\ s3\ s3'\ bisim23\ s2'\ s3'$ **by** *blast*

with $bisim1'$ **show** $\exists s3'. trsys3.silent\text{-}moves\ s3\ s3' \wedge (bisim12 \circ_B bisim23)\ s1'\ s3'$

by(*blast intro: bisim-composeI*)

next

fix $s1\ s3\ s3'$

assume $bisim: (bisim12 \circ_B bisim23)\ s1\ s3$ **and** $tr3: trsys3.silent\text{-}move\ s3\ s3'$

from $bisim$ **obtain** $s2$ **where** $bisim1: bisim12\ s1\ s2$ **and** $bisim2: bisim23\ s2\ s3$ **by** *blast*

from $wb23.simulation\text{-}silent2[OF\ bisim2\ tr3]$ **obtain** $s2'$

where $tr2: trsys2.silent\text{-}moves\ s2\ s2'$ **and** $bisim2': bisim23\ s2'\ s3'$ **by** *blast*

from $wb12.simulation\text{-}silents2[OF\ bisim1\ tr2]$ **obtain** $s1'$

where $trsys1.silent\text{-}moves\ s1\ s1'\ bisim12\ s1'\ s2'$ **by** *blast*

with $bisim2'$ **show** $\exists s1'. trsys1.silent\text{-}moves\ s1\ s1' \wedge (bisim12 \circ_B bisim23)\ s1'\ s3'$

by(*blast intro: bisim-composeI*)

next

fix $s1\ s3\ t11\ s1'$

assume $bisim: (bisim12 \circ_B bisim23)\ s1\ s3$

and $tr1: trsys1\ s1\ tl1\ s1'$ **and** $\tau1: \neg \tau move1\ s1\ tl1\ s1'$
from $bisim$ **obtain** $s2$ **where** $bisim1: bisim12\ s1\ s2$ **and** $bisim2: bisim23\ s2\ s3$ **by** *blast*
from $wb12.simulation1[OF\ bisim1\ tr1\ \tau1]$ **obtain** $s2'\ s2''\ tl2$
where $tr21: trsys2.silent-moves\ s2\ s2'$ **and** $tr22: trsys2\ s2'\ tl2\ s2''$ **and** $\tau2: \neg \tau move2\ s2'\ tl2$
 $s2''$
and $bisim1': bisim12\ s1'\ s2''$ **and** $tlsim1: tlim12\ tl1\ tl2$ **by** *blast*
from $wb23.simulation-silents1[OF\ bisim2\ tr21]$ **obtain** $s3'$
where $tr31: trsys3.silent-moves\ s3\ s3'$ **and** $bisim2': bisim23\ s2'\ s3'$ **by** *blast*
from $wb23.simulation1[OF\ bisim2'\ tr22\ \tau2]$ **obtain** $s3''\ s3'''\ tl3$
where $trsys3.silent-moves\ s3'\ s3''\ trsys3\ s3''\ tl3\ s3'''$
 $\neg \tau move3\ s3''\ tl3\ s3'''\ bisim23\ s2''\ s3'''\ tlim23\ tl2\ tl3$ **by** *blast*
with $tr31\ bisim1'\ tlim1$
show $\exists s3'\ s3''\ tl3. trsys3.silent-moves\ s3\ s3' \wedge trsys3\ s3'\ tl3\ s3'' \wedge \neg \tau move3\ s3'\ tl3\ s3'' \wedge$
 $(bisim12 \circ_B bisim23)\ s1'\ s3'' \wedge (tlsim12 \circ_B tlim23)\ tl1\ tl3$
by(*blast intro: rtranclp-trans bisim-composeI*)
next
fix $s1\ s3\ tl3\ s3'$
assume $bisim: (bisim12 \circ_B bisim23)\ s1\ s3$
and $tr3: trsys3\ s3\ tl3\ s3'$ **and** $\tau3: \neg \tau move3\ s3\ tl3\ s3'$
from $bisim$ **obtain** $s2$ **where** $bisim1: bisim12\ s1\ s2$ **and** $bisim2: bisim23\ s2\ s3$ **by** *blast*
from $wb23.simulation2[OF\ bisim2\ tr3\ \tau3]$ **obtain** $s2'\ s2''\ tl2$
where $tr21: trsys2.silent-moves\ s2\ s2'$ **and** $tr22: trsys2\ s2'\ tl2\ s2''$ **and** $\tau2: \neg \tau move2\ s2'\ tl2$
 $s2''$
and $bisim2': bisim23\ s2''\ s3'$ **and** $tlsim2: tlim23\ tl2\ tl3$ **by** *blast*
from $wb12.simulation-silents2[OF\ bisim1\ tr21]$ **obtain** $s1'$
where $tr11: trsys1.silent-moves\ s1\ s1'$ **and** $bisim1': bisim12\ s1'\ s2'$ **by** *blast*
from $wb12.simulation2[OF\ bisim1'\ tr22\ \tau2]$ **obtain** $s1''\ s1'''\ tl1$
where $trsys1.silent-moves\ s1'\ s1''\ trsys1\ s1''\ tl1\ s1'''$
 $\neg \tau move1\ s1''\ tl1\ s1'''\ bisim12\ s1'''\ s2''\ tlim12\ tl1\ tl2$ **by** *blast*
with $tr11\ bisim2'\ tlim2$
show $\exists s1'\ s1''\ tl1. trsys1.silent-moves\ s1\ s1' \wedge trsys1\ s1'\ tl1\ s1'' \wedge \neg \tau move1\ s1'\ tl1\ s1'' \wedge$
 $(bisim12 \circ_B bisim23)\ s1''\ s3' \wedge (tlsim12 \circ_B tlim23)\ tl1\ tl3$
by(*blast intro: rtranclp-trans bisim-composeI*)
next
fix $s1\ s2$
assume $(bisim12 \circ_B bisim23)\ s1\ s2$
thus $\tau trsys.\tau diverge\ trsys1\ \tau move1\ s1 = \tau trsys.\tau diverge\ trsys3\ \tau move3\ s2$
by(*auto simp add: wb12.\tau diverge-bisim-inv wb23.\tau diverge-bisim-inv*)
qed
qed

lemma *bisimulation-bisim-compose*:

$\llbracket bisimulation\ trsys1\ trsys2\ bisim12\ tlim12; bisimulation\ trsys2\ trsys3\ bisim23\ tlim23 \rrbracket$
 $\implies bisimulation\ trsys1\ trsys3\ (bisim-compose\ bisim12\ bisim23)\ (bisim-compose\ tlim12\ tlim23)$

unfolding *delay-bisimulation-conv-bisimulation[symmetric]*

by(*rule delay-bisimulation-diverge-compose*)

lemma *delay-bisimulation-diverge-final-compose*:

fixes $\tau move1\ \tau move2$

assumes $wbisim12: delay-bisimulation-diverge-final\ trsys1\ trsys2\ bisim12\ tlim12\ \tau move1\ \tau move2$
 $final1\ final2$

and $wbisim23: delay-bisimulation-diverge-final\ trsys2\ trsys3\ bisim23\ tlim23\ \tau move2\ \tau move3\ final2$
 $final3$

shows $delay-bisimulation-diverge-final\ trsys1\ trsys3\ (bisim12 \circ_B bisim23)\ (tlsim12 \circ_B tlim23)$

```

 $\tau move1$   $\tau move3$   $final1$   $final3$ 
proof –
  interpret  $trsys1$ :  $\tau trsys$   $trsys1$   $\tau move1$  .
  interpret  $trsys2$ :  $\tau trsys$   $trsys2$   $\tau move2$  .
  interpret  $trsys3$ :  $\tau trsys$   $trsys3$   $\tau move3$  .
  interpret  $wb12$ :  $delay$ - $bisimulation$ - $diverge$ - $final$   $trsys1$   $trsys2$   $bisim12$   $tlsim12$   $\tau move1$   $\tau move2$   $final1$ 
 $final2$ 
    by( $auto$   $intro$ :  $wbisim12$ )
  interpret  $wb23$ :  $delay$ - $bisimulation$ - $diverge$ - $final$   $trsys2$   $trsys3$   $bisim23$   $tlsim23$   $\tau move2$   $\tau move3$   $final2$ 
 $final3$ 
    by( $auto$   $intro$ :  $wbisim23$ )
  interpret  $delay$ - $bisimulation$ - $diverge$   $trsys1$   $trsys3$   $bisim12$   $\circ_B$   $bisim23$   $tlsim12$   $\circ_B$   $tlsim23$   $\tau move1$ 
 $\tau move3$ 
    by( $rule$   $delay$ - $bisimulation$ - $diverge$ - $compose$ )( $unfold$ - $locales$ )
  show  $?thesis$ 
  proof
    fix  $s1$   $s3$ 
    assume ( $bisim12$   $\circ_B$   $bisim23$ )  $s1$   $s3$   $final1$   $s1$ 
    from  $\langle (bisim12 \circ_B bisim23) s1 s3 \rangle$  obtain  $s2$  where  $bisim12$   $s1$   $s2$  and  $bisim23$   $s2$   $s3$  ..
    from  $wb12$ . $final1$ - $simulation$ [ $OF$   $\langle bisim12 s1 s2 \rangle \langle final1 s1 \rangle$ ]
    obtain  $s2'$  where  $trsys2$ . $silent$ - $moves$   $s2$   $s2'$   $bisim12$   $s1$   $s2'$   $final2$   $s2'$  by  $blast$ 
    from  $wb23$ . $simulation$ - $silents1$ [ $OF$   $\langle bisim23 s2 s3 \rangle \langle trsys2.silent$ - $moves$   $s2$   $s2' \rangle$ ]
    obtain  $s3'$  where  $trsys3$ . $silent$ - $moves$   $s3$   $s3'$   $bisim23$   $s2'$   $s3'$  by  $blast$ 
    from  $wb23$ . $final1$ - $simulation$ [ $OF$   $\langle bisim23 s2' s3' \rangle \langle final2 s2' \rangle$ ]
    obtain  $s3''$  where  $trsys3$ . $silent$ - $moves$   $s3'$   $s3''$   $bisim23$   $s2'$   $s3''$   $final3$   $s3''$  by  $blast$ 
    from  $\langle trsys3.silent$ - $moves$   $s3$   $s3' \rangle \langle trsys3.silent$ - $moves$   $s3' s3'' \rangle$ 
    have  $trsys3.silent$ - $moves$   $s3$   $s3''$  by( $rule$   $rtranclp$ - $trans$ )
    moreover from  $\langle bisim12 s1 s2' \rangle \langle bisim23 s2' s3'' \rangle$ 
    have ( $bisim12$   $\circ_B$   $bisim23$ )  $s1$   $s3''$  ..
    ultimately show  $\exists s3'$ .  $trsys3.silent$ - $moves$   $s3$   $s3' \wedge (bisim12 \circ_B bisim23) s1 s3' \wedge final3 s3'$ 
      using  $\langle final3 s3'' \rangle$  by  $iprover$ 
  next
    fix  $s1$   $s3$ 
    assume ( $bisim12$   $\circ_B$   $bisim23$ )  $s1$   $s3$   $final3$   $s3$ 
    from  $\langle (bisim12 \circ_B bisim23) s1 s3 \rangle$  obtain  $s2$  where  $bisim12$   $s1$   $s2$  and  $bisim23$   $s2$   $s3$  ..
    from  $wb23$ . $final2$ - $simulation$ [ $OF$   $\langle bisim23 s2 s3 \rangle \langle final3 s3 \rangle$ ]
    obtain  $s2'$  where  $trsys2$ . $silent$ - $moves$   $s2$   $s2'$   $bisim23$   $s2'$   $s3$   $final2$   $s2'$  by  $blast$ 
    from  $wb12$ . $simulation$ - $silents2$ [ $OF$   $\langle bisim12 s1 s2 \rangle \langle trsys2.silent$ - $moves$   $s2$   $s2' \rangle$ ]
    obtain  $s1'$  where  $trsys1$ . $silent$ - $moves$   $s1$   $s1'$   $bisim12$   $s1'$   $s2'$  by  $blast$ 
    from  $wb12$ . $final2$ - $simulation$ [ $OF$   $\langle bisim12 s1' s2' \rangle \langle final2 s2' \rangle$ ]
    obtain  $s1''$  where  $trsys1$ . $silent$ - $moves$   $s1'$   $s1''$   $bisim12$   $s1''$   $s2'$   $final1$   $s1''$  by  $blast$ 
    from  $\langle trsys1.silent$ - $moves$   $s1$   $s1' \rangle \langle trsys1.silent$ - $moves$   $s1' s1'' \rangle$ 
    have  $trsys1.silent$ - $moves$   $s1$   $s1''$  by( $rule$   $rtranclp$ - $trans$ )
    moreover from  $\langle bisim12 s1'' s2' \rangle \langle bisim23 s2' s3 \rangle$ 
    have ( $bisim12$   $\circ_B$   $bisim23$ )  $s1''$   $s3$  ..
    ultimately show  $\exists s1'$ .  $trsys1.silent$ - $moves$   $s1$   $s1' \wedge (bisim12 \circ_B bisim23) s1' s3 \wedge final1 s1'$ 
      using  $\langle final1 s1'' \rangle$  by  $iprover$ 
  qed
qed
end

```

1.18 Bisimulation relations for the multithreaded semantics

theory *FWBisimulation*

imports

FWLTS

Bisimulation

begin

1.18.1 Definitions for lifting bisimulation relations

primrec *nta-bisim* :: $(t \Rightarrow (x1 \times m1, x2 \times m2) \text{ bisim}) \Rightarrow ((t, x1, m1) \text{ new-thread-action}, (t, x2, m2) \text{ new-thread-action}) \text{ bisim}$

where

$[code\ del]: \text{nta-bisim bisim (NewThread } t \ x \ m) \ ta = (\exists x' \ m'. \ ta = \text{NewThread } t \ x' \ m' \wedge \text{bisim } t \ (x, m) \ (x', m'))$

$| \text{nta-bisim bisim (ThreadExists } t \ b) \ ta = (ta = \text{ThreadExists } t \ b)$

lemma *nta-bisim-1-code* $[code]:$

$\text{nta-bisim bisim (NewThread } t \ x \ m) \ ta = (\text{case } ta \ \text{of } \text{NewThread } t' \ x' \ m' \Rightarrow t = t' \wedge \text{bisim } t \ (x, m) \ (x', m') \ | \ - \Rightarrow \text{False})$

by(*auto split: new-thread-action.split*)

lemma *nta-bisim-simps-sym* $[simp]:$

$\text{nta-bisim bisim } ta \ (\text{NewThread } t \ x \ m) = (\exists x' \ m'. \ ta = \text{NewThread } t \ x' \ m' \wedge \text{bisim } t \ (x', m') \ (x, m))$

$\text{nta-bisim bisim } ta \ (\text{ThreadExists } t \ b) = (ta = \text{ThreadExists } t \ b)$

by(*cases ta, auto*)**+**

definition *ta-bisim* :: $(t \Rightarrow (x1 \times m1, x2 \times m2) \text{ bisim}) \Rightarrow ((l, t, x1, m1, w, o) \text{ thread-action}, (l, t, x2, m2, w, o) \text{ thread-action}) \text{ bisim}$

where

$\text{ta-bisim bisim } ta1 \ ta2 \equiv$

$\{\!\! \{ ta1 \!\!\} \}_l = \{\!\! \{ ta2 \!\!\} \}_l \wedge \{\!\! \{ ta1 \!\!\} \}_w = \{\!\! \{ ta2 \!\!\} \}_w \wedge \{\!\! \{ ta1 \!\!\} \}_c = \{\!\! \{ ta2 \!\!\} \}_c \wedge \{\!\! \{ ta1 \!\!\} \}_o = \{\!\! \{ ta2 \!\!\} \}_o \wedge \{\!\! \{ ta1 \!\!\} \}_i = \{\!\! \{ ta2 \!\!\} \}_i \wedge$

$\text{list-all2 (nta-bisim bisim) } \{\!\! \{ ta1 \!\!\} \}_t \ \{\!\! \{ ta2 \!\!\} \}_t$

lemma *ta-bisim-empty* $[iff]: \text{ta-bisim bisim } \varepsilon \ \varepsilon$

by(*auto simp add: ta-bisim-def*)

lemma *ta-bisim- ε* $[simp]:$

$\text{ta-bisim } b \ \varepsilon \ ta' \iff ta' = \varepsilon \ \text{ta-bisim } b \ ta \ \varepsilon \iff ta = \varepsilon$

apply(*cases ta', fastforce simp add: ta-bisim-def*)

apply(*cases ta, fastforce simp add: ta-bisim-def*)

done

lemma *nta-bisim-mono*:

assumes *major*: $\text{nta-bisim bisim } ta \ ta'$

and *mono*: $\bigwedge t \ s1 \ s2. \ \text{bisim } t \ s1 \ s2 \implies \text{bisim}' \ t \ s1 \ s2$

shows $\text{nta-bisim bisim}' \ ta \ ta'$

using *major* **by**(*cases ta*)(*auto intro: mono*)

lemma *ta-bisim-mono*:

assumes *major*: $\text{ta-bisim bisim } ta1 \ ta2$

and *mono*: $\bigwedge t \ s1 \ s2. \ \text{bisim } t \ s1 \ s2 \implies \text{bisim}' \ t \ s1 \ s2$

shows $\text{ta-bisim bisim}' \ ta1 \ ta2$

using major

by(auto simp add: ta-bisim-def elim!: List.list-all2-mono nta-bisim-mono intro: mono)

lemma nta-bisim-flip [flip-simps]:

nta-bisim ($\lambda t. \text{flip } (\text{bisim } t)$) = flip (nta-bisim bisim)

by(rule ext)(case-tac x, auto simp add: flip-simps)

lemma ta-bisim-flip [flip-simps]:

ta-bisim ($\lambda t. \text{flip } (\text{bisim } t)$) = flip (ta-bisim bisim)

by(auto simp add: fun-eq-iff flip-simps ta-bisim-def)

locale FWbisimulation-base =

r1: multithreaded-base final1 r1 convert-RA +

r2: multithreaded-base final2 r2 convert-RA

for final1 :: 'x1 \Rightarrow bool

and r1 :: ('l, 't, 'x1, 'm1, 'w, 'o) semantics ($\langle \cdot \vdash \cdot - 1 \dashrightarrow \cdot \rangle \rightarrow [50, 0, 0, 50] 80$)

and final2 :: 'x2 \Rightarrow bool

and r2 :: ('l, 't, 'x2, 'm2, 'w, 'o) semantics ($\langle \cdot \vdash \cdot - 2 \dashrightarrow \cdot \rangle \rightarrow [50, 0, 0, 50] 80$)

and convert-RA :: 'l released-locks \Rightarrow 'o list

+

fixes bisim :: 't \Rightarrow ('x1 \times 'm1, 'x2 \times 'm2) bisim ($\langle \cdot \vdash \cdot / \approx \cdot \rangle \rightarrow [50, 50, 50] 60$)

and bisim-wait :: ('x1, 'x2) bisim ($\langle \cdot / \approx_w \cdot \rangle \rightarrow [50, 50] 60$)

begin

notation r1.redT-syntax1 ($\langle \cdot - 1 \dashrightarrow \cdot \rangle \rightarrow [50, 0, 0, 50] 80$)

notation r2.redT-syntax1 ($\langle \cdot - 2 \dashrightarrow \cdot \rangle \rightarrow [50, 0, 0, 50] 80$)

notation r1.RedT ($\langle \cdot - 1 \dashrightarrow \cdot \rangle \rightarrow [50, 0, 50] 80$)

notation r2.RedT ($\langle \cdot - 2 \dashrightarrow \cdot \rangle \rightarrow [50, 0, 50] 80$)

notation r1.must-sync ($\langle \cdot \vdash \langle \cdot / \cdot \rangle / \wr 1 \rangle [50, 0, 0] 81$)

notation r2.must-sync ($\langle \cdot \vdash \langle \cdot / \cdot \rangle / \wr 2 \rangle [50, 0, 0] 81$)

notation r1.can-sync ($\langle \cdot \vdash \langle \cdot / \cdot \rangle / \cdot / \wr 1 \rangle [50, 0, 0, 0] 81$)

notation r2.can-sync ($\langle \cdot \vdash \langle \cdot / \cdot \rangle / \cdot / \wr 2 \rangle [50, 0, 0, 0] 81$)

abbreviation ta-bisim-bisim-syntax ($\langle \cdot / \sim_m \cdot \rangle \rightarrow [50, 50] 60$)

where ta1 \sim_m ta2 \equiv ta-bisim bisim ta1 ta2

definition tbisim :: bool \Rightarrow 't \Rightarrow ('x1 \times 'l released-locks) option \Rightarrow 'm1 \Rightarrow ('x2 \times 'l released-locks)

option \Rightarrow 'm2 \Rightarrow bool where

$\bigwedge ln. \text{tbisim } nw \ t \ ts1 \ m1 \ ts2 \ m2 \longleftrightarrow$

(case ts1 of None \Rightarrow ts2 = None

| [(x1, ln)] \Rightarrow ($\exists x2. ts2 = [(x2, ln)] \wedge t \vdash (x1, m1) \approx (x2, m2) \wedge (nw \vee x1 \approx_w x2)$))

lemma tbisim-NoneI: tbisim w t None m None m'

by(simp add: tbisim-def)

lemma tbisim-SomeI:

$\bigwedge ln. \llbracket t \vdash (x, m) \approx (x', m'); nw \vee x \approx_w x' \rrbracket \Longrightarrow \text{tbisim } nw \ t \ (\text{Some } (x, ln)) \ m \ (\text{Some } (x', ln)) \ m'$

by(simp add: tbisim-def)

lemma tbisim-cases[consumes 1, case-names None Some]:

assumes major: tbisim nw t ts1 m1 ts2 m2

and $\llbracket ts1 = None; ts2 = None \rrbracket \implies thesis$
and $\bigwedge x \ln x'. \llbracket ts1 = \lfloor (x, \ln) \rfloor; ts2 = \lfloor (x', \ln) \rfloor; t \vdash (x, m1) \approx (x', m2); nw \vee x \approx_w x' \rrbracket \implies thesis$
shows *thesis*
using *assms*
by(*auto simp add: tbisim-def*)

definition *mbisim* :: $((l, 't, 'x1, 'm1, 'w)$ state, $(l, 't, 'x2, 'm2, 'w)$ state) *bisim* $(\simeq \approx_m \rightarrow [50, 50] 60)$
where

$s1 \approx_m s2 \equiv$
 $finite (dom (thr s1)) \wedge locks s1 = locks s2 \wedge wset s1 = wset s2 \wedge wset-thread-ok (wset s1) (thr s1)$
 \wedge
 $interrupts s1 = interrupts s2 \wedge$
 $(\forall t. tbisim (wset s2 t = None) t (thr s1 t) (shr s1) (thr s2 t) (shr s2))$

lemma *mbisim-thrNone-eq*: $s1 \approx_m s2 \implies thr s1 t = None \longleftrightarrow thr s2 t = None$

unfolding *mbisim-def tbisim-def*

apply(*clarify*)

apply(*erule allE[where x=t]*)

apply(*clarsimp*)

done

lemma *mbisim-thrD1*:

$\bigwedge \ln. \llbracket s1 \approx_m s2; thr s1 t = \lfloor (x, \ln) \rfloor \rrbracket$
 $\implies \exists x'. thr s2 t = \lfloor (x', \ln) \rfloor \wedge t \vdash (x, shr s1) \approx (x', shr s2) \wedge (wset s1 t = None \vee x \approx_w x')$

by(*fastforce simp add: mbisim-def tbisim-def*)

lemma *mbisim-thrD2*:

$\bigwedge \ln. \llbracket s1 \approx_m s2; thr s2 t = \lfloor (x, \ln) \rfloor \rrbracket$
 $\implies \exists x'. thr s1 t = \lfloor (x', \ln) \rfloor \wedge t \vdash (x', shr s1) \approx (x, shr s2) \wedge (wset s2 t = None \vee x' \approx_w x)$

by(*frule mbisim-thrNone-eq[where t=t](cases thr s1 t, fastforce simp add: mbisim-def tbisim-def)+*)

lemma *mbisim-dom-eq*: $s1 \approx_m s2 \implies dom (thr s1) = dom (thr s2)$

apply(*clarsimp simp add: dom-def fun-eq-iff simp del: not-None-eq*)

apply(*rule Collect-cong*)

apply(*drule mbisim-thrNone-eq*)

apply(*simp del: not-None-eq*)

done

lemma *mbisim-wset-thread-ok1*:

$s1 \approx_m s2 \implies wset-thread-ok (wset s1) (thr s1)$

by(*clarsimp simp add: mbisim-def*)

lemma *mbisim-wset-thread-ok2*:

assumes $s1 \approx_m s2$

shows $wset-thread-ok (wset s2) (thr s2)$

using *assms*

apply(*clarsimp simp add: mbisim-def*)

apply(*auto intro!: wset-thread-okI simp add: mbisim-thrNone-eq[OF assms, THEN sym] dest: wset-thread-okD*)

done

lemma *mbisimI*:

$\llbracket finite (dom (thr s1)); locks s1 = locks s2; wset s1 = wset s2; interrupts s1 = interrupts s2;$
 $wset-thread-ok (wset s1) (thr s1);$

$\bigwedge t. thr s1 t = None \implies thr s2 t = None;$

$\wedge t \ x1 \ ln. \ thr \ s1 \ t = [(x1, ln)] \implies \exists x2. \ thr \ s2 \ t = [(x2, ln)] \wedge t \vdash (x1, shr \ s1) \approx (x2, shr \ s2)$
 $\wedge (wset \ s2 \ t = None \vee x1 \approx_w x2) \]$
 $\implies s1 \approx_m s2$
by(*fastforce simp add: mbisim-def tbisim-def*)

lemma *mbisimI2*:

$\llbracket \text{finite} \ (\text{dom} \ (\text{thr} \ s2)); \text{locks} \ s1 = \text{locks} \ s2; \text{wset} \ s1 = \text{wset} \ s2; \text{interrupts} \ s1 = \text{interrupts} \ s2;$
 $\text{wset-thread-ok} \ (\text{wset} \ s2) \ (\text{thr} \ s2);$
 $\wedge t. \ thr \ s2 \ t = None \implies \text{thr} \ s1 \ t = None;$
 $\wedge t \ x2 \ ln. \ thr \ s2 \ t = [(x2, ln)] \implies \exists x1. \ thr \ s1 \ t = [(x1, ln)] \wedge t \vdash (x1, shr \ s1) \approx (x2, shr \ s2)$
 $\wedge (wset \ s2 \ t = None \vee x1 \approx_w x2) \]$
 $\implies s1 \approx_m s2$

apply(*auto simp add: mbisim-def tbisim-def*)

prefer 2
apply(*rule wset-thread-okI*)
apply(*case-tac thr s2 t*)
apply(*auto dest!: wset-thread-okD*)[1]
apply *fastforce*
apply(*erule back-subst[where P=finite]*)
apply(*clarsimp simp add: dom-def fun-eq-iff simp del: not-None-eq*)
defer
apply(*rename-tac t*)
apply(*case-tac [!] thr s2 t*)
by *fastforce+*

lemma *mbisim-finite1*:

$s1 \approx_m s2 \implies \text{finite} \ (\text{dom} \ (\text{thr} \ s1))$
by(*simp add: mbisim-def*)

lemma *mbisim-finite2*:

$s1 \approx_m s2 \implies \text{finite} \ (\text{dom} \ (\text{thr} \ s2))$
by(*frule mbisim-finite1*)(*simp add: mbisim-dom-eq*)

definition *mta-bisim* :: $(t \times (l, t, x1, m1, w, o) \text{ thread-action},$
 $t \times (l, t, x2, m2, w, o) \text{ thread-action}) \text{ bisim}$
 $(\langle - / \sim T \rightarrow [50, 50] \ 60)$

where $tta1 \sim T tta2 \equiv \text{fst} \ tta1 = \text{fst} \ tta2 \wedge \text{snd} \ tta1 \sim_m \text{snd} \ tta2$

lemma *mta-bisim-conv* [*simp*]: $(t, ta1) \sim T (t', ta2) \longleftrightarrow t = t' \wedge ta1 \sim_m ta2$

by(*simp add: mta-bisim-def*)

definition *bisim-inv* :: *bool where*

$\text{bisim-inv} \equiv (\forall s1 \ ta1 \ s1' \ s2 \ t. \ t \vdash s1 \approx s2 \longrightarrow t \vdash s1 \ -1-ta1 \rightarrow s1' \longrightarrow (\exists s2'. \ t \vdash s1' \approx s2')) \wedge$
 $(\forall s2 \ ta2 \ s2' \ s1 \ t. \ t \vdash s1 \approx s2 \longrightarrow t \vdash s2 \ -2-ta2 \rightarrow s2' \longrightarrow (\exists s1'. \ t \vdash s1' \approx s2'))$

lemma *bisim-invI*:

$\llbracket \wedge s1 \ ta1 \ s1' \ s2 \ t. \llbracket t \vdash s1 \approx s2; \ t \vdash s1 \ -1-ta1 \rightarrow s1' \rrbracket \implies \exists s2'. \ t \vdash s1' \approx s2';$
 $\wedge s2 \ ta2 \ s2' \ s1 \ t. \llbracket t \vdash s1 \approx s2; \ t \vdash s2 \ -2-ta2 \rightarrow s2' \rrbracket \implies \exists s1'. \ t \vdash s1' \approx s2' \rrbracket$
 $\implies \text{bisim-inv}$

by(*auto simp add: bisim-inv-def*)

lemma *bisim-invD1*:

$\llbracket \text{bisim-inv}; \ t \vdash s1 \approx s2; \ t \vdash s1 \ -1-ta1 \rightarrow s1' \rrbracket \implies \exists s2'. \ t \vdash s1' \approx s2'$
unfolding *bisim-inv-def* **by** *blast*

lemma *bisim-invD2*:

$\llbracket \text{bisim-inv}; t \vdash s1 \approx s2; t \vdash s2 \text{ --}2\text{--}ta2 \rightarrow s2' \rrbracket \implies \exists s1'. t \vdash s1' \approx s2'$
unfolding *bisim-inv-def* **by** *blast*

lemma *thread-oks-bisim-inv*:

$\llbracket \forall t. ts1\ t = \text{None} \longleftrightarrow ts2\ t = \text{None}; \text{list-all2}\ (nta\text{-bisim}\ \text{bisim})\ tas1\ tas2 \rrbracket$
 $\implies \text{thread-oks}\ tas1\ tas1 \longleftrightarrow \text{thread-oks}\ ts2\ tas2$

proof(*induct tas2 arbitrary: tas1 ts1 ts2*)

case Nil thus ?case by(*simp*)

next

case (*Cons ta2 TAS2 tas1 TS1 TS2*)

note $IH = \langle \bigwedge ts1\ tas1\ ts2. \llbracket \forall t. ts1\ t = \text{None} \longleftrightarrow ts2\ t = \text{None}; \text{list-all2}\ (nta\text{-bisim}\ \text{bisim})\ tas1\ TAS2 \rrbracket$

$\implies \text{thread-oks}\ tas1\ tas1 \longleftrightarrow \text{thread-oks}\ ts2\ TAS2 \rangle$

note $eqNone = \langle \forall t. TS1\ t = \text{None} \longleftrightarrow TS2\ t = \text{None} \rangle$ [*rule-format*]

hence *fti*: *free-thread-id* $TS1 = \text{free-thread-id}\ TS2$ **by**(*auto simp add: free-thread-id-def*)

from $\langle \text{list-all2}\ (nta\text{-bisim}\ \text{bisim})\ tas1\ (ta2\ \# TAS2) \rangle$

obtain $ta1\ TAS1$ **where** $tas1 = ta1\ \# TAS1$ *nta-bisim bisim ta1 ta2 list-all2 (nta-bisim bisim) TAS1 TAS2*

by(*auto simp add: list-all2-Cons2*)

moreover

{ **fix** t

from $\langle nta\text{-bisim}\ \text{bisim}\ ta1\ ta2 \rangle$ **have** $\text{redT-updT}'\ TS1\ ta1\ t = \text{None} \longleftrightarrow \text{redT-updT}'\ TS2\ ta2\ t = \text{None}$

by(*cases ta1, auto split: if-split-asm simp add: eqNone*) }

ultimately have $\text{thread-oks}\ (\text{redT-updT}'\ TS1\ ta1)\ TAS1 \longleftrightarrow \text{thread-oks}\ (\text{redT-updT}'\ TS2\ ta2)\ TAS2$

by $-(\text{rule}\ IH, \text{auto})$

moreover from $\langle nta\text{-bisim}\ \text{bisim}\ ta1\ ta2 \rangle$ *fti* **have** $\text{thread-ok}\ TS1\ ta1 = \text{thread-ok}\ TS2\ ta2$ **by**(*cases ta1, auto*)

ultimately show *?case using* $\langle tas1 = ta1\ \# TAS1 \rangle$ **by** *auto*

qed

lemma *redT-updT-nta-bisim-inv*:

$\llbracket nta\text{-bisim}\ \text{bisim}\ ta1\ ta2; ts1\ T = \text{None} \longleftrightarrow ts2\ T = \text{None} \rrbracket \implies \text{redT-updT}\ ts1\ ta1\ T = \text{None} \longleftrightarrow \text{redT-updT}\ ts2\ ta2\ T = \text{None}$

by(*cases ta1, auto*)

lemma *redT-updT-nts-nta-bisim-inv*:

$\llbracket \text{list-all2}\ (nta\text{-bisim}\ \text{bisim})\ tas1\ tas2; ts1\ T = \text{None} \longleftrightarrow ts2\ T = \text{None} \rrbracket$
 $\implies \text{redT-updT}s\ ts1\ tas1\ T = \text{None} \longleftrightarrow \text{redT-updT}s\ ts2\ tas2\ T = \text{None}$

proof(*induct tas1 arbitrary: tas2 ts1 ts2*)

case Nil thus ?case by(*simp*)

next

case (*Cons TA1 TAS1 tas2 TS1 TS2*)

note $IH = \langle \bigwedge tas2\ ts1\ ts2. \llbracket \text{list-all2}\ (nta\text{-bisim}\ \text{bisim})\ TAS1\ tas2; (ts1\ T = \text{None}) = (ts2\ T = \text{None}) \rrbracket$

$\implies (\text{redT-updT}s\ ts1\ TAS1\ T = \text{None}) = (\text{redT-updT}s\ ts2\ tas2\ T = \text{None}) \rangle$

from $\langle \text{list-all2}\ (nta\text{-bisim}\ \text{bisim})\ (TA1\ \# TAS1)\ tas2 \rangle$

obtain $TA2\ TAS2$ **where** $tas2 = TA2\ \# TAS2$ *nta-bisim bisim TA1 TA2 list-all2 (nta-bisim bisim) TAS1 TAS2*

by(*auto simp add: list-all2-Cons1*)

from $\langle nta\text{-bisim}\ \text{bisim}\ TA1\ TA2 \rangle$ $\langle (TS1\ T = \text{None}) = (TS2\ T = \text{None}) \rangle$


```

have redT-updT TS1 TA1 T = None  $\longleftrightarrow$  redT-updT TS2 TA2 T = None
  by(rule redT-updT-nta-bisim-inv)
with IH[OF ‹list-all2 (nta-bisim bisim) TAS1 TAS2›, of redT-updT TS1 TA1 redT-updT TS2 TA2]
‹tas2 = TA2 # TAS2›
  show ?case by simp
qed

```

end

lemma *tbisim-flip* [flip-simps]:

```

FWbisimulation-base.tbisim ( $\lambda t$ . flip (bisim t)) (flip bisim-wait) w t ts2 m2 ts1 m1 =
FWbisimulation-base.tbisim bisim bisim-wait w t ts1 m1 ts2 m2

```

unfolding FWbisimulation-base.tbisim-def flip-simps **by** auto

lemma *mbisim-flip* [flip-simps]:

```

FWbisimulation-base.mbisim ( $\lambda t$ . flip (bisim t)) (flip bisim-wait) s2 s1 =
FWbisimulation-base.mbisim bisim bisim-wait s1 s2

```

apply(rule iffI)

apply(frule FWbisimulation-base.mbisim-dom-eq)

apply(frule FWbisimulation-base.mbisim-wset-thread-ok2)

apply(fastforce simp add: FWbisimulation-base.mbisim-def flip-simps)

apply(frule FWbisimulation-base.mbisim-dom-eq)

apply(frule FWbisimulation-base.mbisim-wset-thread-ok2)

apply(fastforce simp add: FWbisimulation-base.mbisim-def flip-simps)

done

lemma *mta-bisim-flip* [flip-simps]:

```

FWbisimulation-base.mta-bisim ( $\lambda t$ . flip (bisim t)) = flip (FWbisimulation-base.mta-bisim bisim)

```

by(auto simp add: fun-eq-iff flip-simps FWbisimulation-base.mta-bisim-def)

lemma *flip-const* [simp]: flip (λa b. c) = (λa b. c)

by(rule flip-def)

lemma *mbisim-K-flip* [flip-simps]:

```

FWbisimulation-base.mbisim ( $\lambda t$ . flip (bisim t)) ( $\lambda x1$  x2. c) s1 s2 =
FWbisimulation-base.mbisim bisim ( $\lambda x1$  x2. c) s2 s1

```

using mbisim-flip[of bisim $\lambda x1$ x2. c s1 s2]

unfolding flip-const .

context FWbisimulation-base **begin**

lemma *mbisim-actions-ok-bisim-no-join-12*:

assumes mbisim: mbisim s1 s2

and collect-cond-actions $\{ta1\}_c = \{\}$

and ta-bisim bisim ta1 ta2

and r1.actions-ok s1 t ta1

shows r2.actions-ok s2 t ta2

using assms mbisim-thrNone-eq[OF mbisim]

by(auto simp add: ta-bisim-def mbisim-def intro: thread-oks-bisim-inv[THEN iffD1] r2.may-join-cond-action-oks)

lemma *mbisim-actions-ok-bisim-no-join-21*:

\llbracket mbisim s1 s2; collect-cond-actions $\{ta2\}_c = \{\}$; ta-bisim bisim ta1 ta2; r2.actions-ok s2 t ta2 \rrbracket

\implies r1.actions-ok s1 t ta1

using FWbisimulation-base.mbisim-actions-ok-bisim-no-join-12[**where** bisim= λt . flip (bisim t) **and**

bisim-wait=flip bisim-wait
unfolding *flip-simps* .

lemma *mbisim-actions-ok-bisim-no-join*:

\llbracket *mbisim s1 s2; collect-cond-actions* $\{ta1\}_c = \{\}$; *ta-bisim bisim ta1 ta2* \rrbracket
 $\implies r1.actions-ok\ s1\ t\ ta1 = r2.actions-ok\ s2\ t\ ta2$

apply(*rule iffI*)

apply(*erule* (3) *mbisim-actions-ok-bisim-no-join-12*)

apply(*erule* *mbisim-actions-ok-bisim-no-join-21* [**where** *?ta2.0 = ta2*])

apply(*simp add: ta-bisim-def*)

apply *assumption+*

done

end

locale *FWbisimulation-base-aux* = *FWbisimulation-base* +

r1: multithreaded final1 r1 convert-RA +

r2: multithreaded final2 r2 convert-RA +

constrains *final1* :: *'x1* \implies *bool*

and *r1* :: (*'l, 't, 'x1, 'm1, 'w, 'o*) *semantics*

and *final2* :: *'x2* \implies *bool*

and *r2* :: (*'l, 't, 'x2, 'm2, 'w, 'o*) *semantics*

and *convert-RA* :: *'l* *released-locks* \implies *'o* *list*

and *bisim* :: *'t* \implies (*'x1* \times *'m1, 'x2* \times *'m2*) *bisim*

and *bisim-wait* :: (*'x1, 'x2*) *bisim*

begin

lemma *FWbisimulation-base-aux-flip*:

FWbisimulation-base-aux final2 r2 final1 r1

by(*unfold-locales*)

end

lemma *FWbisimulation-base-aux-flip-simps* [*flip-simps*]:

FWbisimulation-base-aux final2 r2 final1 r1 = FWbisimulation-base-aux final1 r1 final2 r2

by(*blast intro: FWbisimulation-base-aux.FWbisimulation-base-aux-flip*)

sublocale *FWbisimulation-base-aux* < *mthr*:

bisimulation-final-base

r1.redT

r2.redT

mbisim

mta-bisim

r1.mfinal

r2.mfinal

.

declare *split-paired-Ex* [*simp del*]

1.18.2 Lifting for delay bisimulations

locale *FWdelay-bisimulation-base* =

FWbisimulation-base - - - *r2 convert-RA bisim bisim-wait* +

r1: τ multithreaded final1 r1 convert-RA τ move1 +

r2: τ multithreaded final2 *r2* convert-RA τ move2
for *r2* :: ('l,'t,'x2,'m2,'w,'o) semantics ($\langle \vdash - -2 \dashrightarrow \rightarrow [50,0,0,50] 80$)
and convert-RA :: 'l released-locks \Rightarrow 'o list
and bisim :: 't \Rightarrow ('x1 \times 'm1, 'x2 \times 'm2) bisim ($\langle \vdash - / \approx \rightarrow [50, 50, 50] 60$)
and bisim-wait :: ('x1, 'x2) bisim ($\langle \vdash / \approx w \rightarrow [50, 50] 60$)
and τ move1 :: ('l,'t,'x1,'m1,'w,'o) τ moves
and τ move2 :: ('l,'t,'x2,'m2,'w,'o) τ moves
begin

abbreviation τ mred1 :: ('l,'t,'x1,'m1,'w) state \Rightarrow ('l,'t,'x1,'m1,'w) state \Rightarrow bool
where τ mred1 \equiv r1. τ mredT

abbreviation τ mred2 :: ('l,'t,'x2,'m2,'w) state \Rightarrow ('l,'t,'x2,'m2,'w) state \Rightarrow bool
where τ mred2 \equiv r2. τ mredT

abbreviation $m\tau$ move1 :: (('l,'t,'x1,'m1,'w) state, 't \times ('l,'t,'x1,'m1,'w,'o) thread-action) trsys
where $m\tau$ move1 \equiv r1. $m\tau$ move

abbreviation $m\tau$ move2 :: (('l,'t,'x2,'m2,'w) state, 't \times ('l,'t,'x2,'m2,'w,'o) thread-action) trsys
where $m\tau$ move2 \equiv r2. $m\tau$ move

abbreviation τ mRed1 :: ('l,'t,'x1,'m1,'w) state \Rightarrow ('l,'t,'x1,'m1,'w) state \Rightarrow bool
where τ mRed1 \equiv τ mred1 $\hat{\sim}^{**}$

abbreviation τ mRed2 :: ('l,'t,'x2,'m2,'w) state \Rightarrow ('l,'t,'x2,'m2,'w) state \Rightarrow bool
where τ mRed2 \equiv τ mred2 $\hat{\sim}^{**}$

abbreviation τ mtRed1 :: ('l,'t,'x1,'m1,'w) state \Rightarrow ('l,'t,'x1,'m1,'w) state \Rightarrow bool
where τ mtRed1 \equiv τ mred1 $\hat{\sim}^{++}$

abbreviation τ mtRed2 :: ('l,'t,'x2,'m2,'w) state \Rightarrow ('l,'t,'x2,'m2,'w) state \Rightarrow bool
where τ mtRed2 \equiv τ mred2 $\hat{\sim}^{++}$

lemma bisim-inv- τ s1-inv:

assumes inv: bisim-inv

and bisim: $t \vdash s1 \approx s2$

and red: r1.silent-moves $t s1 s1'$

obtains $s2'$ **where** $t \vdash s1' \approx s2'$

proof(atomize-elim)

from red bisim **show** $\exists s2'. t \vdash s1' \approx s2'$

by(induct rule: rtranclp-induct)(fastforce elim: bisim-invD1[OF inv])+

qed

lemma bisim-inv- τ s2-inv:

assumes inv: bisim-inv

and bisim: $t \vdash s1 \approx s2$

and red: r2.silent-moves $t s2 s2'$

obtains $s1'$ **where** $t \vdash s1' \approx s2'$

proof(atomize-elim)

from red bisim **show** $\exists s1'. t \vdash s1' \approx s2'$

by(induct rule: rtranclp-induct)(fastforce elim: bisim-invD2[OF inv])+

qed

primrec activate-cond-action1 :: ('l,'t,'x1,'m1,'w) state \Rightarrow ('l,'t,'x2,'m2,'w) state \Rightarrow

$'t$ conditional-action $\Rightarrow ('l, 't, 'x1, 'm1, 'w)$ state

where

activate-cond-action1 $s1$ $s2$ (Join t) =
 (case thr $s1$ t of None $\Rightarrow s1$
 | $[(x1, ln1)] \Rightarrow$ (case thr $s2$ t of None $\Rightarrow s1$
 | $[(x2, ln2)] \Rightarrow$
 if final2 $x2 \wedge ln2 =$ no-wait-locks
 then redT-upd- ε $s1$ t
 (SOME $x1'$. r1.silent-moves t ($x1, shr$ $s1$) ($x1', shr$ $s1$) \wedge final1 $x1' \wedge$
 $t \vdash (x1', shr$ $s1) \approx (x2, shr$ $s2)$)
 (shr $s1$)
 else $s1$))

| activate-cond-action1 $s1$ $s2$ Yield = $s1$

primrec activate-cond-actions1 :: $('l, 't, 'x1, 'm1, 'w)$ state $\Rightarrow ('l, 't, 'x2, 'm2, 'w)$ state
 $\Rightarrow ('t$ conditional-action) list $\Rightarrow ('l, 't, 'x1, 'm1, 'w)$ state

where

activate-cond-actions1 $s1$ $s2$ [] = $s1$

| activate-cond-actions1 $s1$ $s2$ (ct # cts) = activate-cond-actions1 (activate-cond-action1 $s1$ $s2$ ct) $s2$
 cts

primrec activate-cond-action2 :: $('l, 't, 'x1, 'm1, 'w)$ state $\Rightarrow ('l, 't, 'x2, 'm2, 'w)$ state \Rightarrow
 $'t$ conditional-action $\Rightarrow ('l, 't, 'x2, 'm2, 'w)$ state

where

activate-cond-action2 $s1$ $s2$ (Join t) =
 (case thr $s2$ t of None $\Rightarrow s2$
 | $[(x2, ln2)] \Rightarrow$ (case thr $s1$ t of None $\Rightarrow s2$
 | $[(x1, ln1)] \Rightarrow$
 if final1 $x1 \wedge ln1 =$ no-wait-locks
 then redT-upd- ε $s2$ t
 (SOME $x2'$. r2.silent-moves t ($x2, shr$ $s2$) ($x2', shr$ $s2$) \wedge final2 $x2' \wedge$
 $t \vdash (x1, shr$ $s1) \approx (x2', shr$ $s2)$)
 (shr $s2$)
 else $s2$))

| activate-cond-action2 $s1$ $s2$ Yield = $s2$

primrec activate-cond-actions2 :: $('l, 't, 'x1, 'm1, 'w)$ state $\Rightarrow ('l, 't, 'x2, 'm2, 'w)$ state \Rightarrow
 $(t$ conditional-action) list $\Rightarrow ('l, 't, 'x2, 'm2, 'w)$ state

where

activate-cond-actions2 $s1$ $s2$ [] = $s2$

| activate-cond-actions2 $s1$ $s2$ (ct # cts) = activate-cond-actions2 $s1$ (activate-cond-action2 $s1$ $s2$ ct)
 cts

end

lemma activate-cond-action1-flip [flip-simps]:

FWdelay-bisimulation-base.activate-cond-action1 final2 $r2$ final1 ($\lambda t.$ flip (bisim t)) τ move2 $s2$ $s1$ =

FWdelay-bisimulation-base.activate-cond-action2 final1 final2 $r2$ bisim τ move2 $s1$ $s2$

apply(rule ext)

apply(case-tac x)

apply(simp-all only: FWdelay-bisimulation-base.activate-cond-action1.simps

FWdelay-bisimulation-base.activate-cond-action2.simps flip-simps)

done

lemma *activate-cond-actions1-flip* [*flip-simps*]:
FWdelay-bisimulation-base.activate-cond-actions1 final2 r2 final1 ($\lambda t. \text{flip } (\text{bisim } t)$) $\tau \text{move2 } s2 s1$
 =
FWdelay-bisimulation-base.activate-cond-actions2 final1 final2 r2 bisim $\tau \text{move2 } s1 s2$
 (**is** *?lhs = ?rhs*)
proof(*rule ext*)
fix *xs*
show *?lhs xs = ?rhs xs*
by(*induct xs arbitrary: s2*)
 (*simp-all only: FWdelay-bisimulation-base.activate-cond-actions1.simps*
FWdelay-bisimulation-base.activate-cond-actions2.simps flip-simps)
qed

lemma *activate-cond-action2-flip* [*flip-simps*]:
FWdelay-bisimulation-base.activate-cond-action2 final2 final1 r1 ($\lambda t. \text{flip } (\text{bisim } t)$) $\tau \text{move1 } s2 s1$ =
FWdelay-bisimulation-base.activate-cond-action1 final1 r1 final2 bisim $\tau \text{move1 } s1 s2$
apply(*rule ext*)
apply(*case-tac x*)
apply(*simp-all only: FWdelay-bisimulation-base.activate-cond-action1.simps*
FWdelay-bisimulation-base.activate-cond-action2.simps flip-simps)
done

lemma *activate-cond-actions2-flip* [*flip-simps*]:
FWdelay-bisimulation-base.activate-cond-actions2 final2 final1 r1 ($\lambda t. \text{flip } (\text{bisim } t)$) $\tau \text{move1 } s2 s1$
 =
FWdelay-bisimulation-base.activate-cond-actions1 final1 r1 final2 bisim $\tau \text{move1 } s1 s2$
 (**is** *?lhs = ?rhs*)
proof(*rule ext*)
fix *xs*
show *?lhs xs = ?rhs xs*
by(*induct xs arbitrary: s1*)
 (*simp-all only: FWdelay-bisimulation-base.activate-cond-actions1.simps*
FWdelay-bisimulation-base.activate-cond-actions2.simps flip-simps)
qed

context *FWdelay-bisimulation-base* **begin**

lemma *shr-activate-cond-action1* [*simp*]: *shr (activate-cond-action1 s1 s2 ct) = shr s1*
by(*cases ct*) *simp-all*

lemma *shr-activate-cond-actions1* [*simp*]: *shr (activate-cond-actions1 s1 s2 cts) = shr s1*
by(*induct cts arbitrary: s1*) *auto*

lemma *shr-activate-cond-action2* [*simp*]: *shr (activate-cond-action2 s1 s2 ct) = shr s2*
by(*cases ct*) *simp-all*

lemma *shr-activate-cond-actions2* [*simp*]: *shr (activate-cond-actions2 s1 s2 cts) = shr s2*
by(*induct cts arbitrary: s2*) *auto*

lemma *locks-activate-cond-action1* [*simp*]: *locks (activate-cond-action1 s1 s2 ct) = locks s1*
by(*cases ct*) *simp-all*

lemma *locks-activate-cond-actions1* [*simp*]: *locks (activate-cond-actions1 s1 s2 cts) = locks s1*
by(*induct cts arbitrary: s1*) *auto*

lemma *locks-activate-cond-action2* [simp]: *locks (activate-cond-action2 s1 s2 ct) = locks s2*
by(cases ct) simp-all

lemma *locks-activate-cond-actions2* [simp]: *locks (activate-cond-actions2 s1 s2 cts) = locks s2*
by(induct cts arbitrary: s2) auto

lemma *wset-activate-cond-action1* [simp]: *wset (activate-cond-action1 s1 s2 ct) = wset s1*
by(cases ct) simp-all

lemma *wset-activate-cond-actions1* [simp]: *wset (activate-cond-actions1 s1 s2 cts) = wset s1*
by(induct cts arbitrary: s1) auto

lemma *wset-activate-cond-action2* [simp]: *wset (activate-cond-action2 s1 s2 ct) = wset s2*
by(cases ct) simp-all

lemma *wset-activate-cond-actions2* [simp]: *wset (activate-cond-actions2 s1 s2 cts) = wset s2*
by(induct cts arbitrary: s2) auto

lemma *interrupts-activate-cond-action1* [simp]: *interrupts (activate-cond-action1 s1 s2 ct) = interrupts s1*
by(cases ct) simp-all

lemma *interrupts-activate-cond-actions1* [simp]: *interrupts (activate-cond-actions1 s1 s2 cts) = interrupts s1*
by(induct cts arbitrary: s1) auto

lemma *interrupts-activate-cond-action2* [simp]: *interrupts (activate-cond-action2 s1 s2 ct) = interrupts s2*
by(cases ct) simp-all

lemma *interrupts-activate-cond-actions2* [simp]: *interrupts (activate-cond-actions2 s1 s2 cts) = interrupts s2*
by(induct cts arbitrary: s2) auto

end

locale *FWdelay-bisimulation-lift-aux* =
FWdelay-bisimulation-base - - - - - τ move1 τ move2 +
r1: τ multithreaded-wf *final1* *r1* *convert-RA* τ move1 +
r2: τ multithreaded-wf *final2* *r2* *convert-RA* τ move2
for τ move1 :: (*l*,*t*,*x1*,*m1*,*w*,*o*) τ moves
and τ move2 :: (*l*,*t*,*x2*,*m2*,*w*,*o*) τ moves
begin

lemma *FWdelay-bisimulation-lift-aux-flip*:
FWdelay-bisimulation-lift-aux *final2* *r2* *final1* *r1* τ move2 τ move1
by *unfold-locales*

end

lemma *FWdelay-bisimulation-lift-aux-flip-simps* [*flip-simps*]:
FWdelay-bisimulation-lift-aux *final2* *r2* *final1* *r1* τ move2 τ move1 =
FWdelay-bisimulation-lift-aux *final1* *r1* *final2* *r2* τ move1 τ move2

by(auto dest: FWdelay-bisimulation-lift-aux.FWdelay-bisimulation-lift-aux-flip simp only: flip-flip)

context FWdelay-bisimulation-lift-aux begin

lemma cond-actions-ok- τ mred1-inv:

assumes red: τ mred1 s1 s1'

and ct: r1.cond-action-ok s1 t ct

shows r1.cond-action-ok s1' t ct

using ct

proof(cases ct)

case (Join t')

show ?thesis using red ct

proof(cases thr s1 t')

case None with red ct Join show ?thesis

by(fastforce elim!: r1.mthr.silent-move.cases r1.redT.cases r1.m τ move.cases rtrancl3p-cases
dest: r1.silent-tl split: if-split-asm)

next

case (Some a) with red ct Join show ?thesis

by(fastforce elim!: r1.mthr.silent-move.cases r1.redT.cases r1.m τ move.cases rtrancl3p-cases
dest: r1.silent-tl r1.final-no-red split: if-split-asm simp add: redT-updWs-def)

qed

next

case Yield thus ?thesis by simp

qed

lemma cond-actions-ok- τ mred2-inv:

$\llbracket \tau$ mred2 s2 s2'; r2.cond-action-ok s2 t ct $\rrbracket \implies$ r2.cond-action-ok s2' t ct

using FWdelay-bisimulation-lift-aux.cond-actions-ok- τ mred1-inv[OF FWdelay-bisimulation-lift-aux-flip]

.

lemma cond-actions-ok- τ mRed1-inv:

$\llbracket \tau$ mRed1 s1 s1'; r1.cond-action-ok s1 t ct $\rrbracket \implies$ r1.cond-action-ok s1' t ct

by(induct rule: rtranclp-induct)(blast intro: cond-actions-ok- τ mred1-inv)+

lemma cond-actions-ok- τ mRed2-inv:

$\llbracket \tau$ mRed2 s2 s2'; r2.cond-action-ok s2 t ct $\rrbracket \implies$ r2.cond-action-ok s2' t ct

by(rule FWdelay-bisimulation-lift-aux.cond-actions-ok- τ mRed1-inv[OF FWdelay-bisimulation-lift-aux-flip])

end

locale FWdelay-bisimulation-lift =

FWdelay-bisimulation-lift-aux +

constrains final1 :: 'x1 \Rightarrow bool

and r1 :: ('l, 't, 'x1, 'm1, 'w, 'o) semantics

and final2 :: 'x2 \Rightarrow bool

and r2 :: ('l, 't, 'x2, 'm2, 'w, 'o) semantics

and convert-RA :: 'l released-locks \Rightarrow 'o list

and bisim :: 't \Rightarrow ('x1 \times 'm1, 'x2 \times 'm2) bisim

and bisim-wait :: ('x1, 'x2) bisim

and τ move1 :: ('l, 't, 'x1, 'm1, 'w, 'o) τ moves

and τ move2 :: ('l, 't, 'x2, 'm2, 'w, 'o) τ moves

assumes τ inv-locale: τ inv (r1 t) (r2 t) (bisim t) (ta-bisim bisim) τ move1 τ move2

sublocale FWdelay-bisimulation-lift < τ inv r1 t r2 t bisim t ta-bisim bisim τ move1 τ move2 for t

by(rule τ inv-locale)

context *FWdelay-bisimulation-lift* begin

lemma *FWdelay-bisimulation-lift-flip*:

FWdelay-bisimulation-lift final2 r2 final1 r1 ($\lambda t. \text{flip } (\text{bisim } t) \tau \text{move2 } \tau \text{move1}$)
 apply(rule *FWdelay-bisimulation-lift.intro*)
 apply(rule *FWdelay-bisimulation-lift-aux-flip*)
 apply(rule *FWdelay-bisimulation-lift-axioms.intro*)
 apply(unfold *flip-simps*)
 apply(unfold-locale)
 done

end

lemma *FWdelay-bisimulation-lift-flip-simps* [*flip-simps*]:

FWdelay-bisimulation-lift final2 r2 final1 r1 ($\lambda t. \text{flip } (\text{bisim } t) \tau \text{move2 } \tau \text{move1} =$
FWdelay-bisimulation-lift final1 r1 final2 r2 bisim $\tau \text{move1 } \tau \text{move2}$)
 by(auto dest: *FWdelay-bisimulation-lift.FWdelay-bisimulation-lift-flip simp only: flip-flip*)

context *FWdelay-bisimulation-lift* begin

lemma τ inv-lift: τ inv *r1.redT r2.redT mbisim mta-bisim m* τ move1 m τ move2

proof

fix *s1 s2 tl1 s1' tl2 s2'*
 assume *s1* \approx_m *s2 s1'* \approx_m *s2' tl1* \sim_T *tl2 r1.redT s1 tl1 s1' r2.redT s2 tl2 s2'*
 moreover obtain *t ta1* where *tl1*: *tl1* = (*t, ta1*) by(cases *tl1*)
 moreover obtain *t' ta2* where *tl2*: *tl2* = (*t', ta2*) by(cases *tl2*)
 moreover obtain *ls1 ts1 ws1 m1 is1* where *s1*: *s1* = (*ls1, (ts1, m1), ws1, is1*) by(cases *s1*)
 fastforce
 moreover obtain *ls2 ts2 ws2 m2 is2* where *s2*: *s2* = (*ls2, (ts2, m2), ws2, is2*) by(cases *s2*)
 fastforce
 moreover obtain *ls1' ts1' ws1' m1' is1'* where *s1'*: *s1'* = (*ls1', (ts1', m1'), ws1', is1'*) by(cases *s1'*)
 fastforce
 moreover obtain *ls2' ts2' ws2' m2' is2'* where *s2'*: *s2'* = (*ls2', (ts2', m2'), ws2', is2'*) by(cases *s2'*)
 fastforce
 ultimately have *mbisim*: (*ls1, (ts1, m1), ws1, is1*) \approx_m (*ls2, (ts2, m2), ws2, is2*)
 and *mbisim'*: (*ls1', (ts1', m1'), ws1', is1'*) \approx_m (*ls2', (ts2', m2'), ws2', is2'*)
 and *mred1*: (*ls1, (ts1, m1), ws1, is1*) $-1-t \triangleright ta1 \rightarrow$ (*ls1', (ts1', m1'), ws1', is1'*)
 and *mred2*: (*ls2, (ts2, m2), ws2, is2*) $-2-t \triangleright ta2 \rightarrow$ (*ls2', (ts2', m2'), ws2', is2'*)
 and *tasim*: *ta1* \sim_m *ta2* and *tt'*: *t' = t* by *simp-all*
 from *mbisim* have *ls*: *ls1* = *ls2* and *ws*: *ws1* = *ws2* and *is*: *is1* = *is2*
 and *tbisim*: $\bigwedge t. \text{tbisim } (\text{ws2 } t = \text{None}) t (\text{ts1 } t) m1 (\text{ts2 } t) m2$ by(*simp-all add: mbisim-def*)
 from *mbisim'* have *ls'*: *ls1'* = *ls2'* and *ws'*: *ws1'* = *ws2'* and *is'*: *is1'* = *is2'*
 and *tbisim'*: $\bigwedge t. \text{tbisim } (\text{ws2}' t = \text{None}) t (\text{ts1}' t) m1' (\text{ts2}' t) m2'$ by(*simp-all add: mbisim-def*)
 from *mred1* *r1.redT-thread-not-disappear*[*OF mred1*]
 obtain *x1 ln1 x1' ln1'* where *tst1*: *ts1 t* = $\lfloor (x1, ln1) \rfloor$
 and *tst1'*: *ts1' t* = $\lfloor (x1', ln1') \rfloor$
 by(*fastforce elim!: r1.redT.cases*)
 from *mred2* *r2.redT-thread-not-disappear*[*OF mred2*]
 obtain *x2 ln2 x2' ln2'* where *tst2*: *ts2 t* = $\lfloor (x2, ln2) \rfloor$
 and *tst2'*: *ts2' t* = $\lfloor (x2', ln2') \rfloor$ by(*fastforce elim!: r2.redT.cases*)
 from *tbisim*[*of t*] *tst1 tst2 ws* have *bisim*: *t* \vdash (*x1, m1*) \approx (*x2, m2*)
 and *ln*: *ln1* = *ln2* by(*auto simp add: tbisim-def*)


```

from tbsim'[of t] tst1' tst2' have bisim':  $t \vdash (x1', m1') \approx (x2', m2')$ 
  and ln':  $ln1' = ln2'$  by(auto simp add: tbsim-def)
show  $m\tau\text{move1 } s1 \text{ tl1 } s1' = m\tau\text{move2 } s2 \text{ tl2 } s2'$  unfolding s1 s2 s1' s2' tt' tl1 tl2
proof –
  show  $m\tau\text{move1 } (ls1, (ts1, m1), ws1, is1) (t, ta1) (ls1', (ts1', m1'), ws1', is1') =$ 
     $m\tau\text{move2 } (ls2, (ts2, m2), ws2, is2) (t, ta2) (ls2', (ts2', m2'), ws2', is2')$ 
    (is ?lhs = ?rhs)
  proof
    assume mτ: ?lhs
    with tst1 tst1' obtain  $\tau1: \tau\text{move1 } (x1, m1) \text{ ta1 } (x1', m1')$ 
      and ln1:  $ln1 = \text{no-wait-locks}$  by(fastforce elim!: r1.mτmove.cases)
    from  $\tau1$  have  $ta1 = \varepsilon$  by(rule r1.silent-tl)
    with mred1  $\tau1 \text{ } tst1 \text{ } tst1' \text{ } ln1$  have  $red1: t \vdash (x1, m1) -1-ta1 \rightarrow (x1', m1')$ 
      by(auto elim!: r1.redT.cases rtrancl3p-cases)
    from tasim  $\langle ta1 = \varepsilon \rangle$  have [simp]:  $ta2 = \varepsilon$  by(simp)
    with mred2  $ln1 \text{ } ln \text{ } tst2 \text{ } tst2'$  have  $red2: t \vdash (x2, m2) -2-\varepsilon \rightarrow (x2', m2')$ 
      by(fastforce elim!: r2.redT.cases rtrancl3p-cases)
    from  $\tau1 \text{ } \tau\text{inv}[OF \text{ } bisim \text{ } red1 \text{ } red2]$  bisim' tasim
    have  $\tau2: \tau\text{move2 } (x2, m2) \varepsilon (x2', m2')$  by simp
    with tst2 tst2' ln ln1 show ?rhs by –(rule r2.mτmove.intros, auto)
  next
    assume mτ: ?rhs
    with tst2 tst2' obtain  $\tau2: \tau\text{move2 } (x2, m2) \text{ ta2 } (x2', m2')$ 
      and ln2:  $ln2 = \text{no-wait-locks}$  by(fastforce elim!: r2.mτmove.cases)
    from  $\tau2$  have  $ta2 = \varepsilon$  by(rule r2.silent-tl)
    with mred2  $\tau2 \text{ } tst2 \text{ } tst2' \text{ } ln2$  have  $red2: t \vdash (x2, m2) -2-ta2 \rightarrow (x2', m2')$ 
      by(auto elim!: r2.redT.cases rtrancl3p-cases)
    from tasim  $\langle ta2 = \varepsilon \rangle$  have [simp]:  $ta1 = \varepsilon$  by simp
    with mred1  $ln2 \text{ } ln \text{ } tst1 \text{ } tst1'$  have  $red1: t \vdash (x1, m1) -1-\varepsilon \rightarrow (x1', m1')$ 
      by(fastforce elim!: r1.redT.cases rtrancl3p-cases)
    from  $\tau2 \text{ } \tau\text{inv}[OF \text{ } bisim \text{ } red1 \text{ } red2]$  bisim' tasim
    have  $\tau1: \tau\text{move1 } (x1, m1) \varepsilon (x1', m1')$  by auto
    with tst1 tst1' ln ln2 show ?lhs unfolding  $\langle ta1 = \varepsilon \rangle$ 
      by–(rule r1.mτmove.intros, auto)
    qed
  qed
qed
end

sublocale FWdelay-bisimulation-lift < mthr:  $\tau\text{inv } r1.\text{redT } r2.\text{redT } mbisim \text{ } mta\text{-bisim } m\tau\text{move1 } m\tau\text{move2}$ 
by(rule τinv-lift)

locale FWdelay-bisimulation-final-base =
  FWdelay-bisimulation-lift-aux +
  constrains final1 ::  $'x1 \Rightarrow \text{bool}$ 
  and r1 ::  $(l, t, 'x1, 'm1, 'w, 'o)$  semantics
  and final2 ::  $'x2 \Rightarrow \text{bool}$ 
  and r2 ::  $(l, t, 'x2, 'm2, 'w, 'o)$  semantics
  and convert-RA ::  $l \text{ released-locks} \Rightarrow 'o \text{ list}$ 
  and bisim ::  $t \Rightarrow ('x1 \times 'm1, 'x2 \times 'm2)$  bisim
  and bisim-wait ::  $(x1, x2)$  bisim
  and  $\tau\text{move1} :: (l, t, 'x1, 'm1, 'w, 'o)$   $\tau\text{moves}$ 
  and  $\tau\text{move2} :: (l, t, 'x2, 'm2, 'w, 'o)$   $\tau\text{moves}$ 

```

assumes *delay-bisim-locale*:
delay-bisimulation-final-base (*r1 t*) (*r2 t*) (*bisim t*) τ *move1* τ *move2* ($\lambda(x1, m). \text{final1 } x1$) ($\lambda(x2, m). \text{final2 } x2$)

sublocale *FWdelay-bisimulation-final-base* <
delay-bisimulation-final-base *r1 t r2 t bisim t ta-bisim bisim* τ *move1* τ *move2*
 $\lambda(x1, m). \text{final1 } x1 \lambda(x2, m). \text{final2 } x2$
for *t*
by(*rule delay-bisim-locale*)

context *FWdelay-bisimulation-final-base* **begin**

lemma *FWdelay-bisimulation-final-base-flip*:
FWdelay-bisimulation-final-base final2 r2 final1 r1 ($\lambda t. \text{flip } (\text{bisim } t)$) τ *move2* τ *move1*
apply(*rule FWdelay-bisimulation-final-base.intro*)
apply(*rule FWdelay-bisimulation-lift-aux-flip*)
apply(*rule FWdelay-bisimulation-final-base-axioms.intro*)
apply(*rule delay-bisimulation-final-base-flip*)
done

end

lemma *FWdelay-bisimulation-final-base-flip-simps* [*flip-simps*]:
FWdelay-bisimulation-final-base final2 r2 final1 r1 ($\lambda t. \text{flip } (\text{bisim } t)$) τ *move2* τ *move1* =
FWdelay-bisimulation-final-base final1 r1 final2 r2 bisim τ *move1* τ *move2*
by(*auto dest: FWdelay-bisimulation-final-base.FWdelay-bisimulation-final-base-flip simp only: flip-flip*)

context *FWdelay-bisimulation-final-base* **begin**

lemma *cond-actions-ok-bisim-ex- τ 1-inv*:
fixes *ls ts1 m1 ws is ts2 m2 ct*
defines *s1'* \equiv *activate-cond-action1* (*ls*, (*ts1*, *m1*), *ws*, *is*) (*ls*, (*ts2*, *m2*), *ws*, *is*) *ct*
assumes *mbisim*: $\bigwedge t'. t' \neq t \implies \text{tbisim } (\text{ws } t' = \text{None}) t' (\text{ts1 } t') m1 (\text{ts2 } t') m2$
and *ts1t*: *ts1 t = Some xln*
and *ts2t*: *ts2 t = Some xln'*
and *ct*: *r2.cond-action-ok* (*ls*, (*ts2*, *m2*), *ws*, *is*) *t ct*
shows τ *mRed1* (*ls*, (*ts1*, *m1*), *ws*, *is*) *s1'*
and $\bigwedge t'. t' \neq t \implies \text{tbisim } (\text{ws } t' = \text{None}) t' (\text{thr } s1' t') m1 (\text{ts2 } t') m2$
and *r1.cond-action-ok* *s1' t ct*
and *thr s1' t = Some xln*

proof –

have τ *mRed1* (*ls*, (*ts1*, *m1*), *ws*, *is*) *s1'* \wedge
 $(\forall t'. t' \neq t \longrightarrow \text{tbisim } (\text{ws } t' = \text{None}) t' (\text{thr } s1' t') m1 (\text{ts2 } t') m2) \wedge$
 $r1.\text{cond-action-ok } s1' t ct \wedge \text{thr } s1' t = [xln]$
using *ct*
proof(*cases ct*)
case (*Join t'*)
show *?thesis*
proof(*cases ts1 t'*)
case *None*
with *mbisim ts1t* **have** $t \neq t'$ **by** *auto*
moreover from *None Join* **have** $s1' = (\text{ls}, (\text{ts1}, m1), \text{ws}, \text{is})$ **by**(*simp add: s1'-def*)
ultimately show *?thesis* **using** *mbisim Join ct None ts1t* **by**(*simp add: tbisim-def*)
next

case (Some xln)
 moreover obtain $x1\ ln$ where $xln = (x1, ln)$ by(cases xln)
 ultimately have $ts1t'$: $ts1\ t' = [(x1, ln)]$ by simp
 from Join ct Some $ts2t$ have tt' : $t' \neq t$ by auto
 from mbisim[OF tt'] $ts1t'$ obtain $x2$ where $ts2t'$: $ts2\ t' = [(x2, ln)]$
 and bisim: $t' \vdash (x1, m1) \approx (x2, m2)$ by(auto simp add: tbisim-def)
 from ct Join $ts2t'$ have final2: final2 $x2$ and ln : $ln = no-wait-locks$
 and wst' : $ws\ t' = None$ by simp-all
 let $?x1' = SOME\ x.$ $r1.silent-moves\ t'\ (x1, m1)\ (x, m1) \wedge final1\ x \wedge t' \vdash (x, m1) \approx (x2, m2)$
 { from final2-simulation[OF bisim] final2 obtain $x1'\ m1'$
 where $r1.silent-moves\ t'\ (x1, m1)\ (x1', m1')$ and $t' \vdash (x1', m1') \approx (x2, m2)$
 and final1 $x1'$ by auto
 moreover hence $m1' = m1$ using bisim by(auto dest: $r1.red-rtrancl-\tau$ -heapD-inv)
 ultimately have $\exists x.$ $r1.silent-moves\ t'\ (x1, m1)\ (x, m1) \wedge final1\ x \wedge t' \vdash (x, m1) \approx (x2,$
 $m2)$
 by blast }
 from someI-ex[OF this] have red1: $r1.silent-moves\ t'\ (x1, m1)\ (?x1', m1)$
 and final1: final1 $?x1'$ and bisim': $t' \vdash (?x1', m1) \approx (x2, m2)$ by blast+
 let $?S1' = redT-upd-\epsilon\ (ls, (ts1, m1), ws, is)\ t'\ ?x1'\ m1$
 from $r1.silent-moves-into-RedT-\tau$ -inv[where $?s=(ls, (ts1, m1), ws, is)$ and $t=t'$, simplified, OF
 $red1$]
 bisim $ts1t'\ ln\ wst'$
 have Red1: $\tau mRed1\ (ls, (ts1, m1), ws, is)\ ?S1'$ by auto
 moreover from Join $ln\ ts1t'$ final1 $wst'\ tt'$
 have ct' : $r1.cond-action-ok\ ?S1'\ t\ ct$ by(auto intro: finfun-ext)
 { fix t''
 assume $t \neq t''$
 with Join mbisim[OF this[symmetric]] bisim' $ts1t'\ ts2t'\ wst'\ s1'-def$
 have tbisim ($ws\ t'' = None$) $t''\ (thr\ s1'\ t'')\ m1\ (ts2\ t'')\ m2$
 by(auto simp add: tbisim-def redT-updLns-def o-def finfun-Diag-const2) }
 moreover from Join $ts1t'\ ts2t'$ final2 ln have $s1' = ?S1'$ by(simp add: $s1'-def$)
 ultimately show ?thesis using Red1 $ct'\ ts1t'\ tt'\ ts1t$ by(auto)
 qed
 next
 case Yield thus ?thesis using mbisim $ts1t$ by(simp add: $s1'-def$)
 qed
 thus $\tau mRed1\ (ls, (ts1, m1), ws, is)\ s1'$
 and $\bigwedge t'.\ t' \neq t \implies tbisim\ (ws\ t' = None)\ t'\ (thr\ s1'\ t')\ m1\ (ts2\ t')\ m2$
 and $r1.cond-action-ok\ s1'\ t\ ct$
 and $thr\ s1'\ t = [xln]$ by blast+
 qed
 lemma cond-actions-oks-bisim-ex- τ 1-inv:
 fixes $ls\ ts1\ m1\ ws\ is\ ts2\ m2\ cts$
 defines $s1' \equiv activate-cond-actions1\ (ls, (ts1, m1), ws, is)\ (ls, (ts2, m2), ws, is)\ cts$
 assumes tbisim: $\bigwedge t'.\ t' \neq t \implies tbisim\ (ws\ t' = None)\ t'\ (ts1\ t')\ m1\ (ts2\ t')\ m2$
 and $ts1t$: $ts1\ t = Some\ xln$
 and $ts2t$: $ts2\ t = Some\ xln'$
 and ct : $r2.cond-action-oks\ (ls, (ts2, m2), ws, is)\ t\ cts$
 shows $\tau mRed1\ (ls, (ts1, m1), ws, is)\ s1'$
 and $\bigwedge t'.\ t' \neq t \implies tbisim\ (ws\ t' = None)\ t'\ (thr\ s1'\ t')\ m1\ (ts2\ t')\ m2$
 and $r1.cond-action-oks\ s1'\ t\ cts$
 and $thr\ s1'\ t = Some\ xln$
 using tbisim $ts1t\ ct$ unfolding $s1'-def$

proof(*induct cts arbitrary: ts1*)

case (*Cons ct cts*)

note $IH1 = \langle \bigwedge ts1. \llbracket \bigwedge t'. t' \neq t \implies tbisim (ws\ t' = None)\ t' (ts1\ t')\ m1\ (ts2\ t')\ m2; ts1\ t = \lfloor xln \rfloor;$
 $r2.cond-action-oks (ls, (ts2, m2), ws, is)\ t\ cts \rrbracket$

$\implies \tau mred1^{**} (ls, (ts1, m1), ws, is)\ (activate-cond-actions1 (ls, (ts1, m1), ws, is)\ (ls, (ts2, m2), ws, is)\ cts) \rangle$

note $IH2 = \langle \bigwedge t'. ts1. \llbracket t' \neq t; \bigwedge t'. t' \neq t \implies tbisim (ws\ t' = None)\ t' (ts1\ t')\ m1\ (ts2\ t')\ m2; ts1\ t = \lfloor xln \rfloor;$

$r2.cond-action-oks (ls, (ts2, m2), ws, is)\ t\ cts \rrbracket$
 $\implies tbisim (ws\ t' = None)\ t' (thr (activate-cond-actions1 (ls, (ts1, m1), ws, is)\ (ls, (ts2, m2), ws, is)\ cts)\ t')\ m1\ (ts2\ t')\ m2 \rangle$

note $IH3 = \langle \bigwedge ts1. \llbracket \bigwedge t'. t' \neq t \implies tbisim (ws\ t' = None)\ t' (ts1\ t')\ m1\ (ts2\ t')\ m2; ts1\ t = \lfloor xln \rfloor;$
 $r2.cond-action-oks (ls, (ts2, m2), ws, is)\ t\ cts \rrbracket$

$\implies r1.cond-action-oks (activate-cond-actions1 (ls, (ts1, m1), ws, is)\ (ls, (ts2, m2), ws, is)\ cts)\ t\ cts \rangle$

note $IH4 = \langle \bigwedge ts1. \llbracket \bigwedge t'. t' \neq t \implies tbisim (ws\ t' = None)\ t' (ts1\ t')\ m1\ (ts2\ t')\ m2; ts1\ t = \lfloor xln \rfloor;$
 $r2.cond-action-oks (ls, (ts2, m2), ws, is)\ t\ cts \rrbracket$

$\implies thr (activate-cond-actions1 (ls, (ts1, m1), ws, is)\ (ls, (ts2, m2), ws, is)\ cts)\ t = \lfloor xln \rfloor \rangle$

{ **fix** $ts1$

assume $tbisim: \bigwedge t'. t' \neq t \implies tbisim (ws\ t' = None)\ t' (ts1\ t')\ m1\ (ts2\ t')\ m2$

and $ts1t: ts1\ t = \lfloor xln \rfloor$

and $ct: r2.cond-action-oks (ls, (ts2, m2), ws, is)\ t\ (ct \# cts)$

from ct **have** $1: r2.cond-action-ok (ls, (ts2, m2), ws, is)\ t\ ct$

and $2: r2.cond-action-oks (ls, (ts2, m2), ws, is)\ t\ cts$ **by** *auto*

let $?s1' = activate-cond-action1 (ls, (ts1, m1), ws, is)\ (ls, (ts2, m2), ws, is)\ ct$

from $cond-actions-ok-bisim-ex-\tau 1-inv[OF\ tbisim, OF - ts1t\ ts2t\ 1]$

have $tbisim': \bigwedge t'. t' \neq t \implies tbisim (ws\ t' = None)\ t' (thr\ ?s1'\ t')\ m1\ (ts2\ t')\ m2$

and $red: \tau mRed1 (ls, (ts1, m1), ws, is)\ ?s1'$ **and** $ct': r1.cond-action-ok\ ?s1'\ t\ ct$

and $ts1't: thr\ ?s1'\ t = \lfloor xln \rfloor$ **by** *blast+*

let $?s1'' = activate-cond-actions1\ ?s1'\ (ls, (ts2, m2), ws, is)\ cts$

have $locks\ ?s1' = ls\ shr\ ?s1' = m1\ wset\ ?s1' = ws\ interrupts\ ?s1' = is$ **by** *simp-all*

hence $s1': (ls, (thr\ ?s1', m1), ws, is) = ?s1'$ **by**(*cases ?s1'*) *auto*

from $IH1[OF\ tbisim', OF - ts1't\ 2]\ s1'$ **have** $red': \tau mRed1\ ?s1'\ ?s1''$ **by** *simp*

with red **show** $\tau mRed1 (ls, (ts1, m1), ws, is)\ (activate-cond-actions1 (ls, (ts1, m1), ws, is)\ (ls, (ts2, m2), ws, is)\ (ct \# cts))$

by *auto*

{ **fix** t'

assume $t't: t' \neq t$

from $IH2[OF\ t't\ tbisim', OF - ts1't\ 2]\ s1'$

show $tbisim (ws\ t' = None)\ t' (thr (activate-cond-actions1 (ls, (ts1, m1), ws, is)\ (ls, (ts2, m2), ws, is)\ (ct \# cts))\ t')\ m1\ (ts2\ t')\ m2$

by *auto* }

from $red'\ ct'$ **have** $r1.cond-action-ok\ ?s1''\ t\ ct$ **by**(*rule cond-actions-ok-\tau mRed1-inv*)

with $IH3[OF\ tbisim', OF - ts1't\ 2]\ s1'$

show $r1.cond-action-oks (activate-cond-actions1 (ls, (ts1, m1), ws, is)\ (ls, (ts2, m2), ws, is)\ (ct \# cts))\ t\ (ct \# cts)$

by *auto*

from $ts1't\ IH4[OF\ tbisim', OF - ts1't\ 2]\ s1'$

show $thr (activate-cond-actions1 (ls, (ts1, m1), ws, is)\ (ls, (ts2, m2), ws, is)\ (ct \# cts))\ t = \lfloor xln \rfloor$

by *auto* }

qed(*auto*)

lemma *cond-actions-ok-bisim-ex-\tau 2-inv:*

fixes $ls\ ts1\ m1\ is\ ws\ ts2\ m2\ ct$

defines $s2' \equiv \text{activate-cond-action2 } (ls, (ts1, m1), ws, is) (ls, (ts2, m2), ws, is) ct$
assumes $mbisim: \bigwedge t'. t' \neq t \implies tbisim (ws t' = None) t' (ts1 t') m1 (ts2 t') m2$
and $ts1t: ts1 t = \text{Some } xln$
and $ts2t: ts2 t = \text{Some } xln'$
and $ct: r1.\text{cond-action-ok } (ls, (ts1, m1), ws, is) t ct$
shows $\tau mRed2 (ls, (ts2, m2), ws, is) s2'$
and $\bigwedge t'. t' \neq t \implies tbisim (ws t' = None) t' (ts1 t') m1 (thr s2' t') m2$
and $r2.\text{cond-action-ok } s2' t ct$
and $thr s2' t = \text{Some } xln'$
unfolding $s2'\text{-def}$
by(blast intro: FWdelay-bisimulation-final-base.cond-actions-ok-bisim-ex- τ 1-inv[OF FWdelay-bisimulation-final-base-flip,
where $bisim\text{-wait} = \text{flip bisim-wait, unfolded flip-simps, OF mbisim - - ct, OF - ts2t ts1t}$)+

lemma *cond-actions-oks-bisim-ex- τ 2-inv*:

fixes $ls ts1 m1 ws is ts2 m2 cts$
defines $s2' \equiv \text{activate-cond-actions2 } (ls, (ts1, m1), ws, is) (ls, (ts2, m2), ws, is) cts$
assumes $tbisim: \bigwedge t'. t' \neq t \implies tbisim (ws t' = None) t' (ts1 t') m1 (ts2 t') m2$
and $ts1t: ts1 t = \text{Some } xln$
and $ts2t: ts2 t = \text{Some } xln'$
and $ct: r1.\text{cond-action-oks } (ls, (ts1, m1), ws, is) t cts$
shows $\tau mRed2 (ls, (ts2, m2), ws, is) s2'$
and $\bigwedge t'. t' \neq t \implies tbisim (ws t' = None) t' (ts1 t') m1 (thr s2' t') m2$
and $r2.\text{cond-action-oks } s2' t cts$
and $thr s2' t = \text{Some } xln'$

unfolding $s2'\text{-def}$

by(blast intro: FWdelay-bisimulation-final-base.cond-actions-oks-bisim-ex- τ 1-inv[OF FWdelay-bisimulation-final-base-flip,
where $bisim\text{-wait} = \text{flip bisim-wait, unfolded flip-simps, OF tbisim - - ct, OF - ts2t ts1t}$)+

lemma *mfinal1-inv-simulation*:

assumes $s1 \approx_m s2$
shows $\exists s2'. r2.\text{mthr.silent-moves } s2 s2' \wedge s1 \approx_m s2' \wedge r1.\text{final-threads } s1 \subseteq r2.\text{final-threads } s2'$
 $\wedge \text{shr } s2' = \text{shr } s2$

proof –

from $\langle s1 \approx_m s2 \rangle$ **have** $\text{finite } (\text{dom } (thr s1))$ **by**(auto dest: mbisim-finite1)
moreover **have** $r1.\text{final-threads } s1 \subseteq \text{dom } (thr s1)$ **by**(auto simp add: r1.final-thread-def)
ultimately **have** $\text{finite } (r1.\text{final-threads } s1)$ **by**(blast intro: finite-subset)
thus $?thesis$ **using** $\langle s1 \approx_m s2 \rangle$
proof(induct $A \equiv r1.\text{final-threads } s1$ arbitrary: $s1 s2$ rule: finite-induct)
case *empty*
from $\langle \{\} = r1.\text{final-threads } s1 \rangle$ **symmetric** **have** $\forall t. \neg r1.\text{final-thread } s1 t$ **by**(auto)
with $\langle s1 \approx_m s2 \rangle$ **show** $?case$ **by** blast
next
case (*insert t A*)
define $s1'$ **where** $s1' = (\text{locks } s1, ((thr s1)(t := None), \text{shr } s1), \text{wset } s1, \text{interrupts } s1)$
define $s2'$ **where** $s2' = (\text{locks } s2, ((thr s2)(t := None), \text{shr } s2), \text{wset } s2, \text{interrupts } s2)$
from $\langle t \notin A \rangle$ $\langle \text{insert } t A = r1.\text{final-threads } s1 \rangle$ **have** $A = r1.\text{final-threads } s1'$
unfolding $s1'\text{-def}$ **by**(auto simp add: r1.final-thread-def r1.final-threads-def)
moreover **from** $\langle \text{insert } t A = r1.\text{final-threads } s1 \rangle$ **have** $r1.\text{final-thread } s1 t$ **by** auto
hence $\text{wset } s1 t = \text{None}$ **by**(auto simp add: r1.final-thread-def)
with $\langle s1 \approx_m s2 \rangle$ **have** $s1' \approx_m s2'$ **unfolding** $s1'\text{-def } s2'\text{-def}$
by(auto simp add: mbisim-def intro: tbisim-NoneI intro!: wset-thread-okI dest: wset-thread-okD
split: if-split-asm)
ultimately **have** $\exists s2''. r2.\text{mthr.silent-moves } s2' s2'' \wedge s1' \approx_m s2'' \wedge r1.\text{final-threads } s1' \subseteq$
 $r2.\text{final-threads } s2'' \wedge \text{shr } s2'' = \text{shr } s2'$ **by**(rule insert)

then obtain $s2''$ **where** $reds: r2.mthr.silent-moves\ s2'\ s2''$
and $s1' \approx_m s2''$ **and** $fin: \bigwedge t. r1.final-thread\ s1'\ t \implies r2.final-thread\ s2''\ t$ **and** $shr\ s2'' = shr\ s2'$ **by** *blast*
have $thr\ s2'\ t = None$ **unfolding** $s2'-def$ **by** *simp*
with $\langle r2.mthr.silent-moves\ s2'\ s2'' \rangle$
have $r2.mthr.silent-moves\ (locks\ s2', ((thr\ s2')(t \mapsto the\ (thr\ s2\ t)), shr\ s2'), wset\ s2', interrupts\ s2')$
 $(locks\ s2'', ((thr\ s2'')(t \mapsto the\ (thr\ s2\ t)), shr\ s2''), wset\ s2'', interrupts\ s2'')$
by(rule $r2.\tau mRedT-add-thread-inv$)
also let $?s2'' = (locks\ s2, ((thr\ s2'')(t \mapsto the\ (thr\ s2\ t)), shr\ s2), wset\ s2, interrupts\ s2)$
from $\langle shr\ s2'' = shr\ s2' \rangle \langle s1' \approx_m s2'' \rangle \langle s1 \approx_m s2 \rangle$
have $(locks\ s2'', ((thr\ s2'')(t \mapsto the\ (thr\ s2\ t)), shr\ s2''), wset\ s2'', interrupts\ s2'') = ?s2''$
unfolding $s2'-def\ s1'-def$ **by**(*simp add: mbisim-def*)
also (*back-subst*) **from** $\langle s1 \approx_m s2 \rangle$ **have** $dom\ (thr\ s1) = dom\ (thr\ s2)$ **by**(rule *mbisim-dom-eq*)
with $\langle r1.final-thread\ s1\ t \rangle$ **have** $t \in dom\ (thr\ s2)$ **by**(*auto simp add: r1.final-thread-def*)
then obtain $x2\ ln$ **where** $tst2: thr\ s2\ t = [(x2, ln)]$ **by** *auto*
hence $(locks\ s2', ((thr\ s2')(t \mapsto the\ (thr\ s2\ t)), shr\ s2'), wset\ s2', interrupts\ s2') = s2$
unfolding $s2'-def$ **by**(*cases\ s2*)(*auto intro!: ext*)
also from $\langle s1 \approx_m s2 \rangle$ $tst2$ **obtain** $x1$
where $tst1: thr\ s1\ t = [(x1, ln)]$
and $bisim: t \vdash (x1, shr\ s1) \approx (x2, shr\ s2)$ **by**(*auto dest: mbisim-thrD2*)
from $\langle shr\ s2'' = shr\ s2' \rangle$ **have** $shr\ ?s2'' = shr\ s2$ **by**(*simp add: s2'-def*)
from $\langle r1.final-thread\ s1\ t \rangle$ $tst1$
have $final: final1\ x1\ ln = no-wait-locks\ wset\ s1\ t = None$ **by**(*auto simp add: r1.final-thread-def*)
with $final1-simulation[OF\ bisim]\ \langle shr\ ?s2'' = shr\ s2 \rangle$ **obtain** $x2'\ m2'$
where $red: r2.silent-moves\ t\ (x2, shr\ ?s2'')\ (x2', m2')$
and $bisim': t \vdash (x1, shr\ s1) \approx (x2', m2')$ **and** $final2\ x2'$ **by** *auto*
from $\langle wset\ s1\ t = None \rangle \langle s1 \approx_m s2 \rangle$ **have** $wset\ s2\ t = None$ **by**(*simp add: mbisim-def*)
with $bisim\ r2.silent-moves-into-RedT-\tau-inv[OF\ red]\ tst2\ \langle ln = no-wait-locks \rangle$
have $r2.mthr.silent-moves\ ?s2''\ (redT-upd-\varepsilon\ ?s2''\ t\ x2'\ m2')$ **unfolding** $s2'-def$ **by** *auto*
also (*rtranclp-trans*)
from $bisim\ r2.red-rtrancl-\tau-heapD-inv[OF\ red]$ **have** $m2' = shr\ s2$ **by** *auto*
hence $s1 \approx_m (redT-upd-\varepsilon\ ?s2''\ t\ x2'\ m2')$
using $\langle s1' \approx_m s2'' \rangle \langle s1 \approx_m s2 \rangle$ $tst1\ tst2\ \langle shr\ ?s2'' = shr\ s2 \rangle$ $bisim'\ \langle shr\ s2'' = shr\ s2' \rangle \langle wset\ s2\ t = None \rangle$
unfolding $s1'-def\ s2'-def$ **by**(*auto simp add: mbisim-def redT-updLns-def split: if-split-asm intro: tbisim-SomeI*)
moreover {
fix t'
assume $r1.final-thread\ s1\ t'$
with $fin[of\ t']\ \langle final2\ x2' \rangle\ tst2\ \langle ln = no-wait-locks \rangle \langle wset\ s2\ t = None \rangle \langle s1' \approx_m s2'' \rangle \langle s1 \approx_m s2 \rangle$
have $r2.final-thread\ (redT-upd-\varepsilon\ ?s2''\ t\ x2'\ m2')\ t'$ **unfolding** $s1'-def$
by(*fastforce split: if-split-asm simp add: r2.final-thread-def r1.final-thread-def redT-updLns-def finfun-Diag-const2 o-def mbisim-def*)
}
moreover have $shr\ (redT-upd-\varepsilon\ ?s2''\ t\ x2'\ m2') = shr\ s2$ **using** $\langle m2' = shr\ s2 \rangle$ **by** *simp*
ultimately show $?case$ **by** *blast*
qed
qed

lemma *mfinal2-inv-simulation:*

$s1 \approx_m s2 \implies \exists s1'. r1.mthr.silent-moves\ s1\ s1' \wedge s1' \approx_m s2 \wedge r2.final-threads\ s2 \subseteq r1.final-threads\ s1' \wedge shr\ s1' = shr\ s1$

using *FWdelay-bisimulation-final-base.mfinal1-inv-simulation*[*OF FWdelay-bisimulation-final-base-flip*,
where *bisim-wait=flip bisim-wait*]
by(*unfold flip-simps*)

lemma *mfinal1-simulation*:

assumes $s1 \approx_m s2$ **and** $r1.mfinal\ s1$

shows $\exists s2'. r2.mthr.silent-moves\ s2\ s2' \wedge s1 \approx_m s2' \wedge r2.mfinal\ s2' \wedge shr\ s2' = shr\ s2$

proof –

from *mfinal1-inv-simulation*[*OF* $\langle s1 \approx_m s2 \rangle$]

obtain $s2'$ **where** $1: r2.mthr.silent-moves\ s2\ s2'\ s1 \approx_m s2'\ shr\ s2' = shr\ s2$

and $fin: \bigwedge t. r1.final-thread\ s1\ t \implies r2.final-thread\ s2'\ t$ **by** *blast*

have $r2.mfinal\ s2'$

proof(*rule* $r2.mfinalI$)

fix $t\ x2\ ln$

assume $thr\ s2'\ t = \lfloor (x2, ln) \rfloor$

with $\langle s1 \approx_m s2' \rangle$ **obtain** $x1$ **where** $thr\ s1\ t = \lfloor (x1, ln) \rfloor\ t \vdash (x1, shr\ s1) \approx (x2, shr\ s2')$

by(*auto dest: mbisim-thrD2*)

from $\langle thr\ s1\ t = \lfloor (x1, ln) \rfloor \rangle\ \langle r1.mfinal\ s1 \rangle$ **have** $r1.final-thread\ s1\ t$

by(*auto elim!: r1.mfinalE simp add: r1.final-thread-def*)

hence $r2.final-thread\ s2'\ t$ **by**(*rule* fin)

thus $final2\ x2 \wedge ln = no-wait-locks \wedge wset\ s2'\ t = None$

using $\langle thr\ s2'\ t = \lfloor (x2, ln) \rfloor \rangle$ **by**(*auto simp add: r2.final-thread-def*)

qed

with 1 **show** *?thesis* **by** *blast*

qed

lemma *mfinal2-simulation*:

$\llbracket s1 \approx_m s2; r2.mfinal\ s2 \rrbracket$

$\implies \exists s1'. r1.mthr.silent-moves\ s1\ s1' \wedge s1' \approx_m s2 \wedge r1.mfinal\ s1' \wedge shr\ s1' = shr\ s1$

using *FWdelay-bisimulation-final-base.mfinal1-simulation*[*OF FWdelay-bisimulation-final-base-flip*, **where**
bisim-wait = flip bisim-wait]

by(*unfold flip-simps*)

end

locale *FWdelay-bisimulation-obs =*

FWdelay-bisimulation-final-base - - - - - $\tau move1\ \tau move2$

for $\tau move1 :: (l, t, 'x1, 'm1, 'w, 'o) \tau moves$

and $\tau move2 :: (l, t, 'x2, 'm2, 'w, 'o) \tau moves +$

assumes *delay-bisimulation-obs-locale: delay-bisimulation-obs* ($r1\ t$) ($r2\ t$) (*bisim* t) (*ta-bisim* *bisim*)

$\tau move1\ \tau move2$

and *bisim-inv-red-other*:

$\llbracket t' \vdash (x, m1) \approx (xx, m2); t \vdash (x1, m1) \approx (x2, m2);$

$r1.silent-moves\ t\ (x1, m1)\ (x1', m1);$

$t \vdash (x1', m1) -1-ta1 \rightarrow (x1'', m1'); \neg \tau move1\ (x1', m1)\ ta1\ (x1'', m1');$

$r2.silent-moves\ t\ (x2, m2)\ (x2', m2);$

$t \vdash (x2', m2) -2-ta2 \rightarrow (x2'', m2'); \neg \tau move2\ (x2', m2)\ ta2\ (x2'', m2');$

$t \vdash (x1'', m1') \approx (x2'', m2'); ta-bisim\ bisim\ ta1\ ta2 \rrbracket$

$\implies t' \vdash (x, m1') \approx (xx, m2')$

and *bisim-waitI*:

$\llbracket t \vdash (x1, m1) \approx (x2, m2); r1.silent-moves\ t\ (x1, m1)\ (x1', m1);$

$t \vdash (x1', m1) -1-ta1 \rightarrow (x1'', m1'); \neg \tau move1\ (x1', m1)\ ta1\ (x1'', m1');$

$r2.silent-moves\ t\ (x2, m2)\ (x2', m2);$

$t \vdash (x2', m2) -2-ta2 \rightarrow (x2'', m2'); \neg \tau move2\ (x2', m2)\ ta2\ (x2'', m2');$

$t \vdash (x1'', m1') \approx (x2'', m2')$; *ta-bisim bisim ta1 ta2*;
Suspend w \in *set* $\{\{ta1\}_w\}$; *Suspend w* \in *set* $\{\{ta2\}_w\}$]
 $\implies x1'' \approx_w x2''$
and *simulation-Wakeup1*:
 $\llbracket t \vdash (x1, m1) \approx (x2, m2); x1 \approx_w x2; t \vdash (x1, m1) -1-ta1 \rightarrow (x1', m1')$; *Notified* \in *set* $\{\{ta1\}_w\}$
 \vee *WokenUp* \in *set* $\{\{ta1\}_w\}$]
 $\implies \exists ta2 x2' m2'. t \vdash (x2, m2) -2-ta2 \rightarrow (x2', m2') \wedge t \vdash (x1', m1') \approx (x2', m2') \wedge$ *ta-bisim bisim ta1 ta2*
and *simulation-Wakeup2*:
 $\llbracket t \vdash (x1, m1) \approx (x2, m2); x1 \approx_w x2; t \vdash (x2, m2) -2-ta2 \rightarrow (x2', m2')$; *Notified* \in *set* $\{\{ta2\}_w\}$
 \vee *WokenUp* \in *set* $\{\{ta2\}_w\}$]
 $\implies \exists ta1 x1' m1'. t \vdash (x1, m1) -1-ta1 \rightarrow (x1', m1') \wedge t \vdash (x1', m1') \approx (x2', m2') \wedge$ *ta-bisim bisim ta1 ta2*
and *ex-final1-conv-ex-final2*:
 $(\exists x1. \text{final1 } x1) \longleftrightarrow (\exists x2. \text{final2 } x2)$

sublocale *FWdelay-bisimulation-obs* <
delay-bisimulation-obs r1 t r2 t bisim t ta-bisim bisim τ move1 τ move2 **for** *t*
by(*rule delay-bisimulation-obs-locale*)

context *FWdelay-bisimulation-obs* **begin**

lemma *FWdelay-bisimulation-obs-flip*:

FWdelay-bisimulation-obs final2 r2 final1 r1 ($\lambda t. \text{flip } (\text{bisim } t)$) (*flip bisim-wait*) τ move2 τ move1
apply(*rule FWdelay-bisimulation-obs.intro*)
apply(*rule FWdelay-bisimulation-final-base-flip*)
apply(*rule FWdelay-bisimulation-obs-axioms.intro*)
apply(*unfold flip-simps*)
apply(*rule delay-bisimulation-obs-axioms*)
apply(*erule (9) bisim-inv-red-other*)
apply(*erule (10) bisim-waitI*)
apply(*erule (3) simulation-Wakeup2*)
apply(*erule (3) simulation-Wakeup1*)
apply(*rule ex-final1-conv-ex-final2[symmetric]*)
done

end

lemma *FWdelay-bisimulation-obs-flip-simps [flip-simps]*:

FWdelay-bisimulation-obs final2 r2 final1 r1 ($\lambda t. \text{flip } (\text{bisim } t)$) (*flip bisim-wait*) τ move2 τ move1 =

FWdelay-bisimulation-obs final1 r1 final2 r2 bisim bisim-wait τ move1 τ move2
by(*auto dest: FWdelay-bisimulation-obs.FWdelay-bisimulation-obs-flip simp only: flip-flip*)

context *FWdelay-bisimulation-obs* **begin**

lemma *mbisim-redT-upd*:

fixes *s1 t ta1 x1' m1' s2 ta2 x2' m2' ln*
assumes *s1'*: *redT-upd s1 t ta1 x1' m1' s1'*
and *s2'*: *redT-upd s2 t ta2 x2' m2' s2'*
and [*simp*]: *wset s1 = wset s2 locks s1 = locks s2*
and *wset*: *wset s1' = wset s2'*
and *interrupts*: *interrupts s1' = interrupts s2'*
and *fin1*: *finite (dom (thr s1))*

and *wsts*: *wset-thread-ok* (*wset s1*) (*thr s1*)
and *tst*: *thr s1 t* = $\lfloor (x1, ln) \rfloor$
and *tst'*: *thr s2 t* = $\lfloor (x2, ln) \rfloor$
and *aoe1*: *r1.actions-ok s1 t ta1*
and *aoe2*: *r2.actions-ok s2 t ta2*
and *tasim*: *ta-bisim bisim ta1 ta2*
and *bisim'*: $t \vdash (x1', m1') \approx (x2', m2')$
and *bisimw*: $wset s1' t = None \vee x1' \approx_w x2'$
and $\tau red1$: *r1.silent-moves t (x1'', shr s1) (x1, shr s1)*
and *red1*: $t \vdash (x1, shr s1) -1 -ta1 \rightarrow (x1', m1')$
and $\tau red2$: *r2.silent-moves t (x2'', shr s2) (x2, shr s2)*
and *red2*: $t \vdash (x2, shr s2) -2 -ta2 \rightarrow (x2', m2')$
and *bisim*: $t \vdash (x1'', shr s1) \approx (x2'', shr s2)$
and $\tau 1$: $\neg \tau move1 (x1, shr s1) ta1 (x1', m1')$
and $\tau 2$: $\neg \tau move2 (x2, shr s2) ta2 (x2', m2')$
and *tbisim*: $\bigwedge t'. t \neq t' \implies tbisim (wset s1 t' = None) t' (thr s1 t') (shr s1) (thr s2 t') (shr s2)$
shows $s1' \approx_m s2'$
proof(*rule mbisimI*)
from *fin1 s1'* **show** *finite (dom (thr s1'))*
by(*auto simp add: redT-updTs-finite-dom-inv*)
next
from *tasim s1' s2'* **show** *locks s1' = locks s2'*
by(*auto simp add: redT-updLs-def o-def ta-bisim-def*)
next
from *wset* **show** *wset s1' = wset s2'*.
next
from *interrupts* **show** *interrupts s1' = interrupts s2'*.
next
from *wsts s1' s2' wset* **show** *wset-thread-ok (wset s1') (thr s1')*
by(*fastforce intro!: wset-thread-okI split: if-split-asm dest: redT-updTs-None wset-thread-okD redT-updWs-None-implies-None*)
next
fix *T*
assume *thr s1' T = None*
moreover with *tst s1'* **have** [*simp*]: $t \neq T$ **by** *auto*
from *tbisim[OF this]* **have** (*thr s1 T = None*) = (*thr s2 T = None*)
by(*auto simp add: tbisim-def*)
hence (*redT-updTs (thr s1) \{ta1\}_t T = None*) = (*redT-updTs (thr s2) \{ta2\}_t T = None*)
using *tasim* **by** \neg (*rule redT-updTs-nta-bisim-inv, simp-all add: ta-bisim-def*)
ultimately show *thr s2' T = None* **using** $s2' s1'$ **by**(*auto split: if-split-asm*)
next
fix *T X1 LN*
assume *tsT*: *thr s1' T* = $\lfloor (X1, LN) \rfloor$
show $\exists x2. thr s2' T = \lfloor (x2, LN) \rfloor \wedge T \vdash (X1, shr s1') \approx (x2, shr s2') \wedge (wset s2' T = None \vee X1 \approx_w x2)$
proof(*cases thr s1 T*)
case *None*
with *tst* **have** $t \neq T$ **by** *auto*
with *tbisim[OF this] None* **have** *tsT'*: *thr s2 T = None* **by**(*simp add: tbisim-def*)
from *None* $\langle t \neq T \rangle$ *tsT aoe1 s1'* **obtain** *M1*
where *ntset*: *NewThread T X1 M1* \in *set \{ta1\}_t **and** [*simp*]: *LN = no-wait-locks*
by(*auto dest!: redT-updTs-new-thread*)
from *ntset* **obtain** *tas1 tas1'* **where** $\{ta1\}_t = tas1 @ NewThread T X1 M1 \# tas1'$
by(*auto simp add: in-set-conv-decomp*)
with *tasim* **obtain** *tas2 X2 M2 tas2'* **where** $\{ta2\}_t = tas2 @ NewThread T X2 M2 \# tas2'$*

$length\ tas2 = length\ tas2\ length\ tas1' = length\ tas2'$ **and** $Bisim: T \vdash (X1, M1) \approx (X2, M2)$
by(*auto simp add: list-all2-append1 list-all2-Cons1 ta-bisim-def*)
hence $ntset': NewThread\ T\ X2\ M2 \in set\ \{ta2\}_t$ **by** *auto*
with $tsT' \langle t \neq T \rangle\ aoe2\ s2'$ **have** $thr\ s2'\ T = [(X2, no-wait-locks)]$
by(*auto intro: redT-updTs-new-thread-ts*)
moreover from $ntset'\ red2$ **have** $m2' = M2$ **by**(*auto dest: r2.new-thread-memory*)
moreover from $ntset\ red1$ **have** $m1' = M1$
by(*auto dest: r1.new-thread-memory*)
moreover from $wsts\ None$ **have** $wset\ s1\ T = None$ **by**(*rule wset-thread-okD*)
ultimately show $?thesis$ **using** $Bisim\ \langle t \neq T \rangle\ s1'\ s2'$
by(*auto simp add: redT-updWs-None-implies-None*)
next
case (*Some a*)
show $?thesis$
proof(*cases t = T*)
case *True*
with $tst\ tsT\ s1'$ **have** [*simp*]: $X1 = x1'\ LN = redT-updLns\ (locks\ s1)\ t\ ln\ \{ta1\}_l$ **by**(*auto*)
show $?thesis$ **using** $True\ bisim'\ bisimw\ tasim\ tst\ tst'\ s1'\ s2'\ wset$
by(*auto simp add: redT-updLns-def ta-bisim-def*)
next
case *False*
with $Some\ aoe1\ tsT\ s1'$ **have** $thr\ s1\ T = [(X1, LN)]$ **by**(*auto dest: redT-updTs-Some*)
with $tbisim[OF\ False]$ **obtain** $X2$
where $tsT': thr\ s2\ T = [(X2, LN)]$ **and** $Bisim: T \vdash (X1, shr\ s1) \approx (X2, shr\ s2)$
and $bisimw: wset\ s1\ T = None \vee X1 \approx_w X2$ **by**(*auto simp add: tbisim-def*)
with $aoe2\ False\ s2'$ **have** $tsT': thr\ s2'\ T = [(X2, LN)]$ **by**(*auto simp add: redT-updTs-Some*)
moreover from $Bisim\ bisim\ \tau red1\ red1\ \tau 1\ \tau red2\ red2\ \tau 2\ bisim'\ tasim$
have $T \vdash (X1, m1') \approx (X2, m2')$ **by**(*rule bisim-inv-red-other*)
ultimately show $?thesis$ **using** $False\ bisimw\ s1'\ s2'$
by(*auto simp add: redT-updWs-None-implies-None*)
qed
qed
qed

theorem *mbisim-simulation1*:

assumes $mbisim: mbisim\ s1\ s2$ **and** $\neg m\tau move1\ s1\ tl1\ s1'\ r1.redT\ s1\ tl1\ s1'$
shows $\exists s2'\ s2''\ tl2. r2.mthr.silent-moves\ s2\ s2' \wedge r2.redT\ s2'\ tl2\ s2'' \wedge$
 $\neg m\tau move2\ s2'\ tl2\ s2'' \wedge mbisim\ s1'\ s2'' \wedge mta-bisim\ tl1\ tl2$

proof –

from *assms* **obtain** $t\ ta1$ **where** $tl1$ [*simp*]: $tl1 = (t, ta1)$ **and** $redT: s1 -1-t \rightarrow ta1 \rightarrow s1'$
and $m\tau: \neg m\tau move1\ s1\ (t, ta1)\ s1'$ **by**(*cases tl1*) *fastforce*
obtain $ls1\ ts1\ m1\ ws1\ is1$ **where** [*simp*]: $s1 = (ls1, (ts1, m1), ws1, is1)$ **by**(*cases s1*) *fastforce*
obtain $ls1'\ ts1'\ m1'\ ws1'\ is1'$ **where** [*simp*]: $s1' = (ls1', (ts1', m1'), ws1', is1')$ **by**(*cases s1'*)
fastforce
obtain $ls2\ ts2\ m2\ ws2\ is2$ **where** [*simp*]: $s2 = (ls2, (ts2, m2), ws2, is2)$ **by**(*cases s2*) *fastforce*
from $mbisim$ **have** [*simp*]: $ls2 = ls1\ ws2 = ws1\ is2 = is1$ *finite (dom ts1)* **by**(*auto simp add: mbisim-def*)
from $redT$ **show** $?thesis$
proof *cases*
case (*redT-normal x1 x1' M1'*)
hence $red: t \vdash (x1, m1) -1-ta1 \rightarrow (x1', M1')$
and $tst: ts1\ t = [(x1, no-wait-locks)]$
and $aoe: r1.actions-ok\ s1\ t\ ta1$
and $s1': redT-upd\ s1\ t\ ta1\ x1'\ M1'\ s1'$ **by** *auto*

```

from mbisim tst obtain x2 where tst': ts2 t = [(x2, no-wait-locks)]
  and bisim: t ⊢ (x1, m1) ≈ (x2, m2) by(auto dest: mbisim-thrD1)
from mτ have τ:  $\neg \tau \text{move1 } (x1, m1) \text{ ta1 } (x1', M1')$ 
proof(rule contrapos-nn)
  assume τ:  $\tau \text{move1 } (x1, m1) \text{ ta1 } (x1', M1')$ 
  moreover hence [simp]: ta1 = ε by(rule r1.silent-tl)
  moreover have [simp]: M1' = m1 by(rule r1.τmove-heap[OF red τ, symmetric])
  ultimately show mτmove1 s1 (t, ta1) s1' using s1' tst s1'
    by(auto simp add: redT-updLs-def o-def intro: r1.mτmove.intros elim: rtrancl3p-cases)
qed
show ?thesis
proof(cases ws1 t)
  case None
    note wst = this
    from simulation1[OF bisim red τ] obtain x2' M2' x2'' M2'' ta2
      where red21: r2.silent-moves t (x2, m2) (x2', M2')
        and red22: t ⊢ (x2', M2') -2-ta2→ (x2'', M2'') and τ2:  $\neg \tau \text{move2 } (x2', M2') \text{ ta2 } (x2'',$ 
M2'')
        and bisim': t ⊢ (x1', M1') ≈ (x2'', M2'')
        and tasim: ta-bisim bisim ta1 ta2 by auto
        let ?s2' = redT-upd-ε s2 t x2' M2'
        let ?S2' = activate-cond-actions2 s1 ?s2' {ta2}_c
        let ?s2'' = (redT-updLs (locks ?S2') t {ta2}_l, ((redT-updTs (thr ?S2') {ta2}_t)(t ↦ (x2'',
redT-updLns (locks ?S2') t (snd (the (thr ?S2' t))) {ta2}_l)), M2''), wset s1', interrupts s1')
        from red21 tst' wst bisim have τmRed2 s2 ?s2'
          by  $-(\text{rule } r2.\text{silent-moves-into-RedT-}\tau\text{-inv, auto})$ 
        moreover from red21 bisim have [simp]: M2' = m2 by(auto dest: r2.red-rtrancl-τ-heapD-inv)
        from tasim have [simp]:  $\{ \text{ta1} \}_l = \{ \text{ta2} \}_l \{ \text{ta1} \}_w = \{ \text{ta2} \}_w \{ \text{ta1} \}_c = \{ \text{ta2} \}_c \{ \text{ta1} \}_i = \{ \text{ta2} \}_i$ 
          and nta: list-all2 (nta-bisim bisim) {ta1}_t {ta2}_t by(auto simp add: ta-bisim-def)
        from mbisim have tbisim:  $\bigwedge t. \text{tbisim } (ws1 \ t = \text{None}) \ t \ (ts1 \ t) \ m1 \ (ts2 \ t) \ m2$  by(simp add: mbisim-def)
        hence tbisim':  $\bigwedge t'. t' \neq t \implies \text{tbisim } (ws1 \ t' = \text{None}) \ t' \ (ts1 \ t') \ m1 \ (thr \ ?s2' \ t') \ m2$  by(auto)
        from aoe have cao1: r1.cond-action-oks (ls1, (ts1, m1), ws1, is1) t {ta2}_c by auto
        from tst' have thr ?s2' t = [(x2', no-wait-locks)] by(auto simp add: redT-updLns-def o-def finfun-Diag-const2)
        from cond-actions-oks-bisim-ex-τ2-inv[OF tbisim', OF - tst this cao1]
        have red21': τmRed2 ?s2' ?S2' and tbisim'':  $\bigwedge t'. t' \neq t \implies \text{tbisim } (ws1 \ t' = \text{None}) \ t' \ (ts1 \ t')$ 
m1 (thr ?S2' t') m2
          and cao2: r2.cond-action-oks ?S2' t {ta2}_c and tst'': thr ?S2' t = [(x2', no-wait-locks)]
          by(auto simp del: fun-upd-apply)
        note red21' also (rtranclp-trans)
        from tbisim'' tst'' tst have  $\forall t'. ts1 \ t' = \text{None} \iff thr \ ?S2' \ t' = \text{None}$  by(force simp add: tbisim-def)
        from aoe thread-oks-bisim-inv[OF this nta] have thread-oks (thr ?S2') {ta2}_t by simp
        with cao2 aoe have aoe': r2.actions-ok ?S2' t ta2 by auto
        with red22 tst'' s1' have ?S2' -2-t>ta2→ ?s2''
          by  $-(\text{rule } r2.\text{redT.redT-normal, auto})$ 
        moreover
        from τ2 have  $\neg m\tau \text{move2 } ?S2' \ (t, \text{ta2}) \ ?s2''$ 
        proof(rule contrapos-nn)
          assume mτ: mτmove2 ?S2' (t, ta2) ?s2''
          thus τmove2 (x2', M2') ta2 (x2'', M2'') using tst'' tst'
            by cases auto

```

```

qed
moreover
{
  note s1'
  moreover have redT-upd ?S2' t ta2 x2'' M2'' ?s2'' using s1' by auto
  moreover have wset s1 = wset ?S2' locks s1 = locks ?S2' by simp-all
  moreover have wset s1' = wset ?s2'' by simp
  moreover have interrupts s1' = interrupts ?s2'' by simp
  moreover have finite (dom (thr s1)) by simp
  moreover from mbisim have wset-thread-ok (wset s1) (thr s1) by (simp add: mbisim-def)
  moreover from tst have thr s1 t = [(x1, no-wait-locks)] by simp
  moreover note tst'' aoe aoe' tasim bisim'
  moreover have wset s1' t = None  $\vee$  x1'  $\approx_w$  x2''
  proof (cases wset s1' t)
    case None thus ?thesis ..
  next
    case (Some w)
    with wst s1' obtain w' where Suspend1: Suspend w'  $\in$  set {ta1}_w
      by (auto dest: redT-updWs-None-SomeD)
    with tasim have Suspend2: Suspend w'  $\in$  set {ta2}_w by (simp add: ta-bisim-def)
    from bisim-waitI[OF bisim rtranclp.rtrancl-refl red  $\tau$  - - bisim' tasim Suspend1 this, of x2']
red21 red22  $\tau$ 2
    have x1'  $\approx_w$  x2'' by auto
    thus ?thesis ..
  qed
  moreover note rtranclp.rtrancl-refl
  moreover from red have t  $\vdash$  (x1, shr s1) -1-ta1  $\rightarrow$  (x1', M1') by simp
  moreover from red21 have r2.silent-moves t (x2, shr ?S2') (x2', shr ?S2') by simp
  moreover from red22 have t  $\vdash$  (x2', shr ?S2') -2-ta2  $\rightarrow$  (x2'', M2'') by simp
  moreover from bisim have t  $\vdash$  (x1, shr s1)  $\approx$  (x2, shr ?S2') by simp
  moreover from  $\tau$  have  $\neg \tau$ move1 (x1, shr s1) ta1 (x1', M1') by simp
  moreover from  $\tau$ 2 have  $\neg \tau$ move2 (x2', shr ?S2') ta2 (x2'', M2'') by simp
  moreover from tbisim''
  have  $\bigwedge t'. t \neq t' \implies$  tbisim (wset s1 t' = None) t' (thr s1 t') (shr s1) (thr ?S2' t') (shr ?S2')
    by simp
  ultimately have mbisim s1' ?s2'' by (rule mbisim-redT-upd)
}
ultimately show ?thesis using tasim unfolding tl1 s1' by fastforce
next
case (Some w)
with mbisim tst tst' have x1  $\approx_w$  x2
  by (auto dest: mbisim-thrD1)
from aoe Some have wakeup: Notified  $\in$  set {ta1}_w  $\vee$  WokenUp  $\in$  set {ta1}_w
  by (auto simp add: wset-actions-ok-def split: if-split-asm)
from simulation-Wakeup1[OF bisim  $\langle x1 \approx_w x2 \rangle$  red this]
obtain ta2 x2' m2' where red2: t  $\vdash$  (x2, m2) -2-ta2  $\rightarrow$  (x2', m2')
  and bisim': t  $\vdash$  (x1', M1')  $\approx$  (x2', m2')
  and tasim: ta1  $\sim_m$  ta2 by auto

let ?S2' = activate-cond-actions2 s1 s2 {ta2}_c

let ?s2' = (redT-updLs (locks ?S2') t {ta2}_l, ((redT-updTs (thr ?S2') {ta2}_t)(t  $\mapsto$  (x2',
redT-updLns (locks ?S2') t (snd (the (thr ?S2' t))) {ta2}_l)), m2'), wset s1', interrupts s1')

```

from *tasim* **have** [*simp*]: $\{ \{ ta1 \}_l = \{ \{ ta2 \}_l \{ \{ ta1 \}_w = \{ \{ ta2 \}_w \{ \{ ta1 \}_c = \{ \{ ta2 \}_c \{ \{ ta1 \}_i = \{ \{ ta2 \}_i$
and *nta*: *list-all2* (*nta-bisim* *bisim*) $\{ \{ ta1 \}_t \{ \{ ta2 \}_t$ **by** (*auto* *simp* *add*: *ta-bisim-def*)
from *mbisim* **have** *tbisim*: $\bigwedge t. \text{tbisim} (ws1\ t = \text{None})\ t\ (ts1\ t)\ m1\ (ts2\ t)\ m2$ **by** (*simp* *add*: *mbisim-def*)
hence *tbisim'*: $\bigwedge t'. t' \neq t \implies \text{tbisim} (ws1\ t' = \text{None})\ t'\ (ts1\ t')\ m1\ (thr\ s2\ t')\ m2$ **by** (*auto*)
from *aoe* **have** *cao1*: *r1.cond-action-oks* (*ls1*, (*ts1*, *m1*), *ws1*, *is1*) *t* $\{ \{ ta2 \}_c$ **by** *auto*
from *tst'* **have** *thr s2 t* = $[(x2, \text{no-wait-locks})]$
by (*auto* *simp* *add*: *redT-updLns-def* *o-def* *finfun-Diag-const2*)
from *cond-actions-oks-bisim-ex- τ 2-inv*[*OF* *tbisim'*, *OF* - *tst* *this* *cao1*]
have *red21'*: $\tau mRed2\ s2\ ?S2'$ **and** *tbisim''*: $\bigwedge t'. t' \neq t \implies \text{tbisim} (ws1\ t' = \text{None})\ t'\ (ts1\ t')\ m1$
(thr ?S2' t') m2
and *cao2*: *r2.cond-action-oks* $?S2'\ t\ \{ \{ ta2 \}_c$ **and** *tst''*: *thr ?S2' t* = $[(x2, \text{no-wait-locks})]$
by (*auto* *simp* *del*: *fun-upd-apply*)
note *red21'* **moreover**
from *tbisim''* *tst''* *tst* **have** $\forall t'. ts1\ t' = \text{None} \iff thr\ ?S2'\ t' = \text{None}$ **by** (*force* *simp* *add*: *tbisim-def*)
from *aoe* *thread-oks-bisim-inv*[*OF* *this* *nta*] **have** *thread-oks* (*thr ?S2'*) $\{ \{ ta2 \}_t$ **by** *simp*
with *cao2* *aoe* **have** *aoe'*: *r2.actions-ok* $?S2'\ t\ ta2$ **by** *auto*
with *red2* *tst''* *s1'* *tasim* **have** $?S2' - 2 - t \triangleright ta2 \rightarrow ?s2'$
by $-(\text{rule } r2.\text{redT-normal}, \text{auto } \text{simp } \text{add}: \text{ta-bisim-def})$
moreover **from** *wakeup* *tasim*
have $\tau 2: \neg \tau \text{move2 } (x2, m2)\ ta2\ (x2', m2')$ **by** (*auto* *dest*: *r2.silent-tl*)
hence $\neg m\tau \text{move2 } ?S2'\ (t, ta2)\ ?s2'$
proof (*rule* *contrapos-nn*)
assume *m τ* : $m\tau \text{move2 } ?S2'\ (t, ta2)\ ?s2'$
thus $\tau \text{move2 } (x2, m2)\ ta2\ (x2', m2')$ **using** *tst''* *tst'*
by *cases* *auto*
qed
moreover {
note *s1'*
moreover **have** *redT-upd* $?S2'\ t\ ta2\ x2'\ m2'\ ?s2'$ **using** *s1'* *tasim* **by** (*auto* *simp* *add*: *ta-bisim-def*)
moreover **have** *wset* *s1* = *wset* $?S2'\ \text{locks } s1 = \text{locks } ?S2'$ **by** *simp-all*
moreover **have** *wset* *s1'* = *wset* $?s2'$ **by** *simp*
moreover **have** *interrupts* *s1'* = *interrupts* $?s2'$ **by** *simp*
moreover **have** *finite* (*dom* (*thr s1*)) **by** *simp*
moreover **from** *mbisim* **have** *wset-thread-ok* (*wset* *s1*) (*thr s1*) **by** (*rule* *mbisim-wset-thread-ok1*)
moreover **from** *tst* **have** *thr s1 t* = $[(x1, \text{no-wait-locks})]$ **by** *simp*
moreover **from** *tst''* **have** *thr ?S2' t* = $[(x2, \text{no-wait-locks})]$ **by** *simp*
moreover **note** *aoe* *aoe'* *tasim* *bisim'*
moreover **have** *wset* $s1'\ t = \text{None} \vee x1' \approx_w x2'$
proof (*cases* *wset* $s1'\ t$)
case *None* **thus** *?thesis* ..
next
case (*Some* *w'*)
with *redT-updWs-WokenUp-SuspendD*[*OF* - *wakeup*, *of* *t* *wset* *s1* *wset* $s1'\ w'$] *s1'*
obtain *w'* **where** *Suspend1*: *Suspend* $w' \in \text{set } \{ \{ ta1 \}_w$ **by** (*auto*)
with *tasim* **have** *Suspend2*: *Suspend* $w' \in \text{set } \{ \{ ta2 \}_w$ **by** (*simp* *add*: *ta-bisim-def*)
with *bisim* *rtranclp.rtrancl-refl* *red* τ *rtranclp.rtrancl-refl* *red2* $\tau 2$ *bisim'* *tasim* *Suspend1*
have $x1' \approx_w x2'$ **by** (*rule* *bisim-waitI*)
thus *?thesis* ..
qed
moreover **note** *rtranclp.rtrancl-refl*

moreover from *red* **have** $t \vdash (x1, shr\ s1) -1-ta1 \rightarrow (x1', M1')$ **by** *simp*
moreover note *rtranclp.rtrancl-refl*
moreover from *red2* **have** $t \vdash (x2, shr\ ?S2') -2-ta2 \rightarrow (x2', m2')$ **by** *simp*
moreover from *bisim* **have** $t \vdash (x1, shr\ s1) \approx (x2, shr\ ?S2')$ **by** *simp*
moreover from τ **have** $\neg \tau move1\ (x1, shr\ s1)\ ta1\ (x1', M1')$ **by** *simp*
moreover from $\tau 2$ **have** $\neg \tau move2\ (x2, shr\ ?S2')\ ta2\ (x2', m2')$ **by** *simp*
moreover from *tbisim''* **have** $\bigwedge t'. t \neq t' \implies tbisim\ (wset\ s1\ t' = None)\ t'\ (thr\ s1\ t')\ (shr\ s1)\ (thr\ ?S2'\ t')\ (shr\ ?S2')$ **by** *simp*
ultimately have $s1' \approx_m ?s2'$ **by**(*rule mbisim-redT-upd*) }
moreover from *tasim* **have** $tl1 \sim_T (t, ta2)$ **by** *simp*
ultimately show *?thesis unfolding* $s1'$ **by** *blast*
qed
next
case (*redT-acquire* $x1\ n\ ln$)
hence [*simp*]: $ta1 = (K\$ \ [], [], [], [], [], convert-RA\ ln)$
and *tst*: $thr\ s1\ t = [(x1, ln)]$ **and** *wst*: $\neg waiting\ (wset\ s1\ t)$
and *maa*: *may-acquire-all* (*locks* $s1$) $t\ ln$ **and** *ln*: $0 < ln\ \$\ n$
and $s1'$: $s1' = (acquire-all\ ls1\ t\ ln, (ts1(t \mapsto (x1, no-wait-locks)), m1), ws1, is1)$ **by** *auto*
from *tst mbisim* **obtain** $x2$ **where** *tst'*: $ts2\ t = [(x2, ln)]$
and *bisim*: $t \vdash (x1, m1) \approx (x2, m2)$ **by**(*auto dest: mbisim-thrD1*)
let $?s2' = (acquire-all\ ls1\ t\ ln, (ts2(t \mapsto (x2, no-wait-locks)), m2), ws1, is1)$
from *tst' wst maa ln* **have** $s2 -2-t \triangleright (K\$ \ [], [], [], [], [], convert-RA\ ln) \rightarrow ?s2'$
by-(*rule r2.redT.redT-acquire, auto*)
moreover from *tst' ln* **have** $\neg m\tau move2\ s2\ (t, (K\$ \ [], [], [], [], [], convert-RA\ ln))\ ?s2'$
by(*auto simp add: acquire-all-def fun-eq-iff elim!: r2.m\tau move.cases*)
moreover have *mbisim* $s1'\ ?s2'$
proof(*rule mbisimI*)
from $s1'$ **show** *locks* $s1' = locks\ ?s2'$ **by** *auto*
next
from $s1'$ **show** *wset* $s1' = wset\ ?s2'$ **by** *auto*
next
from $s1'$ **show** *interrupts* $s1' = interrupts\ ?s2'$ **by** *auto*
next
fix t' **assume** *thr* $s1'\ t' = None$
with $s1'$ **have** *thr* $s1\ t' = None$ **by**(*auto split: if-split-asm*)
with *mbisim-thrNone-eq*[*OF mbisim*] **have** $ts2\ t' = None$ **by** *simp*
with *tst'* **show** *thr* $?s2'\ t' = None$ **by** *auto*
next
fix $t'\ X1\ LN$
assume *ts't*: $thr\ s1'\ t' = [(X1, LN)]$
show $\exists x2. thr\ ?s2'\ t' = [(x2, LN)] \wedge t' \vdash (X1, shr\ s1') \approx (x2, shr\ ?s2') \wedge (wset\ ?s2'\ t' = None \vee X1 \approx_w x2)$
proof(*cases* $t' = t$)
case *True*
with $s1'\ tst\ ts't$ **have** [*simp*]: $X1 = x1\ LN = no-wait-locks$ **by** *simp-all*
with *mbisim-thrD1*[*OF mbisim tst*] *bisim* $tst\ ts't\ True\ s1'\ wst$ **show** *?thesis* **by**(*auto*)
next
case *False*
with *ts't* $s1'$ **have** $ts1\ t' = [(X1, LN)]$ **by** *auto*
with *mbisim* **obtain** $X2$ **where** $ts2\ t' = [(X2, LN)]\ t' \vdash (X1, m1) \approx (X2, m2)\ wset\ ?s2'\ t' = None \vee X1 \approx_w X2$
by(*auto dest: mbisim-thrD1*)
with *False* $s1'$ **show** *?thesis* **by** *auto*
qed

```

next
  from  $s1'$  show finite (dom (thr  $s1'$ )) by auto
next
  from mbisim-wset-thread-ok1[OF mbisim]
  show wset-thread-ok (wset  $s1'$ ) (thr  $s1'$ ) using  $s1'$  by(auto intro: wset-thread-ok-upd)
qed
moreover have (t, K$ [], [], [], [], [], convert-RA ln)  $\sim_T$  (t, K$ [], [], [], [], [], convert-RA ln)
  by(simp add: ta-bisim-def)
ultimately show ?thesis by fastforce
qed
qed

theorem mbisim-simulation2:
  [[ mbisim  $s1 s2$ ; r2.redT  $s2 tl2 s2'$ ;  $\neg m\tau move2 s2 tl2 s2'$  ]
   $\implies \exists s1' s1'' tl1. r1.mthr.silent-moves s1 s1' \wedge r1.redT s1' tl1 s1'' \wedge \neg m\tau move1 s1' tl1 s1'' \wedge$ 
    mbisim  $s1'' s2' \wedge mta-bisim tl1 tl2$ 
using FWdelay-bisimulation-obs.mbisim-simulation1[OF FWdelay-bisimulation-obs-flip]
unfolding flip-simps .

end

locale FWdelay-bisimulation-diverge =
  FWdelay-bisimulation-obs - - - - -  $\tau move1 \tau move2$ 
  for  $\tau move1 :: ('l, 't, 'x1, 'm1, 'w, 'o) \tau moves$ 
  and  $\tau move2 :: ('l, 't, 'x2, 'm2, 'w, 'o) \tau moves +$ 
  assumes delay-bisimulation-diverge-locale: delay-bisimulation-diverge (r1 t) (r2 t) (bisim t) (ta-bisim
  bisim)  $\tau move1 \tau move2$ 

sublocale FWdelay-bisimulation-diverge <
  delay-bisimulation-diverge r1 t r2 t bisim t ta-bisim bisim  $\tau move1 \tau move2$  for t
by(rule delay-bisimulation-diverge-locale)

context FWdelay-bisimulation-diverge begin

lemma FWdelay-bisimulation-diverge-flip:
  FWdelay-bisimulation-diverge final2 r2 final1 r1 ( $\lambda t. flip (bisim t)$ ) (flip bisim-wait)  $\tau move2 \tau move1$ 
apply(rule FWdelay-bisimulation-diverge.intro)
  apply(rule FWdelay-bisimulation-obs-flip)
apply(rule FWdelay-bisimulation-diverge-axioms.intro)
  apply(unfold flip-simps)
  apply(rule delay-bisimulation-diverge-axioms)
done

end

lemma FWdelay-bisimulation-diverge-flip-simps [flip-simps]:
  FWdelay-bisimulation-diverge final2 r2 final1 r1 ( $\lambda t. flip (bisim t)$ ) (flip bisim-wait)  $\tau move2 \tau move1$ 
=
  FWdelay-bisimulation-diverge final1 r1 final2 r2 bisim bisim-wait  $\tau move1 \tau move2$ 
by(auto dest: FWdelay-bisimulation-diverge.FWdelay-bisimulation-diverge-flip simp only: flip-flip)

context FWdelay-bisimulation-diverge begin

lemma bisim-inv1:

```

assumes $\text{bisim}: t \vdash s1 \approx s2$
and $\text{red}: t \vdash s1 \text{ --1--} \text{ta1} \rightarrow s1'$
obtains $s2'$ **where** $t \vdash s1' \approx s2'$
proof(*atomize-elim*)
show $\exists s2'. t \vdash s1' \approx s2'$
proof(*cases* $\tau\text{move1 } s1 \text{ ta1 } s1'$)
 case *True*
 with red **have** $r1.\text{silent-move } t \text{ } s1 \text{ } s1'$ **by** *auto*
 from *simulation-silent1* [*OF bisim this*]
 show *?thesis* **by** *auto*
 next
 case *False*
 from *simulation1* [*OF bisim red False*] **show** *?thesis* **by** *auto*
 qed
qed

lemma *bisim-inv2*:
 assumes $t \vdash s1 \approx s2$ $t \vdash s2 \text{ --2--} \text{ta2} \rightarrow s2'$
 obtains $s1'$ **where** $t \vdash s1' \approx s2'$
using *assms FWdelay-bisimulation-diverge.bisim-inv1* [*OF FWdelay-bisimulation-diverge-flip*]
unfolding *flip-simps* **by** *blast*

lemma *bisim-inv: bisim-inv*
by(*blast intro!: bisim-invI elim: bisim-inv1 bisim-inv2*)

lemma *bisim-inv- τ s1*:
 assumes $t \vdash s1 \approx s2$ **and** $r1.\text{silent-moves } t \text{ } s1 \text{ } s1'$
 obtains $s2'$ **where** $t \vdash s1' \approx s2'$
using *assms* **by**(*rule bisim-inv- τ s1-inv* [*OF bisim-inv*])

lemma *bisim-inv- τ s2*:
 assumes $t \vdash s1 \approx s2$ **and** $r2.\text{silent-moves } t \text{ } s2 \text{ } s2'$
 obtains $s1'$ **where** $t \vdash s1' \approx s2'$
using *assms* **by**(*rule bisim-inv- τ s2-inv* [*OF bisim-inv*])

lemma *red1-rtrancl- τ -into-RedT- τ* :
 assumes $r1.\text{silent-moves } t \text{ } (x1, \text{shr } s1) \text{ } (x1', m1')$ $t \vdash (x1, \text{shr } s1) \approx (x2, m2)$
 and $\text{thr } s1 \text{ } t = \lfloor (x1, \text{no-wait-locks}) \rfloor$ $\text{wset } s1 \text{ } t = \text{None}$
 shows $\tau\text{mRed1 } s1 \text{ } (redT\text{-upd-}\epsilon \text{ } s1 \text{ } t \text{ } x1' \text{ } m1')$
using *assms* **by**(*blast intro: r1.silent-moves-into-RedT- τ -inv*)

lemma *red2-rtrancl- τ -into-RedT- τ* :
 assumes $r2.\text{silent-moves } t \text{ } (x2, \text{shr } s2) \text{ } (x2', m2')$
 and $t \vdash (x1, m1) \approx (x2, \text{shr } s2)$ $\text{thr } s2 \text{ } t = \lfloor (x2, \text{no-wait-locks}) \rfloor$ $\text{wset } s2 \text{ } t = \text{None}$
 shows $\tau\text{mRed2 } s2 \text{ } (redT\text{-upd-}\epsilon \text{ } s2 \text{ } t \text{ } x2' \text{ } m2')$
using *assms* **by**(*blast intro: r2.silent-moves-into-RedT- τ -inv*)

lemma *red1-rtrancl- τ -heapD*:
 $\llbracket r1.\text{silent-moves } t \text{ } s1 \text{ } s1'; t \vdash s1 \approx s2 \rrbracket \implies \text{snd } s1' = \text{snd } s1$
by(*blast intro: r1.red-rtrancl- τ -heapD-inv*)

lemma *red2-rtrancl- τ -heapD*:
 $\llbracket r2.\text{silent-moves } t \text{ } s2 \text{ } s2'; t \vdash s1 \approx s2 \rrbracket \implies \text{snd } s2' = \text{snd } s2$
by(*blast intro: r2.red-rtrancl- τ -heapD-inv*)

lemma *mbisim-simulation-silent1*:

assumes $m\tau'$: $r1.mthr.silent-move\ s1\ s1'$ and $mbisim$: $s1 \approx_m s2$
 shows $\exists s2'$. $r2.mthr.silent-moves\ s2\ s2' \wedge s1' \approx_m s2'$

proof –

from $m\tau'$ obtain $tl1$ where $m\tau$: $m\tau move1\ s1\ tl1\ s1'\ r1.redT\ s1\ tl1\ s1'$ by *auto*
 obtain $ls1\ ts1\ m1\ ws1\ is1$ where $[simp]$: $s1 = (ls1, (ts1, m1), ws1, is1)$ by $(cases\ s1)$ *fastforce*
 obtain $ls1'\ ts1'\ m1'\ ws1'\ is1'$ where $[simp]$: $s1' = (ls1', (ts1', m1'), ws1', is1')$ by $(cases\ s1')$

fastforce

obtain $ls2\ ts2\ m2\ ws2\ is2$ where $[simp]$: $s2 = (ls2, (ts2, m2), ws2, is2)$ by $(cases\ s2)$ *fastforce*
 from $m\tau$ obtain t where $tl1 = (t, \varepsilon)$ by $(auto\ elim!\ r1.m\tau move.cases\ dest:\ r1.silent-tl)$

with $m\tau$ have $m\tau$: $m\tau move1\ s1\ (t, \varepsilon)\ s1'$ and $redT1$: $s1 -1-t \triangleright \varepsilon \rightarrow s1'$ by *simp-all*

from $m\tau$ obtain $x\ x'\ ln'$ where tst : $ts1\ t = \lfloor (x, no-wait-locks) \rfloor$

and $ts't$: $ts1'\ t = \lfloor (x', ln') \rfloor$ and τ : $\tau move1\ (x, m1)\ \varepsilon\ (x', m1')$

by $(fastforce\ elim:\ r1.m\tau move.cases)$

from $mbisim$ have $[simp]$: $ls2 = ls1\ ws2 = ws1\ is2 = is1$ *finite (dom ts1)* by $(auto\ simp\ add:\ mbisim-def)$

from $redT1$ show *?thesis*

proof *cases*

case $(redT-normal\ x1\ x1'\ M')$

with $tst\ ts't$ have $[simp]$: $x = x1\ x' = x1'$

and red : $t \vdash (x1, m1) -1-\varepsilon \rightarrow (x1', M')$

and tst : $thr\ s1\ t = \lfloor (x1, no-wait-locks) \rfloor$

and wst : $wset\ s1\ t = None$

and $s1'$: $redT-upd\ s1\ t\ \varepsilon\ x1'\ M'\ s1'$ by $(auto)$

from $s1'\ tst$ have $[simp]$: $ls1' = ls1\ ws1' = ws1\ is1' = is1\ M' = m1'\ ts1' = ts1(t \mapsto (x1', no-wait-locks))$

by $(auto\ simp\ add:\ redT-updLs-def\ redT-updLns-def\ o-def\ redT-updWs-def\ elim!\ rtrancl3p-cases)$

from $mbisim\ tst$ obtain $x2$ where tst' : $ts2\ t = \lfloor (x2, no-wait-locks) \rfloor$

and $bisim$: $t \vdash (x1, m1) \approx (x2, m2)$ by $(auto\ dest:\ mbisim-thrD1)$

from $r1.\tau move-heap[OF\ red]\ \tau$ have $[simp]$: $m1 = M'$ by *simp*

from $red\ \tau$ have $r1.silent-move\ t\ (x1, m1)\ (x1', M')$ by *auto*

from *simulation-silent1* $[OF\ bisim\ this]$

obtain $x2'\ m2'$ where red : $r2.silent-moves\ t\ (x2, m2)\ (x2', m2')$

and $bisim'$: $t \vdash (x1', m1) \approx (x2', m2')$ by *auto*

from $red\ bisim$ have $[simp]$: $m2' = m2$

by $(auto\ dest:\ red2-rtrancl-\tau-heapD)$

let $?s2' = redT-upd-\varepsilon\ s2\ t\ x2'\ m2'$

from $red\ tst'\ wst\ bisim$ have $\tau mRed2\ s2\ ?s2'$

by $-(rule\ red2-rtrancl-\tau-into-RedT-\tau,\ auto)$

moreover have $mbisim\ s1'\ ?s2'$

proof $(rule\ mbisimI)$

show $locks\ s1' = locks\ ?s2'\ wset\ s1' = wset\ ?s2'\ interrupts\ s1' = interrupts\ ?s2'$ by *auto*

next

fix t'

assume $thr\ s1'\ t' = None$

hence $ts1\ t' = None$ by $(auto\ split:\ if-split-asm)$

with $mbisim-thrNone-eq[OF\ mbisim]$ have $ts2\ t' = None$ by *simp*

with tst' show $thr\ ?s2'\ t' = None$ by *auto*

next

fix $t'\ X1\ LN$

assume $ts't$: $thr\ s1'\ t' = \lfloor (X1, LN) \rfloor$

show $\exists x2.$ $thr\ ?s2'\ t' = \lfloor (x2, LN) \rfloor \wedge t' \vdash (X1, shr\ s1') \approx (x2, shr\ ?s2') \wedge (wset\ ?s2'\ t' = None \vee X1 \approx_w x2)$

```

proof(cases t' = t)
  case True
  note this[simp]
  with s1' tst ts't' have [simp]: X1 = x1' LN = no-wait-locks
    by(simp-all)(auto simp add: redT-updLns-def o-def finfun-Diag-const2)
  with bisim' tst' wst show ?thesis by(auto simp add: redT-updLns-def o-def finfun-Diag-const2)
next
  case False
  with ts't' have ts1 t' = [(X1, LN)] by auto
    with mbisim obtain X2 where ts2 t' = [(X2, LN)] t' ⊢ (X1, m1) ≈ (X2, m2) ws1 t' =
None ∨ X1 ≈w X2
    by(auto dest: mbisim-thrD1)
    with False show ?thesis by auto
  qed
next
  show finite (dom (thr s1')) by simp
next
  from mbisim-wset-thread-ok1[OF mbisim]
  show wset-thread-ok (wset s1') (thr s1') by(auto intro: wset-thread-ok-upd)
  qed
  ultimately show ?thesis by(auto)
next
  case redT-acquire
  with tst have False by auto
  thus ?thesis ..
  qed
qed

```

lemma *mbisim-simulation-silent2*:

```

[[ mbisim s1 s2; r2.mthr.silent-move s2 s2' ]
⇒ ∃ s1'. r1.mthr.silent-moves s1 s1' ∧ mbisim s1' s2'

```

using *FWdelay-bisimulation-diverge.mbisim-simulation-silent1* [OF *FWdelay-bisimulation-diverge-flip*]
unfolding *flip-simps* .

lemma *mbisim-simulation1'*:

```

assumes mbisim: mbisim s1 s2 and ¬ mτmove1 s1 tl1 s1' r1.redT s1 tl1 s1'
shows ∃ s2' s2'' tl2. r2.mthr.silent-moves s2 s2' ∧ r2.redT s2' tl2 s2'' ∧
  ¬ mτmove2 s2' tl2 s2'' ∧ mbisim s1' s2'' ∧ mta-bisim tl1 tl2

```

using *mbisim-simulation1* *assms* .

lemma *mbisim-simulation2'*:

```

[[ mbisim s1 s2; r2.redT s2 tl2 s2'; ¬ mτmove2 s2 tl2 s2' ]
⇒ ∃ s1' s1'' tl1. r1.mthr.silent-moves s1 s1' ∧ r1.redT s1' tl1 s1'' ∧ ¬ mτmove1 s1' tl1 s1'' ∧
  mbisim s1'' s2' ∧ mta-bisim tl1 tl2

```

using *FWdelay-bisimulation-diverge.mbisim-simulation1'* [OF *FWdelay-bisimulation-diverge-flip*]
unfolding *flip-simps* .

lemma *mτdiverge-simulation1*:

```

assumes s1 ≈m s2
and r1.mthr.τdiverge s1
shows r2.mthr.τdiverge s2

```

proof –

```

from ⟨s1 ≈m s2⟩ have finite (dom (thr s1))
  by(rule mbisim-finite1)+

```

from $r1.\tau\text{diverge-}\tau\text{mredTD}[OF \langle r1.mthr.\tau\text{diverge } s1 \rangle \text{ this}]$
obtain $t \ x$ **where** $\text{thr } s1 \ t = \llbracket (x, \text{no-wait-locks}) \rrbracket \ \text{uset } s1 \ t = \text{None}$ $r1.\tau\text{diverge } t \ (x, \text{shr } s1)$ **by** *blast*
from $\langle s1 \approx m \ s2 \rangle \ \langle \text{thr } s1 \ t = \llbracket (x, \text{no-wait-locks}) \rrbracket \rangle$ **obtain** x'
where $\text{thr } s2 \ t = \llbracket (x', \text{no-wait-locks}) \rrbracket \ t \vdash (x, \text{shr } s1) \approx (x', \text{shr } s2)$
by(*auto dest: mbisim-thrD1*)
from $\langle s1 \approx m \ s2 \rangle \ \langle \text{uset } s1 \ t = \text{None} \rangle$ **have** $\text{uset } s2 \ t = \text{None}$ **by**(*simp add: mbisim-def*)
from $\langle t \vdash (x, \text{shr } s1) \approx (x', \text{shr } s2) \rangle \ \langle r1.\tau\text{diverge } t \ (x, \text{shr } s1) \rangle$
have $r2.\tau\text{diverge } t \ (x', \text{shr } s2)$ **by**(*simp add: \tau\text{diverge-bisim-inv}*)
thus *?thesis* **using** $\langle \text{thr } s2 \ t = \llbracket (x', \text{no-wait-locks}) \rrbracket \rangle \ \langle \text{uset } s2 \ t = \text{None} \rangle$
by(*rule r2.\tau\text{diverge-into-}\tau\text{mredT}*)
qed

lemma $\tau\text{diverge-mbisim-inv}$:

$s1 \approx m \ s2 \implies r1.mthr.\tau\text{diverge } s1 \longleftrightarrow r2.mthr.\tau\text{diverge } s2$

apply(*rule iffI*)

apply(*erule (1) m\tau\text{diverge-simulation1}*)

by(*rule FWdelay-bisimulation-diverge.m\tau\text{diverge-simulation1}[OF FWdelay-bisimulation-diverge-flip, unfolded flip-simps]*)

lemma $\text{mbisim-delay-bisimulation}$:

$\text{delay-bisimulation-diverge } r1.\text{redT } r2.\text{redT } \text{mbisim } \text{mta-bisim } m\tau\text{move1 } m\tau\text{move2}$

apply(*unfold-locales*)

apply(*rule mbisim-simulation1 mbisim-simulation2 mbisim-simulation-silent1 mbisim-simulation-silent2 \tau\text{diverge-mbisim-inv|assumption}*)**+**

done

theorem $\text{mdelay-bisimulation-final-base}$:

$\text{delay-bisimulation-final-base } r1.\text{redT } r2.\text{redT } \text{mbisim } m\tau\text{move1 } m\tau\text{move2 } r1.\text{mfinal } r2.\text{mfinal}$

apply(*unfold-locales*)

apply(*blast dest: mfinal1-simulation mfinal2-simulation*)**+**

done

end

sublocale $\text{FWdelay-bisimulation-diverge} < \text{mthr: delay-bisimulation-diverge } r1.\text{redT } r2.\text{redT } \text{mbisim } \text{mta-bisim } m\tau\text{move1 } m\tau\text{move2}$

by(*rule mbisim-delay-bisimulation*)

sublocale $\text{FWdelay-bisimulation-diverge} <$

$\text{mthr: delay-bisimulation-final-base } r1.\text{redT } r2.\text{redT } \text{mbisim } \text{mta-bisim } m\tau\text{move1 } m\tau\text{move2 } r1.\text{mfinal } r2.\text{mfinal}$

by(*rule mdelay-bisimulation-final-base*)

context $\text{FWdelay-bisimulation-diverge}$ **begin**

lemma $\text{mthr-delay-bisimulation-diverge-final}$:

$\text{delay-bisimulation-diverge-final } r1.\text{redT } r2.\text{redT } \text{mbisim } \text{mta-bisim } m\tau\text{move1 } m\tau\text{move2 } r1.\text{mfinal } r2.\text{mfinal}$

by(*unfold-locales*)

end

sublocale $\text{FWdelay-bisimulation-diverge} <$

$\text{mthr: delay-bisimulation-diverge-final } r1.\text{redT } r2.\text{redT } \text{mbisim } \text{mta-bisim } m\tau\text{move1 } m\tau\text{move2 } r1.\text{mfinal}$

r2.mfinal
by(rule *mthr-delay-bisimulation-diverge-final*)

1.18.3 Strong bisimulation as corollary

locale *FWbisimulation* = *FWbisimulation-base* - - - *r2 convert-RA bisim* $\lambda x1 x2$. *True* +
r1: multithreaded final1 r1 convert-RA +
r2: multithreaded final2 r2 convert-RA
for *r2* :: (*'l,'t,'x2,'m2,'w,'o*) *semantics* ($\langle \cdot \vdash - - 2 \dashrightarrow \cdot \rangle$ [50,0,0,50] 80)
and *convert-RA* :: *'l released-locks* \Rightarrow *'o list*
and *bisim* :: *'t* \Rightarrow (*'x1* \times *'m1*, *'x2* \times *'m2*) *bisim* ($\langle \cdot \vdash - / \approx \cdot \rangle$ [50, 50, 50] 60) +
assumes *bisimulation-locale: bisimulation* (*r1 t*) (*r2 t*) (*bisim t*) (*ta-bisim bisim*)
and *bisim-final: t* \vdash (*x1*, *m1*) \approx (*x2*, *m2*) \Longrightarrow *final1 x1* \longleftrightarrow *final2 x2*
and *bisim-inv-red-other*:
 \llbracket *t'* \vdash (*x*, *m1*) \approx (*xx*, *m2*); *t* \vdash (*x1*, *m1*) \approx (*x2*, *m2*);
t \vdash (*x1*, *m1*) $-1-ta1 \rightarrow$ (*x1'*, *m1'*); *t* \vdash (*x2*, *m2*) $-2-ta2 \rightarrow$ (*x2'*, *m2'*);
t \vdash (*x1'*, *m1'*) \approx (*x2'*, *m2'*); *ta-bisim bisim ta1 ta2* \rrbracket
 \Longrightarrow *t'* \vdash (*x*, *m1'*) \approx (*xx*, *m2'*)
and *ex-final1-conv-ex-final2*:
($\exists x1$. *final1 x1*) \longleftrightarrow ($\exists x2$. *final2 x2*)

sublocale *FWbisimulation* < *bisim?*: *bisimulation r1 t r2 t bisim t ta-bisim bisim* **for** *t*
by(rule *bisimulation-locale*)

sublocale *FWbisimulation* < *bisim-diverge?*:

FWdelay-bisimulation-diverge final1 r1 final2 r2 convert-RA bisim $\lambda x1 x2$. *True* λs *ta s'*. *False* λs *ta s'*. *False*

proof -

interpret *biw: bisimulation-into-delay r1 t r2 t bisim t ta-bisim bisim* λs *ta s'*. *False* λs *ta s'*. *False*
for *t*

by(*unfold-locale*) *simp*

show *FWdelay-bisimulation-diverge final1 r1 final2 r2 bisim* ($\lambda x1 x2$. *True*) (λs *ta s'*. *False*) (λs *ta s'*. *False*)

proof(*unfold-locale*)

fix *t' x m1 xx m2 x1 x2 t x1' ta1 x1'' m1' x2' ta2 x2'' m2'*

assume *bisim: t'* \vdash (*x*, *m1*) \approx (*xx*, *m2*) **and** *bisim12: t* \vdash (*x1*, *m1*) \approx (*x2*, *m2*)

and $\tau 1$: τ *trsys.silent-moves* (*r1 t*) (λs *ta s'*. *False*) (*x1*, *m1*) (*x1'*, *m1*)

and *red1: t* \vdash (*x1'*, *m1*) $-1-ta1 \rightarrow$ (*x1''*, *m1'*)

and $\tau 2$: τ *trsys.silent-moves* (*r2 t*) (λs *ta s'*. *False*) (*x2*, *m2*) (*x2'*, *m2*)

and *red2: t* \vdash (*x2'*, *m2*) $-2-ta2 \rightarrow$ (*x2''*, *m2'*)

and *bisim12': t* \vdash (*x1''*, *m1'*) \approx (*x2''*, *m2'*) **and** *tasim: ta1* \sim_m *ta2*

from $\tau 1$ $\tau 2$ **have** [*simp*]: *x1' = x1 x2' = x2* **by**(*simp-all add: rtranclp-False* τ *moves-False*)

from *bisim12 bisim-inv-red-other*[*OF bisim - red1 red2 bisim12' tasim*]

show *t'* \vdash (*x*, *m1'*) \approx (*xx*, *m2'*) **by** *simp*

next

fix *t x1 m1 x2 m2 ta1 x1' m1'*

assume *t* \vdash (*x1*, *m1*) \approx (*x2*, *m2*) *t* \vdash (*x1*, *m1*) $-1-ta1 \rightarrow$ (*x1'*, *m1'*)

from *simulation1*[*OF this*]

show $\exists ta2 x2' m2'$. *t* \vdash (*x2*, *m2*) $-2-ta2 \rightarrow$ (*x2'*, *m2'*) \wedge *t* \vdash (*x1'*, *m1'*) \approx (*x2'*, *m2'*) \wedge *ta1* \sim_m *ta2*

by *auto*

next

fix *t x1 m1 x2 m2 ta2 x2' m2'*

assume *t* \vdash (*x1*, *m1*) \approx (*x2*, *m2*) *t* \vdash (*x2*, *m2*) $-2-ta2 \rightarrow$ (*x2'*, *m2'*)

```

from simulation2[OF this]
show  $\exists ta1\ x1'\ m1'. t \vdash (x1, m1) -1 -ta1 \rightarrow (x1', m1') \wedge t \vdash (x1', m1') \approx (x2', m2') \wedge ta1 \sim_m$ 
  ta2
  by auto
next
  show  $(\exists x1. final1\ x1) \longleftrightarrow (\exists x2. final2\ x2)$  by(rule ex-final1-conv-ex-final2)
qed(fastforce simp add: bisim-final)+
qed

```

context FWbisimulation **begin**

```

lemma FWbisimulation-flip: FWbisimulation final2 r2 final1 r1 ( $\lambda t. flip\ (bisim\ t)$ )
apply(rule FWbisimulation.intro)
  apply(rule r2.multithreaded-axioms)
  apply(rule r1.multithreaded-axioms)
apply(rule FWbisimulation-axioms.intro)
  apply(unfold flip-simps)
  apply(rule bisimulation-axioms)
  apply(erule bisim-final[symmetric])
  apply(erule (5) bisim-inv-red-other)
apply(rule ex-final1-conv-ex-final2[symmetric])
done

```

end

lemma FWbisimulation-flip-simps [flip-simps]:

$FWbisimulation\ final2\ r2\ final1\ r1\ (\lambda t. flip\ (bisim\ t)) = FWbisimulation\ final1\ r1\ final2\ r2\ bisim$
by(auto dest: FWbisimulation.FWbisimulation-flip simp only: flip-flip)

context FWbisimulation **begin**

The notation for mbisim is lost because *bisim-wait* is instantiated to $\lambda x1\ x2. True$. This reintroduces the syntax, but it does not work for output mode. This would require a new abbreviation.

notation mbisim ($\langle - \approx_m \rightarrow [50, 50] 60$)

theorem mbisim-bisimulation:

$bisimulation\ r1.redT\ r2.redT\ mbisim\ mta-bisim$

proof

fix s1 s2 tta1 s1'

assume mbisim: $s1 \approx_m s2$ **and** r1.redT s1 tta1 s1'

from mthr.simulation1[OF this]

show $\exists s2'\ tta2. r2.redT\ s2\ tta2\ s2' \wedge s1' \approx_m s2' \wedge tta1 \sim_T tta2$

by(auto simp add: $\tau moves-False\ m\tau move-False$)

next

fix s2 s1 tta2 s2'

assume s1 \approx_m s2 **and** r2.redT s2 tta2 s2'

from mthr.simulation2[OF this]

show $\exists s1'\ tta1. r1.redT\ s1\ tta1\ s1' \wedge s1' \approx_m s2' \wedge tta1 \sim_T tta2$

by(auto simp add: $\tau moves-False\ m\tau move-False$)

qed

lemma mbisim-wset-eq:

$s1 \approx_m s2 \implies wset\ s1 = wset\ s2$

by(*simp add: mbisim-def*)

lemma *mbisim-mfinal*:

$s1 \approx_m s2 \implies r1.mfinal\ s1 \longleftrightarrow r2.mfinal\ s2$

apply(*auto intro!: r2.mfinalI r1.mfinalI dest: mbisim-thrD2 mbisim-thrD1 bisim-final elim: r1.mfinalE r2.mfinalE*)

apply(*frule (1) mbisim-thrD2, drule mbisim-wset-eq, auto elim: r1.mfinalE*)

apply(*frule (1) mbisim-thrD1, drule mbisim-wset-eq, auto elim: r2.mfinalE*)

done

end

sublocale *FWbisimulation* < *mthr: bisimulation r1.redT r2.redT mbisim mta-bisim*

by(*rule mbisim-bisimulation*)

sublocale *FWbisimulation* < *mthr: bisimulation-final r1.redT r2.redT mbisim mta-bisim r1.mfinal r2.mfinal*

by(*unfold-locales*)(*rule mbisim-mfinal*)

end

1.19 Preservation of deadlock across bisimulations

theory *FWBisimDeadlock*

imports

FWBisimulation

FWDeadlock

begin

context *FWdelay-bisimulation-obs* begin

lemma *actions-ok1-ex-actions-ok2*:

assumes *r1.actions-ok s1 t ta1*

and $ta1 \sim_m ta2$

obtains *s2* where *r2.actions-ok s2 t ta2*

proof –

let $?s2 = (locks\ s1, (\lambda t. map-option\ (\lambda(x1, ln). (SOME\ x2. if\ final1\ x1\ then\ final2\ x2\ else\ \neg\ final2\ x2, ln))\ (thr\ s1\ t), undefined), wset\ s1, interrupts\ s1)$

from $\langle ta1 \sim_m ta2 \rangle$ have $\llbracket ta1 \rrbracket_c = \llbracket ta2 \rrbracket_c$ by(*simp add: ta-bisim-def*)

with $\langle r1.actions-ok\ s1\ t\ ta1 \rangle$ have *cao1*: *r1.cond-action-oks s1 t* $\llbracket ta2 \rrbracket_c$ by *auto*

have *r2.cond-action-oks ?s2 t* $\llbracket ta2 \rrbracket_c$ unfolding *r2.cond-action-oks-conv-set*

proof

fix *ct*

assume $ct \in set\ \llbracket ta2 \rrbracket_c$

with *cao1* have *r1.cond-action-ok s1 t ct*

unfolding *r1.cond-action-oks-conv-set* by *auto*

thus *r2.cond-action-ok ?s2 t ct* using *ex-final1-conv-ex-final2*

by(*cases ct*)(*fastforce intro: someI-ex[where P=final2]*)+

qed

hence *r2.actions-ok ?s2 t ta2*

using *assms* by(*auto simp add: ta-bisim-def split del: if-split elim: rev-iffD1[OF - thread-oks-bisim-inv]*)

thus *thesis* by(*rule that*)

qed

lemma *actions-ok2-ex-actions-ok1*:

assumes $r2.actions\text{-}ok\ s2\ t\ ta2$

and $ta1 \sim_m ta2$

obtains $s1$ **where** $r1.actions\text{-}ok\ s1\ t\ ta1$

using *FWdelay-bisimulation-obs.actions-ok1-ex-actions-ok2*[*OF FWdelay-bisimulation-obs-flip*] *assms*
unfolding *flip-simps* .

lemma *ex-actions-ok1-conv-ex-actions-ok2*:

$ta1 \sim_m ta2 \implies (\exists s1. r1.actions\text{-}ok\ s1\ t\ ta1) \longleftrightarrow (\exists s2. r2.actions\text{-}ok\ s2\ t\ ta2)$

by(*metis actions-ok1-ex-actions-ok2 actions-ok2-ex-actions-ok1*)

end

context *FWdelay-bisimulation-diverge* **begin**

lemma *no- τ Move1- τ s-to-no- τ Move2*:

fixes $no\text{-}\tau\text{moves}1\ no\text{-}\tau\text{moves}2$

defines $no\text{-}\tau\text{moves}1 \equiv \lambda s1\ t. wset\ s1\ t = None \wedge (\exists x. thr\ s1\ t = [(x, no\text{-}wait\text{-}locks)] \wedge (\forall x'\ m'. \neg r1.silent\text{-}move\ t\ (x, shr\ s1)\ (x', m')))$

defines $no\text{-}\tau\text{moves}2 \equiv \lambda s2\ t. wset\ s2\ t = None \wedge (\exists x. thr\ s2\ t = [(x, no\text{-}wait\text{-}locks)] \wedge (\forall x'\ m'. \neg r2.silent\text{-}move\ t\ (x, shr\ s2)\ (x', m')))$

assumes $mbisim: s1 \approx_m (ls2, (ts2, m2), ws2, is2)$

shows $\exists ts2'. r2.mthr.silent\text{-}moves\ (ls2, (ts2, m2), ws2, is2)\ (ls2, (ts2', m2), ws2, is2) \wedge$
 $(\forall t. no\text{-}\tau\text{moves}1\ s1\ t \longrightarrow no\text{-}\tau\text{moves}2\ (ls2, (ts2', m2), ws2, is2)\ t) \wedge s1 \approx_m (ls2, (ts2', m2), ws2, is2)$

proof –

from $mbisim$ **have** $finite\ (dom\ (thr\ s1))$ **by**(*simp add: mbisim-def*)

hence $finite\ \{t. no\text{-}\tau\text{moves}1\ s1\ t\}$ **unfolding** $no\text{-}\tau\text{moves}1\text{-}def$

by–(*rule finite-subset, auto*)

thus *?thesis* **using** $\langle s1 \approx_m (ls2, (ts2, m2), ws2, is2) \rangle$

proof(*induct A $\equiv\{t. no\text{-}\tau\text{moves}1\ s1\ t\}$ arbitrary: s1 ts2 rule: finite-induct*)

case *empty*

from $\langle \{\} = \{t. no\text{-}\tau\text{moves}1\ s1\ t\} \rangle$ [*symmetric*] **have** $no\text{-}\tau\text{moves}1\ s1 = (\lambda t. False)$

by(*auto intro: ext*)

thus *?case* **using** $\langle s1 \approx_m (ls2, (ts2, m2), ws2, is2) \rangle$ **by** *auto*

next

case (*insert t A*)

note $mbisim = \langle s1 \approx_m (ls2, (ts2, m2), ws2, is2) \rangle$

from $\langle insert\ t\ A = \{t. no\text{-}\tau\text{moves}1\ s1\ t\} \rangle$

have $no\text{-}\tau\text{moves}1\ s1\ t$ **by** *auto*

then obtain $x1$ **where** $ts1t: thr\ s1\ t = [(x1, no\text{-}wait\text{-}locks)]$

and $ws1t: wset\ s1\ t = None$

and $\tau1: \bigwedge x1m1'. \neg r1.silent\text{-}move\ t\ (x1, shr\ s1)\ x1m1'$

by(*auto simp add: no- τ moves1-def*)

from $ts1t\ mbisim$ **obtain** $x2$ **where** $ts2t: ts2\ t = [(x2, no\text{-}wait\text{-}locks)]$

and $t \vdash (x1, shr\ s1) \approx (x2, m2)$ **by**(*auto dest: mbisim-thrD1*)

from $mbisim\ ws1t$ **have** $ws2\ t = None$ **by**(*simp add: mbisim-def*)

let $?s1 = (locks\ s1, ((thr\ s1)(t := None), shr\ s1), wset\ s1, interrupts\ s1)$

let $?s2 = (ls2, (ts2(t := None), m2), ws2, is2)$

from $\langle insert\ t\ A = \{t. no\text{-}\tau\text{moves}1\ s1\ t\} \rangle$ $\langle t \notin A \rangle$

```

have A: A = {t. no- $\tau$ moves1 ?s1 t} by(auto simp add: no- $\tau$ moves1-def)
have ?s1  $\approx_m$  ?s2
proof(rule mbisimI)
  from mbisim
  show finite (dom (thr ?s1)) locks ?s1 = locks ?s2 wset ?s1 = wset ?s2 interrupts ?s1 = interrupts
  ?s2
    by(simp-all add: mbisim-def)
next
from mbisim-wset-thread-ok1[OF mbisim] ws1t show wset-thread-ok (wset ?s1) (thr ?s1)
  by(auto intro!: wset-thread-okI dest: wset-thread-okD split: if-split-asm)
next
fix t'
assume thr ?s1 t' = None
with mbisim-thrNone-eq[OF mbisim, of t']
show thr ?s2 t' = None by auto
next
fix t' x1 ln
assume thr ?s1 t' = [(x1, ln)]
hence thr s1 t' = [(x1, ln)] t'  $\neq$  t by(auto split: if-split-asm)
with mbisim-thrD1[OF mbisim  $\langle$ thr s1 t' = [(x1, ln)] $\rangle$ ] mbisim
show  $\exists$  x2. thr ?s2 t' = [(x2, ln)]  $\wedge$  t'  $\vdash$  (x1, shr ?s1)  $\approx$  (x2, shr ?s2)  $\wedge$  (wset ?s2 t' = None
 $\vee$  x1  $\approx_w$  x2)
  by(auto simp add: mbisim-def)
qed
with A have  $\exists$  ts2'. r2.mthr.silent-moves ?s2 (ls2, (ts2', m2), ws2, is2)  $\wedge$  ( $\forall$  t. no- $\tau$ moves1 ?s1 t
 $\rightarrow$  no- $\tau$ moves2 (ls2, (ts2', m2), ws2, is2) t)  $\wedge$  ?s1  $\approx_m$  (ls2, (ts2', m2), ws2, is2) by(rule insert)
then obtain ts2' where r2.mthr.silent-moves ?s2 (ls2, (ts2', m2), ws2, is2)
  and no- $\tau$ :  $\bigwedge$  t. no- $\tau$ moves1 ?s1 t  $\implies$  no- $\tau$ moves2 (ls2, (ts2', m2), ws2, is2) t
  and ?s1  $\approx_m$  (ls2, (ts2', m2), ws2, is2) by auto
let ?s2' = (ls2, (ts2'(t  $\mapsto$  (x2, no-wait-locks)), m2), ws2, is2)
from ts2t have ts2(t  $\mapsto$  (x2, no-wait-locks)) = ts2 by(auto intro: ext)
with r2. $\tau$ mRedT-add-thread-inv[OF  $\langle$ r2.mthr.silent-moves ?s2 (ls2, (ts2', m2), ws2, is2) $\rangle$ , of t
(x2, no-wait-locks)]
have r2.mthr.silent-moves (ls2, (ts2, m2), ws2, is2) ?s2' by simp
from no- $\tau$ move1- $\tau$ s-to-no- $\tau$ move2[OF  $\langle$ t  $\vdash$  (x1, shr s1)  $\approx$  (x2, m2) $\rangle$   $\tau$ 1]
obtain x2' m2' where r2.silent-moves t (x2, m2) (x2', m2')
  and  $\bigwedge$  x2'' m2''.  $\neg$  r2.silent-move t (x2', m2') (x2'', m2'')
  and t  $\vdash$  (x1, shr s1)  $\approx$  (x2', m2') by auto
let ?s2'' = (ls2, (ts2'(t  $\mapsto$  (x2', no-wait-locks)), m2'), ws2, is2)
from red2-rtrancl- $\tau$ -heapD[OF  $\langle$ r2.silent-moves t (x2, m2) (x2', m2') $\rangle$   $\langle$ t  $\vdash$  (x1, shr s1)  $\approx$  (x2,
m2) $\rangle$ ]
have m2' = m2 by simp
with  $\langle$ r2.silent-moves t (x2, m2) (x2', m2') $\rangle$  have r2.silent-moves t (x2, shr ?s2') (x2', m2) by
simp
hence r2.mthr.silent-moves ?s2' (redT-upd- $\varepsilon$  ?s2' t x2' m2)
  by(rule red2-rtrancl- $\tau$ -into-RedT- $\tau$ )(auto simp add:  $\langle$ ws2 t = None $\rangle$  intro:  $\langle$ t  $\vdash$  (x1, shr s1)  $\approx$ 
(x2, m2) $\rangle$ )
also have redT-upd- $\varepsilon$  ?s2' t x2' m2 = ?s2'' using  $\langle$ m2' = m2 $\rangle$ 
  by(auto simp add: fun-eq-iff redT-updLns-def finfun-Diag-const2 o-def)
finally (back-subst) have r2.mthr.silent-moves (ls2, (ts2, m2), ws2, is2) ?s2''
  using  $\langle$ r2.mthr.silent-moves (ls2, (ts2, m2), ws2, is2) ?s2' $\rangle$  by-(rule rtranclp-trans)
moreover {
  fix t'
  assume no- $\tau$ 1: no- $\tau$ moves1 s1 t'

```



```

have no-τmoves2 ?s2'' t'
proof(cases t' = t)
  case True thus ?thesis
    using ⟨ws2 t = None⟩ ⟨∧x2'' m2''. ¬ r2.silent-move t (x2', m2') (x2'', m2'')⟩ by(simp add:
no-τmoves2-def)
  next
    case False
    with no-τ1 have no-τmoves1 ?s1 t' by(simp add: no-τmoves1-def)
    hence no-τmoves2 (ls2, (ts2', m2), ws2, is2) t'
      by(rule ⟨no-τmoves1 ?s1 t' ⇒ no-τmoves2 (ls2, (ts2', m2), ws2, is2) t'⟩)
    with False ⟨m2' = m2⟩ show ?thesis by(simp add: no-τmoves2-def)
  qed }
moreover have s1 ≈m ?s2''
proof(rule mbisimI)
  from mbisim
  show finite (dom (thr s1)) locks s1 = locks ?s2'' wset s1 = wset ?s2'' interrupts s1 = interrupts
?s2''
    by(simp-all add: mbisim-def)
  next
    from mbisim show wset-thread-ok (wset s1) (thr s1) by(rule mbisim-wset-thread-ok1)
  next
    fix t'
    assume thr s1 t' = None
    hence thr ?s1 t' = None t' ≠ t using ts1t by auto
    with mbisim-thrNone-eq[OF ⟨?s1 ≈m (ls2, (ts2', m2), ws2, is2)⟩, of t']
    show thr ?s2'' t' = None by simp
  next
    fix t' x1' ln'
    assume thr s1 t' = [(x1', ln')]
    show ∃x2. thr ?s2'' t' = [(x2, ln')] ∧ t' ⊢ (x1', shr s1) ≈ (x2, shr ?s2'') ∧ (wset ?s2'' t' =
None ∨ x1' ≈w x2)
    proof(cases t = t')
      case True
      with ⟨thr s1 t' = [(x1', ln')]⟩ ts1t ⟨t ⊢ (x1, shr s1) ≈ (x2', m2')⟩ ⟨m2' = m2⟩ ⟨ws2 t = None⟩
      show ?thesis by auto
    next
      case False
      with mbisim-thrD1[OF ⟨?s1 ≈m (ls2, (ts2', m2), ws2, is2)⟩, of t' x1' ln'] ⟨thr s1 t' = [(x1',
ln')]⟩ ⟨m2' = m2⟩ mbisim
      show ?thesis by(auto simp add: mbisim-def)
    qed
  qed
  ultimately show ?case unfolding ⟨m2' = m2⟩ by blast
qed
qed

```

lemma no-τMove2-τs-to-no-τMove1:

```

fixes no-τmoves1 no-τmoves2
defines no-τmoves1 ≡ λs1 t. wset s1 t = None ∧ (∃x. thr s1 t = [(x, no-wait-locks)] ∧ (∀x' m'.
¬ r1.silent-move t (x, shr s1) (x', m')))
defines no-τmoves2 ≡ λs2 t. wset s2 t = None ∧ (∃x. thr s2 t = [(x, no-wait-locks)] ∧ (∀x' m'.
¬ r2.silent-move t (x, shr s2) (x', m')))
assumes (ls1, (ts1, m1), ws1, is1) ≈m s2

```

shows $\exists ts1'. r1.mthr.silent-moves (ls1, (ts1, m1), ws1, is1) (ls1, (ts1', m1), ws1, is1) \wedge$
 $(\forall t. no-\tau moves2 s2 t \longrightarrow no-\tau moves1 (ls1, (ts1', m1), ws1, is1) t) \wedge (ls1, (ts1', m1),$
 $ws1, is1) \approx_m s2$
using *assms FWdelay-bisimulation-diverge.no- τ Move1- τ s-to-no- τ Move2[OF FWdelay-bisimulation-diverge-flip]*
unfolding *flip-simps* **by** *blast*

lemma *deadlock-mbisim-not-final-thread-pres:*

assumes *dead*: $t \in r1.deadlocked s1 \vee r1.deadlock s1$

and *nfin*: $r1.not-final-thread s1 t$

and *fin*: $r1.final-thread s1 t \implies r2.final-thread s2 t$

and *mbisim*: $s1 \approx_m s2$

shows $r2.not-final-thread s2 t$

proof –

from *nfin* **obtain** $x1 ln$ **where** $thr s1 t = \lfloor (x1, ln) \rfloor$ **by** *cases auto*

with *mbisim* **obtain** $x2$ **where** $thr s2 t = \lfloor (x2, ln) \rfloor$ $t \vdash (x1, shr s1) \approx (x2, shr s2)$ $wset s1 t =$
 $None \vee x1 \approx_w x2$

by (*auto dest: mbisim-thrD1*)

show $r2.not-final-thread s2 t$

proof (*cases wset s1 t = None \wedge ln = no-wait-locks*)

case *False*

with $\langle r1.not-final-thread s1 t \rangle \langle thr s1 t = \lfloor (x1, ln) \rfloor \rangle \langle thr s2 t = \lfloor (x2, ln) \rfloor \rangle$ *mbisim*

show *?thesis* **by** *cases(auto simp add: mbisim-def r2.not-final-thread-iff)*

next

case *True*

with $\langle r1.not-final-thread s1 t \rangle \langle thr s1 t = \lfloor (x1, ln) \rfloor \rangle$ **have** $\neg final1 x1$ **by** (*cases*) *auto*

have $\neg final2 x2$

proof

assume *final2 x2*

with *final2-simulation[OF $\langle t \vdash (x1, shr s1) \approx (x2, shr s2) \rangle$]*

obtain $x1' m1'$ **where** $r1.silent-moves t (x1, shr s1) (x1', m1') t \vdash (x1', m1') \approx (x2, shr s2)$

final1 x1' **by** *auto*

from $\langle r1.silent-moves t (x1, shr s1) (x1', m1') \rangle$ **have** $x1' = x1$

proof (*cases rule: converse-rtranclpE2[consumes 1, case-names refl step]*)

case (*step x1'' m1''*)

from $\langle r1.silent-move t (x1, shr s1) (x1'', m1'') \rangle$

have $t \vdash (x1, shr s1) -1-\varepsilon \rightarrow (x1'', m1'')$ **by** (*auto dest: r1.silent-tl*)

hence $r1.redT s1 (t, \varepsilon) (redT-upd-\varepsilon s1 t x1'' m1'')$

using $\langle thr s1 t = \lfloor (x1, ln) \rfloor \rangle$ *True*

by $-(erule r1.redT-normal, auto simp add: redT-updLns-def finfun-Diag-const2 o-def redT-updWs-def)$

hence *False* **using** *dead* **by** (*auto intro: r1.deadlock-no-red r1.red-no-deadlock*)

thus *?thesis ..*

qed *simp*

with $\langle \neg final1 x1 \rangle \langle final1 x1' \rangle$ **show** *False* **by** *simp*

qed

thus *?thesis* **using** $\langle thr s2 t = \lfloor (x2, ln) \rfloor \rangle$ **by** (*auto simp add: r2.not-final-thread-iff*)

qed

qed

lemma *deadlocked1-imp- τ s-deadlocked2:*

assumes *mbisim*: $s1 \approx_m s2$

and *dead*: $t \in r1.deadlocked s1$

shows $\exists s2'. r2.mthr.silent-moves s2 s2' \wedge t \in r2.deadlocked s2' \wedge s1 \approx_m s2'$

proof –

from *mfinal1-inv-simulation*[*OF mbisim*]
obtain $ls2\ ts2\ m2\ ws2\ is2$ **where** $red1: r2.mthr.silent-moves\ s2\ (ls2,\ (ts2,\ m2),\ ws2,\ is2)$
and $s1 \approx m\ (ls2,\ (ts2,\ m2),\ ws2,\ is2)$ **and** $m2 = shr\ s2$
and $fin: \bigwedge t. r1.final-thread\ s1\ t \implies r2.final-thread\ (ls2,\ (ts2,\ m2),\ ws2,\ is2)\ t$ **by** *fastforce*
from *no- τ Move1- τ s-to-no- τ Move2*[*OF* $\langle s1 \approx m\ (ls2,\ (ts2,\ m2),\ ws2,\ is2) \rangle$]
obtain $ts2'$ **where** $red2: r2.mthr.silent-moves\ (ls2,\ (ts2,\ m2),\ ws2,\ is2)\ (ls2,\ (ts2',\ m2),\ ws2,\ is2)$
and $no\text{-}\tau: \bigwedge t\ x1\ x2\ x2'\ m2'. \llbracket wset\ s1\ t = None; thr\ s1\ t = \llbracket (x1,\ no\text{-}wait\text{-}locks) \rrbracket; ts2'\ t = \llbracket (x2,\ no\text{-}wait\text{-}locks) \rrbracket;$
 $\bigwedge x'\ m'. r1.silent-move\ t\ (x1,\ shr\ s1)\ (x',\ m') \implies False \rrbracket$
 $\implies \neg r2.silent-move\ t\ (x2,\ m2)\ (x2',\ m2')$
and $mbisim: s1 \approx m\ (ls2,\ (ts2',\ m2),\ ws2,\ is2)$ **by** *fastforce*
from *mbisim* **have** $mbisim\text{-}eqs: ls2 = locks\ s1\ ws2 = wset\ s1\ is2 = interrupts\ s1$
by (*simp-all add: mbisim-def*)
let $?s2 = (ls2,\ (ts2',\ m2),\ ws2,\ is2)$
from $red2$ **have** $fin': \bigwedge t. r1.final-thread\ s1\ t \implies r2.final-thread\ ?s2\ t$
by (*rule* $r2.\tau mRedT\text{-}preserves\text{-}final\text{-}thread$) (*rule* fin)
from *dead*
have $t \in r2.deadlocked\ ?s2$
proof (*coinduct*)
case (*deadlocked* t)
thus $?case$
proof (*cases rule: r1.deadlocked-elim*s)
case (*lock* $x1$)
hence $csmw: \bigwedge LT. r1.can-sync\ t\ x1\ (shr\ s1)\ LT \implies$
 $\exists lt \in LT. r1.must-wait\ s1\ t\ lt\ (r1.deadlocked\ s1 \cup r1.final-threads\ s1)$
by *blast*
from $\langle thr\ s1\ t = \llbracket (x1,\ no\text{-}wait\text{-}locks) \rrbracket \rangle$ *mbisim* **obtain** $x2$
where $ts2'\ t = \llbracket (x2,\ no\text{-}wait\text{-}locks) \rrbracket$ **and** $bisim: t \vdash (x1,\ shr\ s1) \approx (x2,\ m2)$
by (*auto dest: mbisim-thrD1*)
note $\langle ts2'\ t = \llbracket (x2,\ no\text{-}wait\text{-}locks) \rrbracket \rangle$ **moreover**
{ **from** $\langle r1.must-sync\ t\ x1\ (shr\ s1) \rangle$ **obtain** $ta1\ x1'\ m1'$
where $r1: t \vdash (x1,\ shr\ s1) -1-ta1 \rightarrow (x1',\ m1')$
and $s1': \exists s1'. r1.actions-ok\ s1'\ t\ ta1$ **by** (*fastforce elim: r1.must-syncE*)
have $\neg \tau move1\ (x1,\ shr\ s1)\ ta1\ (x1',\ m1')$ (**is** $\neg ?\tau$)
proof
assume $? \tau$
hence $ta1 = \varepsilon$ **by** (*rule* $r1.silent-tl$)
with $r1$ **have** $r1.can-sync\ t\ x1\ (shr\ s1)$ **{}**
by (*auto intro!:* $r1.can-syncI$ *simp add: collect-locks-def collect-interrupts-def*)
from $csmw$ [*OF this*] **show** *False* **by** *blast*
qed
from *simulation1*[*OF bisim r1 this*]
obtain $x2'\ m2'\ x2''\ m2''\ ta2$ **where** $r2: r2.silent-moves\ t\ (x2,\ m2)\ (x2',\ m2')$
and $r2': t \vdash (x2',\ m2') -2-ta2 \rightarrow (x2'',\ m2'')$
and $\tau2: \neg \tau move2\ (x2',\ m2')\ ta2\ (x2'',\ m2'')$
and $bisim': t \vdash (x1',\ m1') \approx (x2'',\ m2'')$ **and** $tasim: ta1 \sim m\ ta2$ **by** *auto*
from $r2$
have $\exists ta2\ x2'\ m2'\ s2'. t \vdash (x2,\ m2) -2-ta2 \rightarrow (x2',\ m2') \wedge r2.actions-ok\ s2'\ t\ ta2$
proof (*cases rule: converse-rtranclpE2*[*consumes 1, case-names base step*])
case *base*
from $r2'$ [*folded base*] $s1'$ [*unfolded ex-actions-ok1-conv-ex-actions-ok2*][*OF tasim*]
show $?thesis$ **by** *blast*
next
case (*step* $x2'''\ m2'''$)

```

    hence  $t \vdash (x2, m2) -2-\varepsilon \rightarrow (x2''', m2''')$  by(auto dest: r2.silent-tl)
    moreover have  $r2.actions-ok$  (undefined, (undefined, undefined), Map.empty, undefined)  $t \varepsilon$ 
by auto
    ultimately show ?thesis by-(rule exI conjI|assumption)+
    qed
    hence  $r2.must-sync$   $t$   $x2$   $m2$  unfolding  $r2.must-sync-def2$  . }
moreover
{ fix  $LT$ 
assume  $r2.can-sync$   $t$   $x2$   $m2$   $LT$ 
then obtain  $ta2$   $x2'$   $m2'$  where  $r2: t \vdash (x2, m2) -2-ta2 \rightarrow (x2', m2')$ 
and  $LT: LT = collect-locks \{\{ta2\}\}_l \langle + \rangle collect-cond-actions \{\{ta2\}\}_c \langle + \rangle collect-interrupts$ 
 $\{\{ta2\}\}_i$ 
by(auto elim: r2.can-syncE)
from  $\langle wset\ s1\ t = None \rangle \langle thr\ s1\ t = [(x1, no-wait-locks)] \rangle \langle ts2'\ t = [(x2, no-wait-locks)] \rangle$ 
have  $\neg r2.silent-move$   $t$   $(x2, m2)$   $(x2', m2')$ 
proof(rule no- $\tau$ )
fix  $x1'$   $m1'$ 
assume  $r1.silent-move$   $t$   $(x1, shr\ s1)$   $(x1', m1')$ 
hence  $t \vdash (x1, shr\ s1) -1-\varepsilon \rightarrow (x1', m1')$  by(auto dest: r1.silent-tl)
hence  $r1.can-sync$   $t$   $x1$   $(shr\ s1)$  { }
by(auto intro: r1.can-syncI simp add: collect-locks-def collect-interrupts-def)
with csmw[OF this] show False by blast
qed
with  $r2$  have  $\neg \tau move2$   $(x2, m2)$   $ta2$   $(x2', m2')$  by auto
from simulation2[OF bisim r2 this] obtain  $x1'$   $m1'$   $x1''$   $m1''$   $ta1$ 
where  $\tau r1: r1.silent-moves$   $t$   $(x1, shr\ s1)$   $(x1', m1')$ 
and  $r1: t \vdash (x1', m1') -1-ta1 \rightarrow (x1'', m1'')$ 
and  $\neg \tau r1: \neg \tau move1$   $(x1', m1')$   $ta1$   $(x1'', m1'')$ 
and  $bisim': t \vdash (x1'', m1'') \approx (x2', m2')$ 
and  $tlsim: ta1 \sim_m ta2$  by auto
from  $\tau r1$  obtain [simp]:  $x1' = x1$   $m1' = shr\ s1$ 
proof(cases rule: converse-rtranclpE2[consumes 1, case-names refl step])
case (step X M)
from  $\langle r1.silent-move$   $t$   $(x1, shr\ s1)$   $(X, M) \rangle$ 
have  $t \vdash (x1, shr\ s1) -1-\varepsilon \rightarrow (X, M)$  by(auto dest: r1.silent-tl)
hence  $r1.can-sync$   $t$   $x1$   $(shr\ s1)$  { }
by(auto intro: r1.can-syncI simp add: collect-locks-def collect-interrupts-def)
with csmw[OF this] have False by blast
thus ?thesis ..
qed blast
from  $tlsim$   $LT$  have  $LT = collect-locks \{\{ta1\}\}_l \langle + \rangle collect-cond-actions \{\{ta1\}\}_c \langle + \rangle col-$ 
 $lect-interrupts \{\{ta1\}\}_i$ 
by(auto simp add: ta-bisim-def)
with  $r1$  have  $r1.can-sync$   $t$   $x1$   $(shr\ s1)$   $LT$  by(auto intro: r1.can-syncI)
from csmw[OF this] obtain  $lt$ 
where  $lt: lt \in LT$  and  $mw: r1.must-wait$   $s1$   $t$   $lt$   $(r1.deadlocked\ s1 \cup r1.final-threads\ s1)$  by
blast
have  $subset: r1.deadlocked\ s1 \cup r1.final-threads\ s1 \subseteq r1.deadlocked\ s1 \cup r2.deadlocked\ s2 \cup$ 
 $r2.final-threads\ ?s2$ 
by(auto dest: fin')
from  $mw$  have  $r2.must-wait$   $?s2$   $t$   $lt$   $(r1.deadlocked\ s1 \cup r2.deadlocked\ ?s2 \cup r2.final-threads$ 
 $?s2)$ 
proof(cases rule: r1.must-wait-elims)
case lock thus ?thesis by(auto simp add: mbisim-egs dest!: fin')

```

```

next
  case (join t')
  from ⟨r1.not-final-thread s1 t'⟩ obtain x1 ln
    where thr s1 t' = [(x1, ln)] by cases auto
    with mbisim obtain x2 where ts2' t' = [(x2, ln)] t' ⊢ (x1, shr s1) ≈ (x2, m2) by(auto
dest: mbisim-thrD1)
  show ?thesis
  proof(cases wset s1 t' = None ∧ ln = no-wait-locks)
    case False
    with ⟨r1.not-final-thread s1 t'⟩ ⟨thr s1 t' = [(x1, ln)]⟩ ⟨ts2' t' = [(x2, ln)]⟩ ⟨lt = Inr (Inl
t')⟩ join
    show ?thesis by(auto simp add: mbisim-eqs r2.not-final-thread-iff r1.final-thread-def)
  next
  case True
  with ⟨r1.not-final-thread s1 t'⟩ ⟨thr s1 t' = [(x1, ln)]⟩ have ¬ final1 x1 by(cases) auto
    with join ⟨thr s1 t' = [(x1, ln)]⟩ have t' ∈ r1.deadlocked s1 by(auto simp add:
r1.final-thread-def)
  have ¬ final2 x2
  proof
    assume final2 x2
    with final2-simulation[OF ⟨t' ⊢ (x1, shr s1) ≈ (x2, m2)⟩]
    obtain x1' m1' where r1.silent-moves t' (x1, shr s1) (x1', m1')
      and t' ⊢ (x1', m1') ≈ (x2, m2) final1 x1' by auto
    from ⟨r1.silent-moves t' (x1, shr s1) (x1', m1')⟩ have x1' = x1
    proof(cases rule: converse-rtranclpE2[consumes 1, case-names refl step])
      case (step x1'' m1'')
      from ⟨r1.silent-move t' (x1, shr s1) (x1'', m1'')⟩
      have t' ⊢ (x1, shr s1) -1-ε→ (x1'', m1'') by(auto dest: r1.silent-tl)
      hence r1.redT s1 (t', ε) (redT-upd-ε s1 t' x1'' m1'')
      using ⟨thr s1 t' = [(x1, ln)]⟩ True
      by -(erule r1.redT-normal, auto simp add: redT-updLns-def redT-updWs-def fin-
fun-Diag-const2 o-def)
      hence False using ⟨t' ∈ r1.deadlocked s1⟩ by(rule r1.red-no-deadlock)
    thus ?thesis ..
  qed simp
  with ⟨¬ final1 x1⟩ ⟨final1 x1'⟩ show False by simp
  qed
  thus ?thesis using ⟨ts2' t' = [(x2, ln)]⟩ join
    by(auto simp add: r2.not-final-thread-iff r1.final-thread-def)
  qed
next
case (interrupt t')
have r2.all-final-except ?s2 (r1.deadlocked s1 ∪ r2.deadlocked ?s2 ∪ r2.final-threads ?s2)
proof(rule r2.all-final-exceptI)
  fix t''
  assume r2.not-final-thread ?s2 t''
  then obtain x2 ln where thr ?s2 t'' = [(x2, ln)]
    and fin: ¬ final2 x2 ∨ ln ≠ no-wait-locks ∨ wset ?s2 t'' ≠ None
    by(auto simp add: r2.not-final-thread-iff)
  from ⟨thr ?s2 t'' = [(x2, ln)]⟩ mbisim
  obtain x1 where ts1t'': thr s1 t'' = [(x1, ln)]
    and bisim'': t'' ⊢ (x1, shr s1) ≈ (x2, shr ?s2)
    by(auto dest: mbisim-thrD2)
  have r1.not-final-thread s1 t''

```

```

proof(cases wset ?s2 t'' = None ∧ ln = no-wait-locks)
  case True
  with fin have ¬ final2 x2 by simp
  hence ¬ final1 x1
  proof(rule contrapos-nn)
    assume final1 x1
    with final1-simulation[OF bisim'']
    obtain x2' m2' where τs2: r2.silent-moves t'' (x2, shr ?s2) (x2', m2')
      and bisim''': t'' ⊢ (x1, shr s1) ≈ (x2', m2')
      and final2 x2' by auto
    from τs2 have x2' = x2
    proof(cases rule: converse-rtranclpE2[consumes 1, case-names refl step])
      case refl thus ?thesis by simp
    next
      case (step x2'' m2'')
      from True have wset s1 t'' = None thr s1 t'' = [(x1, no-wait-locks)] ts2' t'' = [(x2,
no-wait-locks)]
        using ts1t'' ⟨thr ?s2 t'' = [(x2, ln)]⟩ mbisim by(simp-all add: mbisim-def)
      hence no-τ2: ¬ r2.silent-move t'' (x2, m2) (x2'', m2'')
      proof(rule no-τ)
        fix x1' m1'
        assume r1.silent-move t'' (x1, shr s1) (x1', m1')
        with ⟨final1 x1⟩ show False by(auto dest: r1.final-no-red)
      qed
      with ⟨r2.silent-move t'' (x2, shr ?s2) (x2'', m2'')⟩ have False by simp
      thus ?thesis ..
    qed
    with ⟨final2 x2'⟩ show final2 x2 by simp
  qed
  with ts1t'' show ?thesis ..
next
  case False
  with ts1t'' mbisim show ?thesis by(auto simp add: r1.not-final-thread-iff mbisim-def)
qed
  with ⟨r1.all-final-except s1 (r1.deadlocked s1 ∪ r1.final-threads s1)⟩
  have t'' ∈ r1.deadlocked s1 ∪ r1.final-threads s1 by(rule r1.all-final-exceptD)
  thus t'' ∈ r1.deadlocked s1 ∪ r2.deadlocked ?s2 ∪ r2.final-threads ?s2
    by(auto dest: fin' simp add: mbisim-eqs)
qed
  thus ?thesis using interrupt mbisim by(auto simp add: mbisim-def)
qed
  hence ∃ lt ∈ LT. r2.must-wait ?s2 t lt (r1.deadlocked s1 ∪ r2.deadlocked ?s2 ∪ r2.final-threads
?s2)
    using ⟨lt ∈ LT⟩ by blast }
  moreover from mbisim ⟨wset s1 t = None⟩ have wset ?s2 t = None by(simp add: mbisim-def)
  ultimately have ?Lock by simp
  thus ?thesis ..
next
  case (wait x1 ln)
  from mbisim ⟨thr s1 t = [(x1, ln)]⟩
  obtain x2 where ts2' t = [(x2, ln)] by(auto dest: mbisim-thrD1)
  moreover
  have r2.all-final-except ?s2 (r1.deadlocked s1)
  proof(rule r2.all-final-exceptI)

```

```

fix t
assume r2.not-final-thread ?s2 t
then obtain x2 ln where ts2' t = [(x2, ln)] by(auto simp add: r2.not-final-thread-iff)
with mbisim obtain x1 where thr s1 t = [(x1, ln)] t ⊢ (x1, shr s1) ≈ (x2, m2) by(auto dest:
mbisim-thrD2)
hence r1.not-final-thread s1 t using ⟨r2.not-final-thread ?s2 t⟩ ⟨ts2' t = [(x2, ln)]⟩ mbisim
fin'[of t]
by(cases wset s1 t)(auto simp add: r1.not-final-thread-iff r2.not-final-thread-iff mbisim-def
r1.final-thread-def r2.final-thread-def)
with ⟨r1.all-final-except s1 (r1.deadlocked s1)⟩
show t ∈ r1.deadlocked s1 by(rule r1.all-final-exceptD)
qed
hence r2.all-final-except ?s2 (r1.deadlocked s1 ∪ r2.deadlocked ?s2)
by(rule r2.all-final-except-mono') blast
moreover
from ⟨waiting (wset s1 t)⟩ mbisim
have waiting (wset ?s2 t) by(simp add: mbisim-def)
ultimately have ?Wait by simp
thus ?thesis by blast
next
case (acquire x1 ln l t')
from mbisim ⟨thr s1 t = [(x1, ln)]⟩
obtain x2 where ts2' t = [(x2, ln)] by(auto dest: mbisim-thrD1)
moreover
from ⟨t' ∈ r1.deadlocked s1 ∨ r1.final-thread s1 t'⟩
have (t' ∈ r1.deadlocked s1 ∨ t' ∈ r2.deadlocked ?s2) ∨ r2.final-thread ?s2 t' by(blast dest: fin')
moreover
from mbisim ⟨has-lock (locks s1 $ l) t'⟩
have has-lock (locks ?s2 $ l) t' by(simp add: mbisim-def)
ultimately have ?Acquire
using ⟨0 < ln $ l⟩ ⟨t ≠ t'⟩ ⟨¬ waiting (wset s1 t)⟩ mbisim
by(auto simp add: mbisim-def)
thus ?thesis by blast
qed
qed
with red1 red2 mbisim show ?thesis by(blast intro: rtranclp-trans)
qed

```

lemma *deadlocked2-imp- τ s-deadlocked1*:

$\llbracket s1 \approx_m s2; t \in r2.deadlocked s2 \rrbracket$

$\implies \exists s1'. r1.mthr.silent-moves s1 s1' \wedge t \in r1.deadlocked s1' \wedge s1' \approx_m s2$

using *FWdelay-bisimulation-diverge.deadlocked1-imp- τ s-deadlocked2*[*OF FWdelay-bisimulation-diverge-flip*]
unfolding *flip-simps* .

lemma *deadlock1-imp- τ s-deadlock2*:

assumes mbisim: $s1 \approx_m s2$

and dead: $r1.deadlock s1$

shows $\exists s2'. r2.mthr.silent-moves s2 s2' \wedge r2.deadlock s2' \wedge s1 \approx_m s2'$

proof(cases $\exists t. r1.not-final-thread s1 t$)

case *True*

then obtain t **where** nfin: $r1.not-final-thread s1 t$..

from *mfinal1-inv-simulation*[*OF mbisim*]

obtain ls2 ts2 m2 ws2 is2 **where** red1: $r2.mthr.silent-moves s2 (ls2, (ts2, m2), ws2, is2)$

and $s1 \approx_m (ls2, (ts2, m2), ws2, is2)$ **and** $m2 = shr s2$

and $fin: \bigwedge t. r1.final-thread\ s1\ t \implies r2.final-thread\ (ls2, (ts2, m2), ws2, is2)\ t$ **by** *fastforce*
from $no\text{-}\tau\ Move1\text{-}\tau\ s\text{-}to\text{-}no\text{-}\tau\ Move2[OF\ \langle s1 \approx m\ (ls2, (ts2, m2), ws2, is2) \rangle]$
obtain $ts2'$ **where** $red2: r2.mthr.silent\ moves\ (ls2, (ts2, m2), ws2, is2)\ (ls2, (ts2', m2), ws2, is2)$
and $no\text{-}\tau: \bigwedge t\ x1\ x2\ x2'\ m2'. \llbracket wset\ s1\ t = None; thr\ s1\ t = \llbracket (x1, no\text{-}wait\text{-}locks) \rrbracket; ts2'\ t = \llbracket (x2, no\text{-}wait\text{-}locks) \rrbracket;$
 $\bigwedge x'\ m'. r1.silent\ move\ t\ (x1, shr\ s1)\ (x', m') \implies False \rrbracket$
 $\implies \neg r2.silent\ move\ t\ (x2, m2)\ (x2', m2')$
and $mbisim: s1 \approx m\ (ls2, (ts2', m2), ws2, is2)$ **by** *fastforce*
from $mbisim$ **have** $mbisim\ eqs: ls2 = locks\ s1\ ws2 = wset\ s1\ is2 = interrupts\ s1$
by (*simp-all add: mbisim-def*)
let $?s2 = (ls2, (ts2', m2), ws2, is2)$
from $red2$ **have** $fin': \bigwedge t. r1.final-thread\ s1\ t \implies r2.final-thread\ ?s2\ t$
by (*rule r2. τ mRedT-preserves-final-thread*)(*rule fin*)
have $r2.deadlock\ ?s2$
proof (*rule r2.deadlockI, goal-cases*)
case ($1\ t\ x2$)
note $ts2t = \langle thr\ ?s2\ t = \llbracket (x2, no\text{-}wait\text{-}locks) \rrbracket \rangle$
with $mbisim$ **obtain** $x1$ **where** $ts1t: thr\ s1\ t = \llbracket (x1, no\text{-}wait\text{-}locks) \rrbracket$
and $bisim: t \vdash (x1, shr\ s1) \approx (x2, m2)$ **by** (*auto dest: mbisim-thrD2*)
from $\langle wset\ ?s2\ t = None \rangle$ $mbisim$ **have** $ws1t: wset\ s1\ t = None$ **by** (*simp add: mbisim-def*)
have $\neg final1\ x1$
proof
assume $final1\ x1$
with $ts1t\ ws1t$ **have** $r1.final-thread\ s1\ t$ **by** (*simp add: r1.final-thread-def*)
hence $r2.final-thread\ ?s2\ t$ **by** (*rule fin'*)
with $\langle \neg final2\ x2 \rangle\ ts2t\ \langle wset\ ?s2\ t = None \rangle$ **show** $False$ **by** (*simp add: r2.final-thread-def*)
qed
from $r1.deadlockD1[OF\ dead\ ts1t\ this\ \langle wset\ s1\ t = None \rangle]$
have $ms: r1.must\ sync\ t\ x1\ (shr\ s1)$
and $csmw: \bigwedge LT. r1.can\ sync\ t\ x1\ (shr\ s1)\ LT \implies \exists lt \in LT. r1.must\ wait\ s1\ t\ lt\ (dom\ (thr\ s1))$
by *blast+*
 $\{$
from $\langle r1.must\ sync\ t\ x1\ (shr\ s1) \rangle$ **obtain** $ta1\ x1'\ m1'$
where $r1: t \vdash (x1, shr\ s1) -1-ta1 \rightarrow (x1', m1')$
and $s1': \exists s1'. r1.actions\ ok\ s1'\ t\ ta1$ **by** (*fastforce elim: r1.must-syncE*)
have $\neg \tau move1\ (x1, shr\ s1)\ ta1\ (x1', m1')$ (**is** $\neg ?\tau$)
proof
assume $?\tau$
hence $ta1 = \varepsilon$ **by** (*rule r1.silent-tl*)
with $r1$ **have** $r1.can\ sync\ t\ x1\ (shr\ s1)$ $\{ \}$
by (*auto intro!: r1.can-syncI simp add: collect-locks-def collect-interrupts-def*)
from $csmw[OF\ this]$ **show** $False$ **by** *blast*
qed
from $simulation1[OF\ bisim\ r1\ this]$
obtain $x2'\ m2'\ x2''\ m2''\ ta2$ **where** $r2: r2.silent\ moves\ t\ (x2, m2)\ (x2', m2')$
and $r2': t \vdash (x2', m2') -2-ta2 \rightarrow (x2'', m2'')$
and $bisim': t \vdash (x1', m1') \approx (x2'', m2'')$ **and** $tasim: ta1 \sim m\ ta2$ **by** *auto*
from $r2$
have $\exists ta2\ x2'\ m2'\ s2'. t \vdash (x2, m2) -2-ta2 \rightarrow (x2', m2') \wedge r2.actions\ ok\ s2'\ t\ ta2$
proof (*cases rule: converse-rtranclpE2[consumes 1, case-names base step]*)
case *base*
from $r2'[folded\ base]\ s1'[unfolded\ ex\ actions\ ok1\ conv\ ex\ actions\ ok2[OF\ tasim]]$
show $?thesis$ **by** *blast*
next


```

    case (step x2''' m2''')
    hence t ⊢ (x2, m2) -2-ε→ (x2''', m2''') by(auto dest: r2.silent-tl)
    moreover have r2.actions-ok (undefined, (undefined, undefined), Map.empty, undefined) t ε
by auto
  ultimately show ?thesis by-(rule exI conjI|assumption)+
  qed
  hence r2.must-sync t x2 m2 unfolding r2.must-sync-def2 . }
moreover
{ fix LT
  assume r2.can-sync t x2 m2 LT
  then obtain ta2 x2' m2' where r2: t ⊢ (x2, m2) -2-ta2→ (x2', m2')
    and LT: LT = collect-locks {ta2}_l <+> collect-cond-actions {ta2}_c <+> collect-interrupts
{ta2}_i
  by(auto elim: r2.can-syncE)
  from ts2t have ts2' t = [(x2, no-wait-locks)] by simp
  with ws1t ts1t have ¬ r2.silent-move t (x2, m2) (x2', m2')
  proof(rule no-τ)
    fix x1' m1'
    assume r1.silent-move t (x1, shr s1) (x1', m1')
    hence t ⊢ (x1, shr s1) -1-ε→ (x1', m1') by(auto dest: r1.silent-tl)
    hence r1.can-sync t x1 (shr s1) {}
    by(auto intro: r1.can-syncI simp add: collect-locks-def collect-interrupts-def)
    with csmw[OF this] show False by blast
  qed
  with r2 have ¬ τmove2 (x2, m2) ta2 (x2', m2') by auto
  from simulation2[OF bisim r2 this] obtain x1' m1' x1'' m1'' ta1
    where τr1: r1.silent-moves t (x1, shr s1) (x1', m1')
    and r1: t ⊢ (x1', m1') -1-ta1→ (x1'', m1'')
    and nτ1: ¬ τmove1 (x1', m1') ta1 (x1'', m1'')
    and bisim': t ⊢ (x1'', m1'') ≈ (x2', m2')
    and t1sim: ta1 ~m ta2 by auto
  from τr1 obtain [simp]: x1' = x1 m1' = shr s1
  proof(cases rule: converse-rtranclpE2[consumes 1, case-names refl step])
    case (step X M)
    from ⟨r1.silent-move t (x1, shr s1) (X, M)⟩
    have t ⊢ (x1, shr s1) -1-ε→ (X, M) by(auto dest: r1.silent-tl)
    hence r1.can-sync t x1 (shr s1) {}
    by(auto intro: r1.can-syncI simp add: collect-locks-def collect-interrupts-def)
    with csmw[OF this] have False by blast
    thus ?thesis ..
  qed blast
  from t1sim LT have LT = collect-locks {ta1}_l <+> collect-cond-actions {ta1}_c <+> col-
lect-interrupts {ta1}_i
  by(auto simp add: ta-bisim-def)
  with r1 have r1.can-sync t x1 (shr s1) LT by(auto intro: r1.can-syncI)
  from csmw[OF this] obtain lt
    where lt: lt ∈ LT r1.must-wait s1 t lt (dom (thr s1)) by blast
  from ⟨r1.must-wait s1 t lt (dom (thr s1))⟩ have r2.must-wait ?s2 t lt (dom (thr ?s2))
  proof(cases rule: r1.must-wait-elim)
    case (lock l)
    with mbisim-dom-eq[OF mbisim] show ?thesis by(auto simp add: mbisim-egs)
  next
  case (join t')
  from dead deadlock-mbisim-not-final-thread-pres[OF - ⟨r1.not-final-thread s1 t'⟩ fin' mbisim]

```

```

have  $r2.not\text{-}final\text{-}thread\ ?s2\ t'$  by auto
thus  $?thesis$  using join mbisim-dom-eq[OF mbisim] by auto
next
  case (interrupt t')
  have  $r2.all\text{-}final\text{-}except\ ?s2\ (dom\ (thr\ ?s2))$  by(auto intro!: r2.all-final-exceptI)
  with interrupt show ?thesis by(auto simp add: mbisim-eqs)
  qed
  with  $lt$  have  $\exists lt \in LT. r2.must\text{-}wait\ ?s2\ t\ lt\ (dom\ (thr\ ?s2))$  by blast }
ultimately show  $?case$  by fastforce
next
  case ( $2\ t\ x2\ ln\ l$ )
  note dead moreover
  from  $mbisim\ \langle thr\ ?s2\ t = [(x2, ln)] \rangle$ 
  obtain  $x1$  where  $thr\ s1\ t = [(x1, ln)]$  by(auto dest: mbisim-thrD2)
  moreover note  $\langle 0 < ln\ \$\ l \rangle$ 
  moreover from  $\langle \neg\ waiting\ (wset\ ?s2\ t) \rangle\ mbisim$ 
  have  $\neg\ waiting\ (wset\ s1\ t)$  by(simp add: mbisim-def)
  ultimately obtain  $l'\ t'$  where  $0 < ln\ \$\ l'\ t \neq t'\ thr\ s1\ t' \neq None\ has\text{-}lock\ (locks\ s1\ \$\ l')\ t'$ 
  by(rule r1.deadlockD2)
  thus  $?case$  using mbisim-thrNone-eq[OF mbisim, of t'] mbisim by(auto simp add: mbisim-def)
next
  case ( $3\ t\ x2\ w$ )
  from  $mbisim\text{-}thrD2[OF\ mbisim\ this]$ 
  obtain  $x1$  where  $thr\ s1\ t = [(x1, no\text{-}wait\text{-}locks)]$  by auto
  with dead have wset s1 t  $\neq$  [PostWS w] by(rule r1.deadlockD3[rule-format])
  with mbisim show ?case by(simp add: mbisim-def)
  qed
  with  $red1\ red2\ mbisim$  show  $?thesis$  by(blast intro: rtranclp-trans)
next
  case False
  hence  $r1.mfinal\ s1$  by(auto intro: r1.mfinalI simp add: r1.not-final-thread-iff)
  from  $mfinal1\text{-}simulation[OF\ mbisim\ this]$ 
  obtain  $s2'$  where  $\tau mRed2\ s2\ s2'\ s1 \approx_m s2'\ r2.mfinal\ s2'\ shr\ s2' = shr\ s2$  by blast
  thus  $?thesis$  by(blast intro: r2.mfinal-deadlock)
qed

lemma deadlock2-imp- $\tau$ s-deadlock1:
   $\llbracket s1 \approx_m s2; r2.deadlock\ s2 \rrbracket$ 
   $\implies \exists s1'. r1.mthr.silent\text{-}moves\ s1\ s1' \wedge r1.deadlock\ s1' \wedge s1' \approx_m s2$ 
using FWdelay-bisimulation-diverge.deadlock1-imp- $\tau$ s-deadlock2[OF FWdelay-bisimulation-diverge-flip]
unfolding flip-simps .

lemma deadlocked'1-imp- $\tau$ s-deadlocked'2:
   $\llbracket s1 \approx_m s2; r1.deadlocked'\ s1 \rrbracket$ 
   $\implies \exists s2'. r2.mthr.silent\text{-}moves\ s2\ s2' \wedge r2.deadlocked'\ s2' \wedge s1 \approx_m s2'$ 
unfolding  $r1.deadlock\text{-}eq\text{-}deadlocked'[symmetric]$   $r2.deadlock\text{-}eq\text{-}deadlocked'[symmetric]$ 
by(rule deadlock1-imp- $\tau$ s-deadlock2)

lemma deadlocked'2-imp- $\tau$ s-deadlocked'1:
   $\llbracket s1 \approx_m s2; r2.deadlocked'\ s2 \rrbracket \implies \exists s1'. r1.mthr.silent\text{-}moves\ s1\ s1' \wedge r1.deadlocked'\ s1' \wedge s1' \approx_m s2$ 
unfolding  $r1.deadlock\text{-}eq\text{-}deadlocked'[symmetric]$   $r2.deadlock\text{-}eq\text{-}deadlocked'[symmetric]$ 
by(rule deadlock2-imp- $\tau$ s-deadlock1)

```

end

context *FWbisimulation* begin

lemma *mbisim-final-thread-preserve1*:

assumes *mbisim*: $s1 \approx_m s2$ and *fin*: $r1.\text{final-thread } s1 \ t$
shows $r2.\text{final-thread } s2 \ t$

proof –

from *fin* obtain $x1$ where $ts1t$: $\text{thr } s1 \ t = \lfloor (x1, \text{no-wait-locks}) \rfloor$
and *fin1*: $\text{final1 } x1$ and *ws1t*: $\text{wset } s1 \ t = \text{None}$
by(*auto elim*: $r1.\text{final-threadE}$)
from *mbisim* $ts1t$ obtain $x2$
where $ts2t$: $\text{thr } s2 \ t = \lfloor (x2, \text{no-wait-locks}) \rfloor$
and *bisim*: $t \vdash (x1, \text{shr } s1) \approx (x2, \text{shr } s2)$ by(*auto dest*: mbisim-thrD1)
note $ts2t$ moreover from *fin1* *bisim* have $\text{final2 } x2$ by(*auto dest*: bisim-final)
moreover from *mbisim* *ws1t* have $\text{wset } s2 \ t = \text{None}$ by(*simp add*: mbisim-def)
ultimately show *?thesis* by(*rule* $r2.\text{final-threadI}$)

qed

lemma *mbisim-final-thread-preserve2*:

$\llbracket s1 \approx_m s2; r2.\text{final-thread } s2 \ t \rrbracket \implies r1.\text{final-thread } s1 \ t$

using *FWbisimulation.mbisim-final-thread-preserve1*[*OF FWbisimulation-flip*]

unfolding *flip-simps* .

lemma *mbisim-final-thread-inv*:

$s1 \approx_m s2 \implies r1.\text{final-thread } s1 \ t \longleftrightarrow r2.\text{final-thread } s2 \ t$

by(*blast intro*: $\text{mbisim-final-thread-preserve1 } \text{mbisim-final-thread-preserve2}$)

lemma *mbisim-not-final-thread-inv*:

assumes *bisim*: $\text{mbisim } s1 \ s2$

shows $r1.\text{not-final-thread } s1 = r2.\text{not-final-thread } s2$

proof(*rule ext*)

fix t

show $r1.\text{not-final-thread } s1 \ t = r2.\text{not-final-thread } s2 \ t$

proof(*cases thr s1 t*)

case *None*

with mbisim-thrNone-eq [*OF bisim*, *of t*] have $\text{thr } s2 \ t = \text{None}$ by *simp*

with *None* show *?thesis*

by(*auto elim!*: $r2.\text{not-final-thread.cases } r1.\text{not-final-thread.cases}$
intro: $r2.\text{not-final-thread.intros } r1.\text{not-final-thread.intros}$)

next

case (*Some a*)

then obtain $x1 \ ln$ where $tst1$: $\text{thr } s1 \ t = \lfloor (x1, \text{ln}) \rfloor$ by(*cases a*) *auto*

from mbisim-thrD1 [*OF bisim tst1*] obtain $x2$

where $tst2$: $\text{thr } s2 \ t = \lfloor (x2, \text{ln}) \rfloor$ and *bisimt*: $t \vdash (x1, \text{shr } s1) \approx (x2, \text{shr } s2)$ by *blast*

from *bisim* have $\text{wset } s2 = \text{wset } s1$ by(*simp add*: mbisim-def)

with $tst2 \ tst1$ bisim-final [*OF bisimt*] show *?thesis*

by(*simp add*: $r1.\text{not-final-thread-conv } r2.\text{not-final-thread-conv}$)(*rule* $\text{mbisim-final-thread-inv}$ [*OF*

bisim])

qed

qed

lemma *mbisim-deadlocked-preserve1*:

assumes *mbisim*: $s1 \approx_m s2$ and *dead*: $t \in r1.\text{deadlocked } s1$

shows $t \in r2.deadlocked\ s2$
proof –
from $deadlocked1-imp-\tau$ - $deadlocked2$ [OF $mbisim\ dead$]
obtain $s2'$ **where** $r2.mthr.silent-moves\ s2\ s2'$
and $t \in r2.deadlocked\ s2'$ **by** $blast$
from $\langle r2.mthr.silent-moves\ s2\ s2' \rangle$ **have** $s2' = s2$
by (rule $converse-rtranclpE$) (auto $elim: r2.m\tau\ move.cases$)
with $\langle t \in r2.deadlocked\ s2' \rangle$ **show** $?thesis$ **by** $simp$
qed

lemma $mbisim-deadlocked-preserve2$:
 $\llbracket s1 \approx_m s2; t \in r2.deadlocked\ s2 \rrbracket \implies t \in r1.deadlocked\ s1$
using $FWbisimulation.mbisim-deadlocked-preserve1$ [OF $FWbisimulation-flip$]
unfolding $flip-simps$.

lemma $mbisim-deadlocked-inv$:
 $s1 \approx_m s2 \implies r1.deadlocked\ s1 = r2.deadlocked\ s2$
by ($blast\ intro!$: $mbisim-deadlocked-preserve1\ mbisim-deadlocked-preserve2$)

lemma $mbisim-deadlocked'-inv$:
 $s1 \approx_m s2 \implies r1.deadlocked'\ s1 \longleftrightarrow r2.deadlocked'\ s2$
unfolding $r1.deadlocked'-def\ r2.deadlocked'-def$
by ($simp\ add: mbisim-not-final-thread-inv\ mbisim-deadlocked-inv$)

lemma $mbisim-deadlock-inv$:
 $s1 \approx_m s2 \implies r1.deadlock\ s1 = r2.deadlock\ s2$
unfolding $r1.deadlock-eq-deadlocked'\ r2.deadlock-eq-deadlocked'$
by (rule $mbisim-deadlocked'-inv$)

end

context $FWbisimulation$ **begin**

lemma $bisim-can-sync-preserve1$:
assumes $bisim: t \vdash (x1, m1) \approx (x2, m2)$ **and** $cs: t \vdash \langle x1, m1 \rangle\ LT\ \wr 1$
shows $t \vdash \langle x2, m2 \rangle\ LT\ \wr 2$
proof –
from cs **obtain** $ta1\ x1'\ m1'$ **where** $red1: t \vdash (x1, m1) -1-ta1 \rightarrow (x1', m1')$
and $LT: LT = collect-locks\ \{\{ta1\}\}_l\ \langle + \rangle\ collect-cond-actions\ \{\{ta1\}\}_c\ \langle + \rangle\ collect-interrupts\ \{\{ta1\}\}_i$
by (rule $r1.can-syncE$)
from $bisimulation.simulation1$ [OF $bisimulation-axioms$, OF $bisim\ red1$] **obtain** $x2'\ ta2\ m2'$
where $red2: t \vdash (x2, m2) -2-ta2 \rightarrow (x2', m2')$
and $tasim: ta1 \sim_m ta2$ **by** $fastforce$
from $tasim\ LT$ **have** $LT = collect-locks\ \{\{ta2\}\}_l\ \langle + \rangle\ collect-cond-actions\ \{\{ta2\}\}_c\ \langle + \rangle\ collect-interrupts\ \{\{ta2\}\}_i$
by (auto $simp\ add: ta-bisim-def$)
with $red2$ **show** $?thesis$ **by** (rule $r2.can-syncI$)
qed

lemma $bisim-can-sync-preserve2$:
 $\llbracket t \vdash (x1, m1) \approx (x2, m2); t \vdash \langle x2, m2 \rangle\ LT\ \wr 2 \rrbracket \implies t \vdash \langle x1, m1 \rangle\ LT\ \wr 1$
using $FWbisimulation.bisim-can-sync-preserve1$ [OF $FWbisimulation-flip$]

unfolding *flip-simps* .

lemma *bisim-can-sync-inv*:

$t \vdash (x1, m1) \approx (x2, m2) \implies t \vdash \langle x1, m1 \rangle LT \wr 1 \longleftrightarrow t \vdash \langle x2, m2 \rangle LT \wr 2$
by(*blast intro: bisim-can-sync-preserve1 bisim-can-sync-preserve2*)

lemma *bisim-must-sync-preserve1*:

assumes *bisim*: $t \vdash (x1, m1) \approx (x2, m2)$ **and** *ms*: $t \vdash \langle x1, m1 \rangle \wr 1$
shows $t \vdash \langle x2, m2 \rangle \wr 2$

proof –

from *ms* **obtain** *ta1 x1' m1'* **where** *red1*: $t \vdash (x1, m1) -1-ta1 \rightarrow (x1', m1')$

and *s1'*: $\exists s1'. r1.actions-ok s1' t ta1$ **by**(*fastforce elim: r1.must-syncE*)

from *bisimulation.simulation1*[*OF bisimulation-axioms, OF bisim red1*] **obtain** *x2' ta2 m2'*

where *red2*: $t \vdash (x2, m2) -2-ta2 \rightarrow (x2', m2')$

and *tasim*: $ta1 \sim_m ta2$ **by** *fastforce*

from *ex-actions-ok1-conv-ex-actions-ok2*[*OF tasim, of t*] *s1' red2*

show *?thesis unfolding r2.must-sync-def2* **by** *blast*

qed

lemma *bisim-must-sync-preserve2*:

$\llbracket t \vdash (x1, m1) \approx (x2, m2); t \vdash \langle x2, m2 \rangle \wr 2 \rrbracket \implies t \vdash \langle x1, m1 \rangle \wr 1$
using *FWbisimulation.bisim-must-sync-preserve1*[*OF FWbisimulation-flip*]
unfolding *flip-simps* .

lemma *bisim-must-sync-inv*:

$t \vdash (x1, m1) \approx (x2, m2) \implies t \vdash \langle x1, m1 \rangle \wr 1 \longleftrightarrow t \vdash \langle x2, m2 \rangle \wr 2$
by(*blast intro: bisim-must-sync-preserve1 bisim-must-sync-preserve2*)

end

end

1.20 Semantic properties of lifted predicates

theory *FWLiftingSem*

imports

FWSemantics

FWLifting

begin

context *multithreaded-base* **begin**

lemma *redT-preserves-ts-inv-ok*:

$\llbracket s -t>ta \rightarrow s'; ts-inv-ok (thr s) I \rrbracket$
 $\implies ts-inv-ok (thr s') (upd-invs I P \{ta\}_t)$
by(*erule redT.cases*)(*fastforce intro: ts-inv-ok-upd-invs ts-inv-ok-upd-ts redT-updTs-Some*)+

lemma *RedT-preserves-ts-inv-ok*:

$\llbracket s -\triangleright tta s \rightarrow * s'; ts-inv-ok (thr s) I \rrbracket$
 $\implies ts-inv-ok (thr s') (upd-invs I Q (concat (map (thr-a \circ snd) tta)))$
by(*induct rule: RedT-induct*)(*auto intro: redT-preserves-ts-inv-ok*)

lemma *redT-upd-inv-ext*:

fixes $I :: 't \rightarrow 'i$
 shows $\llbracket s \rightarrow ta \rightarrow s'; ts\text{-inv-ok } (thr\ s) I \rrbracket \Longrightarrow I \subseteq_m \text{upd-invs } I P \llbracket ta \rrbracket_t$
 by(*erule redT.cases, auto intro: ts-inv-ok-inv-ext-upd-invs*)

lemma *RedT-upd-inv-ext*:

fixes $I :: 't \rightarrow 'i$
 shows $\llbracket s \rightarrow ttas \rightarrow * s'; ts\text{-inv-ok } (thr\ s) I \rrbracket$
 $\Longrightarrow I \subseteq_m \text{upd-invs } I P (\text{concat } (\text{map } (thr\text{-a } \circ \text{snd})\ ttas))$
 proof(*induct rule: RedT-induct*)
 case *refl* thus ?case by *simp*
 next
 case (*step S TTAS S' T TA S''*)
 hence $ts\text{-inv-ok } (thr\ S') (\text{upd-invs } I P (\text{concat } (\text{map } (thr\text{-a } \circ \text{snd})\ TTAS)))$
 by $\text{-(rule RedT-preserves-ts-inv-ok)}$
 hence $\text{upd-invs } I P (\text{concat } (\text{map } (thr\text{-a } \circ \text{snd})\ TTAS)) \subseteq_m \text{upd-invs } (\text{upd-invs } I P (\text{concat } (\text{map } (thr\text{-a } \circ \text{snd})\ TTAS))) P \llbracket TA \rrbracket_t$
 using *step* by $\text{-(rule redT-upd-inv-ext)}$
 with *step* show ?case by(*auto elim!: map-le-trans simp add: comp-def*)
 qed

end

locale *lifting-inv = multithreaded final r convert-RA*

for *final* :: $'x \Rightarrow \text{bool}$
 and $r :: ('l, 't, 'x, 'm, 'w, 'o)$ *semantics* $(\langle - \vdash - \dashrightarrow - \rangle [50, 0, 0, 50] 80)$
 and *convert-RA* :: $'l \text{ released-locks} \Rightarrow 'o \text{ list}$
 +
 fixes $P :: 'i \Rightarrow 't \Rightarrow 'x \Rightarrow 'm \Rightarrow \text{bool}$
 assumes *invariant-red*: $\llbracket t \vdash \langle x, m \rangle \rightarrow ta \rightarrow \langle x', m' \rangle; P\ i\ t\ x\ m \rrbracket \Longrightarrow P\ i\ t\ x'\ m'$
 and *invariant-NewThread*: $\llbracket t \vdash \langle x, m \rangle \rightarrow ta \rightarrow \langle x', m' \rangle; P\ i\ t\ x\ m; \text{NewThread } t''\ x''\ m' \in \text{set } \llbracket ta \rrbracket_t$
 \rrbracket
 $\Longrightarrow \exists i''. P\ i''\ t''\ x''\ m'$
 and *invariant-other*: $\llbracket t \vdash \langle x, m \rangle \rightarrow ta \rightarrow \langle x', m' \rangle; P\ i\ t\ x\ m; P\ i''\ t''\ x''\ m' \rrbracket \Longrightarrow P\ i''\ t''\ x''\ m'$
 begin

lemma *redT-updTs-invariant*:

fixes ln
 assumes *tsiP*: $ts\text{-inv } P\ I\ ts\ m$
 and *red*: $t \vdash \langle x, m \rangle \rightarrow ta \rightarrow \langle x', m' \rangle$
 and *tao*: $\text{thread-oks } ts \llbracket ta \rrbracket_t$
 and *tst*: $ts\ t = \lfloor (x, ln) \rfloor$
 shows $ts\text{-inv } P (\text{upd-invs } I P \llbracket ta \rrbracket_t) ((\text{redT-updTs } ts \llbracket ta \rrbracket_t)(t \mapsto (x', ln))) m'$
 proof(*rule ts-invI*)
 fix $T\ X\ LN$
 assume $XLN: ((\text{redT-updTs } ts \llbracket ta \rrbracket_t)(t \mapsto (x', ln))) T = \lfloor (X, LN) \rfloor$
 from *tsiP* $\langle ts\ t = \lfloor (x, ln) \rfloor \rangle$ obtain i where $I\ t = \lfloor i \rfloor P\ i\ t\ x\ m$
 by(*auto dest: ts-invD*)
 show $\exists i. \text{upd-invs } I P \llbracket ta \rrbracket_t T = \lfloor i \rfloor \wedge P\ i\ T\ X\ m'$
 proof(*cases T = t*)
 case *True*
 from $\text{red } \langle P\ i\ t\ x\ m \rangle$ have $P\ i\ t\ x'\ m'$ by(*rule invariant-red*)
 moreover from $\langle I\ t = \lfloor i \rfloor \rangle \langle ts\ t = \lfloor (x, ln) \rfloor \rangle$ tao
 have $\text{upd-invs } I P \llbracket ta \rrbracket_t t = \lfloor i \rfloor$
 by(*simp add: upd-invs-Some*)

```

ultimately show ?thesis using True XLN by simp
next
case False
show ?thesis
proof(cases ts T)
  case None
  with XLN tao False have  $\exists m'. \text{NewThread } T \ X \ m' \in \text{set } \{ta\}_t$ 
    by(auto dest: redT-updTs-new-thread)
  with red have nt:  $\text{NewThread } T \ X \ m' \in \text{set } \{ta\}_t$  by(auto dest: new-thread-memory)
  with red  $\langle P \ i \ t \ x \ m \rangle$  have  $\exists i''. P \ i'' \ T \ X \ m'$  by(rule invariant-NewThread)
  hence  $P \ (\text{SOME } i. P \ i \ T \ X \ m') \ T \ X \ m'$  by(rule someI-ex)
  with nt tao show ?thesis by(auto intro: SOME-new-thread-upd-invs)
next
case (Some a)
obtain  $X' \ LN'$  where [simp]:  $a = (X', LN')$  by(cases a)
with  $\langle ts \ T = \lfloor a \rfloor \rangle$  have esT:  $ts \ T = \lfloor (X', LN') \rfloor$  by simp
hence  $\text{redT-updTs } ts \ \{ta\}_t \ T = \lfloor (X', LN') \rfloor$ 
  using  $\langle \text{thread-oks } ts \ \{ta\}_t \rangle$  by(auto intro: redT-updTs-Some)
moreover from esT tsiP obtain  $i'$  where  $I \ T = \lfloor i' \rfloor \ P \ i' \ T \ X' \ m$ 
  by(auto dest: ts-invD)
from red  $\langle P \ i \ t \ x \ m \rangle \langle P \ i' \ T \ X' \ m \rangle$ 
have  $P \ i' \ T \ X' \ m'$  by(rule invariant-other)
moreover from  $\langle I \ T = \lfloor i' \rfloor \rangle$  esT tao have  $\text{upd-invs } I \ P \ \{ta\}_t \ T = \lfloor i' \rfloor$ 
  by(simp add: upd-invs-Some)
ultimately show ?thesis using XLN False by simp
qed
qed
qed

theorem redT-invariant:
  assumes redT:  $s \dashv\rightarrow ta \rightarrow s'$ 
  and esinvP:  $ts\text{-inv } P \ I \ (\text{thr } s) \ (\text{shr } s)$ 
  shows  $ts\text{-inv } P \ (\text{upd-invs } I \ P \ \{ta\}_t) \ (\text{thr } s') \ (\text{shr } s')$ 
using redT
proof(cases rule: redT-elim)
  case acquire thus ?thesis using esinvP
    by(auto intro!: ts-invI split: if-split-asm dest: ts-invD)
next
  case (normal x x' m')
  with esinvP
  have  $ts\text{-inv } P \ (\text{upd-invs } I \ P \ \{ta\}_t) \ ((\text{redT-updTs } (\text{thr } s) \ \{ta\}_t)(t \mapsto (x', \text{redT-updLns } (\text{locks } s) \ t \ \text{no-wait-locks } \{ta\}_t))) \ m'$ 
    by(auto intro: redT-updTs-invariant)
  thus ?thesis using normal by simp
qed

theorem RedT-invariant:
  assumes RedT:  $s \dashv\rightarrow ttas \rightarrow^* s'$ 
  and esinvQ:  $ts\text{-inv } P \ I \ (\text{thr } s) \ (\text{shr } s)$ 
  shows  $ts\text{-inv } P \ (\text{upd-invs } I \ P \ (\text{concat } (\text{map } (\text{thr-a } \circ \text{snd}) \ ttas))) \ (\text{thr } s') \ (\text{shr } s')$ 
using RedT esinvQ
proof(induct rule: RedT-induct)
  case refl thus ?case by(simp (no-asm))
next

```

case (*step S TTAS S' T TA S''*)
note $IH = \langle ts\text{-inv } P \ I \ (thr \ S) \ (shr \ S) \rangle \implies ts\text{-inv } P \ (upd\text{-invs } I \ P \ (concat \ (map \ (thr\text{-}a \circ \ snd) \ TTAS))) \ (thr \ S') \ (shr \ S')$
with $\langle ts\text{-inv } P \ I \ (thr \ S) \ (shr \ S) \rangle$
have $ts\text{-inv } P \ (upd\text{-invs } I \ P \ (concat \ (map \ (thr\text{-}a \circ \ snd) \ TTAS))) \ (thr \ S') \ (shr \ S')$ **by** *blast*
with $\langle S' - T \triangleright TA \rightarrow S'' \rangle$
have $ts\text{-inv } P \ (upd\text{-invs } (upd\text{-invs } I \ P \ (concat \ (map \ (thr\text{-}a \circ \ snd) \ TTAS))) \ P \ \{\!\{TA\}\!\}_t) \ (thr \ S'') \ (shr \ S'')$
by(*rule redT-invariant*)
thus *?case by(simp add: comp-def)*
qed

lemma *invariant3p-ts-inv: invariant3p redT {s. $\exists I. ts\text{-inv } P \ I \ (thr \ s) \ (shr \ s)$ }*
by(*auto intro!: invariant3pI dest: redT-invariant*)

end

locale *lifting-wf = multithreaded final r convert-RA*
for *final :: 'x \Rightarrow bool*
and *r :: ('l,'t,'x,'m,'w,'o) semantics ($\langle - \vdash - \dashrightarrow - \rangle \ [50,0,0,50] \ 80$)*
and *convert-RA :: 'l released-locks \Rightarrow 'o list*
+
fixes *P :: 't \Rightarrow 'x \Rightarrow 'm \Rightarrow bool*
assumes *preserves-red: $\llbracket t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle; P \ t \ x \ m \rrbracket \implies P \ t \ x' \ m'$*
and *preserves-NewThread: $\llbracket t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle; P \ t \ x \ m; NewThread \ t'' \ x'' \ m' \in \text{set } \{\!\{ta\}\!\}_t \rrbracket \implies P \ t'' \ x'' \ m'$*
and *preserves-other: $\llbracket t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle; P \ t \ x \ m; P \ t'' \ x'' \ m \rrbracket \implies P \ t'' \ x'' \ m'$*
begin

lemma *lifting-inv: lifting-inv final r ($\lambda\text{-} :: \text{unit. } P$)*
by(*unfold-locales*)(*blast intro: preserves-red preserves-NewThread preserves-other*)+

lemma *redT-updTs-preserves:*

fixes *ln*
assumes *esokQ: ts-ok P ts m*
and *red: $t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle$*
and *ts t = $\lfloor (x, ln) \rfloor$*
and *thread-oks ts $\{\!\{ta\}\!\}_t$*
shows *ts-ok P ((redT-updTs ts $\{\!\{ta\}\!\}_t)(t \mapsto (x', ln')))) \ m'$*

proof –

interpret *lifting-inv final r convert-RA $\lambda\text{-} :: \text{unit. } P$* **by**(*rule lifting-inv*)
from *esokQ* **obtain** *I :: 't \rightarrow unit* **where** *ts-inv ($\lambda\text{-}.$ P) I ts m* **by**(*rule ts-ok-into-ts-inv-const*)
hence *ts-inv ($\lambda\text{-}.$ P) (upd-invs I ($\lambda\text{-}.$ P) $\{\!\{ta\}\!\}_t)$ ((redT-updTs ts $\{\!\{ta\}\!\}_t)(t \mapsto (x', ln')))) \ m'$
using *red \langle thread-oks ts $\{\!\{ta\}\!\}_t \langle$ ts t = $\lfloor (x, ln) \rfloor \rangle$* **by**(*rule redT-updTs-invariant*)
thus *?thesis* **by**(*rule ts-inv-const-into-ts-ok*)*

qed

theorem *redT-preserves:*

assumes *redT: $s -t \triangleright ta \rightarrow s'$*
and *esokQ: ts-ok P (thr s) (shr s)*
shows *ts-ok P (thr s') (shr s')*

proof –

interpret *lifting-inv final r convert-RA $\lambda\text{-} :: \text{unit. } P$* **by**(*rule lifting-inv*)
from *esokQ* **obtain** *I :: 't \rightarrow unit* **where** *ts-inv ($\lambda\text{-}.$ P) I (thr s) (shr s)* **by**(*rule ts-ok-into-ts-inv-const*)

with *redT* **have** *ts-inv* ($\lambda\cdot. P$) (*upd-invs* *I* ($\lambda\cdot. P$) $\{\{ta\}_t\}$) (*thr* *s'*) (*shr* *s'*) **by**(*rule redT-invariant*)
thus *?thesis* **by**(*rule ts-inv-const-into-ts-ok*)
qed

theorem *RedT-preserves*:

$\llbracket s \rightarrow_{ttas} s'; ts-ok P (thr s) (shr s) \rrbracket \implies ts-ok P (thr s') (shr s')$
by(*erule (1) RedT-lift-preserveD*)(*fastforce elim: redT-preserves*)

lemma *invariant3p-ts-ok: invariant3p redT* $\{s. ts-ok P (thr s) (shr s)\}$
by(*auto intro!: invariant3pI intro: redT-preserves*)

end

lemma *lifting-wf-Const* [*intro!*]:

assumes *multithreaded final r*

shows *lifting-wf final r* ($\lambda t x m. k$)

proof –

interpret *multithreaded final r* **using** *assms* .

show *?thesis* **by** *unfold-locales blast+*

qed

end

1.21 Synthetic first and last actions for each thread

theory *FWInitFinLift*

imports

FWLTS

FWLiftingSem

begin

datatype *status* =

PreStart

| *Running*

| *Finished*

abbreviation *convert-TA-initial* :: $(l, t, 'x, 'm, 'w, 'o)$ *thread-action* $\Rightarrow (l, t, status \times 'x, 'm, 'w, 'o)$ *thread-action*
where *convert-TA-initial* == *convert-extTA* (*Pair PreStart*)

lemma *convert-obs-initial-convert-TA-initial*:

convert-obs-initial (*convert-TA-initial* *ta*) = *convert-TA-initial* (*convert-obs-initial* *ta*)

by(*simp add: convert-obs-initial-def*)

lemma *convert-TA-initial-inject* [*simp*]:

convert-TA-initial *ta* = *convert-TA-initial* *ta'* $\longleftrightarrow ta = ta'$

by(*cases ta*)(*cases ta', auto*)

context *final-thread* **begin**

primrec *init-fin-final* :: *status* $\times 'x \Rightarrow bool$

where *init-fin-final* (*status, x*) $\longleftrightarrow status = Finished \wedge final x$

end

context *multithreaded-base* **begin**

inductive *init-fin* :: ('l,'t,status × 'x,'m,'w,'o action) semantics (⟨- ⊢ - ⟩ → i → [50,0,0,51] 51)

where

NormalAction:

$t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle$

$\implies t \vdash ((Running, x), m) -convert-TA-initial (convert-obs-initial ta) \rightarrow i ((Running, x'), m')$

| *InitialThreadAction*:

$t \vdash ((PreStart, x), m) -\{\!-\!\}InitialThreadAction \rightarrow i ((Running, x), m)$

| *ThreadFinishAction*:

$final\ x \implies t \vdash ((Running, x), m) -\{\!-\!\}ThreadFinishAction \rightarrow i ((Finished, x), m)$

end

declare *split-paired-Ex* [*simp del*]

inductive-simps (**in** *multithreaded-base*) *init-fin-simps* [*simp*]:

$t \vdash ((Finished, x), m) -ta \rightarrow i\ xm'$

$t \vdash ((PreStart, x), m) -ta \rightarrow i\ xm'$

$t \vdash ((Running, x), m) -ta \rightarrow i\ xm'$

$t \vdash xm -ta \rightarrow i ((Finished, x'), m')$

$t \vdash xm -ta \rightarrow i ((Running, x'), m')$

$t \vdash xm -ta \rightarrow i ((PreStart, x'), m')$

declare *split-paired-Ex* [*simp*]

context *multithreaded* **begin**

lemma *multithreaded-init-fin*: *multithreaded init-fin-final init-fin*

by(*unfold-locales*)(*fastforce simp add: init-fin.simps convert-obs-initial-def ta-upd-simps dest: new-thread-memory*)-

end

locale *if-multithreaded-base* = *multithreaded-base* +

constrains *final* :: 'x ⇒ bool

and *r* :: ('l,'t,'x,'m,'w,'o) semantics

and *convert-RA* :: 'l released-locks ⇒ 'o list

sublocale *if-multithreaded-base* < *if*: *multithreaded-base*

init-fin-final

init-fin

map NormalAction ○ *convert-RA*

.

locale *if-multithreaded* = *if-multithreaded-base* + *multithreaded* +

constrains *final* :: 'x ⇒ bool

and *r* :: ('l,'t,'x,'m,'w,'o) semantics

and *convert-RA* :: 'l released-locks ⇒ 'o list

sublocale *if-multithreaded* < *if*: *multithreaded*

init-fin-final

init-fin
 map *NormalAction* \circ *convert-RA*
 by(*rule multithreaded-init-fin*)

context τ *multithreaded begin*

inductive *init-fin- τ move* :: ('l,'t,status \times 'x,'m,'w,'o action) τ *moves*

where

τ *move* (x, m) ta (x', m')
 \implies *init-fin- τ move* ((*Running*, x), m) (*convert-TA-initial* (*convert-obs-initial* ta)) ((*Running*, x'), m')

lemma *init-fin- τ move-simps* [*simp*]:

init-fin- τ move ((*PreStart*, x), m) ta x'm' = *False*
init-fin- τ move xm ta ((*PreStart*, x'), m') = *False*
init-fin- τ move ((*Running*, x), m) ta ((s, x'), m') \longleftrightarrow
 $(\exists ta'. ta = \text{convert-TA-initial } (\text{convert-obs-initial } ta') \wedge s = \text{Running} \wedge \tau\text{move } (x, m) \text{ ta}' (x', m'))$
init-fin- τ move ((s, x), m) ta ((*Running*, x'), m') \longleftrightarrow
 $s = \text{Running} \wedge (\exists ta'. ta = \text{convert-TA-initial } (\text{convert-obs-initial } ta') \wedge \tau\text{move } (x, m) \text{ ta}' (x', m'))$
init-fin- τ move ((*Finished*, x), m) ta x'm' = *False*
init-fin- τ move xm ta ((*Finished*, x'), m') = *False*

by(*simp-all add: init-fin- τ move.simps*)

lemma *init-fin-silent-move-RunningI*:

assumes *silent-move* t (x, m) (x', m')
shows τ *trsys.silent-move* (*init-fin* t) *init-fin- τ move* ((*Running*, x), m) ((*Running*, x'), m')
using *assms* by(*cases*)(*auto intro: τ trsys.silent-move.intros init-fin.NormalAction*)

lemma *init-fin-silent-moves-RunningI*:

assumes *silent-moves* t (x, m) (x', m')
shows τ *trsys.silent-moves* (*init-fin* t) *init-fin- τ move* ((*Running*, x), m) ((*Running*, x'), m')
using *assms*

by(*induct rule: rtranclp-induct2*)(*auto elim: rtranclp.rtrancl-into-rtrancl intro: init-fin-silent-move-RunningI*)

lemma *init-fin-silent-moveD*:

assumes τ *trsys.silent-move* (*init-fin* t) *init-fin- τ move* ((s, x), m) ((s', x'), m')
shows *silent-move* t (x, m) (x', m') \wedge s = s' \wedge s' = *Running*
using *assms* by(*auto elim!: τ trsys.silent-move.cases init-fin.cases*)

lemma *init-fin-silent-movesD*:

assumes τ *trsys.silent-moves* (*init-fin* t) *init-fin- τ move* ((s, x), m) ((s', x'), m')
shows *silent-moves* t (x, m) (x', m') \wedge s = s'
using *assms*

by(*induct* ((s, x), m) ((s', x'), m') *arbitrary: s' x' m'*)

(*auto 7 2 simp only: dest!: init-fin-silent-moveD intro: rtranclp.rtrancl-into-rtrancl*)

lemma *init-fin- τ divergeD*:

assumes τ *trsys. τ diverge* (*init-fin* t) *init-fin- τ move* ((*status*, x), m)
shows τ *diverge* t (x, m) \wedge *status* = *Running*

proof

from *assms* **show** *status* = *Running*

by(*cases rule: τ trsys. τ diverge.cases[consumes 1]*)(*auto dest: init-fin-silent-moveD*)

moreover **define** xm **where** xm = (x, m)

ultimately **have** \exists x m. xm = (x, m) \wedge τ *trsys. τ diverge* (*init-fin* t) *init-fin- τ move* ((*Running*, x), m)

```

    using assms by blast
  thus  $\tau$  diverge t xm
  proof(coinduct)
    case ( $\tau$  diverge xm)
    then obtain x m
      where diverge:  $\tau$  trsys. $\tau$  diverge (init-fin t) init-fin- $\tau$ move ((Running, x), m)
      and xm: xm = (x, m) by blast
    thus ?case
      by(cases rule: $\tau$  trsys. $\tau$  diverge.cases[consumes 1])(auto dest!: init-fin-silent-moveD)
  qed
  qed

```

```

lemma init-fin- $\tau$ diverge-RunningI:
  assumes  $\tau$  diverge t (x, m)
  shows  $\tau$  trsys. $\tau$  diverge (init-fin t) init-fin- $\tau$ move ((Running, x), m)
  proof -
    define sxm where sxm = ((Running, x), m)
    with assms have  $\exists x m. \tau$  diverge t (x, m)  $\wedge$  sxm = ((Running, x), m) by blast
    thus  $\tau$  trsys. $\tau$  diverge (init-fin t) init-fin- $\tau$ move sxm
    proof(coinduct rule:  $\tau$  trsys. $\tau$  diverge.coinduct[consumes 1, case-names  $\tau$  diverge])
      case ( $\tau$  diverge sxm)
      then obtain x m where  $\tau$  diverge t (x, m) and sxm = ((Running, x), m) by blast
      thus ?case by(cases)(auto intro: init-fin-silent-move-RunningI)
    qed
  qed

```

```

lemma init-fin- $\tau$ diverge-conv:
   $\tau$  trsys. $\tau$  diverge (init-fin t) init-fin- $\tau$ move ((status, x), m)  $\longleftrightarrow$ 
   $\tau$  diverge t (x, m)  $\wedge$  status = Running
  by(blast intro: init-fin- $\tau$ diverge-RunningI dest: init-fin- $\tau$ divergeD)
end

```

```

lemma init-fin- $\tau$ moves-False:
   $\tau$  multithreaded.init-fin- $\tau$ move ( $\lambda$  - - . False) = ( $\lambda$  - - . False)
  by(simp add: fun-eq-iff  $\tau$  multithreaded.init-fin- $\tau$ move.simps)

```

```

locale if- $\tau$ multithreaded = if-multithreaded-base +  $\tau$  multithreaded +
  constrains final :: 'x  $\Rightarrow$  bool
  and r :: ('l, 't, 'x, 'm, 'w, 'o) semantics
  and convert-RA :: 'l released-locks  $\Rightarrow$  'o list
  and  $\tau$  move :: ('l, 't, 'x, 'm, 'w, 'o)  $\tau$  moves

```

```

sublocale if- $\tau$ multithreaded < if:  $\tau$  multithreaded
  init-fin-final
  init-fin
  map NormalAction  $\circ$  convert-RA
  init-fin- $\tau$ move
  .

```

```

locale if- $\tau$ multithreaded-wf = if-multithreaded-base +  $\tau$  multithreaded-wf +
  constrains final :: 'x  $\Rightarrow$  bool
  and r :: ('l, 't, 'x, 'm, 'w, 'o) semantics
  and convert-RA :: 'l released-locks  $\Rightarrow$  'o list

```

and $\tau move :: ('l, 't, 'x, 'm, 'w, 'o) \tau moves$

sublocale $if\text{-}\tau\text{multithreaded}\text{-}wf < if\text{-}multithreaded$
by $unfold\text{-}locales$

sublocale $if\text{-}\tau\text{multithreaded}\text{-}wf < if\text{-}\tau\text{multithreaded} .$

context $\tau\text{multithreaded}\text{-}wf$ **begin**

lemma $\tau\text{multithreaded}\text{-}wf\text{-}init\text{-}fin$:
 $\tau\text{multithreaded}\text{-}wf\text{-}init\text{-}fin\text{-}final\text{-}init\text{-}fin\text{-}init\text{-}fin\text{-}\tau\text{move}$

proof –

interpret if : $multithreaded\text{-}init\text{-}fin\text{-}final\text{-}init\text{-}fin\text{-}map\text{-}NormalAction \circ convert\text{-}RA$
by $(rule\text{-}multithreaded\text{-}init\text{-}fin)$

show $?thesis$

proof $(unfold\text{-}locales)$

fix $t\ x\ m\ ta\ x'\ m'$

assume $init\text{-}fin\text{-}\tau\text{move}\ (x, m)\ ta\ (x', m')\ t \vdash (x, m) \text{-}ta \rightarrow i\ (x', m')$

thus $m = m'$ **by** $(cases)(auto\ dest: \tau\text{move}\text{-}heap)$

next

fix $s\ ta\ s'$

assume $init\text{-}fin\text{-}\tau\text{move}\ s\ ta\ s'$

thus $ta = \varepsilon$ **by** $(cases)(auto\ dest: silent\text{-}tl)$

qed

qed

end

sublocale $if\text{-}\tau\text{multithreaded}\text{-}wf < if$: $\tau\text{multithreaded}\text{-}wf$
 $init\text{-}fin\text{-}final$
 $init\text{-}fin$
 $map\ NormalAction \circ convert\text{-}RA$
 $init\text{-}fin\text{-}\tau\text{move}$
by $(rule\ \tau\text{multithreaded}\text{-}wf\text{-}init\text{-}fin)$

primrec $init\text{-}fin\text{-}lift\text{-}inv :: ('i \Rightarrow 't \Rightarrow 'x \Rightarrow 'm \Rightarrow bool) \Rightarrow 'i \Rightarrow 't \Rightarrow status \times 'x \Rightarrow 'm \Rightarrow bool$
where $init\text{-}fin\text{-}lift\text{-}inv\ P\ I\ t\ (s, x) = P\ I\ t\ x$

context $lifting\text{-}inv$ **begin**

lemma $lifting\text{-}inv\text{-}init\text{-}fin\text{-}lift\text{-}inv$:
 $lifting\text{-}inv\text{-}init\text{-}fin\text{-}final\text{-}init\text{-}fin\text{-}(init\text{-}fin\text{-}lift\text{-}inv\ P)$

proof –

interpret if : $multithreaded\text{-}init\text{-}fin\text{-}final\text{-}init\text{-}fin\text{-}map\text{-}NormalAction \circ convert\text{-}RA$
by $(rule\ multithreaded\text{-}init\text{-}fin)$

show $?thesis$

by $(unfold\text{-}locales)(fastforce\ elim!: init\text{-}fin.cases\ dest: invariant\text{-}red\ invariant\text{-}NewThread\ invariant\text{-}other)+$

qed

end

locale $if\text{-}lifting\text{-}inv =$

```

if-multithreaded +
lifting-inv +
constrains final :: 'x ⇒ bool
and r :: ('l,'t,'x,'m,'w,'o) semantics
and convert-RA :: 'l released-locks ⇒ 'o list
and P :: 'i ⇒ 't ⇒ 'x ⇒ 'm ⇒ bool

```

```

sublocale if-lifting-inv < if: lifting-inv
  init-fin-final
  init-fin
  map NormalAction ◦ convert-RA
  init-fin-lift-inv P
by(rule lifting-inv-init-fin-lift-inv)

```

```

primrec init-fin-lift :: ('t ⇒ 'x ⇒ 'm ⇒ bool) ⇒ 't ⇒ status × 'x ⇒ 'm ⇒ bool
where init-fin-lift P t (s, x) = P t x

```

```

context lifting-wf begin

```

```

lemma lifting-wf-init-fin-lift:
  lifting-wf init-fin-final init-fin (init-fin-lift P)

```

```

proof –

```

```

  interpret if: multithreaded init-fin-final init-fin map NormalAction ◦ convert-RA
  by(rule multithreaded-init-fin)

```

```

  show ?thesis

```

```

  by(unfold-locales)(fastforce elim!: init-fin.cases dest: dest: preserves-red preserves-other preserves-NewThread) +
qed

```

```

end

```

```

locale if-lifting-wf =
  if-multithreaded +
  lifting-wf +
  constrains final :: 'x ⇒ bool
  and r :: ('l,'t,'x,'m,'w,'o) semantics
  and convert-RA :: 'l released-locks ⇒ 'o list
  and P :: 't ⇒ 'x ⇒ 'm ⇒ bool

```

```

sublocale if-lifting-wf < if: lifting-wf
  init-fin-final
  init-fin
  map NormalAction ◦ convert-RA
  init-fin-lift P
by(rule lifting-wf-init-fin-lift)

```

```

lemma (in if-lifting-wf) if-lifting-inv:
  if-lifting-inv final r ( $\lambda-$ ::unit. P)

```

```

proof –

```

```

  interpret lifting-inv final r convert-RA  $\lambda-$ ::unit. P by(rule lifting-inv)

```

```

  show ?thesis by unfold-locales

```

```

qed

```

```

locale  $\tau$ lifting-inv =  $\tau$ multithreaded-wf +
  lifting-inv +

```

```

constrains final :: 'x ⇒ bool
and r :: ('l,'t,'x,'m,'w,'o) semantics
and convert-RA :: 'l released-locks ⇒ 'o list
and τmove :: ('l,'t,'x,'m,'w,'o) τmoves
and P :: 'i ⇒ 't ⇒ 'x ⇒ 'm ⇒ bool
begin

lemma redT-silent-move-invariant:
  [[ τmredT s s'; ts-inv P Is (thr s) (shr s) ]] ⇒ ts-inv P Is (thr s') (shr s')
by(auto dest!: redT-invariant mτmove-silentD)

lemma redT-silent-moves-invariant:
  [[ mthr.silent-moves s s'; ts-inv P Is (thr s) (shr s) ]] ⇒ ts-inv P Is (thr s') (shr s')
by(induct rule: rtranclp-induct)(auto dest: redT-silent-move-invariant)

lemma redT-τrtrancl3p-invariant:
  [[ mthr.τrtrancl3p s ttas s'; ts-inv P Is (thr s) (shr s) ]]
  ⇒ ts-inv P (upd-invs Is P (concat (map (thr-a ∘ snd) ttas))) (thr s') (shr s')
proof(induct arbitrary: Is rule: mthr.τrtrancl3p.induct)
  case τrtrancl3p-refl thus ?case by simp
next
  case (τrtrancl3p-step s s' tls s'' tl)
  thus ?case by(cases tl)(force dest: redT-invariant)
next
  case (τrtrancl3p-τstep s s' tls s'' tl)
  thus ?case by(cases tl)(force dest: redT-invariant mτmove-silentD)
qed

end

locale τlifting-wf = τmultithreaded +
  lifting-wf +
  constrains final :: 'x ⇒ bool
  and r :: ('l,'t,'x,'m,'w,'o) semantics
  and convert-RA :: 'l released-locks ⇒ 'o list
  and τmove :: ('l,'t,'x,'m,'w,'o) τmoves
  and P :: 't ⇒ 'x ⇒ 'm ⇒ bool
begin

lemma redT-silent-move-preserves:
  [[ τmredT s s'; ts-ok P (thr s) (shr s) ]] ⇒ ts-ok P (thr s') (shr s')
by(auto dest: redT-preserves)

lemma redT-silent-moves-preserves:
  [[ mthr.silent-moves s s'; ts-ok P (thr s) (shr s) ]] ⇒ ts-ok P (thr s') (shr s')
by(induct rule: rtranclp.induct)(auto dest: redT-silent-move-preserves)

lemma redT-τrtrancl3p-preserves:
  [[ mthr.τrtrancl3p s ttas s'; ts-ok P (thr s) (shr s) ]] ⇒ ts-ok P (thr s') (shr s')
by(induct rule: mthr.τrtrancl3p.induct)(auto dest: redT-silent-moves-preserves redT-preserves)

end

definition init-fin-lift-state :: status ⇒ ('l,'t,'x,'m,'w) state ⇒ ('l,'t,status × 'x,'m,'w) state

```

where $\text{init-fin-lift-state } s \sigma = (\text{locks } \sigma, (\lambda t. \text{map-option } (\lambda(x, \text{ln}). ((s, x), \text{ln})) (\text{thr } \sigma t), \text{shr } \sigma), \text{wset } \sigma, \text{interrupts } \sigma)$

definition $\text{init-fin-descend-thr} :: ('l, 't, 'status \times 'x) \text{ thread-info} \Rightarrow ('l, 't, 'x) \text{ thread-info}$
where $\text{init-fin-descend-thr } ts = \text{map-option } (\lambda((s, x), \text{ln}). (x, \text{ln})) \circ ts$

definition $\text{init-fin-descend-state} :: ('l, 't, 'status \times 'x, 'm, 'w) \text{ state} \Rightarrow ('l, 't, 'x, 'm, 'w) \text{ state}$
where $\text{init-fin-descend-state } \sigma = (\text{locks } \sigma, (\text{init-fin-descend-thr } (\text{thr } \sigma), \text{shr } \sigma), \text{wset } \sigma, \text{interrupts } \sigma)$

lemma $\text{ts-ok-init-fin-lift-init-fin-lift-state}$ [simp]:

$\text{ts-ok } (\text{init-fin-lift } P) (\text{thr } (\text{init-fin-lift-state } s \sigma)) (\text{shr } (\text{init-fin-lift-state } s \sigma)) \longleftrightarrow \text{ts-ok } P (\text{thr } \sigma) (\text{shr } \sigma)$

by(*auto simp add: init-fin-lift-state-def intro!: ts-okI dest: ts-okD*)

lemma $\text{ts-inv-init-fin-lift-inv-init-fin-lift-state}$ [simp]:

$\text{ts-inv } (\text{init-fin-lift-inv } P) I (\text{thr } (\text{init-fin-lift-state } s \sigma)) (\text{shr } (\text{init-fin-lift-state } s \sigma)) \longleftrightarrow \text{ts-inv } P I (\text{thr } \sigma) (\text{shr } \sigma)$

by(*auto simp add: init-fin-lift-state-def intro!: ts-invI dest: ts-invD*)

lemma $\text{init-fin-lift-state-conv-simps}$:

shows $\text{shr-init-fin-lift-state: shr } (\text{init-fin-lift-state } s \sigma) = \text{shr } \sigma$

and $\text{locks-init-fin-lift-state: locks } (\text{init-fin-lift-state } s \sigma) = \text{locks } \sigma$

and $\text{wset-init-fin-lift-state: wset } (\text{init-fin-lift-state } s \sigma) = \text{wset } \sigma$

and $\text{interrupts-init-fin-lift-state: interrupts } (\text{init-fin-lift-state } s \sigma) = \text{interrupts } \sigma$

and $\text{thr-init-fin-lift-state:}$

$\text{thr } (\text{init-fin-lift-state } s \sigma) t = \text{map-option } (\lambda(x, \text{ln}). ((s, x), \text{ln})) (\text{thr } \sigma t)$

by(*simp-all add: init-fin-lift-state-def*)

lemma $\text{thr-init-fin-lift-state}'$:

$\text{thr } (\text{init-fin-lift-state } s \sigma) = \text{map-option } (\lambda(x, \text{ln}). ((s, x), \text{ln})) \circ \text{thr } \sigma$

by(*simp add: fun-eq-iff thr-init-fin-lift-state*)

lemma $\text{init-fin-descend-thr-Some-conv}$ [simp]:

$\bigwedge \text{ln. } ts t = \lfloor ((\text{status}, x), \text{ln}) \rfloor \Longrightarrow \text{init-fin-descend-thr } ts t = \lfloor (x, \text{ln}) \rfloor$

by(*simp add: init-fin-descend-thr-def*)

lemma $\text{init-fin-descend-thr-None-conv}$ [simp]:

$ts t = \text{None} \Longrightarrow \text{init-fin-descend-thr } ts t = \text{None}$

by(*simp add: init-fin-descend-thr-def*)

lemma $\text{init-fin-descend-thr-eq-None}$ [simp]:

$\text{init-fin-descend-thr } ts t = \text{None} \longleftrightarrow ts t = \text{None}$

by(*simp add: init-fin-descend-thr-def*)

lemma $\text{init-fin-descend-state-simps}$ [simp]:

$\text{init-fin-descend-state } (ls, (ts, m), ws, is) = (ls, (\text{init-fin-descend-thr } ts, m), ws, is)$

$\text{locks } (\text{init-fin-descend-state } s) = \text{locks } s$

$\text{thr } (\text{init-fin-descend-state } s) = \text{init-fin-descend-thr } (\text{thr } s)$

$\text{shr } (\text{init-fin-descend-state } s) = \text{shr } s$

$\text{wset } (\text{init-fin-descend-state } s) = \text{wset } s$

$\text{interrupts } (\text{init-fin-descend-state } s) = \text{interrupts } s$

by(*simp-all add: init-fin-descend-state-def*)

lemma $\text{init-fin-descend-thr-update}$ [simp]:

$init_fin_descend_thr (ts(t := v)) = (init_fin_descend_thr ts)(t := map_option (\lambda((status, x), ln). (x, ln)) v)$

by(simp add: init-fin-descend-thr-def fun-eq-iff)

lemma ts-ok-init-fin-descend-state:

$ts_ok P (init_fin_descend_thr ts) = ts_ok (init_fin_lift P) ts$

by(rule ext)(auto 4 3 intro!: ts-okI dest: ts-okD simp add: init-fin-descend-thr-def)

lemma free-thread-id-init-fin-descend-thr [simp]:

$free_thread_id (init_fin_descend_thr ts) = free_thread_id ts$

by(simp add: free-thread-id.simps fun-eq-iff)

lemma redT-updT'-init-fin-descend-thr-eq-None [simp]:

$redT_updT' (init_fin_descend_thr ts) nt t = None \longleftrightarrow redT_updT' ts nt t = None$

by(cases nt) simp-all

lemma thread-ok-init-fin-descend-thr [simp]:

$thread_ok (init_fin_descend_thr ts) nta = thread_ok ts nta$

by(cases nta) simp-all

lemma threads-ok-init-fin-descend-thr [simp]:

$thread_oks (init_fin_descend_thr ts) ntas = thread_oks ts ntas$

by(induct ntas arbitrary: ts)(auto elim!: thread-oks-ts-change[THEN iffD1, rotated 1])

lemma init-fin-descend-thr-redT-updT [simp]:

$init_fin_descend_thr (redT_updT ts (convert_new_thread_action (Pair status) nt)) =$

$redT_updT (init_fin_descend_thr ts) nt$

by(cases nt) simp-all

lemma init-fin-descend-thr-redT-updT_s [simp]:

$init_fin_descend_thr (redT_updTs ts (map (convert_new_thread_action (Pair status)) nts)) =$

$redT_updTs (init_fin_descend_thr ts) nts$

by(induct nts arbitrary: ts) simp-all

context final-thread **begin**

lemma cond-action-ok-init-fin-descend-stateI [simp]:

$final_thread.cond_action_ok init_fin_final s t ct \implies cond_action_ok (init_fin_descend_state s) t ct$

by(cases ct)(auto simp add: final-thread.cond-action-ok.simps init-fin-descend-thr-def)

lemma cond-action-oks-init-fin-descend-stateI [simp]:

$final_thread.cond_action_oks init_fin_final s t cts \implies cond_action_oks (init_fin_descend_state s) t cts$

by(induct cts)(simp-all add: final-thread.cond-action-oks.simps cond-action-ok-init-fin-descend-stateI)

end

definition lift-start-obs :: 't \Rightarrow 'o list \Rightarrow ('t \times 'o action) list

where lift-start-obs t obs = (t, InitialThreadAction) # map ($\lambda ob. (t, NormalAction ob)$) obs

lemma length-lift-start-obs [simp]: length (lift-start-obs t obs) = Suc (length obs)

by(simp add: lift-start-obs-def)

lemma set-lift-start-obs [simp]:

$set (lift-start-obs\ t\ obs) =$
 $insert (t, InitialThreadAction) ((Pair\ t\ \circ\ NormalAction)\ 'set\ obs)$
by(*auto simp add: lift-start-obs-def o-def*)

lemma *distinct-lift-start-obs [simp]: distinct (lift-start-obs\ t\ obs) = distinct\ obs*
by(*auto simp add: lift-start-obs-def distinct-map intro: inj-onI*)

end

theory *FWBisimLift imports*

FWInitFinLift

FWBisimulation

begin

context *FWbisimulation-base begin*

inductive *init-fin-bisim* :: $'t \Rightarrow ((status \times 'x1) \times 'm1, (status \times 'x2) \times 'm2)$ *bisim*
 $(\langle - \vdash - \approx_i - \rangle [50,50,50]\ 60)$

for $t :: 't$

where

$PreStart: t \vdash (x1, m1) \approx (x2, m2) \Longrightarrow t \vdash ((PreStart, x1), m1) \approx_i ((PreStart, x2), m2)$
 $Running: t \vdash (x1, m1) \approx (x2, m2) \Longrightarrow t \vdash ((Running, x1), m1) \approx_i ((Running, x2), m2)$
 $Finished:$
 $\llbracket t \vdash (x1, m1) \approx (x2, m2); final1\ x1; final2\ x2 \rrbracket$
 $\Longrightarrow t \vdash ((Finished, x1), m1) \approx_i ((Finished, x2), m2)$

definition *init-fin-bisim-wait* :: $(status \times 'x1, status \times 'x2)$ *bisim* $(\langle - \approx_{iw} - \rangle [50,50]\ 60)$

where

$init-fin-bisim-wait = (\lambda(status1, x1)\ (status2, x2). status1 = Running \wedge status2 = Running \wedge x1 \approx_w x2)$

inductive-simps *init-fin-bisim-simps [simp]:*

$t \vdash ((PreStart, x1), m1) \approx_i ((s2, x2), m2)$
 $t \vdash ((Running, x1), m1) \approx_i ((s2, x2), m2)$
 $t \vdash ((Finished, x1), m1) \approx_i ((s2, x2), m2)$
 $t \vdash ((s1, x1), m1) \approx_i ((PreStart, x2), m2)$
 $t \vdash ((s1, x1), m1) \approx_i ((Running, x2), m2)$
 $t \vdash ((s1, x1), m1) \approx_i ((Finished, x2), m2)$

lemma *init-fin-bisim-iff:*

$t \vdash ((s1, x1), m1) \approx_i ((s2, x2), m2) \longleftrightarrow$
 $s1 = s2 \wedge t \vdash (x1, m1) \approx (x2, m2) \wedge (s2 = Finished \longrightarrow final1\ x1 \wedge final2\ x2)$

by(*cases\ s1*) *auto*

lemma *nta-bisim-init-fin-bisim [simp]:*

$nta-bisim\ init-fin-bisim\ (convert-new-thread-action\ (Pair\ PreStart)\ nt1)$
 $(convert-new-thread-action\ (Pair\ PreStart)\ nt2) =$
 $nta-bisim\ bisim\ nt1\ nt2$

by(*cases\ nt1*) *simp-all*

lemma *ta-bisim-init-fin-bisim-convert [simp]:*

$ta-bisim\ init-fin-bisim\ (convert-TA-initial\ (convert-obs-initial\ ta1))\ (convert-TA-initial\ (convert-obs-initial\ ta2)) \longleftrightarrow ta1 \sim_m ta2$

by(*auto simp add: ta-bisim-def list-all2-map1 list-all2-map2*)

lemma *ta-bisim-init-fin-bisim-InitialThreadAction* [simp]:

ta-bisim init-fin-bisim {InitialThreadAction} {InitialThreadAction}
by(simp add: ta-bisim-def)

lemma *ta-bisim-init-fin-bisim-ThreadFinishAction* [simp]:

ta-bisim init-fin-bisim {ThreadFinishAction} {ThreadFinishAction}
by(simp add: ta-bisim-def)

lemma *init-fin-bisim-wait-simps* [simp]:

$(\text{status1}, x1) \approx_{iw} (\text{status2}, x2) \iff \text{status1} = \text{Running} \wedge \text{status2} = \text{Running} \wedge x1 \approx_w x2$
by(simp add: init-fin-bisim-wait-def)

lemma *init-fin-lift-state-mbisimI*:

$s \approx_m s' \implies$
FWbisimulation-base.mbisim init-fin-bisim init-fin-bisim-wait (*init-fin-lift-state Running s*) (*init-fin-lift-state Running s'*)

apply(rule *FWbisimulation-base.mbisimI*)

apply(simp add: *thr-init-fin-list-state' o-def dom-map-option mbisim-finite1*)

apply(simp add: *locks-init-fin-lift-state mbisim-def*)

apply(simp add: *wset-init-fin-lift-state mbisim-def*)

apply(simp add: *interrupts-init-fin-lift-stae mbisim-def*)

apply(*clarsimp simp add: wset-init-fin-lift-state mbisim-def thr-init-fin-list-state' o-def wset-thread-ok-conv-dom dom-map-option del: subsetI*)

apply(*drule-tac t=t in mbisim-thrNone-eq*)

apply(simp add: *thr-init-fin-list-state*)

apply(*clarsimp simp add: thr-init-fin-list-state shr-init-fin-lift-state wset-init-fin-lift-state init-fin-bisim-iff*)

apply(*frule* (1) *mbisim-thrD1*)

apply(simp add: *mbisim-def*)

done

end

context *FWdelay-bisimulation-base* **begin**

lemma *init-fin-delay-bisimulation-final-base*:

delay-bisimulation-final-base (*r1.init-fin t*) (*r2.init-fin t*) (*init-fin-bisim t*)
 $r1.\text{init-fin-}\tau\text{move } r2.\text{init-fin-}\tau\text{move } (\lambda(x1, m). r1.\text{init-fin-final } x1) (\lambda(x2, m). r2.\text{init-fin-final } x2)$
by(*unfold-locales*)(*auto* 4 3)

end

lemma *init-fin-bisim-flip* [*flip-simps*]:

FWbisimulation-base.init-fin-bisim final2 final1 ($\lambda t. \text{flip } (\text{bisim } t)$) =
 $(\lambda t. \text{flip } (\text{FWbisimulation-base.init-fin-bisim final1 final2 bisim } t))$
by(*auto simp only: FWbisimulation-base.init-fin-bisim-iff flip-simps fun-eq-iff split-paired-Ex*)

lemma *init-fin-bisim-wait-flip* [*flip-simps*]:

FWbisimulation-base.init-fin-bisim-wait (*flip bisim-wait*) =
 $\text{flip } (\text{FWbisimulation-base.init-fin-bisim-wait bisim-wait})$
by(*auto simp add: fun-eq-iff FWbisimulation-base.init-fin-bisim-wait-simps flip-simps*)

context *FWdelay-bisimulation-lift-aux* **begin**

lemma *init-fin-FWdelay-bisimulation-lift-aux*:

FWdelay-bisimulation-lift-aux $r1.init-fin-final\ r1.init-fin\ r2.init-fin-final\ r2.init-fin\ r1.init-fin-\tau\ move$
 $r2.init-fin-\tau\ move$
by(*intro FWdelay-bisimulation-lift-aux.intro* $r1.\tau\ multithreaded-wf-init-fin\ r2.\tau\ multithreaded-wf-init-fin$)

lemma *init-fin-FWdelay-bisimulation-final-base*:

FWdelay-bisimulation-final-base
 $r1.init-fin-final\ r1.init-fin\ r2.init-fin-final\ r2.init-fin$
 $init-fin-bisim\ r1.init-fin-\tau\ move\ r2.init-fin-\tau\ move$

by(*intro FWdelay-bisimulation-final-base.intro init-fin-FWdelay-bisimulation-lift-aux FWdelay-bisimulation-final-b*
init-fin-delay-bisimulation-final-base)

end

context *FWdelay-bisimulation-obs* **begin**

lemma *init-fin-simulation1*:

assumes *bisim*: $t \vdash s1 \approx_i s2$
and *red1*: $r1.init-fin\ t\ s1\ tl1\ s1'$
and $\tau1: \neg r1.init-fin-\tau\ move\ s1\ tl1\ s1'$
shows $\exists s2'\ s2''\ tl2. (\tau\ trsys.silent-move\ (r2.init-fin\ t)\ r2.init-fin-\tau\ move)^{**}\ s2\ s2' \wedge$
 $r2.init-fin\ t\ s2'\ tl2\ s2'' \wedge \neg r2.init-fin-\tau\ move\ s2'\ tl2\ s2'' \wedge$
 $t \vdash s1' \approx_i s2'' \wedge ta-bisim\ init-fin-bisim\ tl1\ tl2$

proof –

from *bisim* **obtain** *status* $x1\ m1\ x2\ m2$
where $s1: s1 = ((status, x1), m1)$
and $s2: s2 = ((status, x2), m2)$
and *bisim*: $t \vdash (x1, m1) \approx (x2, m2)$
and *finished*: $status = Finished \implies final1\ x1 \wedge final2\ x2$
by(*cases* $s1$)(*cases* $s2$, *fastforce simp add: init-fin-bisim-iff*)

from *red1* **show** *?thesis unfolding* $s1$

proof(*cases*)

case (*NormalAction* $ta1\ x1'\ m1'$)

with $\tau1\ s1$ **have** $\neg \tau\ move1\ (x1, m1)\ ta1\ (x1', m1')$ **by**(*simp*)

from *simulation1*[*OF* *bisim* $\langle t \vdash (x1, m1) -1-ta1 \rightarrow (x1', m1') \rangle$] *this*

obtain $x2'\ m2'\ x2''\ m2''\ ta2$

where *red2*: $r2.silent-moves\ t\ (x2, m2)\ (x2', m2')$

and *red2'*: $t \vdash (x2', m2') -2-ta2 \rightarrow (x2'', m2'')$

and $\tau2: \neg \tau\ move2\ (x2', m2')\ ta2\ (x2'', m2'')$

and *bisim'*: $t \vdash (x1', m1') \approx (x2'', m2'')$

and *tasim*: $ta1 \sim_m ta2$ **by** *auto*

let $?s2' = ((Running, x2'), m2')$

let $?s2'' = ((Running, x2''), m2'')$

let $?ta2 = (convert-TA-initial\ (convert-obs-initial\ ta2))$

from *red2* **have** $\tau\ trsys.silent-moves\ (r2.init-fin\ t)\ r2.init-fin-\tau\ move\ s2\ ?s2'$

unfolding $s2\ \langle status = Running \rangle$ **by**(*rule* $r2.init-fin-silent-moves-RunningI$)

moreover from *red2'* **have** $r2.init-fin\ t\ ?s2'\ ?ta2\ ?s2''$ **by**(*rule* $r2.init-fin.NormalAction$)

moreover from $\tau2$ **have** $\neg r2.init-fin-\tau\ move\ ?s2'\ ?ta2\ ?s2''$ **by** *simp*

moreover from *bisim'* **have** $t \vdash s1' \approx_i ?s2''$ **using** $\langle s1' = ((Running, x1'), m1') \rangle$ **by** *simp*

moreover from *tasim* $\langle tl1 = convert-TA-initial\ (convert-obs-initial\ ta1) \rangle$

have *ta-bisim init-fin-bisim* $tl1\ ?ta2$ **by** *simp*

ultimately show *?thesis* **by** *blast*

next

case *InitialThreadAction*

with $s1\ s2$ *bisim* **show** *?thesis* **by**(*auto simp del: split-paired-Ex*)

next
case *ThreadFinishAction*
from *final1-simulation*[*OF* *bisim*] \langle *final1* *x1* \rangle
obtain $x2' m2'$ **where** *red2*: *r2.silent-moves* *t* (*x2*, *m2*) ($x2'$, $m2'$)
and *bisim'*: $t \vdash (x1, m1) \approx (x2', m2')$
and *fin2*: *final2* $x2'$ **by** *auto*
let $?s2' = ((\text{Running}, x2'), m2')$
let $?s2'' = ((\text{Finished}, x2'), m2')$
from *red2* **have** $\tau\text{trsys.silent-moves}$ (*r2.init-fin* *t*) *r2.init-fin- τ move* *s2* $?s2'$
unfolding *s2* \langle *status = Running* \rangle **by** (*rule* *r2.init-fin-silent-moves-RunningI*)
moreover from *fin2* **have** *r2.init-fin* *t* $?s2'$ $\{\{ \text{ThreadFinishAction} \}\}$ $?s2''$..
moreover have \neg *r2.init-fin- τ move* $?s2'$ $\{\{ \text{ThreadFinishAction} \}\}$ $?s2''$ **by** *simp*
moreover have $t \vdash s1' \approx_i ?s2''$
using \langle *s1' = ((\text{Finished}, x1), m1)* \rangle *fin2* \langle *final1* *x1* \rangle *bisim'* **by** *simp*
ultimately show *?thesis* **unfolding** \langle *tl1 = \{\{ \text{ThreadFinishAction} \}\}* \rangle
by(*blast intro: ta-bisim-init-fin-bisim-ThreadFinishAction*)
qed
qed

lemma *init-fin-simulation2*:

$\llbracket t \vdash s1 \approx_i s2; r2.\text{init-fin } t \ s2 \ \text{tl2} \ s2'; \neg r2.\text{init-fin-}\tau\text{move } s2 \ \text{tl2} \ s2' \rrbracket$
 $\implies \exists s1' s1'' \ \text{tl1}. (\tau\text{trsys.silent-move } (r1.\text{init-fin } t) \ r1.\text{init-fin-}\tau\text{move})^{**} \ s1 \ s1' \wedge$
 $r1.\text{init-fin } t \ s1' \ \text{tl1} \ s1'' \wedge \neg r1.\text{init-fin-}\tau\text{move } s1' \ \text{tl1} \ s1'' \wedge$
 $t \vdash s1'' \approx_i s2' \wedge \text{ta-bisim } \text{init-fin-bisim } \ \text{tl1} \ \text{tl2}$

using *FWdelay-bisimulation-obs.init-fin-simulation1*[*OF* *FWdelay-bisimulation-obs-flip*]
unfolding *flip-simps* .

lemma *init-fin-simulation-Wakeup1*:

assumes *bisim*: $t \vdash (sx1, m1) \approx_i (sx2, m2)$
and *wait*: $sx1 \approx_{iw} sx2$
and *red1*: *r1.init-fin* *t* (*sx1*, *m1*) *ta1* ($sx1'$, $m1'$)
and *wakeup*: *Notified* \in *set* $\{\{ \text{ta1} \}'_w \} \vee$ *WokenUp* \in *set* $\{\{ \text{ta1} \}'_w \}$
shows $\exists \text{ta2 } sx2' m2'. r2.\text{init-fin } t \ (sx2, m2) \ \text{ta2} \ (sx2', m2') \wedge t \vdash (sx1', m1') \approx_i (sx2', m2') \wedge$
 $\text{ta-bisim } \text{init-fin-bisim } \ \text{ta1} \ \text{ta2}$

proof –

from *bisim* *wait* **obtain** *status* $x1 \ x2$
where *sx1*: $sx1 = (\text{status}, x1)$
and *sx2*: $sx2 = (\text{status}, x2)$
and *Bisim*: $t \vdash (x1, m1) \approx (x2, m2)$
and *Wait*: $x1 \approx_w x2$ **by** *cases auto*
from *red1* *wakeup* *sx1* **obtain** $x1' \ \text{ta1}'$
where *sx1'*: $sx1' = (\text{Running}, x1')$
and *status*: *status = Running*
and *Red1*: $t \vdash (x1, m1) \text{--}1\text{--} \text{ta1}' \rightarrow (x1', m1')$
and *ta1*: *ta1 = convert-TA-initial* (*convert-obs-initial* *ta1'*)
and *Wakeup*: *Notified* \in *set* $\{\{ \text{ta1}' \}'_w \} \vee$ *WokenUp* \in *set* $\{\{ \text{ta1}' \}'_w \}$
by *cases auto*
from *simulation-Wakeup1*[*OF* *Bisim* *Wait* *Red1* *Wakeup*] **obtain** $\text{ta2}' \ x2' \ m2'$
where *red2*: $t \vdash (x2, m2) \text{--}2\text{--} \text{ta2}' \rightarrow (x2', m2')$
and *bisim'*: $t \vdash (x1', m1') \approx (x2', m2')$
and *tasim*: $\text{ta1}' \sim_m \text{ta2}'$ **by** *blast*
let $?sx2' = (\text{Running}, x2')$
let $?ta2 = \text{convert-TA-initial}$ (*convert-obs-initial* *ta2'*)
from *red2* **have** *r2.init-fin* *t* (*sx2*, *m2*) $?ta2$ ($?sx2', m2'$) **unfolding** *sx2* *status* ..

moreover from *bisim' sx1'* have $t \vdash (sx1', m1') \approx_i (?sx2', m2')$ by *simp*
 moreover from *tasim ta1* have *ta-bisim init-fin-bisim ta1 ?ta2* by *simp*
 ultimately show *?thesis* by *blast*

qed

lemma *init-fin-simulation-Wakeup2*:

$\llbracket t \vdash (sx1, m1) \approx_i (sx2, m2); sx1 \approx_{iw} sx2; r2.\text{init-fin } t (sx2, m2) \text{ ta2 } (sx2', m2');$
 $\text{Notified} \in \text{set } \{\text{ta2}\}_w \vee \text{WokenUp} \in \text{set } \{\text{ta2}\}_w \rrbracket$
 $\implies \exists ta1 sx1' m1'. r1.\text{init-fin } t (sx1, m1) \text{ ta1 } (sx1', m1') \wedge t \vdash (sx1', m1') \approx_i (sx2', m2') \wedge$
 $\text{ta-bisim init-fin-bisim ta1 ta2}$

using *FWdelay-bisimulation-obs.init-fin-simulation-Wakeup1[OF FWdelay-bisimulation-obs-flip]*
unfolding *flip-simps* .

lemma *init-fin-delay-bisimulation-obs*:

delay-bisimulation-obs (r1.init-fin t) (r2.init-fin t) (init-fin-bisim t) (ta-bisim init-fin-bisim)
 $r1.\text{init-fin-}\tau\text{move } r2.\text{init-fin-}\tau\text{move}$

by(*unfold-locales*)(*erule (2) init-fin-simulation1 init-fin-simulation2*)+

lemma *init-fin-FWdelay-bisimulation-obs*:

FWdelay-bisimulation-obs r1.init-fin-final r1.init-fin r2.init-fin-final r2.init-fin init-fin-bisim init-fin-bisim-wait
 $r1.\text{init-fin-}\tau\text{move } r2.\text{init-fin-}\tau\text{move}$

proof(*intro FWdelay-bisimulation-obs.intro init-fin-FWdelay-bisimulation-final-base FWdelay-bisimulation-obs-ars*
init-fin-delay-bisimulation-obs)

fix $t' sx m1 sxx m2 t sx1 sx2 sx1' ta1 sx1'' m1' sx2' ta2 sx2'' m2'$

assume *bisim*: $t' \vdash (sx, m1) \approx_i (sxx, m2)$

and *bisim1*: $t \vdash (sx1, m1) \approx_i (sx2, m2)$

and *red1*: $\tau\text{trsys.silent-moves } (r1.\text{init-fin } t) r1.\text{init-fin-}\tau\text{move } (sx1, m1) (sx1', m1)$

and *red1'*: $r1.\text{init-fin } t (sx1', m1) \text{ ta1 } (sx1'', m1')$

and $\tau1: \neg r1.\text{init-fin-}\tau\text{move } (sx1', m1) \text{ ta1 } (sx1'', m1')$

and *red2*: $\tau\text{trsys.silent-moves } (r2.\text{init-fin } t) r2.\text{init-fin-}\tau\text{move } (sx2, m2) (sx2', m2)$

and *red2'*: $r2.\text{init-fin } t (sx2', m2) \text{ ta2 } (sx2'', m2')$

and $\tau2: \neg r2.\text{init-fin-}\tau\text{move } (sx2', m2) \text{ ta2 } (sx2'', m2')$

and *bisim1'*: $t \vdash (sx1'', m1') \approx_i (sx2'', m2')$

and *tasim*: *ta-bisim init-fin-bisim ta1 ta2*

from *bisim* **obtain** *status x xx*

where $sx:sx = (\text{status}, x)$

and $sxx: sxx = (\text{status}, xx)$

and *Bisim*: $t' \vdash (x, m1) \approx (xx, m2)$

and *Finish*: $\text{status} = \text{Finished} \implies \text{final1 } x \wedge \text{final2 } xx$

by(*cases sx*)(*cases sxx, auto simp add: init-fin-bisim-iff*)

from *bisim1* **obtain** *status1 x1 x2*

where $sx1: sx1 = (\text{status1}, x1)$

and $sx2: sx2 = (\text{status1}, x2)$

and *Bisim1*: $t \vdash (x1, m1) \approx (x2, m2)$

by(*cases sx1*)(*cases sx2, auto simp add: init-fin-bisim-iff*)

from *bisim1'* **obtain** *status1' x1'' x2''*

where $sx1'': sx1'' = (\text{status1}', x1'')$

and $sx2'': sx2'' = (\text{status1}', x2'')$

and *Bisim1'*: $t \vdash (x1'', m1') \approx (x2'', m2')$

by(*cases sx1''*)(*cases sx2'', auto simp add: init-fin-bisim-iff*)

from *red1 sx1* **obtain** *x1' where sx1': sx1' = (status1, x1')*

and *Red1*: $r1.\text{silent-moves } t (x1, m1) (x1', m1)$

by(*cases sx1'*)(*auto dest: r1.init-fin-silent-movesD*)

from *red2 sx2* **obtain** *x2' where sx2': sx2' = (status1, x2')*

```

and Red2: r2.silent-moves t (x2, m2) (x2', m2)
by(cases sx2')(auto dest: r2.init-fin-silent-movesD)
show t' ⊢ (sx, m1') ≈i (sxx, m2')
proof(cases status1 = Running ∧ status1' = Running)
  case True
    with red1' sx1' sx1'' obtain ta1'
      where Red1': t ⊢ (x1', m1) -1-ta1' → (x1'', m1')
      and ta1: ta1 = convert-TA-initial (convert-obs-initial ta1')
      by cases auto
    from red2' sx2' sx2'' True obtain ta2'
      where Red2': t ⊢ (x2', m2) -2-ta2' → (x2'', m2')
      and ta2: ta2 = convert-TA-initial (convert-obs-initial ta2')
      by cases auto
    from τ1 sx1' sx1'' ta1 True have τ1': ¬ τmove1 (x1', m1) ta1' (x1'', m1') by simp
    from τ2 sx2' sx2'' ta2 True have τ2': ¬ τmove2 (x2', m2) ta2' (x2'', m2') by simp
    from tasim ta1 ta2 have ta1' ~m ta2' by simp
    with Bisim Bisim1 Red1 Red1' τ1' Red2 Red2' τ2' Bisim1'
    have t' ⊢ (x, m1') ≈ (sx, m2') by(rule bisim-inv-red-other)
    with True Finish show ?thesis unfolding sx sxx by(simp add: init-fin-bisim-iff)
  next
    case False
      with red1' sx1' sx1'' have m1' = m1 by cases auto
      moreover from red2' sx2' sx2'' False have m2' = m2 by cases auto
      ultimately show ?thesis using bisim by simp
  qed
next
fix t sx1 m1 sx2 m2 sx1' ta1 sx1'' m1' sx2' ta2 sx2'' m2' w
assume bisim: t ⊢ (sx1, m1) ≈i (sx2, m2)
  and red1: τtrsys.silent-moves (r1.init-fin t) r1.init-fin-τmove (sx1, m1) (sx1', m1)
  and red1': r1.init-fin t (sx1', m1) ta1 (sx1'', m1')
  and τ1: ¬ r1.init-fin-τmove (sx1', m1) ta1 (sx1'', m1')
  and red2: τtrsys.silent-moves (r2.init-fin t) r2.init-fin-τmove (sx2, m2) (sx2', m2)
  and red2': r2.init-fin t (sx2', m2) ta2 (sx2'', m2')
  and τ2: ¬ r2.init-fin-τmove (sx2', m2) ta2 (sx2'', m2')
  and bisim': t ⊢ (sx1'', m1') ≈i (sx2'', m2')
  and tasim: ta-bisim init-fin-bisim ta1 ta2
  and suspend1: Suspend w ∈ set {ta1} w
  and suspend2: Suspend w ∈ set {ta2} w
from bisim obtain status x1 x2
  where sx1: sx1 = (status, x1)
  and sx2: sx2 = (status, x2)
  and Bisim: t ⊢ (x1, m1) ≈ (x2, m2)
  by(cases sx1)(cases sx2, auto simp add: init-fin-bisim-iff)
from bisim' obtain status' x1'' x2''
  where sx1'': sx1'' = (status', x1'')
  and sx2'': sx2'' = (status', x2'')
  and Bisim': t ⊢ (x1'', m1') ≈ (x2'', m2')
  by(cases sx1'')(cases sx2'', auto simp add: init-fin-bisim-iff)
from red1 sx1 obtain x1' where sx1': sx1' = (status, x1')
  and Red1: r1.silent-moves t (x1, m1) (x1', m1)
  by(cases sx1')(auto dest: r1.init-fin-silent-movesD)
from red2 sx2 obtain x2' where sx2': sx2' = (status, x2')
  and Red2: r2.silent-moves t (x2, m2) (x2', m2)
  by(cases sx2')(auto dest: r2.init-fin-silent-movesD)

```

```

from  $red1' \text{ } sx1' \text{ } sx1'' \text{ } suspend1$  obtain  $ta1'$ 
  where  $Red1': t \vdash (x1', m1) -1-ta1' \rightarrow (x1'', m1')$ 
  and  $ta1: ta1 = \text{convert-TA-initial} (\text{convert-obs-initial } ta1')$ 
  and  $Suspend1: Suspend \ w \in \text{set } \{ta1'\}_w$ 
  and  $status: status = \text{Running } status' = \text{Running}$  by cases auto
from  $red2' \text{ } sx2' \text{ } sx2'' \text{ } suspend2$  obtain  $ta2'$ 
  where  $Red2': t \vdash (x2', m2) -2-ta2' \rightarrow (x2'', m2')$ 
  and  $ta2: ta2 = \text{convert-TA-initial} (\text{convert-obs-initial } ta2')$ 
  and  $Suspend2: Suspend \ w \in \text{set } \{ta2'\}_w$  by cases auto
from  $\tau1 \text{ } sx1' \text{ } sx1'' \text{ } ta1 \text{ } status$  have  $\tau1': \neg \tau \text{move1} (x1', m1) \text{ } ta1' (x1'', m1')$  by simp
from  $\tau2 \text{ } sx2' \text{ } sx2'' \text{ } ta2 \text{ } status$  have  $\tau2': \neg \tau \text{move2} (x2', m2) \text{ } ta2' (x2'', m2')$  by simp
from  $tasim \text{ } ta1 \text{ } ta2$  have  $ta1' \sim m \text{ } ta2'$  by simp
with  $Bisim \ Red1 \ Red1' \ \tau1' \ Red2 \ Red2' \ \tau2' \ Bisim'$  have  $x1'' \approx w \ x2''$ 
  using  $Suspend1 \ Suspend2$  by(rule bisim-waitI)
thus  $sx1'' \approx iw \ sx2''$  using  $sx1'' \text{ } sx2'' \text{ } status$  by simp
next
  fix  $t \text{ } sx1 \text{ } m1 \text{ } sx2 \text{ } m2 \text{ } ta1 \text{ } sx1' \text{ } m1'$ 
  assume  $t \vdash (sx1, m1) \approx i (sx2, m2)$  and  $sx1 \approx iw \ sx2$ 
  and  $r1.\text{init-fin} \ t \ (sx1, m1) \ ta1 \ (sx1', m1')$ 
  and  $Notified \in \text{set } \{ta1\}_w \vee \text{WokenUp} \in \text{set } \{ta1\}_w$ 
  thus  $\exists \ ta2 \text{ } sx2' \text{ } m2'. \ r2.\text{init-fin} \ t \ (sx2, m2) \ ta2 \ (sx2', m2') \wedge t \vdash (sx1', m1') \approx i (sx2', m2') \wedge$ 
     $ta\text{-bisim} \ \text{init-fin-bisim} \ ta1 \ ta2$ 
  by(rule init-fin-simulation-Wakeup1)
next
  fix  $t \text{ } sx1 \text{ } m1 \text{ } sx2 \text{ } m2 \text{ } ta2 \text{ } sx2' \text{ } m2'$ 
  assume  $t \vdash (sx1, m1) \approx i (sx2, m2)$  and  $sx1 \approx iw \ sx2$ 
  and  $r2.\text{init-fin} \ t \ (sx2, m2) \ ta2 \ (sx2', m2')$ 
  and  $Notified \in \text{set } \{ta2\}_w \vee \text{WokenUp} \in \text{set } \{ta2\}_w$ 
  thus  $\exists \ ta1 \text{ } sx1' \text{ } m1'. \ r1.\text{init-fin} \ t \ (sx1, m1) \ ta1 \ (sx1', m1') \wedge t \vdash (sx1', m1') \approx i (sx2', m2') \wedge$ 
     $ta\text{-bisim} \ \text{init-fin-bisim} \ ta1 \ ta2$ 
  by(rule init-fin-simulation-Wakeup2)
next
  show  $(\exists \ sx1. \ r1.\text{init-fin-final} \ sx1) = (\exists \ sx2. \ r2.\text{init-fin-final} \ sx2)$ 
  using  $ex\text{-final1-conv-ex-final2}$  by(auto)
qed
end

context  $FW\text{delay-bisimulation-diverge}$  begin

lemma  $init\text{-fin-simulation-silent1}$ :
   $\llbracket t \vdash \text{sxm1} \approx i \ \text{sxm2}; \ \tau\text{trsys}.\text{silent-move} (r1.\text{init-fin} \ t) \ r1.\text{init-fin-}\tau\text{move} \ \text{sxm1} \ \text{sxm1}' \rrbracket$ 
   $\implies \exists \ \text{sxm2}'. \ \tau\text{trsys}.\text{silent-moves} (r2.\text{init-fin} \ t) \ r2.\text{init-fin-}\tau\text{move} \ \text{sxm2} \ \text{sxm2}' \wedge t \vdash \text{sxm1}' \approx i \ \text{sxm2}'$ 
by(cases sxm1')(auto 4 4 elim!:  $init\text{-fin-bisim}.$ cases dest!:  $r1.\text{init-fin-silent-moveD}$  dest:  $simulation\text{-silent1}$ 
intro!:  $r2.\text{init-fin-silent-moves-RunningI}$ )

lemma  $init\text{-fin-simulation-silent2}$ :
   $\llbracket t \vdash \text{sxm1} \approx i \ \text{sxm2}; \ \tau\text{trsys}.\text{silent-move} (r2.\text{init-fin} \ t) \ r2.\text{init-fin-}\tau\text{move} \ \text{sxm2} \ \text{sxm2}' \rrbracket$ 
   $\implies \exists \ \text{sxm1}'. \ \tau\text{trsys}.\text{silent-moves} (r1.\text{init-fin} \ t) \ r1.\text{init-fin-}\tau\text{move} \ \text{sxm1} \ \text{sxm1}' \wedge t \vdash \text{sxm1}' \approx i \ \text{sxm2}'$ 
using  $FW\text{delay-bisimulation-diverge}.\text{init-fin-simulation-silent1}$  [OF  $FW\text{delay-bisimulation-diverge-flip}$ ]
unfolding  $flip\text{-simps}$  .

lemma  $init\text{-fin-}\tau\text{diverge-bisim-inv}$ :
   $t \vdash \text{sxm1} \approx i \ \text{sxm2}$ 

```


$\implies \tau \text{trsys}.\tau \text{diverge } (r1.\text{init-fin } t) r1.\text{init-fin-}\tau \text{move } sxm1 =$
 $\tau \text{trsys}.\tau \text{diverge } (r2.\text{init-fin } t) r2.\text{init-fin-}\tau \text{move } sxm2$
by(cases $sxm1$)(cases $sxm2$, auto simp add: $r1.\text{init-fin-}\tau \text{diverge-conv } r2.\text{init-fin-}\tau \text{diverge-conv } \text{init-fin-bisim-iff}$
 $\tau \text{diverge-bisim-inv}$)

lemma *init-fin-delay-bisimulation-diverge*:

$\text{delay-bisimulation-diverge } (r1.\text{init-fin } t) (r2.\text{init-fin } t) (\text{init-fin-bisim } t) (\text{ta-bisim } \text{init-fin-bisim})$
 $r1.\text{init-fin-}\tau \text{move } r2.\text{init-fin-}\tau \text{move}$

by(blast intro: $\text{delay-bisimulation-diverge.intro } \text{init-fin-delay-bisimulation-obs } \text{delay-bisimulation-diverge-axioms.intro}$
 $\text{init-fin-simulation-silent1 } \text{init-fin-simulation-silent2 } \text{init-fin-}\tau \text{diverge-bisim-inv } \text{del: iffI}$)+

lemma *init-fin-FWdelay-bisimulation-diverge*:

$\text{FWdelay-bisimulation-diverge } r1.\text{init-fin-final } r1.\text{init-fin } r2.\text{init-fin-final } r2.\text{init-fin } \text{init-fin-bisim } \text{init-fin-bisim-wait}$
 $r1.\text{init-fin-}\tau \text{move } r2.\text{init-fin-}\tau \text{move}$

by(intro $\text{FWdelay-bisimulation-diverge.intro } \text{init-fin-FWdelay-bisimulation-obs } \text{FWdelay-bisimulation-diverge-axioms.intro}$
 $\text{init-fin-delay-bisimulation-diverge}$)

end

context *FWbisimulation* **begin**

lemma *init-fin-simulation1*:

assumes $t \vdash s1 \approx_i s2$ **and** $r1.\text{init-fin } t s1 \text{ tl1 } s1'$

shows $\exists s2' \text{ tl2}. r2.\text{init-fin } t s2 \text{ tl2 } s2' \wedge t \vdash s1' \approx_i s2' \wedge \text{ta-bisim } \text{init-fin-bisim } \text{tl1 } \text{tl2}$

using $\text{init-fin-simulation1}[OF \text{assms}]$ **by**(auto simp add: $\tau \text{moves-False } \text{init-fin-}\tau \text{moves-False}$)

lemma *init-fin-simulation2*:

$\llbracket t \vdash s1 \approx_i s2; r2.\text{init-fin } t s2 \text{ tl2 } s2' \rrbracket$

$\implies \exists s1' \text{ tl1}. r1.\text{init-fin } t s1 \text{ tl1 } s1' \wedge t \vdash s1' \approx_i s2' \wedge \text{ta-bisim } \text{init-fin-bisim } \text{tl1 } \text{tl2}$

using $\text{FWbisimulation.init-fin-simulation1}[OF \text{FWbisimulation-flip}]$

unfolding flip-simps .

lemma *init-fin-bisimulation*:

$\text{bisimulation } (r1.\text{init-fin } t) (r2.\text{init-fin } t) (\text{init-fin-bisim } t) (\text{ta-bisim } \text{init-fin-bisim})$

by(unfold-locales)(erule (1) $\text{init-fin-simulation1 } \text{init-fin-simulation2}$)+

lemma *init-fin-FWbisimulation*:

$\text{FWbisimulation } r1.\text{init-fin-final } r1.\text{init-fin } r2.\text{init-fin-final } r2.\text{init-fin } \text{init-fin-bisim}$

proof(intro $\text{FWbisimulation.intro } r1.\text{multithreaded-init-fin } r2.\text{multithreaded-init-fin } \text{FWbisimulation-axioms.intro}$
 $\text{init-fin-bisimulation}$)

fix $t \text{ sx1 } m1 \text{ sx2 } m2$

assume $t \vdash (sx1, m1) \approx_i (sx2, m2)$

thus $r1.\text{init-fin-final } sx1 = r2.\text{init-fin-final } sx2$

by cases simp-all

next

fix $t' \text{ sx } m1 \text{ sxx } m2 \text{ t } sx1 \text{ sx2 } ta1 \text{ sx1}' \text{ m1}' \text{ ta2 } sx2' \text{ m2}'$

assume $t' \vdash (sx, m1) \approx_i (sxx, m2)$ $t \vdash (sx1, m1) \approx_i (sx2, m2)$

and $r1.\text{init-fin } t (sx1, m1) \text{ ta1 } (sx1', m1')$

and $r2.\text{init-fin } t (sx2, m2) \text{ ta2 } (sx2', m2')$

and $t \vdash (sx1', m1') \approx_i (sx2', m2')$

and $\text{ta-bisim } \text{init-fin-bisim } \text{ta1 } \text{ta2}$

from $\text{FWdelay-bisimulation-obs.bisim-inv-red-other}$

$[OF \text{init-fin-FWdelay-bisimulation-obs}, OF \text{this}(1-2) - \text{this}(3) - - \text{this}(4) - \text{this}(5-6)]$

show $t' \vdash (sx, m1') \approx_i (sxx, m2')$ **by**($\text{simp add: } \text{init-fin-}\tau \text{moves-False}$)

```
next
  show  $(\exists sx1. r1.init-fin-final\ sx1) = (\exists sx2. r2.init-fin-final\ sx2)$ 
    using ex-final1-conv-ex-final2 by(auto)
qed
end
end
```

Chapter 2

Data Flow Analysis Framework

2.1 Semilattices

```
theory Semilat
imports Main HOL-Library.While-Combinator
begin

type-synonym 'a ord    = 'a ⇒ 'a ⇒ bool
type-synonym 'a binop  = 'a ⇒ 'a ⇒ 'a
type-synonym 'a sl     = 'a set × 'a ord × 'a binop

definition lesub :: 'a ⇒ 'a ord ⇒ 'a ⇒ bool
  where lesub x r y ⟷ r x y

definition lessub :: 'a ⇒ 'a ord ⇒ 'a ⇒ bool
  where lessub x r y ⟷ lesub x r y ∧ x ≠ y

definition plussub :: 'a ⇒ ('a ⇒ 'b ⇒ 'c) ⇒ 'b ⇒ 'c
  where plussub x f y = f x y

notation (ASCII)
  lesub  (⟨(- /<='-- -)⟩ [50, 1000, 51] 50) and
  lessub (⟨(- /<'-- -)⟩ [50, 1000, 51] 50) and
  plussub (⟨(- /+'-- -)⟩ [65, 1000, 66] 65)

notation
  lesub  (⟨(- /⊑- -)⟩ [50, 0, 51] 50) and
  lessub (⟨(- /⊒- -)⟩ [50, 0, 51] 50) and
  plussub (⟨(- /⊔- -)⟩ [65, 0, 66] 65)

abbreviation (input)
  lesub1 :: 'a ⇒ 'a ord ⇒ 'a ⇒ bool (⟨(- /⊑- -)⟩ [50, 1000, 51] 50)
  where x ⊑r y == x ⊑r y

abbreviation (input)
  lessub1 :: 'a ⇒ 'a ord ⇒ 'a ⇒ bool (⟨(- /⊒- -)⟩ [50, 1000, 51] 50)
  where x ⊒r y == x ⊒r y

abbreviation (input)
```

plussub1 :: 'a ⇒ ('a ⇒ 'b ⇒ 'c) ⇒ 'b ⇒ 'c (⟨(- /⊔ -)⟩ [65, 1000, 66] 65)
where $x \sqcup_f y == x \sqcup_f y$

definition *ord* :: ('a × 'a) set ⇒ 'a ord

where

$ord\ r = (\lambda x\ y. (x,y) \in r)$

definition *order* :: 'a ord ⇒ bool

where

$order\ r \longleftrightarrow (\forall x. x \sqsubseteq_r x) \wedge (\forall x\ y. x \sqsubseteq_r y \wedge y \sqsubseteq_r x \longrightarrow x=y) \wedge (\forall x\ y\ z. x \sqsubseteq_r y \wedge y \sqsubseteq_r z \longrightarrow x \sqsubseteq_r z)$

definition *top* :: 'a ord ⇒ 'a ⇒ bool

where

$top\ r\ T \longleftrightarrow (\forall x. x \sqsubseteq_r T)$

definition *acc* :: 'a set ⇒ 'a ord ⇒ bool

where

$acc\ A\ r \longleftrightarrow wf\ \{(y,x). x \in A \wedge y \in A \wedge x \sqsubseteq_r y\}$

definition *closed* :: 'a set ⇒ 'a binop ⇒ bool

where

$closed\ A\ f \longleftrightarrow (\forall x \in A. \forall y \in A. x \sqcup_f y \in A)$

definition *semilat* :: 'a sl ⇒ bool

where

$semilat = (\lambda(A,r,f). order\ r \wedge closed\ A\ f \wedge$
 $(\forall x \in A. \forall y \in A. x \sqsubseteq_r x \sqcup_f y) \wedge$
 $(\forall x \in A. \forall y \in A. y \sqsubseteq_r x \sqcup_f y) \wedge$
 $(\forall x \in A. \forall y \in A. \forall z \in A. x \sqsubseteq_r z \wedge y \sqsubseteq_r z \longrightarrow x \sqcup_f y \sqsubseteq_r z))$

definition *is-ub* :: ('a × 'a) set ⇒ 'a ⇒ 'a ⇒ 'a ⇒ bool

where

$is-ub\ r\ x\ y\ u \longleftrightarrow (x,u) \in r \wedge (y,u) \in r$

definition *is-lub* :: ('a × 'a) set ⇒ 'a ⇒ 'a ⇒ 'a ⇒ bool

where

$is-lub\ r\ x\ y\ u \longleftrightarrow is-ub\ r\ x\ y\ u \wedge (\forall z. is-ub\ r\ x\ y\ z \longrightarrow (u,z) \in r)$

definition *some-lub* :: ('a × 'a) set ⇒ 'a ⇒ 'a ⇒ 'a

where

$some-lub\ r\ x\ y = (SOME\ z. is-lub\ r\ x\ y\ z)$

locale *Semilat* =

fixes $A :: 'a\ set$

fixes $r :: 'a\ ord$

fixes $f :: 'a\ binop$

assumes *semilat*: $semilat\ (A, r, f)$

lemma *order-refl* [*simp, intro*]: $order\ r \Longrightarrow x \sqsubseteq_r x$

lemma *order-antisym*: $\llbracket order\ r; x \sqsubseteq_r y; y \sqsubseteq_r x \rrbracket \Longrightarrow x = y$

lemma *order-trans*: $\llbracket order\ r; x \sqsubseteq_r y; y \sqsubseteq_r z \rrbracket \Longrightarrow x \sqsubseteq_r z$

lemma *order-less-irrefl* [*intro*, *simp*]: $\text{order } r \implies \neg x \sqsubseteq_r x$

lemma *order-less-trans*: $\llbracket \text{order } r; x \sqsubseteq_r y; y \sqsubseteq_r z \rrbracket \implies x \sqsubseteq_r z$

lemma *topD* [*simp*, *intro*]: $\text{top } r \ T \implies x \sqsubseteq_r T$

lemma *top-le-conv* [*simp*]: $\llbracket \text{order } r; \text{top } r \ T \rrbracket \implies (T \sqsubseteq_r x) = (x = T)$

lemma *semilat-Def*:

$\text{semilat}(A, r, f) \iff \text{order } r \wedge \text{closed } A \ f \wedge$
 $(\forall x \in A. \forall y \in A. x \sqsubseteq_r x \sqcup_f y) \wedge$
 $(\forall x \in A. \forall y \in A. y \sqsubseteq_r x \sqcup_f y) \wedge$
 $(\forall x \in A. \forall y \in A. \forall z \in A. x \sqsubseteq_r z \wedge y \sqsubseteq_r z \longrightarrow x \sqcup_f y \sqsubseteq_r z)$

lemma (**in** *Semilat*) *orderI* [*simp*, *intro*]: $\text{order } r$

lemma (**in** *Semilat*) *closedI* [*simp*, *intro*]: $\text{closed } A \ f$

lemma *closedD*: $\llbracket \text{closed } A \ f; x \in A; y \in A \rrbracket \implies x \sqcup_f y \in A$

lemma *closed-UNIV* [*simp*]: $\text{closed } UNIV \ f$

lemma (**in** *Semilat*) *closed-f* [*simp*, *intro*]: $\llbracket x \in A; y \in A \rrbracket \implies x \sqcup_f y \in A$

lemma (**in** *Semilat*) *refl-r* [*intro*, *simp*]: $x \sqsubseteq_r x$ **by** *simp*

lemma (**in** *Semilat*) *antisym-r* [*intro?*]: $\llbracket x \sqsubseteq_r y; y \sqsubseteq_r x \rrbracket \implies x = y$

lemma (**in** *Semilat*) *trans-r* [*trans*, *intro?*]: $\llbracket x \sqsubseteq_r y; y \sqsubseteq_r z \rrbracket \implies x \sqsubseteq_r z$

lemma (**in** *Semilat*) *ub1* [*simp*, *intro?*]: $\llbracket x \in A; y \in A \rrbracket \implies x \sqsubseteq_r x \sqcup_f y$

lemma (**in** *Semilat*) *ub2* [*simp*, *intro?*]: $\llbracket x \in A; y \in A \rrbracket \implies y \sqsubseteq_r x \sqcup_f y$

lemma (**in** *Semilat*) *lub* [*simp*, *intro?*]:

$\llbracket x \sqsubseteq_r z; y \sqsubseteq_r z; x \in A; y \in A; z \in A \rrbracket \implies x \sqcup_f y \sqsubseteq_r z$

lemma (**in** *Semilat*) *plus-le-conv* [*simp*]:

$\llbracket x \in A; y \in A; z \in A \rrbracket \implies (x \sqcup_f y \sqsubseteq_r z) = (x \sqsubseteq_r z \wedge y \sqsubseteq_r z)$

lemma (**in** *Semilat*) *le-iff-plus-unchanged*:

assumes $x \in A$ **and** $y \in A$

shows $x \sqsubseteq_r y \iff x \sqcup_f y = y$ (**is** $?P \iff ?Q$)

lemma (**in** *Semilat*) *le-iff-plus-unchanged2*:

assumes $x \in A$ **and** $y \in A$

shows $x \sqsubseteq_r y \iff y \sqcup_f x = y$ (**is** $?P \iff ?Q$)

lemma (**in** *Semilat*) *plus-assoc* [*simp*]:

assumes $a: a \in A$ **and** $b: b \in A$ **and** $c: c \in A$

shows $a \sqcup_f (b \sqcup_f c) = a \sqcup_f b \sqcup_f c$

lemma (**in** *Semilat*) *plus-com-lemma*:

$\llbracket a \in A; b \in A \rrbracket \implies a \sqcup_f b \sqsubseteq_r b \sqcup_f a$

lemma (**in** *Semilat*) *plus-commutative*:

$\llbracket a \in A; b \in A \rrbracket \implies a \sqcup_f b = b \sqcup_f a$

lemma *is-lubD*:

$$is-lub\ r\ x\ y\ u \implies is-ub\ r\ x\ y\ u \wedge (\forall z. is-ub\ r\ x\ y\ z \longrightarrow (u, z) \in r)$$

lemma *is-ubI*:

$$\llbracket (x, u) \in r; (y, u) \in r \rrbracket \implies is-ub\ r\ x\ y\ u$$

lemma *is-ubD*:

$$is-ub\ r\ x\ y\ u \implies (x, u) \in r \wedge (y, u) \in r$$

lemma *is-lub-bigger1* [iff]:

$$is-lub\ (r\hat{*})\ x\ y\ y = ((x, y) \in r\hat{*})$$

lemma *is-lub-bigger2* [iff]:

$$is-lub\ (r\hat{*})\ x\ y\ x = ((y, x) \in r\hat{*})$$

lemma *extend-lub*:

assumes *single-valued* *r*

and *is-lub* $(r^*)\ x\ y\ u$

and $(x', x) \in r$

shows $\exists v. is-lub\ (r^*)\ x'\ y\ v$

lemma *single-valued-has-lubs*:

assumes *single-valued* *r*

and *in-r*: $(x, u) \in r^*\ (y, u) \in r^*$

shows $\exists z. is-lub\ (r^*)\ x\ y\ z$

lemma *some-lub-conv*:

$$\llbracket acyclic\ r; is-lub\ (r\hat{*})\ x\ y\ u \rrbracket \implies some-lub\ (r\hat{*})\ x\ y = u$$

lemma *is-lub-some-lub*:

$$\llbracket single-valued\ r; acyclic\ r; (x, u) \in r\hat{*}; (y, u) \in r\hat{*} \rrbracket \\ \implies is-lub\ (r\hat{*})\ x\ y\ (some-lub\ (r\hat{*})\ x\ y)$$

2.1.1 An executable lub-finder

definition *exec-lub* :: $(\ 'a * \ 'a)\ set \Rightarrow (\ 'a \Rightarrow \ 'a) \Rightarrow \ 'a\ binop$

where

$$exec-lub\ r\ f\ x\ y = while\ (\lambda z. (x, z) \notin r^*)\ f\ y$$

lemma *exec-lub-refl*: *exec-lub* *r* *f* *T* *T* = *T*

by (*simp* *add*: *exec-lub-def* *while-unfold*)

lemma *acyclic-single-valued-finite*:

$$\llbracket acyclic\ r; single-valued\ r; (x, y) \in r^* \rrbracket \\ \implies finite\ (r \cap \{a. (x, a) \in r^*\} \times \{b. (b, y) \in r^*\})$$

lemma *exec-lub-conv*:

$$\llbracket acyclic\ r; \forall x\ y. (x, y) \in r \longrightarrow f\ x = y; is-lub\ (r^*)\ x\ y\ u \rrbracket \implies \\ exec-lub\ r\ f\ x\ y = u$$

lemma *is-lub-exec-lub*:

$$\llbracket single-valued\ r; acyclic\ r; (x, u):r\hat{*}; (y, u):r\hat{*}; \forall x\ y. (x, y) \in r \longrightarrow f\ x = y \rrbracket \\ \implies is-lub\ (r\hat{*})\ x\ y\ (exec-lub\ r\ f\ x\ y)$$

end

2.2 The Error Type

```

theory Err
imports Semilat
begin

datatype 'a err = Err | OK 'a

type-synonym 'a ebinop = 'a  $\Rightarrow$  'a  $\Rightarrow$  'a err
type-synonym 'a esl = 'a set  $\times$  'a ord  $\times$  'a ebinop

primrec ok-val :: 'a err  $\Rightarrow$  'a
where
  ok-val (OK x) = x

definition lift :: ('a  $\Rightarrow$  'b err)  $\Rightarrow$  ('a err  $\Rightarrow$  'b err)
where
  lift f e = (case e of Err  $\Rightarrow$  Err | OK x  $\Rightarrow$  f x)

definition lift2 :: ('a  $\Rightarrow$  'b  $\Rightarrow$  'c err)  $\Rightarrow$  'a err  $\Rightarrow$  'b err  $\Rightarrow$  'c err
where
  lift2 f e1 e2 =
    (case e1 of Err  $\Rightarrow$  Err | OK x  $\Rightarrow$  (case e2 of Err  $\Rightarrow$  Err | OK y  $\Rightarrow$  f x y))

definition le :: 'a ord  $\Rightarrow$  'a err ord
where
  le r e1 e2 =
    (case e2 of Err  $\Rightarrow$  True | OK y  $\Rightarrow$  (case e1 of Err  $\Rightarrow$  False | OK x  $\Rightarrow$  x  $\sqsubseteq_r$  y))

definition sup :: ('a  $\Rightarrow$  'b  $\Rightarrow$  'c)  $\Rightarrow$  ('a err  $\Rightarrow$  'b err  $\Rightarrow$  'c err)
where
  sup f = lift2 ( $\lambda$ x y. OK (x  $\sqcup_f$  y))

definition err :: 'a set  $\Rightarrow$  'a err set
where
  err A = insert Err {OK x | x. x  $\in$  A}

definition esl :: 'a sl  $\Rightarrow$  'a esl
where
  esl = ( $\lambda$ (A,r,f). (A, r,  $\lambda$ x y. OK(f x y)))

definition sl :: 'a esl  $\Rightarrow$  'a err sl
where
  sl = ( $\lambda$ (A,r,f). (err A, le r, lift2 f))

abbreviation
  err-semilat :: 'a esl  $\Rightarrow$  bool where
  err-semilat L == semilat(sl L)

primrec strict :: ('a  $\Rightarrow$  'b err)  $\Rightarrow$  ('a err  $\Rightarrow$  'b err)
where
  strict f Err = Err
  | strict f (OK x) = f x

```

lemma *err-def'*:

$$\text{err } A = \text{insert } \text{Err } \{x. \exists y \in A. x = \text{OK } y\}$$

lemma *strict-Some* [simp]:

$$(\text{strict } f \ x = \text{OK } y) = (\exists z. x = \text{OK } z \wedge f \ z = \text{OK } y)$$

lemma *not-Err-eq*: $(x \neq \text{Err}) = (\exists a. x = \text{OK } a)$

lemma *not-OK-eq*: $(\forall y. x \neq \text{OK } y) = (x = \text{Err})$

lemma *unfold-lesub-err*: $e1 \sqsubseteq_{le \ r} e2 = le \ r \ e1 \ e2$

lemma *le-err-reft*: $\forall x. x \sqsubseteq_r x \implies e \sqsubseteq_{le \ r} e$

lemma *le-err-trans* [rule-format]:

$$\text{order } r \implies e1 \sqsubseteq_{le \ r} e2 \longrightarrow e2 \sqsubseteq_{le \ r} e3 \longrightarrow e1 \sqsubseteq_{le \ r} e3$$

lemma *le-err-antisym* [rule-format]:

$$\text{order } r \implies e1 \sqsubseteq_{le \ r} e2 \longrightarrow e2 \sqsubseteq_{le \ r} e1 \longrightarrow e1 = e2$$

lemma *OK-le-err-OK*: $(\text{OK } x \sqsubseteq_{le \ r} \text{OK } y) = (x \sqsubseteq_r y)$

lemma *order-le-err* [iff]: $\text{order}(le \ r) = \text{order } r$

lemma *le-Err* [iff]: $e \sqsubseteq_{le \ r} \text{Err}$

lemma *Err-le-conv* [iff]: $\text{Err} \sqsubseteq_{le \ r} e = (e = \text{Err})$

lemma *le-OK-conv* [iff]: $e \sqsubseteq_{le \ r} \text{OK } x = (\exists y. e = \text{OK } y \wedge y \sqsubseteq_r x)$

lemma *OK-le-conv*: $\text{OK } x \sqsubseteq_{le \ r} e = (e = \text{Err} \vee (\exists y. e = \text{OK } y \wedge x \sqsubseteq_r y))$

lemma *top-Err* [iff]: $\text{top}(le \ r) \ \text{Err}$

lemma *OK-less-conv* [rule-format, iff]:

$$\text{OK } x \sqsubseteq_{le \ r} e = (e = \text{Err} \vee (\exists y. e = \text{OK } y \wedge x \sqsubseteq_r y))$$

lemma *not-Err-less* [rule-format, iff]: $\neg(\text{Err} \sqsubseteq_{le \ r} x)$

lemma *semilat-errI* [intro]: **assumes** *Semilat* $A \ r \ f$

shows *semilat*(*err* A , *le* r , *lift2*($\lambda x \ y. \text{OK}(f \ x \ y)$))

lemma *err-semilat-eslI-aux*:

assumes *Semilat* $A \ r \ f$ **shows** *err-semilat*(*esl*(A, r, f))

lemma *err-semilat-eslI* [intro, simp]:

$$\text{semilat } L \implies \text{err-semilat } (\text{esl } L)$$

lemma *acc-err* [simp, intro!]: $\text{acc } A \ r \implies \text{acc } (\text{err } A) \ (le \ r)$

lemma *Err-in-err* [iff]: $\text{Err} : \text{err } A$

lemma *Ok-in-err* [iff]: $(\text{OK } x \in \text{err } A) = (x \in A)$

2.2.1 lift

lemma *lift-in-errI*:

$$\llbracket e \in \text{err } S; \forall x \in S. e = \text{OK } x \longrightarrow f \ x \in \text{err } S \rrbracket \implies \text{lift } f \ e \in \text{err } S$$

lemma *Err-lift2* [simp]: $\text{Err} \sqcup_{\text{lift2}} f \ x = \text{Err}$

lemma *lift2-Err* [simp]: $x \sqcup_{\text{lift2}} f \ \text{Err} = \text{Err}$

lemma *OK-lift2-OK* [simp]: $\text{OK } x \sqcup_{\text{lift2}} f \ \text{OK } y = x \sqcup_f y$

2.2.2 sup

lemma *Err-sup-Err* [simp]: $\text{Err} \sqcup_{\text{sup}} f \ x = \text{Err}$

lemma *Err-sup-Err2* [simp]: $x \sqcup_{\text{sup}} f \ \text{Err} = \text{Err}$

lemma *Err-sup-OK* [simp]: $\text{OK } x \sqcup_{\text{sup}} f \ \text{OK } y = \text{OK } (x \sqcup_f y)$

lemma *Err-sup-eq-OK-conv* [iff]:

$$(\text{sup } f \ ex \ ey = \text{OK } z) = (\exists x \ y. ex = \text{OK } x \wedge ey = \text{OK } y \wedge f \ x \ y = z)$$

lemma *Err-sup-eq-Err* [iff]: $(\text{sup } f \ ex \ ey = \text{Err}) = (ex = \text{Err} \vee ey = \text{Err})$

2.2.3 semilat (err A) (le r) f

lemma *semilat-le-err-Err-plus* [simp]:

$$\llbracket x \in \text{err } A; \text{semilat}(\text{err } A, le \ r, f) \rrbracket \implies \text{Err} \sqcup_f x = \text{Err}$$

lemma *semilat-le-err-plus-Err* [simp]:

$\llbracket x \in \text{err } A; \text{semilat}(\text{err } A, \text{le } r, f) \rrbracket \implies x \sqcup_f \text{Err} = \text{Err}$

lemma *semilat-le-err-OK1*:

$\llbracket x \in A; y \in A; \text{semilat}(\text{err } A, \text{le } r, f); \text{OK } x \sqcup_f \text{OK } y = \text{OK } z \rrbracket$
 $\implies x \sqsubseteq_r z$

lemma *semilat-le-err-OK2*:

$\llbracket x \in A; y \in A; \text{semilat}(\text{err } A, \text{le } r, f); \text{OK } x \sqcup_f \text{OK } y = \text{OK } z \rrbracket$
 $\implies y \sqsubseteq_r z$

lemma *eq-order-le*:

$\llbracket x=y; \text{order } r \rrbracket \implies x \sqsubseteq_r y$

lemma *OK-plus-OK-eq-Err-conv* [*simp*]:

assumes $x \in A \quad y \in A \quad \text{semilat}(\text{err } A, \text{le } r, fe)$

shows $(\text{OK } x \sqcup_{fe} \text{OK } y = \text{Err}) = (\neg(\exists z \in A. x \sqsubseteq_r z \wedge y \sqsubseteq_r z))$

2.2.4 semilat (err(Union AS))

lemma *all-bex-swap-lemma* [*iff*]:

$(\forall x. (\exists y \in A. x = f y) \longrightarrow P x) = (\forall y \in A. P(f y))$

lemma *closed-err-Union-lift2I*:

$\llbracket \forall A \in AS. \text{closed}(\text{err } A) (\text{lift2 } f); AS \neq \{\};$
 $\forall A \in AS. \forall B \in AS. A \neq B \longrightarrow (\forall a \in A. \forall b \in B. a \sqcup_f b = \text{Err}) \rrbracket$
 $\implies \text{closed}(\text{err}(\text{Union } AS)) (\text{lift2 } f)$

If $AS = \{\}$ the thm collapses to $\text{order } r \wedge \text{closed } \{\text{Err}\} f \wedge \text{Err} \sqcup_f \text{Err} = \text{Err}$ which may not hold

lemma *err-semilat-UnionI*:

$\llbracket \forall A \in AS. \text{err-semilat}(A, r, f); AS \neq \{\};$
 $\forall A \in AS. \forall B \in AS. A \neq B \longrightarrow (\forall a \in A. \forall b \in B. \neg a \sqsubseteq_r b \wedge a \sqcup_f b = \text{Err}) \rrbracket$
 $\implies \text{err-semilat}(\text{Union } AS, r, f)$

end

2.3 More about Options

theory *Opt*

imports

Err

begin

definition *le* :: 'a ord \Rightarrow 'a option ord

where

$le \ r \ o_1 \ o_2 =$
 $(\text{case } o_2 \text{ of } \text{None} \Rightarrow o_1 = \text{None} \mid \text{Some } y \Rightarrow (\text{case } o_1 \text{ of } \text{None} \Rightarrow \text{True} \mid \text{Some } x \Rightarrow x \sqsubseteq_r y))$

definition *opt* :: 'a set \Rightarrow 'a option set

where

$opt \ A = \text{insert } \text{None} \ \{\text{Some } y \mid y. y \in A\}$

definition *sup* :: 'a ebinop \Rightarrow 'a option ebinop

where

$sup \ f \ o_1 \ o_2 =$
 $(\text{case } o_1 \text{ of } \text{None} \Rightarrow \text{OK } o_2$
 $\mid \text{Some } x \Rightarrow (\text{case } o_2 \text{ of } \text{None} \Rightarrow \text{OK } o_1$
 $\mid \text{Some } y \Rightarrow (\text{case } f \ x \ y \text{ of } \text{Err} \Rightarrow \text{Err} \mid \text{OK } z \Rightarrow \text{OK } (\text{Some } z))))$

definition *esl* :: 'a esl \Rightarrow 'a option esl

where

$$esl = (\lambda(A,r,f). (opt\ A, le\ r, sup\ f))$$

lemma *unfold-le-opt*:

$$\begin{aligned} o_1 \sqsubseteq_{le\ r} o_2 = \\ (case\ o_2\ of\ None \Rightarrow o_1 = None \mid \\ \quad Some\ y \Rightarrow (case\ o_1\ of\ None \Rightarrow True \mid Some\ x \Rightarrow x \sqsubseteq_r y)) \end{aligned}$$

lemma *le-opt-refl*: $order\ r \Longrightarrow x \sqsubseteq_{le\ r} x$

2.4 Products as Semilattices

theory *Product*

imports *Err*

begin

definition *le* :: $'a\ ord \Rightarrow 'b\ ord \Rightarrow ('a \times 'b)\ ord$

where

$$le\ r_A\ r_B = (\lambda(a_1,b_1)\ (a_2,b_2). a_1 \sqsubseteq_{r_A} a_2 \wedge b_1 \sqsubseteq_{r_B} b_2)$$

definition *sup* :: $'a\ ebinop \Rightarrow 'b\ ebinop \Rightarrow ('a \times 'b)\ ebinop$

where

$$sup\ f\ g = (\lambda(a_1,b_1)(a_2,b_2). Err.sup\ Pair\ (a_1 \sqcup_f a_2)\ (b_1 \sqcup_g b_2))$$

definition *esl* :: $'a\ esl \Rightarrow 'b\ esl \Rightarrow ('a \times 'b)\ esl$

where

$$esl = (\lambda(A,r_A,f_A)\ (B,r_B,f_B). (A \times B, le\ r_A\ r_B, sup\ f_A\ f_B))$$

abbreviation

$$\begin{aligned} lesubprod :: 'a \times 'b \Rightarrow ('a \Rightarrow 'a \Rightarrow bool) \Rightarrow ('b \Rightarrow 'b \Rightarrow bool) \Rightarrow 'a \times 'b \Rightarrow bool \\ (\langle (- / \sqsubseteq'(-,-) -) \rangle [50, 0, 0, 51] 50) \textbf{ where} \\ p \sqsubseteq (r_A, r_B)\ q == p \sqsubseteq_{Product.le\ r_A\ r_B} q \end{aligned}$$

lemma *unfold-lesub-prod*: $x \sqsubseteq (r_A, r_B)\ y = le\ r_A\ r_B\ x\ y$

lemma *le-prod-Pair-conv* [iff]: $((a_1, b_1) \sqsubseteq (r_A, r_B)\ (a_2, b_2)) = (a_1 \sqsubseteq_{r_A} a_2 \ \&\ b_1 \sqsubseteq_{r_B} b_2)$

lemma *less-prod-Pair-conv*:

$$\begin{aligned} ((a_1, b_1) \sqsubset_{Product.le\ r_A\ r_B} (a_2, b_2)) = \\ (a_1 \sqsubset_{r_A} a_2 \ \&\ b_1 \sqsubset_{r_B} b_2 \mid a_1 \sqsubseteq_{r_A} a_2 \ \&\ b_1 \sqsubset_{r_B} b_2) \end{aligned}$$

lemma *order-le-prod* [iff]: $order(Product.le\ r_A\ r_B) = (order\ r_A \ \&\ order\ r_B)$

lemma *acc-le-prodI* [intro!]:

$$\llbracket acc\ A\ r_A; acc\ B\ r_B \rrbracket \Longrightarrow acc\ (A \times B)\ (Product.le\ r_A\ r_B)$$

lemma *closed-lift2-sup*:

$$\llbracket closed\ (err\ A)\ (lift2\ f); closed\ (err\ B)\ (lift2\ g) \rrbracket \Longrightarrow \\ closed\ (err\ (A \times B))\ (lift2\ (sup\ f\ g))$$

lemma *unfold-plussub-lift2*: $e_1 \sqcup_{lift2\ f} e_2 = lift2\ f\ e_1\ e_2$

lemma *plus-eq-Err-conv* [simp]:

$$\begin{aligned} \textbf{assumes } x \in A\ y \in A\ semilat(err\ A, Err.le\ r, lift2\ f) \\ \textbf{shows } (x \sqcup_f y = Err) = (\neg(\exists z \in A. x \sqsubseteq_r z \wedge y \sqsubseteq_r z)) \end{aligned}$$

lemma *err-semilat-Product-esl*:

$$\bigwedge L_1\ L_2. \llbracket err-semilat\ L_1; err-semilat\ L_2 \rrbracket \Longrightarrow err-semilat(Product.esl\ L_1\ L_2)$$

end

2.5 Fixed Length Lists

theory *Listn*
imports *Err*
begin

definition *list* :: *nat* \Rightarrow *'a set* \Rightarrow *'a list set*
where
list *n A* = {*xs. size xs = n* \wedge *set xs* \subseteq *A*}

definition *le* :: *'a ord* \Rightarrow (*'a list*)*ord*
where
le r = *list-all2* ($\lambda x y. x \sqsubseteq_r y$)

abbreviation
lesublist :: *'a list* \Rightarrow *'a ord* \Rightarrow *'a list* \Rightarrow *bool* ($\langle(- /[\sqsubseteq_r] -)\rangle$ [50, 0, 51] 50) **where**
x [\sqsubseteq_r] *y* == *x* \leq -(*Listn.le r*) *y*

abbreviation
lessublist :: *'a list* \Rightarrow *'a ord* \Rightarrow *'a list* \Rightarrow *bool* ($\langle(- /[\sqsubset_r] -)\rangle$ [50, 0, 51] 50) **where**
x [\sqsubset_r] *y* == *x* \leq -(*Listn.le r*) *y*

abbreviation
plussublist :: *'a list* \Rightarrow (*'a* \Rightarrow *'b* \Rightarrow *'c*) \Rightarrow *'b list* \Rightarrow *'c list*
($\langle(- /[\sqcup_r] -)\rangle$ [65, 0, 66] 65) **where**
x [\sqcup_r] *y* == *x* \sqcup_{map2} *f y*

primrec *coalesce* :: *'a err list* \Rightarrow *'a list err*
where
coalesce [] = *OK* []
| *coalesce* (*ex#exs*) = *Err.sup* (#) *ex* (*coalesce exs*)

definition *sl* :: *nat* \Rightarrow *'a sl* \Rightarrow *'a list sl*
where
sl n = ($\lambda(A,r,f). (list\ n\ A, le\ r, map2\ f)$)

definition *sup* :: (*'a* \Rightarrow *'b* \Rightarrow *'c err*) \Rightarrow *'a list* \Rightarrow *'b list* \Rightarrow *'c list err*
where
sup f = ($\lambda xs\ ys. if\ size\ xs = size\ ys\ then\ coalesce(xs\ [\sqcup_r]\ ys)\ else\ Err$)

definition *upto-esl* :: *nat* \Rightarrow *'a esl* \Rightarrow *'a list esl*
where
upto-esl m = ($\lambda(A,r,f). (Union\{list\ n\ A\ |\ n. n \leq m\}, le\ r, sup\ f)$)

lemmas [*simp*] = *set-update-subsetI*

lemma *unfold-lesub-list*: *xs* [\sqsubseteq_r] *ys* = *Listn.le r xs ys*

lemma *Nil-le-conv* [*iff*]: ([] [\sqsubseteq_r] *ys*) = (*ys* = [])

lemma *Cons-notle-Nil* [*iff*]: $\neg x\#\#xs$ [\sqsubseteq_r] []

lemma *Cons-le-Cons* [iff]: $x\#xs \sqsubseteq_r y\#ys = (x \sqsubseteq_r y \wedge xs \sqsubseteq_r ys)$

lemma *Cons-less-Conss* [simp]:

$order\ r \implies x\#xs \sqsubseteq_r y\#ys = (x \sqsubseteq_r y \wedge xs \sqsubseteq_r ys \vee x = y \wedge xs \sqsubseteq_r ys)$

lemma *list-update-le-cong*:

$\llbracket i < size\ xs; xs \sqsubseteq_r ys; x \sqsubseteq_r y \rrbracket \implies xs[i:=x] \sqsubseteq_r ys[i:=y]$

lemma *le-listD*: $\llbracket xs \sqsubseteq_r ys; p < size\ xs \rrbracket \implies xs!p \sqsubseteq_r ys!p$

lemma *le-list-refl*: $\forall x. x \sqsubseteq_r x \implies xs \sqsubseteq_r xs$

lemma *le-list-trans*: $\llbracket order\ r; xs \sqsubseteq_r ys; ys \sqsubseteq_r zs \rrbracket \implies xs \sqsubseteq_r zs$

lemma *le-list-antisym*: $\llbracket order\ r; xs \sqsubseteq_r ys; ys \sqsubseteq_r xs \rrbracket \implies xs = ys$

lemma *order-listI* [simp, intro!]: $order\ r \implies order(Listn.le\ r)$

lemma *lesub-list-impl-same-size* [simp]: $xs \sqsubseteq_r ys \implies size\ ys = size\ xs$

lemma *lessub-lengthD*: $xs \sqsubseteq_r ys \implies size\ ys = size\ xs$

lemma *le-list-appendI*: $a \sqsubseteq_r b \implies c \sqsubseteq_r d \implies a@c \sqsubseteq_r b@d$

lemma *le-listI*:

assumes $length\ a = length\ b$

assumes $\bigwedge n. n < length\ a \implies a!n \sqsubseteq_r b!n$

shows $a \sqsubseteq_r b$

lemma *listI*: $\llbracket size\ xs = n; set\ xs \subseteq A \rrbracket \implies xs \in list\ n\ A$

lemma *listE-length* [simp]: $xs \in list\ n\ A \implies size\ xs = n$

lemma *less-lengthI*: $\llbracket xs \in list\ n\ A; p < n \rrbracket \implies p < size\ xs$

lemma *listE-set* [simp]: $xs \in list\ n\ A \implies set\ xs \subseteq A$

lemma *list-0* [simp]: $list\ 0\ A = \{\}\}$

lemma *in-list-Suc-iff*:

$(xs \in list\ (Suc\ n)\ A) = (\exists y \in A. \exists ys \in list\ n\ A. xs = y\#ys)$

lemma *Cons-in-list-Suc* [iff]:

$(x\#xs \in list\ (Suc\ n)\ A) = (x \in A \wedge xs \in list\ n\ A)$

lemma *list-not-empty*:

$\exists a. a \in A \implies \exists xs. xs \in list\ n\ A$

lemma *nth-in* [rule-format, simp]:

$\forall i\ n. size\ xs = n \longrightarrow set\ xs \subseteq A \longrightarrow i < n \longrightarrow (xs!i) \in A$

lemma *listE-nth-in*: $\llbracket xs \in list\ n\ A; i < n \rrbracket \implies xs!i \in A$

lemma *listn-Cons-Suc* [elim!]:

$l\#xs \in list\ n\ A \implies (\bigwedge n'. n = Suc\ n' \implies l \in A \implies xs \in list\ n'\ A \implies P) \implies P$

lemma *listn-appendE* [elim!]:

$a@c \in list\ n\ A \implies (\bigwedge n1\ n2. n = n1 + n2 \implies a \in list\ n1\ A \implies b \in list\ n2\ A \implies P) \implies P$

lemma *listt-update-in-list* [simp, intro!]:

$\llbracket xs \in list\ n\ A; x \in A \rrbracket \implies xs[i := x] \in list\ n\ A$

lemma *list-appendI* [intro?]:

$\llbracket a \in list\ n\ A; b \in list\ m\ A \rrbracket \implies a @ b \in list\ (n+m)\ A$

lemma *list-map* [simp]: $(map\ f\ xs \in list\ (size\ xs)\ A) = (f\ ' set\ xs \subseteq A)$

lemma *list-replicateI* [intro]: $x \in A \implies replicate\ n\ x \in list\ n\ A$

lemma *plus-list-Nil* [simp]: $\llbracket \sqcup_f\ xs = [] \rrbracket$

lemma *plus-list-Cons* [simp]:

$(x\#xs) \sqcup_f\ ys = (case\ ys\ of\ [] \Rightarrow [] \mid y\#ys \Rightarrow (x \sqcup_f\ y)\#(xs \sqcup_f\ ys))$

lemma *length-plus-list* [rule-format, simp]:

$\forall ys. size(xs \sqcup_f\ ys) = \min(size\ xs)\ (size\ ys)$

lemma *nth-plus-list* [rule-format, simp]:

$\forall xs\ ys\ i. size\ xs = n \longrightarrow size\ ys = n \longrightarrow i < n \longrightarrow (xs \sqcup_f\ ys)!i = (xs!i) \sqcup_f\ (ys!i)$

lemma (in *Semilat*) *plus-list-ub1* [rule-format]:

$\llbracket \text{set } xs \subseteq A; \text{ set } ys \subseteq A; \text{ size } xs = \text{ size } ys \rrbracket$
 $\implies xs \llbracket \sqsubseteq_r \rrbracket xs \llbracket \sqcup_f \rrbracket ys$

lemma (in *Semilat*) *plus-list-ub2*:

$\llbracket \text{set } xs \subseteq A; \text{ set } ys \subseteq A; \text{ size } xs = \text{ size } ys \rrbracket \implies ys \llbracket \sqsubseteq_r \rrbracket xs \llbracket \sqcup_f \rrbracket ys$

lemma (in *Semilat*) *plus-list-lub* [rule-format]:

shows $\forall xs\ ys\ zs. \text{ set } xs \subseteq A \longrightarrow \text{ set } ys \subseteq A \longrightarrow \text{ set } zs \subseteq A$
 $\longrightarrow \text{ size } xs = n \wedge \text{ size } ys = n \longrightarrow$
 $xs \llbracket \sqsubseteq_r \rrbracket zs \wedge ys \llbracket \sqsubseteq_r \rrbracket zs \longrightarrow xs \llbracket \sqcup_f \rrbracket ys \llbracket \sqsubseteq_r \rrbracket zs$

lemma (in *Semilat*) *list-update-incr* [rule-format]:

$x \in A \implies \text{ set } xs \subseteq A \longrightarrow$
 $(\forall i. i < \text{ size } xs \longrightarrow xs \llbracket \sqsubseteq_r \rrbracket xs[i := x \sqcup_f xs!i])$

lemma *acc-le-list1'* [intro!]:

$\llbracket \text{ order } r; \text{ acc } A\ r \rrbracket \implies \text{ acc } (\bigcup n. \text{ list } n\ A) (\text{ Listn.le } r)$

2.6 Typing and Dataflow Analysis Framework

theory *Typing-Framework*

imports

Semilattices

begin

The relationship between dataflow analysis and a welltyped-instruction predicate.

type-synonym

$'s \text{ step-type} = \text{ nat} \Rightarrow 's \Rightarrow (\text{ nat} \times 's) \text{ list}$

definition *stable* :: $'s \text{ ord} \Rightarrow 's \text{ step-type} \Rightarrow 's \text{ list} \Rightarrow \text{ nat} \Rightarrow \text{ bool}$

where

$\text{ stable } r \text{ step } \tau s\ p \longleftrightarrow (\forall (q, \tau) \in \text{ set } (\text{ step } p (\tau s!p)). \tau \llbracket \sqsubseteq_r \rrbracket \tau s!q)$

definition *stables* :: $'s \text{ ord} \Rightarrow 's \text{ step-type} \Rightarrow 's \text{ list} \Rightarrow \text{ bool}$

where

$\text{ stables } r \text{ step } \tau s \longleftrightarrow (\forall p < \text{ size } \tau s. \text{ stable } r \text{ step } \tau s\ p)$

definition *wt-step* :: $'s \text{ ord} \Rightarrow 's \Rightarrow 's \text{ step-type} \Rightarrow 's \text{ list} \Rightarrow \text{ bool}$

where

$\text{ wt-step } r\ T \text{ step } \tau s \longleftrightarrow (\forall p < \text{ size } \tau s. \tau s!p \neq T \wedge \text{ stable } r \text{ step } \tau s\ p)$

definition *is-bcv* :: $'s \text{ ord} \Rightarrow 's \Rightarrow 's \text{ step-type} \Rightarrow \text{ nat} \Rightarrow 's \text{ set} \Rightarrow ('s \text{ list} \Rightarrow 's \text{ list}) \Rightarrow \text{ bool}$

where

$\text{ is-bcv } r\ T \text{ step } n\ A\ \text{ bcv} \longleftrightarrow (\forall \tau s_0 \in \text{ list } n\ A.$

$(\forall p < n. (\text{ bcv } \tau s_0)!p \neq T) = (\exists \tau s \in \text{ list } n\ A. \tau s_0 \llbracket \sqsubseteq_r \rrbracket \tau s \wedge \text{ wt-step } r\ T \text{ step } \tau s))$

end

2.7 More on Semilattices

theory *SemilatAlg*

imports *Typing-Framework*

begin

definition *lesubstep-type* :: $(\text{ nat} \times 's) \text{ set} \Rightarrow 's \text{ ord} \Rightarrow (\text{ nat} \times 's) \text{ set} \Rightarrow \text{ bool}$

$(\langle \cdot / \{ \sqsubseteq \cdot \} \cdot \rangle [50, 0, 51] 50)$

where $A \llbracket \sqsubseteq_r \rrbracket B \equiv \forall (p, \tau) \in A. \exists \tau'. (p, \tau') \in B \wedge \tau \llbracket \sqsubseteq_r \rrbracket \tau'$

notation (*ASCII*)

lesubstep-type $\langle (- / \{ \leq' - \} -) \rangle$ [50, 0, 51] 50)

primrec *pluslssub* :: 'a list \Rightarrow ('a \Rightarrow 'a \Rightarrow 'a) \Rightarrow 'a \Rightarrow 'a $\langle (- / \sqcup -) \rangle$ [65, 0, 66] 65)

where

pluslssub [] $f y = y$

| *pluslssub* (x#xs) $f y = pluslssub xs f (x \sqcup_f y)$

definition *bounded* :: 's step-type \Rightarrow nat \Rightarrow bool

where

bounded step n $\longleftrightarrow (\forall p < n. \forall \tau. \forall (q, \tau') \in set (step p \tau). q < n)$

definition *pres-type* :: 's step-type \Rightarrow nat \Rightarrow 's set \Rightarrow bool

where

pres-type step n A $\longleftrightarrow (\forall \tau \in A. \forall p < n. \forall (q, \tau') \in set (step p \tau). \tau' \in A)$

definition *mono* :: 's ord \Rightarrow 's step-type \Rightarrow nat \Rightarrow 's set \Rightarrow bool

where

mono r step n A \longleftrightarrow

$(\forall \tau p \tau'. \tau \in A \wedge p < n \wedge \tau \sqsubseteq_r \tau' \longrightarrow set (step p \tau) \{\sqsubseteq_r\} set (step p \tau'))$

lemma [*iff*]: $\{\} \{\sqsubseteq_r\} B$

lemma [*iff*]: $(A \{\sqsubseteq_r\} \{\}) = (A = \{\})$

lemma *lesubstep-union*:

$\llbracket A_1 \{\sqsubseteq_r\} B_1; A_2 \{\sqsubseteq_r\} B_2 \rrbracket \Longrightarrow A_1 \cup A_2 \{\sqsubseteq_r\} B_1 \cup B_2$

lemma *pres-typeD*:

$\llbracket pres-type step n A; s \in A; p < n; (q, s') \in set (step p s) \rrbracket \Longrightarrow s' \in A$

lemma *monoD*:

$\llbracket mono r step n A; p < n; s \in A; s \sqsubseteq_r t \rrbracket \Longrightarrow set (step p s) \{\sqsubseteq_r\} set (step p t)$

lemma *boundedD*:

$\llbracket bounded step n; p < n; (q, t) \in set (step p xs) \rrbracket \Longrightarrow q < n$

lemma *lesubstep-type-refl* [*simp, intro*]:

$(\bigwedge x. x \sqsubseteq_r x) \Longrightarrow A \{\sqsubseteq_r\} A$

lemma *lesub-step-typeD*:

$A \{\sqsubseteq_r\} B \Longrightarrow (x, y) \in A \Longrightarrow \exists y'. (x, y') \in B \wedge y \sqsubseteq_r y'$

lemma *list-update-le-listI* [*rule-format*]:

$set xs \subseteq A \longrightarrow set ys \subseteq A \longrightarrow xs \{\sqsubseteq_r\} ys \longrightarrow p < size xs \longrightarrow$

$x \sqsubseteq_r ys!p \longrightarrow semilat(A, r, f) \longrightarrow x \in A \longrightarrow$

$xs[p := x \sqcup_f xs!p] \{\sqsubseteq_r\} ys$

lemma *plusplus-closed*: **assumes** *Semilat A r f* **shows**

$\bigwedge y. \llbracket set x \subseteq A; y \in A \rrbracket \Longrightarrow x \sqcup_f y \in A$

lemma (**in** *Semilat*) *pp-ub2*:

$\bigwedge y. \llbracket set x \subseteq A; y \in A \rrbracket \Longrightarrow y \sqsubseteq_r x \sqcup_f y$

lemma (**in** *Semilat*) *pp-ub1*:

shows $\bigwedge y. \llbracket set ls \subseteq A; y \in A; x \in set ls \rrbracket \Longrightarrow x \sqsubseteq_r ls \sqcup_f y$

lemma (**in** *Semilat*) *pp-lub*:

assumes $z: z \in A$

shows

$$\bigwedge y. y \in A \implies \text{set } xs \subseteq A \implies \forall x \in \text{set } xs. x \sqsubseteq_r z \implies y \sqsubseteq_r z \implies xs \sqcup_f y \sqsubseteq_r z$$

lemma ub1': *assumes* *Semilat A r f*
shows $\llbracket \forall (p,s) \in \text{set } S. s \in A; y \in A; (a,b) \in \text{set } S \rrbracket$
 $\implies b \sqsubseteq_r \text{map snd } \llbracket (p', t') \leftarrow S. p' = a \rrbracket \sqcup_f y$

lemma plusplus-empty:
 $\forall s'. (q, s') \in \text{set } S \longrightarrow s' \sqcup_f ss ! q = ss ! q \implies$
 $(\text{map snd } \llbracket (p', t') \leftarrow S. p' = q \rrbracket \sqcup_f ss ! q) = ss ! q$

end

2.8 Lifting the Typing Framework to err, app, and eff

theory *Typing-Framework-err*

imports

Typing-Framework

SemilatAlg

begin

definition *wt-err-step* :: *'s ord* \Rightarrow *'s err step-type* \Rightarrow *'s err list* \Rightarrow *bool*

where

wt-err-step r step $\tau s \longleftrightarrow \text{wt-step } (\text{Err.le } r) \text{ Err step } \tau s$

definition *wt-app-eff* :: *'s ord* \Rightarrow (*nat* \Rightarrow *'s* \Rightarrow *bool*) \Rightarrow *'s step-type* \Rightarrow *'s list* \Rightarrow *bool*

where

wt-app-eff r app step $\tau s \longleftrightarrow$
 $(\forall p < \text{size } \tau s. \text{app } p (\tau s ! p) \wedge (\forall (q,\tau) \in \text{set } (\text{step } p (\tau s ! p)). \tau \leq\text{-}r \tau s ! q))$

definition *map-snd* :: (*'b* \Rightarrow *'c*) \Rightarrow (*'a* \times *'b*) *list* \Rightarrow (*'a* \times *'c*) *list*

where

map-snd f = *map* $(\lambda(x,y). (x, f y))$

definition *error* :: *nat* \Rightarrow (*nat* \times *'a err*) *list*

where

error n = *map* $(\lambda x. (x, \text{Err})) [0..<n]$

definition *err-step* :: *nat* \Rightarrow (*nat* \Rightarrow *'s* \Rightarrow *bool*) \Rightarrow *'s step-type* \Rightarrow *'s err step-type*

where

err-step n app step $p t =$
 $(\text{case } t \text{ of}$
 $\quad \text{Err} \Rightarrow \text{error } n$
 $\quad | \text{OK } \tau \Rightarrow \text{if app } p \tau \text{ then map-snd OK (step } p \tau) \text{ else error } n)$

definition *app-mono* :: *'s ord* \Rightarrow (*nat* \Rightarrow *'s* \Rightarrow *bool*) \Rightarrow *nat* \Rightarrow *'s set* \Rightarrow *bool*

where

app-mono r app n A \longleftrightarrow
 $(\forall s p t. s \in A \wedge p < n \wedge s \sqsubseteq_r t \longrightarrow \text{app } p t \longrightarrow \text{app } p s)$

lemmas *err-step-defs* = *err-step-def map-snd-def error-def*

lemma *bounded-err-stepD*:

$\llbracket \text{bounded } (\text{err-step } n \text{ app step}) \ n;$
 $p < n; \text{ app } p \ a; (q, b) \in \text{set } (\text{step } p \ a) \rrbracket \implies q < n$

lemma *in-map-sndD*: $(a, b) \in \text{set } (\text{map-snd } f \ xs) \implies \exists b'. (a, b') \in \text{set } xs$

lemma *bounded-err-stepI*:

$\forall p. p < n \longrightarrow (\forall s. \text{ ap } p \ s \longrightarrow (\forall (q, s') \in \text{set } (\text{step } p \ s). q < n))$
 $\implies \text{bounded } (\text{err-step } n \ \text{ap step}) \ n$

lemma *bounded-lift*:

$\text{bounded step } n \implies \text{bounded } (\text{err-step } n \ \text{app step}) \ n$

lemma *le-list-map-OK* [*simp*]:

$\bigwedge b. (\text{map } \text{OK } a \ [\sqsubseteq_{\text{Err.le } r}] \ \text{map } \text{OK } b) = (a \ [\sqsubseteq_r] \ b)$

lemma *map-snd-lessI*:

$\text{set } xs \ \{\sqsubseteq_r\} \ \text{set } ys \implies \text{set } (\text{map-snd } \text{OK } xs) \ \{\sqsubseteq_{\text{Err.le } r}\} \ \text{set } (\text{map-snd } \text{OK } ys)$

lemma *mono-lift*:

$\llbracket \text{order } r; \text{ app-mono } r \ \text{app } n \ A; \text{ bounded } (\text{err-step } n \ \text{app step}) \ n;$
 $\forall s \ p \ t. s \in A \wedge p < n \wedge s \ \sqsubseteq_r \ t \longrightarrow \text{app } p \ t \longrightarrow \text{set } (\text{step } p \ s) \ \{\sqsubseteq_r\} \ \text{set } (\text{step } p \ t) \rrbracket$
 $\implies \text{mono } (\text{Err.le } r) \ (\text{err-step } n \ \text{app step}) \ n \ (\text{err } A)$

lemma *in-errorD*: $(x, y) \in \text{set } (\text{error } n) \implies y = \text{Err}$

lemma *pres-type-lift*:

$\forall s \in A. \forall p. p < n \longrightarrow \text{app } p \ s \longrightarrow (\forall (q, s') \in \text{set } (\text{step } p \ s). s' \in A)$
 $\implies \text{pres-type } (\text{err-step } n \ \text{app step}) \ n \ (\text{err } A)$

lemma *wt-err-imp-wt-app-eff*:

assumes *wt*: $\text{wt-err-step } r \ (\text{err-step } (\text{size } ts) \ \text{app step}) \ ts$
assumes *b*: $\text{bounded } (\text{err-step } (\text{size } ts) \ \text{app step}) \ (\text{size } ts)$
shows $\text{wt-app-eff } r \ \text{app step } (\text{map } \text{ok-val } ts)$

lemma *wt-app-eff-imp-wt-err*:

assumes *app-eff*: $\text{wt-app-eff } r \ \text{app step } ts$
assumes *bounded*: $\text{bounded } (\text{err-step } (\text{size } ts) \ \text{app step}) \ (\text{size } ts)$
shows $\text{wt-err-step } r \ (\text{err-step } (\text{size } ts) \ \text{app step}) \ (\text{map } \text{OK } ts)$

end

2.9 Kildall's Algorithm

theory *Kildall*

imports *SemilatAlg ../Basic/Auxiliary*

begin

locale *Kildall-base* =

fixes $s\text{-}\alpha :: 'w \Rightarrow \text{nat set}$
and $s\text{-empty} :: 'w$
and $s\text{-is-empty} :: 'w \Rightarrow \text{bool}$
and $s\text{-choose} :: 'w \Rightarrow \text{nat}$
and $s\text{-remove} :: \text{nat} \Rightarrow 'w \Rightarrow 'w$
and $s\text{-insert} :: \text{nat} \Rightarrow 'w \Rightarrow 'w$

begin

primrec *propa* :: 's binop \Rightarrow (nat \times 's) list \Rightarrow 's list \Rightarrow 'w \Rightarrow 's list * 'w

where

propa f [] τs w = ($\tau s, w$)
| *propa* f (q'#qs) τs w = (let (q, τ) = q';
 $u = \tau \sqcup_f \tau s!q$;
 $w' = (\text{if } u = \tau s!q \text{ then } w \text{ else } s\text{-insert } q \text{ } w)$
in *propa* f qs ($\tau s[q := u]$) w')

definition *iter* :: 's binop \Rightarrow 's step-type \Rightarrow 's list \Rightarrow 'w \Rightarrow 's list \times 'w

where

iter f step τs w =
while ($\lambda(\tau s, w). \neg s\text{-is-empty } w$)
($\lambda(\tau s, w). \text{let } p = s\text{-choose } w \text{ in } \text{propa } f \text{ (step } p \text{ } (\tau s!p)) \tau s \text{ (s-remove } p \text{ } w)$)
($\tau s, w$)

definition *unstables* :: 's ord \Rightarrow 's step-type \Rightarrow 's list \Rightarrow 'w

where

unstables r step $\tau s = \text{foldr } s\text{-insert (filter } (\lambda p. \neg \text{stable } r \text{ step } \tau s \text{ } p) [0..<\text{size } \tau s]) \text{ } s\text{-empty}$

definition *kildall* :: 's ord \Rightarrow 's binop \Rightarrow 's step-type \Rightarrow 's list \Rightarrow 's list

where *kildall* r f step $\tau s \equiv \text{fst}(\text{iter } f \text{ step } \tau s \text{ (unstables } r \text{ step } \tau s))$

primrec *t- α* :: 's list \times 'w \Rightarrow 's list \times nat set

where *t- α* ($\tau s, w$) = ($\tau s, s\text{-}\alpha \text{ } w$)

end

primrec *merges* :: 's binop \Rightarrow (nat \times 's) list \Rightarrow 's list \Rightarrow 's list

where

merges f [] $\tau s = \tau s$
| *merges* f (p'#ps) $\tau s = (\text{let } (p, \tau) = p' \text{ in } \text{merges } f \text{ } ps \text{ } (\tau s[p := \tau \sqcup_f \tau s!p]))$

locale *Kildall* =

Kildall-base +

assumes *empty-spec* [*simp*]: $s\text{-}\alpha \text{ } s\text{-empty} = \{\}$
and *is-empty-spec* [*simp*]: $s\text{-is-empty } A \longleftrightarrow s\text{-}\alpha \text{ } A = \{\}$
and *choose-spec*: $s\text{-}\alpha \text{ } A \neq \{\} \implies s\text{-choose } A \in s\text{-}\alpha \text{ } A$
and *remove-spec* [*simp*]: $s\text{-}\alpha \text{ (s-remove } n \text{ } A) = s\text{-}\alpha \text{ } A - \{n\}$
and *insert-spec* [*simp*]: $s\text{-}\alpha \text{ (s-insert } n \text{ } A) = \text{insert } n \text{ (s-}\alpha \text{ } A)$

begin

lemma *s- α -foldr-s-insert*:

$s\text{-}\alpha \text{ (foldr } s\text{-insert } xs \text{ } A) = \text{foldr } \text{insert } xs \text{ (s-}\alpha \text{ } A)$

by(*induct xs arbitrary: A simp-all*)

lemma *unstables-spec* [*simp*]: $s\text{-}\alpha \text{ (unstables } r \text{ step } \tau s) = \{p. p < \text{size } \tau s \wedge \neg \text{stable } r \text{ step } \tau s \text{ } p\}$

proof –

have $\{p. p < \text{size } \tau s \wedge \neg \text{stable } r \text{ step } \tau s \text{ } p\} = \text{foldr } \text{insert (filter } (\lambda p. \neg \text{stable } r \text{ step } \tau s \text{ } p) [0..<\text{size } \tau s]) \{\}$

unfolding *foldr-insert-conv-set* **by** *auto*

thus *?thesis* **by**(*simp add: unstables-def s- α -foldr-s-insert*)

qed

end

lemmas [simp] = Let-def Semilat.le-iff-plus-unchanged [OF Semilat.intro, symmetric]

lemma (in Semilat) nth-merges:

$\bigwedge ss. \llbracket p < \text{length } ss; ss \in \text{list } n \ A; \forall (p,t) \in \text{set } ps. p < n \wedge t \in A \rrbracket \implies$
 $(\text{merges } f \ ps \ ss)!p = \text{map } \text{snd} \llbracket (p',t') \leftarrow ps. p'=p \rrbracket \sqcup_f \text{ss!}p$
 $(\text{is } \bigwedge ss. \llbracket -; -, ?\text{steptype } ps \rrbracket \implies ?P \ ss \ ps)$

lemma length-merges [simp]:

$\bigwedge ss. \text{size}(\text{merges } f \ ps \ ss) = \text{size } ss$

lemma (in Semilat) merges-preserves-type-lemma:

shows $\forall xs. xs \in \text{list } n \ A \longrightarrow (\forall (p,x) \in \text{set } ps. p < n \wedge x \in A)$
 $\longrightarrow \text{merges } f \ ps \ xs \in \text{list } n \ A$

lemma (in Semilat) merges-preserves-type [simp]:

$\llbracket xs \in \text{list } n \ A; \forall (p,x) \in \text{set } ps. p < n \wedge x \in A \rrbracket$
 $\implies \text{merges } f \ ps \ xs \in \text{list } n \ A$

by (simp add: merges-preserves-type-lemma)

lemma (in Semilat) merges-incr-lemma:

$\forall xs. xs \in \text{list } n \ A \longrightarrow (\forall (p,x) \in \text{set } ps. p < \text{size } xs \wedge x \in A) \longrightarrow xs \llbracket \sqsubseteq_r \rrbracket \text{merges } f \ ps \ xs$

lemma (in Semilat) merges-incr:

$\llbracket xs \in \text{list } n \ A; \forall (p,x) \in \text{set } ps. p < \text{size } xs \wedge x \in A \rrbracket$
 $\implies xs \llbracket \sqsubseteq_r \rrbracket \text{merges } f \ ps \ xs$

by (simp add: merges-incr-lemma)

lemma (in Semilat) merges-same-conv [rule-format]:

$(\forall xs. xs \in \text{list } n \ A \longrightarrow (\forall (p,x) \in \text{set } ps. p < \text{size } xs \wedge x \in A) \longrightarrow$
 $(\text{merges } f \ ps \ xs = xs) = (\forall (p,x) \in \text{set } ps. x \llbracket \sqsubseteq_r \rrbracket \text{xs!}p))$

lemma (in Semilat) list-update-le-listI [rule-format]:

$\text{set } xs \subseteq A \longrightarrow \text{set } ys \subseteq A \longrightarrow xs \llbracket \sqsubseteq_r \rrbracket ys \longrightarrow p < \text{size } xs \longrightarrow$
 $x \llbracket \sqsubseteq_r \rrbracket \text{ys!}p \longrightarrow x \in A \longrightarrow xs[p := x \sqcup_f \text{xs!}p] \llbracket \sqsubseteq_r \rrbracket ys$

lemma (in Semilat) merges-pres-le-ub:

assumes $\text{set } ts \subseteq A \ \text{set } ss \subseteq A$

$\forall (p,t) \in \text{set } ps. t \llbracket \sqsubseteq_r \rrbracket \text{ts!}p \wedge t \in A \wedge p < \text{size } ts \ \text{ss} \llbracket \sqsubseteq_r \rrbracket \text{ts}$

shows $\text{merges } f \ ps \ ss \llbracket \sqsubseteq_r \rrbracket \text{ts}$

context Kildall begin

2.9.1 propa

lemma decomp-propa:

$\bigwedge ss \ w. (\forall (q,t) \in \text{set } qs. q < \text{size } ss) \implies$
 $t\text{-}\alpha \ (\text{propa } f \ qs \ ss \ w) =$
 $(\text{merges } f \ qs \ ss, \{q. \exists t. (q,t) \in \text{set } qs \wedge t \sqcup_f \text{ss!}q \neq \text{ss!}q\} \cup s\text{-}\alpha \ w)$

apply (induct qs)

apply simp

apply (simp (no-asm))

apply clarify

apply simp

```

apply (rule conjI)
  apply blast
apply (simp add: nth-list-update)
apply blast
done

```

end

lemma (in *Semilat*) *stable-pres-lemma*:

```

shows  $\llbracket$  pres-type step n A; bounded step n;
  ss  $\in$  list n A; p  $\in$  w;  $\forall q \in w. q < n$ ;
   $\forall q. q < n \longrightarrow q \notin w \longrightarrow$  stable r step ss q; q < n;
   $\forall s'. (q, s') \in$  set (step p (ss!p))  $\longrightarrow s' \sqcup_f$  ss!q = ss!q;
  q  $\notin$  w  $\vee$  q = p  $\rrbracket$ 
 $\implies$  stable r step (merges f (step p (ss!p)) ss) q

```

lemma (in *Semilat*) *merges-bounded-lemma*:

```

 $\llbracket$  mono r step n A; bounded step n;
   $\forall (p', s') \in$  set (step p (ss!p)). s'  $\in$  A; ss  $\in$  list n A; ts  $\in$  list n A; p < n;
  ss  $\llbracket \sqsubseteq_r \rrbracket$  ts;  $\forall p. p < n \longrightarrow$  stable r step ts p  $\rrbracket$ 
 $\implies$  merges f (step p (ss!p)) ss  $\llbracket \sqsubseteq_r \rrbracket$  ts

```

lemma *termination-lemma*: **assumes** *Semilat* A r f

```

shows  $\llbracket$  ss  $\in$  list n A;  $\forall (q, t) \in$  set qs. q < n  $\wedge$  t  $\in$  A; p  $\in$  w  $\rrbracket \implies$ 
  ss  $\llbracket \sqsubseteq_r \rrbracket$  merges f qs ss  $\vee$ 
  merges f qs ss = ss  $\wedge$  {q.  $\exists t. (q, t) \in$  set qs  $\wedge$  t  $\sqcup_f$  ss!q  $\neq$  ss!q}  $\cup$  (w - {p})  $\subset$  w

```

context *Kildall-base* **begin**

definition *s-finite-psubset* :: ('w * 'w) set

where *s-finite-psubset* == {(A, B). s- α A < s- α B & finite (s- α B)}

lemma *s-finite-psubset-inv-image*:

s-finite-psubset = inv-image finite-psubset s- α

by (auto simp add: s-finite-psubset-def finite-psubset-def)

lemma *wf-s-finite-psubset* [simp]: wf s-finite-psubset

unfolding s-finite-psubset-inv-image **by** simp

end

context *Kildall* **begin**

2.9.2 iter

lemma *iter-properties*[rule-format]: **assumes** *Semilat* A r f

```

shows  $\llbracket$  acc A r; pres-type step n A; mono r step n A;
  bounded step n;  $\forall p \in$  s- $\alpha$  w0. p < n; ss0  $\in$  list n A;
   $\forall p < n. p \notin$  s- $\alpha$  w0  $\longrightarrow$  stable r step ss0 p  $\rrbracket \implies$ 
  t- $\alpha$  (iter f step ss0 w0) = (ss', w')
 $\longrightarrow$ 
  ss'  $\in$  list n A  $\wedge$  stables r step ss'  $\wedge$  ss0  $\llbracket \sqsubseteq_r \rrbracket$  ss'  $\wedge$ 
  ( $\forall ts \in$  list n A. ss0  $\llbracket \sqsubseteq_r \rrbracket$  ts  $\wedge$  stables r step ts  $\longrightarrow$  ss'  $\llbracket \sqsubseteq_r \rrbracket$  ts)

```

lemma *kildall-properties*: **assumes** *Semilat* A r f

shows \llbracket acc A r; pres-type step n A; mono r step n A;

```

    bounded step n; ss0 ∈ list n A ] ==>
    kildall r f step ss0 ∈ list n A ∧
    stables r step (kildall r f step ss0) ∧
    ss0 [⊆r] kildall r f step ss0 ∧
    (∀ ts ∈ list n A. ss0 [⊆r] ts ∧ stables r step ts →
      kildall r f step ss0 [⊆r] ts)

```

end

interpretation *Kildall set* [] λxs. xs = [] *hd removeAll Cons*
by(*unfold-locales*) *auto*

lemmas *kildall-code* [code] =
kildall-def
Kildall-base.propo.simps
Kildall-base.iter-def
Kildall-base.unstables-def
Kildall-base.kildall-def

end

2.10 The Lightweight Bytecode Verifier

theory *LBVSpec*
imports *SemilatAlg Opt*
begin

type-synonym
's certificate = *'s list*

primrec *merge* :: *'s certificate* ⇒ *'s binop* ⇒ *'s ord* ⇒ *'s* ⇒ *nat* ⇒ (*nat* × *'s*) *list* ⇒ *'s* ⇒ *'s*
where

```

    merge cert f r T pc [] x = x
  | merge cert f r T pc (s#ss) x = merge cert f r T pc ss (let (pc',s') = s in
    if pc'=pc+1 then s' ⊔f x
    else if s' [⊆r] cert!pc' then x
    else T)

```

definition *wtl-inst* :: *'s certificate* ⇒ *'s binop* ⇒ *'s ord* ⇒ *'s* ⇒
's step-type ⇒ *nat* ⇒ *'s* ⇒ *'s*

where

```

    wtl-inst cert f r T step pc s = merge cert f r T pc (step pc s) (cert!(pc+1))

```

definition *wtl-cert* :: *'s certificate* ⇒ *'s binop* ⇒ *'s ord* ⇒ *'s* ⇒ *'s* ⇒
's step-type ⇒ *nat* ⇒ *'s* ⇒ *'s*

where

```

    wtl-cert cert f r T B step pc s =
    (if cert!pc = B then
      wtl-inst cert f r T step pc s
    else
      if s [⊆r] cert!pc then wtl-inst cert f r T step pc (cert!pc) else T)

```

primrec *wtl-inst-list* :: *'a list* ⇒ *'s certificate* ⇒ *'s binop* ⇒ *'s ord* ⇒ *'s* ⇒ *'s* ⇒
's step-type ⇒ *nat* ⇒ *'s* ⇒ *'s*

where

$wtl-inst-list [] \quad cert\ f\ r\ T\ B\ step\ pc\ s = s$
 $| wtl-inst-list\ (i\#\ is)\ cert\ f\ r\ T\ B\ step\ pc\ s =$
 $(let\ s' = wtl-cert\ cert\ f\ r\ T\ B\ step\ pc\ s\ in$
 $if\ s' = T \vee s = T\ then\ T\ else\ wtl-inst-list\ is\ cert\ f\ r\ T\ B\ step\ (pc+1)\ s')$

definition $cert-ok :: 's\ certificate \Rightarrow nat \Rightarrow 's \Rightarrow 's \Rightarrow 's\ set \Rightarrow bool$

where

$cert-ok\ cert\ n\ T\ B\ A \longleftrightarrow (\forall i < n.\ cert!i \in A \wedge cert!i \neq T) \wedge (cert!n = B)$

definition $bottom :: 'a\ ord \Rightarrow 'a \Rightarrow bool$

where

$bottom\ r\ B \longleftrightarrow (\forall x.\ B \sqsubseteq_r x)$

locale $lbv = Semilat +$

fixes $T :: 'a\ (\langle \top \rangle)$

fixes $B :: 'a\ (\langle \perp \rangle)$

fixes $step :: 'a\ step-type$

assumes $top: top\ r\ \top$

assumes $T-A: \top \in A$

assumes $bot: bottom\ r\ \perp$

assumes $B-A: \perp \in A$

fixes $merge :: 'a\ certificate \Rightarrow nat \Rightarrow (nat \times 'a)\ list \Rightarrow 'a \Rightarrow 'a$

defines $mrg-def: merge\ cert \equiv LBVSpec.merge\ cert\ f\ r\ \top$

fixes $wti :: 'a\ certificate \Rightarrow nat \Rightarrow 'a \Rightarrow 'a$

defines $wti-def: wti\ cert \equiv wtl-inst\ cert\ f\ r\ \top\ step$

fixes $wtc :: 'a\ certificate \Rightarrow nat \Rightarrow 'a \Rightarrow 'a$

defines $wtc-def: wtc\ cert \equiv wtl-cert\ cert\ f\ r\ \top\ \perp\ step$

fixes $wtl :: 'b\ list \Rightarrow 'a\ certificate \Rightarrow nat \Rightarrow 'a \Rightarrow 'a$

defines $wtl-def: wtl\ ins\ cert \equiv wtl-inst-list\ ins\ cert\ f\ r\ \top\ \perp\ step$

lemma (in lbv) wti :

$wti\ c\ pc\ s = merge\ c\ pc\ (step\ pc\ s)\ (c!(pc+1))$

lemma (in lbv) wtc :

$wtc\ c\ pc\ s = (if\ c!pc = \perp\ then\ wti\ c\ pc\ s\ else\ if\ s \sqsubseteq_r\ c!pc\ then\ wti\ c\ pc\ (c!pc)\ else\ \top)$

lemma $cert-okD1$ [intro?]:

$cert-ok\ c\ n\ T\ B\ A \Longrightarrow pc < n \Longrightarrow c!pc \in A$

lemma $cert-okD2$ [intro?]:

$cert-ok\ c\ n\ T\ B\ A \Longrightarrow c!n = B$

lemma $cert-okD3$ [intro?]:

$cert-ok\ c\ n\ T\ B\ A \Longrightarrow B \in A \Longrightarrow pc < n \Longrightarrow c!Suc\ pc \in A$

lemma $cert-okD4$ [intro?]:

$cert-ok\ c\ n\ T\ B\ A \Longrightarrow pc < n \Longrightarrow c!pc \neq T$

declare *Let-def* [*simp*]

2.10.1 more semilattice lemmas

lemma (**in** *lbv*) *sup-top* [*simp*, *elim*]:

assumes $x: x \in A$
shows $x \sqcup_f \top = \top$

lemma (**in** *lbv*) *plusplussup-top* [*simp*, *elim*]:

set $xs \subseteq A \implies xs \sqcup_f \top = \top$
by (*induct xs*) *auto*

lemma (**in** *Semilat*) *pp-ub1'*:

assumes $S: \text{snd}'\text{set } S \subseteq A$
assumes $y: y \in A$ **and** $ab: (a, b) \in \text{set } S$
shows $b \sqsubseteq_r \text{map snd } [(p', t') \leftarrow S . p' = a] \sqcup_f y$

lemma (**in** *lbv*) *bottom-le* [*simp*, *intro!*]: $\perp \sqsubseteq_r x$

by (*insert bot*) (*simp add: bottom-def*)

lemma (**in** *lbv*) *le-bottom* [*simp*]: $x \sqsubseteq_r \perp = (x = \perp)$

by (*blast intro: antisym-r*)

2.10.2 merge

lemma (**in** *lbv*) *merge-Nil* [*simp*]:

merge c pc [] x = x **by** (*simp add: mrg-def*)

lemma (**in** *lbv*) *merge-Cons* [*simp*]:

*merge c pc (l#ls) x = merge c pc ls (if fst l=pc+1 then snd l +-f x
else if snd l \sqsubseteq_r c!fst l then x
else \top)*

by (*simp add: mrg-def split-beta*)

lemma (**in** *lbv*) *merge-Err* [*simp*]:

snd' set ss $\subseteq A \implies \text{merge c pc ss } \top = \top$
by (*induct ss*) *auto*

lemma (**in** *lbv*) *merge-not-top*:

$\bigwedge x. \text{snd}'\text{set } ss \subseteq A \implies \text{merge c pc ss } x \neq \top \implies$
 $\forall (pc', s') \in \text{set } ss. (pc' \neq pc+1 \longrightarrow s' \sqsubseteq_r c!pc')$
(is $\bigwedge x. ?\text{set } ss \implies ?\text{merge } ss x \implies ?P ss)$

lemma (**in** *lbv*) *merge-def*:

shows

$\bigwedge x. x \in A \implies \text{snd}'\text{set } ss \subseteq A \implies$
 $\text{merge c pc ss } x =$
(if $\forall (pc', s') \in \text{set } ss. pc' \neq pc+1 \longrightarrow s' \sqsubseteq_r c!pc'$ then
 $\text{map snd } [(p', t') \leftarrow ss. p' = pc+1] \sqcup_f x$
else \top)

(is $\bigwedge x. - \implies - \implies ?\text{merge } ss x = ?\text{if } ss x \text{ is } \bigwedge x. - \implies - \implies ?P ss x)$

lemma (**in** *lbv*) *merge-not-top-s*:

assumes $x: x \in A$ **and** $ss: \text{snd}'\text{set } ss \subseteq A$
assumes $m: \text{merge c pc ss } x \neq \top$

shows merge c pc ss x = (map snd [(p',t') ← ss. p'=pc+1] \sqcup_f x)

2.10.3 wtl-inst-list

lemmas [iff] = not-Err-eq

lemma (in lbv) wtl-Nil [simp]: wtl [] c pc s = s
by (simp add: wtl-def)

lemma (in lbv) wtl-Cons [simp]:
wtl (i#is) c pc s =
(let s' = wtc c pc s in if s' = $\top \vee s = \top$ then \top else wtl is c (pc+1) s')
by (simp add: wtl-def wtc-def)

lemma (in lbv) wtl-Cons-not-top:
wtl (i#is) c pc s $\neq \top$ =
(wtc c pc s $\neq \top \wedge s \neq \top \wedge$ wtl is c (pc+1) (wtc c pc s) $\neq \top$)
by (auto simp del: split-paired-Ex)

lemma (in lbv) wtl-top [simp]: wtl ls c pc \top = \top
by (cases ls) auto

lemma (in lbv) wtl-not-top:
wtl ls c pc s $\neq \top \implies s \neq \top$
by (cases s= \top) auto

lemma (in lbv) wtl-append [simp]:
 $\bigwedge pc s. wtl (a@b) c pc s = wtl b c (pc+length a) (wtl a c pc s)$
by (induct a) auto

lemma (in lbv) wtl-take:
wtl is c pc s $\neq \top \implies wtl (take pc' is) c pc s \neq \top$
(is ?wtl is $\neq - \implies -$)

lemma take-Suc:
 $\forall n. n < length l \implies take (Suc n) l = (take n l)@[!n]$ (is ?P l)

lemma (in lbv) wtl-Suc:
assumes suc: pc+1 < length is
assumes wtl: wtl (take pc is) c 0 s $\neq \top$
shows wtl (take (pc+1) is) c 0 s = wtc c pc (wtl (take pc is) c 0 s)

lemma (in lbv) wtl-all:
assumes all: wtl is c 0 s $\neq \top$ (is ?wtl is $\neq -$)
assumes pc: pc < length is
shows wtc c pc (wtl (take pc is) c 0 s) $\neq \top$

2.10.4 preserves-type

lemma (in lbv) merge-pres:
assumes s0: snd'set ss $\subseteq A$ and x: x $\in A$
shows merge c pc ss x $\in A$

lemma pres-typeD2:
pres-type step n A $\implies s \in A \implies p < n \implies$ snd'set (step p s) $\subseteq A$
by auto (drule pres-typeD)

lemma (in lbv) wti-pres [intro?]:

```

  assumes pres: pres-type step n A
  assumes cert: c!(pc+1) ∈ A
  assumes s-pc: s ∈ A pc < n
  shows wtl c pc s ∈ A
lemma (in lbv) wtc-pres:
  assumes pres-type step n A
  assumes c!pc ∈ A and c!(pc+1) ∈ A
  assumes s ∈ A and pc < n
  shows wtc c pc s ∈ A
lemma (in lbv) wtl-pres:
  assumes pres: pres-type step (length is) A
  assumes cert: cert-ok c (length is) ⊤ ⊥ A
  assumes s: s ∈ A
  assumes all: wtl is c 0 s ≠ ⊤
  shows pc < length is ⇒ wtl (take pc is) c 0 s ∈ A
  (is ?len pc ⇒ ?wtl pc ∈ A)
end

```

2.11 Correctness of the LBV

```

theory LBVCorrect
imports LBVSpec Typing-Framework
begin

locale lbs = lbv +
  fixes s0 :: 'a
  fixes c   :: 'a list
  fixes ins :: 'b list
  fixes τs  :: 'a list
  defines phi-def:
    τs ≡ map (λpc. if c!pc = ⊥ then wtl (take pc ins) c 0 s0 else c!pc)
      [0..size ins]

  assumes bounded: bounded step (size ins)
  assumes cert: cert-ok c (size ins) ⊤ ⊥ A
  assumes pres: pres-type step (size ins) A

lemma (in lbs) phi-None [intro?]:
  [ pc < size ins; c!pc = ⊥ ] ⇒ τs!pc = wtl (take pc ins) c 0 s0
lemma (in lbs) phi-Some [intro?]:
  [ pc < size ins; c!pc ≠ ⊥ ] ⇒ τs!pc = c!pc
lemma (in lbs) phi-len [simp]: size τs = size ins
lemma (in lbs) wtl-suc-pc:
  assumes all: wtl ins c 0 s0 ≠ ⊤
  assumes pc: pc+1 < size ins
  shows wtl (take (pc+1) ins) c 0 s0 ⊆r τs!(pc+1)
lemma (in lbs) wtl-stable:
  assumes wtl: wtl ins c 0 s0 ≠ ⊤
  assumes s0: s0 ∈ A and pc: pc < size ins
  shows stable r step τs pc
lemma (in lbs) phi-not-top:
  assumes wtl: wtl ins c 0 s0 ≠ ⊤ and pc: pc < size ins
  shows τs!pc ≠ ⊤

```


lemma (in *lbs*) *phi-in-A*:
assumes *wtl*: *wtl ins c 0 s₀ ≠ ⊤* **and** *s₀*: *s₀ ∈ A*
shows $\tau s \in \text{list } (\text{size } \text{ins}) A$

lemma (in *lbs*) *phi0*:
assumes *wtl*: *wtl ins c 0 s₀ ≠ ⊤* **and** *0*: *0 < size ins*
shows $s_0 \sqsubseteq_r \tau s!0$

theorem (in *lbs*) *wtl-sound*:
assumes *wtl*: *wtl ins c 0 s₀ ≠ ⊤* **and** *s₀*: *s₀ ∈ A*
shows $\exists \tau s. \text{wt-step } r \top \text{step } \tau s$

theorem (in *lbs*) *wtl-sound-strong*:
assumes *wtl*: *wtl ins c 0 s₀ ≠ ⊤*
assumes *s₀*: *s₀ ∈ A* **and** *ins*: *0 < size ins*
shows $\exists \tau s \in \text{list } (\text{size } \text{ins}) A. \text{wt-step } r \top \text{step } \tau s \wedge s_0 \sqsubseteq_r \tau s!0$

end

2.12 Completeness of the LBV

theory *LBVComplete*
imports *LBVSpec Typing-Framework*
begin

definition *is-target* :: '*s* *step-type* ⇒ '*s* *list* ⇒ *nat* ⇒ *bool* **where**
is-target *step* τs *pc'* $\longleftrightarrow (\exists pc\ s'. pc' \neq pc+1 \wedge pc < \text{size } \tau s \wedge (pc', s') \in \text{set } (\text{step } pc (\tau s!pc)))$

definition *make-cert* :: '*s* *step-type* ⇒ '*s* *list* ⇒ '*s* ⇒ '*s* *certificate* **where**
make-cert *step* τs *B* = *map* ($\lambda pc. \text{if } \text{is-target } \text{step } \tau s\ pc \text{ then } \tau s!pc \text{ else } B$) [0..*size* τs] @ [*B*]

lemma [*code*]:
is-target *step* τs *pc'* =
list-ex ($\lambda pc. pc' \neq pc+1 \wedge \text{List.member } (\text{map } \text{fst } (\text{step } pc (\tau s!pc)))\ pc'$) [0..*size* τs]

locale *lbvc* = *lbv* +
fixes τs :: '*a* *list*
fixes *c* :: '*a* *list*
defines *cert-def*: $c \equiv \text{make-cert } \text{step } \tau s \perp$

assumes *mono*: *mono r step (size* τs) *A*
assumes *pres*: *pres-type step (size* τs) *A*
assumes τs : $\forall pc < \text{size } \tau s. \tau s!pc \in A \wedge \tau s!pc \neq \top$
assumes *bounded*: *bounded step (size* τs)

assumes *B-neq-T*: $\perp \neq \top$

lemma (in *lbvc*) *cert*: *cert-ok c (size* τs) $\top \perp A$

lemmas [*simp del*] = *split-paired-Ex*

lemma (in *lbvc*) *cert-target* [*intro?*]:
 $\llbracket (pc', s') \in \text{set } (\text{step } pc (\tau s!pc));$
 $pc' \neq pc+1; pc < \text{size } \tau s; pc' < \text{size } \tau s \rrbracket$
 $\implies c!pc' = \tau s!pc'$

lemma (in *lbvc*) *cert-approx* [*intro?*]:

$\llbracket pc < size \tau s; c!pc \neq \perp \rrbracket \implies c!pc = \tau s!pc$
lemma (in *lbv*) *le-top* [*simp, intro*]: $x \leq -r \top$
lemma (in *lbv*) *merge-mono*:
 assumes *less*: $set\ ss_2 \sqsubseteq_r set\ ss_1$
 assumes *x*: $x \in A$
 assumes *ss1*: $snd'set\ ss_1 \subseteq A$
 assumes *ss2*: $snd'set\ ss_2 \subseteq A$
 shows *merge* *c pc ss2 x* \sqsubseteq_r *merge* *c pc ss1 x* (is $?s_2 \sqsubseteq_r ?s_1$)
lemma (in *lbvc*) *wti-mono*:
 assumes *less*: $s_2 \sqsubseteq_r s_1$
 assumes *pc*: $pc < size \tau s$ and *s1*: $s_1 \in A$ and *s2*: $s_2 \in A$
 shows *wti* *c pc s2* \sqsubseteq_r *wti* *c pc s1* (is $?s_2' \sqsubseteq_r ?s_1'$)
lemma (in *lbvc*) *wtc-mono*:
 assumes *less*: $s_2 \sqsubseteq_r s_1$
 assumes *pc*: $pc < size \tau s$ and *s1*: $s_1 \in A$ and *s2*: $s_2 \in A$
 shows *wtc* *c pc s2* \sqsubseteq_r *wtc* *c pc s1* (is $?s_2' \sqsubseteq_r ?s_1'$)
lemma (in *lbv*) *top-le-conv* [*simp*]: $\top \sqsubseteq_r x = (x = \top)$
lemma (in *lbv*) *neq-top* [*simp, elim*]: $\llbracket x \sqsubseteq_r y; y \neq \top \rrbracket \implies x \neq \top$
lemma (in *lbvc*) *stable-wti*:
 assumes *stable*: *stable* *r step* $\tau s pc$ and *pc*: $pc < size \tau s$
 shows *wti* *c pc* $(\tau s!pc) \neq \top$
lemma (in *lbvc*) *wti-less*:
 assumes *stable*: *stable* *r step* $\tau s pc$ and *suc-pc*: *Suc* $pc < size \tau s$
 shows *wti* *c pc* $(\tau s!pc) \sqsubseteq_r \tau s!Suc\ pc$ (is $?wti \sqsubseteq_r -$)
lemma (in *lbvc*) *stable-wtc*:
 assumes *stable*: *stable* *r step* $\tau s pc$ and *pc*: $pc < size \tau s$
 shows *wtc* *c pc* $(\tau s!pc) \neq \top$
lemma (in *lbvc*) *wtc-less*:
 assumes *stable*: *stable* *r step* $\tau s pc$ and *suc-pc*: *Suc* $pc < size \tau s$
 shows *wtc* *c pc* $(\tau s!pc) \sqsubseteq_r \tau s!Suc\ pc$ (is $?wtc \sqsubseteq_r -$)
lemma (in *lbvc*) *wt-step-wtl-lemma*:
 assumes *wt-step*: *wt-step* *r* \top *step* τs
 shows $\bigwedge pc\ s. pc + size\ ls = size\ \tau s \implies s \sqsubseteq_r \tau s!pc \implies s \in A \implies s \neq \top \implies$
 $wtl\ ls\ c\ pc\ s \neq \top$
 (is $\bigwedge pc\ s. - \implies - \implies - \implies - \implies ?wtl\ ls\ pc\ s \neq -$)
theorem (in *lbvc*) *wtl-complete*:
 assumes *wt*: *wt-step* *r* \top *step* τs
 assumes *s*: $s \sqsubseteq_r \tau s!0$ $s \in A$ $s \neq \top$ and *eq*: *size* *ins* = *size* τs
 shows *wtl* *ins* *c* 0 $s \neq \top$
end

Chapter 3

Concepts for all JinjaThreads Languages

3.1 JinjaThreads types

```
theory Type
imports
  ../Basic/Auxiliary
begin

type-synonym cname = String.literal — class names
type-synonym mname = String.literal — method name
type-synonym vname = String.literal — names for local/field variables

definition Object :: cname
where Object ≡ STR "java/lang/Object"

definition Thread :: cname
where Thread ≡ STR "java/lang/Thread"

definition Throwable :: cname
where Throwable ≡ STR "java/lang/Throwable"

definition this :: vname
where this ≡ STR "this"

definition run :: mname
where run ≡ STR "run() V"

definition start :: mname
where start ≡ STR "start() V"

definition wait :: mname
where wait ≡ STR "wait() V"

definition notify :: mname
where notify ≡ STR "notify() V"

definition notifyAll :: mname
```

where *notifyAll* \equiv *STR* "notifyAll() V"

definition *join* :: *mname*

where *join* \equiv *STR* "join() V"

definition *interrupt* :: *mname*

where *interrupt* \equiv *STR* "interrupt() V"

definition *isInterrupted* :: *mname*

where *isInterrupted* \equiv *STR* "isInterrupted() Z"

definition *hashCode* :: *mname*

where *hashCode* = *STR* "hashCode() I"

definition *clone* :: *mname*

where *clone* = *STR* "clone()Ljava/lang/Object;"

definition *print* :: *mname*

where *print* = *STR* "~print(I) V"

definition *currentThread* :: *mname*

where *currentThread* = *STR* "~Thread.currentThread()Ljava/lang/Thread;"

definition *interrupted* :: *mname*

where *interrupted* = *STR* "~Thread.interrupted() Z"

definition *yield* :: *mname*

where *yield* = *STR* "~Thread.yield() V"

lemmas *identifier-name-defs* [*code-unfold*] =

*this-def run-def start-def wait-def notify-def notifyAll-def join-def interrupt-def isInterrupted-def
hashCode-def clone-def print-def currentThread-def interrupted-def yield-def*

lemma *Object-Thread-Throwable-neq* [*simp*]:

Thread \neq *Object* *Object* \neq *Thread*

Object \neq *Throwable* *Throwable* \neq *Object*

Thread \neq *Throwable* *Throwable* \neq *Thread*

by(*auto simp add: Thread-def Object-def Throwable-def*)

lemma *synth-method-names-neq-aux*:

start \neq *wait* *start* \neq *notify* *start* \neq *notifyAll* *start* \neq *join* *start* \neq *interrupt* *start* \neq *isInterrupted*

start \neq *hashCode* *start* \neq *clone* *start* \neq *print* *start* \neq *currentThread*

start \neq *interrupted* *start* \neq *yield* *start* \neq *run*

wait \neq *notify* *wait* \neq *notifyAll* *wait* \neq *join* *wait* \neq *interrupt* *wait* \neq *isInterrupted*

wait \neq *hashCode* *wait* \neq *clone* *wait* \neq *print* *wait* \neq *currentThread*

wait \neq *interrupted* *wait* \neq *yield* *wait* \neq *run*

notify \neq *notifyAll* *notify* \neq *join* *notify* \neq *interrupt* *notify* \neq *isInterrupted*

notify \neq *hashCode* *notify* \neq *clone* *notify* \neq *print* *notify* \neq *currentThread*

notify \neq *interrupted* *notify* \neq *yield* *notify* \neq *run*

notifyAll \neq *join* *notifyAll* \neq *interrupt* *notifyAll* \neq *isInterrupted*

notifyAll \neq *hashCode* *notifyAll* \neq *clone* *notifyAll* \neq *print* *notifyAll* \neq *currentThread*

notifyAll \neq *interrupted* *notifyAll* \neq *yield* *notifyAll* \neq *run*

```

join ≠ interrupt join ≠ isInterrupted
join ≠ hashCode join ≠ clone join ≠ print join ≠ currentThread
join ≠ interrupted join ≠ yield join ≠ run
interrupt ≠ isInterrupted
interrupt ≠ hashCode interrupt ≠ clone interrupt ≠ print interrupt ≠ currentThread
interrupt ≠ interrupted interrupt ≠ yield interrupt ≠ run
isInterrupted ≠ hashCode isInterrupted ≠ clone isInterrupted ≠ print isInterrupted ≠ currentThread

isInterrupted ≠ interrupted isInterrupted ≠ yield isInterrupted ≠ run
hashCode ≠ clone hashCode ≠ print hashCode ≠ currentThread
hashCode ≠ interrupted hashCode ≠ yield hashCode ≠ run
clone ≠ print clone ≠ currentThread
clone ≠ interrupted clone ≠ yield clone ≠ run
print ≠ currentThread
print ≠ interrupted print ≠ yield print ≠ run
currentThread ≠ interrupted currentThread ≠ yield currentThread ≠ run
interrupted ≠ yield interrupted ≠ run
yield ≠ run
by (simp-all add: identifier-name-defs)

lemmas synth-method-names-neq [simp] = synth-method-names-neq-aux synth-method-names-neq-aux[symmetric]

— types
datatype ty
  = Void      — type of statements
  | Boolean
  | Integer
  | NT        — null type
  | Class cname — class type
  | Array ty  (<-[]> 95) — array type

context
  notes [[inductive-internals]]
begin

inductive is-refT :: ty ⇒ bool where
  is-refT NT
| is-refT (Class C)
| is-refT (A[])

declare is-refT.intros[iff]

end

lemmas refTE [consumes 1, case-names NT Class Array] = is-refT.cases

lemma not-refTE [consumes 1, case-names Void Boolean Integer]:
  [[ ¬is-refT T; T = Void ⇒ P; T = Boolean ⇒ P; T = Integer ⇒ P ]] ⇒ P
by (cases T, auto)

fun ground-type :: ty ⇒ ty where
  ground-type (Array T) = ground-type T
| ground-type T = T

```

abbreviation *is-NT-Array* :: $ty \Rightarrow bool$ **where**
is-NT-Array $T \equiv \text{ground-type } T = NT$

primrec *the-Class* :: $ty \Rightarrow cname$
where
the-Class (*Class* C) = C

primrec *the-Array* :: $ty \Rightarrow ty$
where
the-Array ($T[]$) = T

datatype *htype* =
Class-type $cname$
| *Array-type* $ty\ nat$

primrec *ty-of-htype* :: $htype \Rightarrow ty$
where
ty-of-htype (*Class-type* C) = *Class* C
| *ty-of-htype* (*Array-type* $T\ n$) = *Array* T

primrec *alen-of-htype* :: $htype \Rightarrow nat$
where
alen-of-htype (*Array-type* $T\ n$) = n

primrec *class-type-of* :: $htype \Rightarrow cname$
where
class-type-of (*Class-type* C) = C
| *class-type-of* (*Array-type* $T\ n$) = *Object*

fun *class-type-of'* :: $ty \Rightarrow cname\ option$
where
class-type-of' (*Class* C) = $[C]$
| *class-type-of'* (*Array* T) = $[Object]$
| *class-type-of'* - = *None*

lemma *rec-htype-is-case* [*simp*]: *rec-htype* = *case-htype*
by(*auto simp add: fun-eq-iff split: htype.split*)

lemma *ty-of-htype-eq-convs* [*simp*]:
shows *ty-of-htype-eq-Boolean*: *ty-of-htype* $hT \neq Boolean$
and *ty-of-htype-eq-Void*: *ty-of-htype* $hT \neq Void$
and *ty-of-htype-eq-Integer*: *ty-of-htype* $hT \neq Integer$
and *ty-of-htype-eq-NT*: *ty-of-htype* $hT \neq NT$
and *ty-of-htype-eq-Class*: *ty-of-htype* $hT = Class\ C \iff hT = Class\text{-type}\ C$
and *ty-of-htype-eq-Array*: *ty-of-htype* $hT = Array\ T \iff (\exists n. hT = Array\text{-type}\ T\ n)$
by(*case-tac [!] hT*) *simp-all*

lemma *class-type-of-eq*:
class-type-of $hT =$
(*case* hT *of* *Class-type* $C \Rightarrow C$ | *Array-type* $T\ n \Rightarrow Object$)
by(*simp split: htype.split*)

lemma *class-type-of'-ty-of-htype* [*simp*]:

```

class-type-of' (ty-of-hType hT) = [class-type-of hT]
by(cases hT) simp-all

```

```

fun is-Array :: ty ⇒ bool
where
  is-Array (Array T) = True
| is-Array - = False

```

```

lemma is-Array-conv [simp]: is-Array T ⟷ (∃ U. T = Array U)
by(cases T) simp-all

```

```

fun is-Class :: ty ⇒ bool
where
  is-Class (Class C) = True
| is-Class - = False

```

```

lemma is-Class-conv [simp]: is-Class T ⟷ (∃ C. T = Class C)
by(cases T) simp-all

```

3.1.1 Code generator setup

```
code-pred is-refT .
```

```
end
```

3.2 Class Declarations and Programs

```
theory Decl
```

```
imports
```

```
  Type
```

```
begin
```

```
type-synonym volatile = bool
```

```
record fmod =
  volatile :: volatile
```

```
type-synonym fdecl = vname × ty × fmod — field declaration
```

```
type-synonym 'm mdecl = mname × ty list × ty × 'm — method = name, arg. types, return
type, body
```

```
type-synonym 'm mdecl' = mname × ty list × ty × 'm option — method = name, arg. types,
return type, possible body
```

```
type-synonym 'm class = cname × fdecl list × 'm mdecl' list — class = superclass, fields,
methods
```

```
type-synonym 'm cdecl = cname × 'm class — class declaration
```

```
datatype
```

```
'm prog = Program 'm cdecl list
```

```
translations
```

```
(type) fdecl <= (type) String.literal × ty × fmod
```

```
(type) 'c mdecl <= (type) String.literal × ty list × ty × 'c
```

```
(type) 'c mdecl' <= (type) String.literal × ty list × ty × 'c option
```

```
(type) 'c class <= (type) String.literal × fdecl list × ('c mdecl) list
```

(*type*) 'c cdecl ≤ (type) *String.literal* × ('c class)

notation (*input*) *None* (⟨*Native*⟩)

primrec *classes* :: 'm prog ⇒ 'm cdecl list

where

classes (*Program P*) = *P*

primrec *class* :: 'm prog ⇒ *cname* → 'm class

where

class (*Program p*) = *map-of p*

locale *prog* =

fixes *P* :: 'm prog

definition *is-class* :: 'm prog ⇒ *cname* ⇒ bool

where

is-class P C ≡ *class P C* ≠ *None*

lemma *finite-is-class*: *finite* {*C. is-class P C*}

primrec *is-type* :: 'm prog ⇒ *ty* ⇒ bool

where

is-type-void: *is-type P Void* = *True*

| *is-type-bool*: *is-type P Boolean* = *True*

| *is-type-int*: *is-type P Integer* = *True*

| *is-type-nt*: *is-type P NT* = *True*

| *is-type-class*: *is-type P (Class C)* = *is-class P C*

| *is-type-array*: *is-type P (A[])* = (case *ground-type A* of *NT* ⇒ *False* | *Class C* ⇒ *is-class P C* | - ⇒ *True*)

lemma *is-type-ArrayD*: *is-type P (T[])* ⇒ *is-type P T*

by(*induct T*) *auto*

lemma *is-type-ground-type*:

is-type P T ⇒ *is-type P (ground-type T)*

by(*induct T*)(*auto*, *metis is-type-ArrayD is-type-array*)

abbreviation *types* :: 'm prog ⇒ *ty set*

where *types P* ≡ {*T. is-type P T*}

abbreviation *is-htype* :: 'm prog ⇒ *htype* ⇒ bool

where *is-htype P hT* ≡ *is-type P (ty-of-htype hT)*

3.2.1 Code generation

lemma *is-class-intros* [*code-pred-intro*]:

class P C ≠ *None* ⇒ *is-class P C*

by(*auto simp add: is-class-def*)

code-pred

(*modes: i* ⇒ *i* ⇒ bool)

is-class

unfolding *is-class-def* **by** *simp*

declare *is-class-def*[code]

end

3.3 Relations between Jinja Types

theory *TypeRel*

imports

Decl

begin

3.3.1 The subclass relations

inductive *subcls1* :: 'm prog \Rightarrow cname \Rightarrow cname \Rightarrow bool ($\langle \cdot \vdash \cdot \prec^1 \cdot \rangle$ [71, 71, 71] 70)

for *P* :: 'm prog

where *subcls1I*: $\llbracket \text{class } P \ C = \text{Some } (D, \text{rest}); C \neq \text{Object} \rrbracket \Longrightarrow P \vdash C \prec^1 D$

abbreviation *subcls* :: 'm prog \Rightarrow cname \Rightarrow cname \Rightarrow bool ($\langle \cdot \vdash \cdot \preceq^* \cdot \rangle$ [71, 71, 71] 70)

where $P \vdash C \preceq^* D \equiv (\text{subcls1 } P)^{**} C D$

lemma *subcls1D*:

$P \vdash C \prec^1 D \Longrightarrow C \neq \text{Object} \wedge (\exists fs \ ms. \text{class } P \ C = \text{Some } (D, fs, ms))$

by(*auto elim: subcls1.cases*)

lemma *Object-subcls1 [iff]*: $\neg P \vdash \text{Object} \prec^1 C$

by(*simp add: subcls1.simps*)

lemma *Object-subcls-conv [iff]*: $(P \vdash \text{Object} \preceq^* C) = (C = \text{Object})$

by(*auto elim: converse-rtranclpE*)

lemma *finite-subcls1*: *finite* $\{(C, D). P \vdash C \prec^1 D\}$

proof –

let *?A* = *SIGMA* *C*: $\{C. \text{is-class } P \ C\}. \{D. C \neq \text{Object} \wedge \text{fst } (\text{the } (\text{class } P \ C)) = D\}$

have *finite ?A* **by**(*rule finite-SigmaI [OF finite-is-class]*) *auto*

also have *?A* = $\{(C, D). P \vdash C \prec^1 D\}$

by(*fastforce simp:is-class-def dest: subcls1D elim: subcls1I*)

finally show *?thesis* .

qed

lemma *finite-subcls1'*:

finite $\{(D, C). P \vdash C \prec^1 D\}$

by(*subst finite-converse[symmetric]*)

(*simp add: converse-unfold finite-subcls1 del: finite-converse*)

lemma *subcls-is-class*: $(\text{subcls1 } P)^{++} C D \Longrightarrow \text{is-class } P \ C$

by(*auto elim: converse-tranclpE dest!: subcls1D simp add: is-class-def*)

lemma *subcls-is-class1*: $\llbracket P \vdash C \preceq^* D; \text{is-class } P \ D \rrbracket \Longrightarrow \text{is-class } P \ C$

by(*auto elim: converse-rtranclpE dest!: subcls1D simp add: is-class-def*)

3.3.2 The subtype relations

inductive *widen* :: 'm prog \Rightarrow ty \Rightarrow ty \Rightarrow bool ($\langle \cdot \vdash \cdot \leq \cdot \rangle$ [71, 71, 71] 70)

for *P* :: 'm prog

where

$widen-refl[iff]: P \vdash T \leq T$
 $widen-subcls: P \vdash C \preceq^* D \implies P \vdash \text{Class } C \leq \text{Class } D$
 $widen-null[iff]: P \vdash NT \leq \text{Class } C$
 $widen-null-array[iff]: P \vdash NT \leq \text{Array } A$
 $widen-array-object: P \vdash \text{Array } A \leq \text{Class } \text{Object}$
 $widen-array-array: P \vdash A \leq B \implies P \vdash \text{Array } A \leq \text{Array } B$

abbreviation

$widens :: 'm \text{ prog} \Rightarrow \text{ty list} \Rightarrow \text{ty list} \Rightarrow \text{bool} (\lambda _ \vdash _ \leq _ \rightarrow [71, 71, 71] \ 70)$

where

$P \vdash Ts \leq Ts' == \text{list-all2 } (widen \ P) \ Ts \ Ts'$

lemma $[iff]: (P \vdash T \leq \text{Void}) = (T = \text{Void})$

lemma $[iff]: (P \vdash T \leq \text{Boolean}) = (T = \text{Boolean})$

lemma $[iff]: (P \vdash T \leq \text{Integer}) = (T = \text{Integer})$

lemma $[iff]: (P \vdash \text{Void} \leq T) = (T = \text{Void})$

lemma $[iff]: (P \vdash \text{Boolean} \leq T) = (T = \text{Boolean})$

lemma $[iff]: (P \vdash \text{Integer} \leq T) = (T = \text{Integer})$

lemma *Class-widen*: $P \vdash \text{Class } C \leq T \implies \exists D. T = \text{Class } D$

by(*erule widen.cases, auto*)

lemma *Array-Array-widen*:

$P \vdash \text{Array } T \leq \text{Array } U \implies P \vdash T \leq U$

by(*auto elim: widen.cases*)

lemma *widen-Array*: $(P \vdash T \leq U[]) \iff (T = NT \vee (\exists V. T = V[] \wedge P \vdash V \leq U))$

by(*induct T*)(*auto dest: Array-Array-widen elim: widen.cases intro: widen-array-array*)

lemma *Array-widen*: $P \vdash \text{Array } A \leq T \implies (\exists B. T = \text{Array } B \wedge P \vdash A \leq B) \vee T = \text{Class } \text{Object}$

by(*auto elim: widen.cases*)

lemma $[iff]: (P \vdash T \leq NT) = (T = NT)$

by(*induct T*)(*auto dest: Class-widen Array-widen*)

lemma *Class-widen-Class* $[iff]: (P \vdash \text{Class } C \leq \text{Class } D) = (P \vdash C \preceq^* D)$

by (*auto elim: widen-subcls widen.cases*)

lemma *widen-Class*: $(P \vdash T \leq \text{Class } C) = (T = NT \vee (\exists D. T = \text{Class } D \wedge P \vdash D \preceq^* C) \vee (C = \text{Object} \wedge (\exists A. T = \text{Array } A)))$

by(*induct T*)(*auto dest: Array-widen intro: widen-array-object*)

lemma *NT-widen*:

$P \vdash NT \leq T = (T = NT \vee (\exists C. T = \text{Class } C) \vee (\exists U. T = U[]))$

by(*cases T*) *auto*

lemma *Class-widen2*: $P \vdash \text{Class } C \leq T = (\exists D. T = \text{Class } D \wedge P \vdash C \preceq^* D)$

by (*cases T, auto elim: widen.cases*)

lemma *Object-widen*: $P \vdash \text{Class } \text{Object} \leq T \implies T = \text{Class } \text{Object}$

by(*cases T, auto elim: widen.cases*)

lemma *NT-Array-widen-Object*:

$is-NT-Array \ T \implies P \vdash T \leq \text{Class } \text{Object}$

by(*induct T*, *auto intro: widen-array-object*)

lemma *widen-trans*[*trans*]:

assumes $P \vdash S \leq U \ P \vdash U \leq T$

shows $P \vdash S \leq T$

using *assms*

proof(*induct arbitrary: T*)

case (*widen-refl T T'*) **thus** $P \vdash T \leq T'$.

next

case (*widen-subcls C D T*)

then obtain *E* **where** $T = \text{Class } E$ **by** (*blast dest: Class-widen*)

with *widen-subcls* **show** $P \vdash \text{Class } C \leq T$ **by** (*auto elim: rtrancl-trans*)

next

case (*widen-null C RT*)

then obtain *D* **where** $RT = \text{Class } D$ **by** (*blast dest: Class-widen*)

thus $P \vdash NT \leq RT$ **by** *auto*

next

case *widen-null-array* **thus** ?*case* **by**(*auto dest: Array-widen*)

next

case (*widen-array-object A T*)

hence $T = \text{Class Object}$ **by**(*rule Object-widen*)

with *widen-array-object* **show** $P \vdash A[] \leq T$

by(*auto intro: widen.widen-array-object*)

next

case *widen-array-array* **thus** ?*case*

by(*auto dest!: Array-widen intro: widen.widen-array-array widen-array-object*)

qed

lemma *widens-trans*: $\llbracket P \vdash Ss \llbracket \leq \rrbracket Ts; P \vdash Ts \llbracket \leq \rrbracket Us \rrbracket \implies P \vdash Ss \llbracket \leq \rrbracket Us$

by (*rule list-all2-trans*)(*rule widen-trans*)

lemma *class-type-of'-widenD*:

class-type-of' $T = \lfloor C \rfloor \implies P \vdash T \leq \text{Class } C$

by(*cases T*)(*auto intro: widen-array-object*)

lemma *widen-is-class-type-of*:

assumes *class-type-of'* $T = \lfloor C \rfloor$ $P \vdash T' \leq T$ $T' \neq NT$

obtains *C'* **where** *class-type-of'* $T' = \lfloor C' \rfloor$ $P \vdash C' \preceq^* C$

using *assms* **by**(*cases T*)(*auto simp add: widen-Class widen-Array*)

lemma *widens-refl*: $P \vdash Ts \llbracket \leq \rrbracket Ts$

by(*rule list-all2-refl*[*OF widen-refl*])

lemma *widen-append1*:

$P \vdash (xs @ ys) \llbracket \leq \rrbracket Ts = (\exists Ts1 Ts2. Ts = Ts1 @ Ts2 \wedge \text{length } xs = \text{length } Ts1 \wedge \text{length } ys = \text{length } Ts2 \wedge P \vdash xs \llbracket \leq \rrbracket Ts1 \wedge P \vdash ys \llbracket \leq \rrbracket Ts2)$

unfolding *list-all2-append1* **by** *fastforce*

lemmas *widens-Cons* [*iff*] = *list-all2-Cons1* [*of widen P*] **for** *P*

lemma *widens-lengthD*:

$P \vdash xs \llbracket \leq \rrbracket ys \implies \text{length } xs = \text{length } ys$

by(*rule list-all2-lengthD*)

lemma *widen-refT*: $\llbracket \text{is-refT } T; P \vdash U \leq T \rrbracket \Longrightarrow \text{is-refT } U$
by(*erule refTE*)(*auto simp add: widen-Class widen-Array*)

lemma *refT-widen*: $\llbracket \text{is-refT } T; P \vdash T \leq U \rrbracket \Longrightarrow \text{is-refT } U$
by(*erule widen.cases*) *auto*

inductive *is-lub* :: '*m prog* \Rightarrow *ty* \Rightarrow *ty* \Rightarrow *ty* \Rightarrow *bool* ($\langle \vdash \text{lub}((-,/ -)' \rangle = \rightarrow [51,51,51,51] 50$)
for *P* :: '*m prog* **and** *U* :: *ty* **and** *V* :: *ty* **and** *T* :: *ty*

where

$\llbracket P \vdash U \leq T; P \vdash V \leq T; \\ \bigwedge T'. \llbracket P \vdash U \leq T'; P \vdash V \leq T' \rrbracket \Longrightarrow P \vdash T \leq T' \rrbracket \\ \Longrightarrow P \vdash \text{lub}(U, V) = T$

lemma *is-lub-upper*:

$P \vdash \text{lub}(U, V) = T \Longrightarrow P \vdash U \leq T \wedge P \vdash V \leq T$

by(*auto elim: is-lub.cases*)

lemma *is-lub-least*:

$\llbracket P \vdash \text{lub}(U, V) = T; P \vdash U \leq T'; P \vdash V \leq T' \rrbracket \Longrightarrow P \vdash T \leq T'$

by(*auto elim: is-lub.cases*)

lemma *is-lub-Void* [*iff*]:

$P \vdash \text{lub}(\text{Void}, \text{Void}) = T \longleftrightarrow T = \text{Void}$

by(*auto intro: is-lub.intros elim: is-lub.cases*)

lemma *is-lubI* [*code-pred-intro*]:

$\llbracket P \vdash U \leq T; P \vdash V \leq T; \forall T'. P \vdash U \leq T' \longrightarrow P \vdash V \leq T' \longrightarrow P \vdash T \leq T' \rrbracket \Longrightarrow P \vdash \text{lub}(U, V) = T$

by(*blast intro: is-lub.intros*)

3.3.3 Method lookup

inductive *Methods* :: '*m prog* \Rightarrow *cname* \Rightarrow (*mname* \rightarrow (*ty list* \times *ty* \times '*m option*) \times *cname*) \Rightarrow *bool*
($\langle \vdash - \text{sees}'\text{-methods} \rightarrow [51,51,51] 50$)

for *P* :: '*m prog*

where

sees-methods-Object:

$\llbracket \text{class } P \text{ Object} = \text{Some}(D, fs, ms); Mm = \text{map-option } (\lambda m. (m, \text{Object})) \circ \text{map-of } ms \rrbracket \\ \Longrightarrow P \vdash \text{Object sees-methods } Mm$

| *sees-methods-rec*:

$\llbracket \text{class } P \text{ C} = \text{Some}(D, fs, ms); C \neq \text{Object}; P \vdash D \text{ sees-methods } Mm; \\ Mm' = Mm ++ (\text{map-option } (\lambda m. (m, C)) \circ \text{map-of } ms) \rrbracket \\ \Longrightarrow P \vdash C \text{ sees-methods } Mm'$

lemma *sees-methods-fun*:

assumes $P \vdash C \text{ sees-methods } Mm$

shows $P \vdash C \text{ sees-methods } Mm' \Longrightarrow Mm' = Mm$

using *assms*

proof(*induction arbitrary: Mm'*)

case *sees-methods-Object* **thus** ?*case* **by**(*auto elim: Methods.cases*)

next

case (*sees-methods-rec* *C D fs ms Dres Cres Cres'*)

from $\langle P \vdash C \text{ sees-methods } Cres' \rangle \langle C \neq \text{Object} \rangle \langle \text{class } P \text{ C} = [(D, fs, ms)] \rangle$

obtain *Dres'* **where** *Dmethods'*: $P \vdash D \text{ sees-methods } Dres'$

and $Cres'$: $Cres' = Dres' ++ (map-option (\lambda m. (m, C)) \circ map-of ms)$
by *cases auto*
from *sees-methods-rec.IH[OF Dmethods']* $\langle Cres = Dres ++ (map-option (\lambda m. (m, C)) \circ map-of ms) \rangle$ $Cres'$
show *?case by simp*
qed

lemma *visible-methods-exist*:

$P \vdash C$ *sees-methods* $Mm \implies Mm M = Some(m, D) \implies$
 $(\exists D' fs ms. class P D = Some(D', fs, ms) \wedge map-of ms M = Some m)$
by (*induct rule:Methods.induct*) *auto*

lemma *sees-methods-decl-above*:

assumes $P \vdash C$ *sees-methods* Mm
shows $Mm M = Some(m, D) \implies P \vdash C \preceq^* D$
using *assms*
by *induct(auto elim: converse-rtranclp-into-rtranclp[where r = subcls1 P, OF subcls1I])*

lemma *sees-methods-idemp*:

assumes $P \vdash C$ *sees-methods* Mm **and** $Mm M = Some(m, D)$
shows $\exists Mm'. (P \vdash D$ *sees-methods* $Mm') \wedge Mm' M = Some(m, D)$
using *assms*
by (*induct arbitrary: m D*)(*fastforce dest: Methods.intros*)+

lemma *sees-methods-decl-mono*:

assumes *sub*: $P \vdash C' \preceq^* C$ **and** $P \vdash C$ *sees-methods* Mm
shows $\exists Mm' Mm2. P \vdash C'$ *sees-methods* $Mm' \wedge Mm' = Mm ++ Mm2 \wedge (\forall M m D. Mm2 M = Some(m, D) \longrightarrow P \vdash D \preceq^* C)$
(is $\exists Mm' Mm2. ?Q C' C Mm' Mm2)$

using *assms*

proof (*induction rule: converse-rtranclp-induct*)

case *base*

hence $?Q C C Mm$ *Map.empty* **by** *simp*

thus $\exists Mm' Mm2. ?Q C C Mm' Mm2$ **by** *blast*

next

case (*step C'' C'*)

note *sub1* = $\langle P \vdash C'' \prec^1 C' \rangle$ **and** *sub* = $\langle P \vdash C' \preceq^* C \rangle$

and *Csees* = $\langle P \vdash C$ *sees-methods* $Mm \rangle$

from *step.IH[OF Csees]* **obtain** $Mm' Mm2$ **where** *C'sees*: $P \vdash C'$ *sees-methods* Mm'

and Mm' : $Mm' = Mm ++ Mm2$

and *subC*: $\forall M m D. Mm2 M = Some(m, D) \longrightarrow P \vdash D \preceq^* C$ **by** *blast*

obtain *fs ms* **where** *class*: $class P C'' = Some(C', fs, ms)$ $C'' \neq Object$

using *subcls1D[OF sub1]* **by** *blast*

let $?Mm3 = map-option (\lambda m. (m, C'')) \circ map-of ms$

have $P \vdash C''$ *sees-methods* $(Mm ++ Mm2) ++ ?Mm3$

using *sees-methods-rec[OF class C'sees refl]* Mm' **by** *simp*

hence $?Q C'' C ((Mm ++ Mm2) ++ ?Mm3)$ $(Mm2 ++ ?Mm3)$

using *converse-rtranclp-into-rtranclp[OF sub1 sub]*

by *simp (simp add:map-add-def subC split:option.split)*

thus $\exists Mm' Mm2. ?Q C'' C Mm' Mm2$ **by** *blast*

qed

definition *Method* :: $'m$ *prog* \Rightarrow *cname* \Rightarrow *mname* \Rightarrow *ty list* \Rightarrow *ty* \Rightarrow $'m$ *option* \Rightarrow *cname* \Rightarrow *bool*
 $(\langle - \vdash - sees - : - \rightarrow - = - in - \rangle [51, 51, 51, 51, 51, 51, 51, 51] 50)$

where

$$P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D \equiv \\ \exists Mm. P \vdash C \text{ sees-methods } Mm \wedge Mm M = \text{Some}((Ts, T, m), D)$$

Output translation to replace *None* with its notation *Native* when used as method body in *Method*.

abbreviation (output)

$$\text{Method-native} :: 'm \text{ prog} \Rightarrow \text{cname} \Rightarrow \text{mname} \Rightarrow \text{ty list} \Rightarrow \text{ty} \Rightarrow \text{cname} \Rightarrow \text{bool} \\ (\leftarrow \vdash - \text{sees} - : - \rightarrow - = \text{Native in } \rightarrow [51, 51, 51, 51, 51, 51] 50)$$

where $\text{Method-native } P C M Ts T D \equiv \text{Method } P C M Ts T \text{ Native } D$

definition *has-method* :: $'m \text{ prog} \Rightarrow \text{cname} \Rightarrow \text{mname} \Rightarrow \text{bool} (\leftarrow \vdash - \text{has} \rightarrow [51, 0, 51] 50)$

where

$$P \vdash C \text{ has } M \equiv \exists Ts T m D. P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D$$

lemma *has-methodI*:

$$P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D \Longrightarrow P \vdash C \text{ has } M$$

by (*unfold has-method-def*) *blast*

lemma *sees-method-fun*:

$$\llbracket P \vdash C \text{ sees } M: TS \rightarrow T = m \text{ in } D; P \vdash C \text{ sees } M: TS' \rightarrow T' = m' \text{ in } D' \rrbracket \\ \Longrightarrow TS' = TS \wedge T' = T \wedge m' = m \wedge D' = D$$

lemma *sees-method-decl-above*:

$$P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D \Longrightarrow P \vdash C \preceq^* D$$

lemma *visible-method-exists*:

$$P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D \Longrightarrow \\ \exists D' fs ms. \text{class } P D = \text{Some}(D', fs, ms) \wedge \text{map-of } ms M = \text{Some}(Ts, T, m)$$

lemma *sees-method-idemp*:

$$P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D \Longrightarrow P \vdash D \text{ sees } M: Ts \rightarrow T = m \text{ in } D$$

lemma *sees-method-decl-mono*:

$$\llbracket P \vdash C' \preceq^* C; P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D; \\ P \vdash C' \text{ sees } M: Ts' \rightarrow T' = m' \text{ in } D' \rrbracket \Longrightarrow P \vdash D' \preceq^* D$$

apply(*frule sees-method-decl-above*)

apply(*unfold Method-def*)

apply *clarsimp*

apply(*drule (1) sees-methods-decl-mono*)

apply *clarsimp*

apply(*drule (1) sees-methods-fun*)

apply *clarsimp*

apply(*blast intro: rtranclp-trans*)

done

lemma *sees-method-is-class*:

$$P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D \Longrightarrow \text{is-class } P C$$

by (*auto simp add: is-class-def Method-def elim: Methods.cases*)

3.3.4 Field lookup

inductive *Fields* :: $'m \text{ prog} \Rightarrow \text{cname} \Rightarrow ((\text{vname} \times \text{cname}) \times (\text{ty} \times \text{fmod})) \text{ list} \Rightarrow \text{bool}$

$$(\leftarrow \vdash - \text{has}'\text{-fields} \rightarrow [51, 51, 51] 50)$$

```

for P :: 'm prog
where
  has-fields-rec:
  [[ class P C = Some(D,fs,ms); C ≠ Object; P ⊢ D has-fields FDTs;
    FDTs' = map (λ(F,Tm). ((F,C),Tm)) fs @ FDTs ]
    ⇒ P ⊢ C has-fields FDTs'

| has-fields-Object:
  [[ class P Object = Some(D,fs,ms); FDTs = map (λ(F,T). ((F,Object),T)) fs ]
    ⇒ P ⊢ Object has-fields FDTs

lemma has-fields-fun:
  assumes P ⊢ C has-fields FDTs and P ⊢ C has-fields FDTs'
  shows FDTs' = FDTs
using assms
proof(induction arbitrary: FDTs')
  case has-fields-Object thus ?case by(auto elim: Fields.cases)
next
  case (has-fields-rec C D fs ms Dres Cres Cres')
  from ⟨P ⊢ C has-fields Cres'⟩ ⟨C ≠ Object⟩ ⟨class P C = Some (D, fs, ms)⟩
  obtain Dres' where DFields': P ⊢ D has-fields Dres'
  and Cres': Cres' = map (λ(F,Tm). ((F,C),Tm)) fs @ Dres'
  by cases auto
  from has-fields-rec.IH[OF DFields'] ⟨Cres = map (λ(F,Tm). ((F,C),Tm)) fs @ Dres⟩ Cres'
  show ?case by simp
qed

lemma all-fields-in-has-fields:
  assumes P ⊢ C has-fields FDTs
  and P ⊢ C ≼* D class P D = Some(D',fs,ms) (F,Tm) ∈ set fs
  shows ((F,D),Tm) ∈ set FDTs
using assms
by induct (auto 4 3 elim: converse-rtranclpE dest: subcls1D)

lemma has-fields-decl-above:
  assumes P ⊢ C has-fields FDTs ((F,D),Tm) ∈ set FDTs
  shows P ⊢ C ≼* D
using assms
by induct (auto intro: converse-rtranclp-into-rtranclp subcls1I)

lemma subcls-notin-has-fields:
  assumes P ⊢ C has-fields FDTs ((F,D),Tm) ∈ set FDTs
  shows ¬ (subcls1 P)++ D C
using assms apply(induct)
prefer 2 apply(fastforce dest: tranclpD)
apply clarsimp
apply(erule disjE)
apply(clarsimp simp add:image-def)
apply(drule tranclpD)
apply clarify
apply(frule subcls1D)
apply(fastforce dest:tranclpD all-fields-in-has-fields)
apply(blast dest:subcls1I tranclp.trancl-into-trancl)
done

```

lemma *has-fields-mono-lem*:

```

  assumes  $P \vdash D \preceq^* C$   $P \vdash C$  has-fields FDTs
  shows  $\exists$  pre.  $P \vdash D$  has-fields pre@FDTs  $\wedge$   $\text{dom}(\text{map-of } \textit{pre}) \cap \text{dom}(\text{map-of } \textit{FDTs}) = \{\}$ 
using assms
apply(induct rule: converse-rtranclp-induct)
  apply(rule-tac  $x = []$  in exI)
  apply simp
apply clarsimp
apply(rename-tac  $D' D$  pre)
apply(subgoal-tac (subcls1  $P$ )++  $D' C$ )
  prefer 2 apply(erule (1) rtranclp-into-tranclp2)
apply(drule subcls1D)
apply clarsimp
apply(rename-tac fs ms)
apply(drule (2) has-fields-rec)
  apply(rule refl)
apply(rule-tac  $x = \text{map } (\lambda(F, Tm). ((F, D'), Tm))$  fs @ pre in exI)
apply simp
apply(simp add: Int-Un-distrib2)
apply(rule equalsOI)
apply(auto dest: subcls-notin-has-fields simp: dom-map-of-conv-image-fst image-def)
done

```

lemma *has-fields-is-class*:

```

   $P \vdash C$  has-fields FDTs  $\implies$  is-class  $P C$ 
by (auto simp add: is-class-def elim: Fields.cases)

```

lemma *Object-has-fields-Object*:

```

  assumes  $P \vdash$  Object has-fields FDTs
  shows snd 'fst' set FDTs  $\subseteq$  {Object}
using assms by cases auto

```

definition

```

  has-field :: 'm prog  $\Rightarrow$  cname  $\Rightarrow$  vname  $\Rightarrow$  ty  $\Rightarrow$  fmod  $\Rightarrow$  cname  $\Rightarrow$  bool
  ( $\langle \cdot \vdash \cdot \text{has} \cdot \rangle$  in  $\rightarrow$  [51,51,51,51,51,51] 50)

```

where

```

   $P \vdash C$  has  $F:T$  (fm) in  $D \equiv$ 
   $\exists$  FDTs.  $P \vdash C$  has-fields FDTs  $\wedge$   $\text{map-of FDTs } (F, D) = \text{Some } (T, \textit{fm})$ 

```

lemma *has-field-mono*:

```

   $\llbracket P \vdash C$  has  $F:T$  (fm) in  $D$ ;  $P \vdash C' \preceq^* C \rrbracket \implies P \vdash C'$  has  $F:T$  (fm) in  $D$ 
by(fastforce simp: has-field-def map-add-def dest: has-fields-mono-lem)

```

lemma *has-field-is-class*:

```

   $P \vdash C$  has  $M:T$  (fm) in  $D \implies$  is-class  $P C$ 
by (auto simp add: is-class-def has-field-def elim: Fields.cases)

```

lemma *has-field-decl-above*:

```

   $P \vdash C$  has  $F:T$  (fm) in  $D \implies P \vdash C \preceq^* D$ 
unfolding has-field-def
by(auto dest: map-of-SomeD has-fields-decl-above)

```

lemma *has-field-fun*:

$\llbracket P \vdash C \text{ has } F:T (fm) \text{ in } D; P \vdash C \text{ has } F:T' (fm') \text{ in } D \rrbracket \implies T' = T \wedge fm = fm'$
by(*auto simp:has-field-def dest:has-fields-fun*)

definition

sees-field :: 'm prog \Rightarrow cname \Rightarrow vname \Rightarrow ty \Rightarrow fmod \Rightarrow cname \Rightarrow bool
 ($\langle \vdash - \text{ sees } - \text{ '(-) in } \rightarrow [51,51,51,51,51,51] 50$)

where

$P \vdash C \text{ sees } F:T (fm) \text{ in } D \equiv$
 $\exists FDTs. P \vdash C \text{ has-fields } FDTs \wedge$
 $\text{map-of } (\text{map } (\lambda((F,D),Tm). (F,(D,Tm))) FDTs) F = \text{Some}(D,T,fm)$

lemma *map-of-remap-SomeD*:

$\text{map-of } (\text{map } (\lambda((k,k'),x). (k,(k',x))) t) k = \text{Some } (k',x) \implies \text{map-of } t (k, k') = \text{Some } x$
by (*induct t*) (*auto simp:fun-upd-apply split: if-split-asm*)

lemma *has-visible-field*:

$P \vdash C \text{ sees } F:T (fm) \text{ in } D \implies P \vdash C \text{ has } F:T (fm) \text{ in } D$
by(*auto simp add:has-field-def sees-field-def map-of-remap-SomeD*)

lemma *sees-field-fun*:

$\llbracket P \vdash C \text{ sees } F:T (fm) \text{ in } D; P \vdash C \text{ sees } F:T' (fm') \text{ in } D \rrbracket \implies T' = T \wedge D' = D \wedge fm = fm'$
by(*fastforce simp:sees-field-def dest:has-fields-fun*)

lemma *sees-field-decl-above*:

$P \vdash C \text{ sees } F:T (fm) \text{ in } D \implies P \vdash C \preceq^* D$
by(*clarsimp simp add: sees-field-def*)
 (*blast intro: has-fields-decl-above map-of-SomeD map-of-remap-SomeD*)

lemma *sees-field-idemp*:

assumes $P \vdash C \text{ sees } F:T (fm) \text{ in } D$
shows $P \vdash D \text{ sees } F:T (fm) \text{ in } D$
proof –
from *assms* **obtain** *FDTs* **where** *has*: $P \vdash C \text{ has-fields } FDTs$
and F : $\text{map-of } (\text{map } (\lambda((F, D), Tm). (F, D, Tm)) FDTs) F = [(D, T, fm)]$
unfolding *sees-field-def* **by** *blast*
thus *?thesis*
proof *induct*
case *has-fields-rec* **thus** *?case* **unfolding** *sees-field-def*
by(*auto*)(*fastforce dest: map-of-SomeD intro!: exI intro: Fields.has-fields-rec*)
next
case *has-fields-Object* **thus** *?case* **unfolding** *sees-field-def*
by(*fastforce dest: map-of-SomeD intro: Fields.has-fields-Object intro!: exI*)
qed
qed

3.3.5 Functional lookup

definition *method* :: 'm prog \Rightarrow cname \Rightarrow mname \Rightarrow cname \times ty list \times ty \times 'm option
where *method* $P C M \equiv \text{THE } (D,Ts,T,m). P \vdash C \text{ sees } M:Ts \rightarrow T = m \text{ in } D$

definition *field* :: 'm prog \Rightarrow cname \Rightarrow vname \Rightarrow cname \times ty \times fmod

where *field* $P C F \equiv \text{THE } (D,T,fm). P \vdash C \text{ sees } F:T (fm) \text{ in } D$

definition *fields* :: 'm prog \Rightarrow cname \Rightarrow ((vname \times cname) \times (ty \times fmod)) list
where *fields* P C \equiv THE FDTs. P \vdash C has-fields FDTs

lemma [*simp*]: P \vdash C has-fields FDTs \Longrightarrow *fields* P C = FDTs

lemma *field-def2* [*simp*]: P \vdash C sees F:T (fm) in D \Longrightarrow *field* P C F = (D,T,fm)

lemma *method-def2* [*simp*]: P \vdash C sees M: Ts \rightarrow T = m in D \Longrightarrow *method* P C M = (D,Ts,T,m)

lemma *has-fields-b-fields*:

P \vdash C has-fields FDTs \Longrightarrow *fields* P C = FDTs

unfolding *fields-def*

by (*blast intro: the-equality has-fields-fun*)

lemma *has-field-map-of-fields* [*simp*]:

P \vdash C has F:T (fm) in D \Longrightarrow *map-of* (*fields* P C) (F, D) = [(T, fm)]

by(*auto simp add: has-field-def*)

3.3.6 Code generation

New introduction rules for *subcls1*

code-pred

— Disallow mode *i-o-o* to force *code-pred* in subsequent predicates not to use this inefficient mode

(*modes*: *i* \Rightarrow *i* \Rightarrow *i* \Rightarrow *bool*, *i* \Rightarrow *i* \Rightarrow *o* \Rightarrow *bool*)

subcls1

.

Introduce proper constant *subcls'* for *subcls* and generate executable equation for *subcls'*

definition *subcls'* **where** *subcls'* = *subcls*

code-pred

(*modes*: *i* \Rightarrow *i* \Rightarrow *i* \Rightarrow *bool*, *i* \Rightarrow *i* \Rightarrow *o* \Rightarrow *bool*)

[*inductify*]

subcls'

.

lemma *subcls-conv-subcls'* [*code-unfold*]:

(*subcls1* P) $\hat{=}$ *subcls'* P

by(*simp add: subcls'-def*)

Change rule $?P \vdash ?A[] \leq \text{Class Object}$ such that predicate compiler tests on class *Object* first. Otherwise *widen-i-o-i* never terminates.

lemma *widen-array-object-code*:

C = *Object* \Longrightarrow P \vdash *Array* A \leq *Class* C

by(*auto intro: widen.intros*)

lemmas [*code-pred-intro*] =

widen-refl *widen-subcls* *widen-null* *widen-null-array* *widen-array-object-code* *widen-array-array*

code-pred

(*modes*: *i* \Rightarrow *i* \Rightarrow *i* \Rightarrow *bool*)

widen

by(*erule widen.cases*) *auto*

Readjust the code equations for *widen* such that *widen-i-i-i* is guaranteed to contain () at most once (even in the code representation!). This is important for the scheduler and the small-step semantics because of the weaker code equations for *the*.

A similar problem cannot hit the subclass relation because, for acyclic subclass hierarchies, the paths in the hierarchy are unique and cycle-free.

definition *widen-i-i-i'* **where** *widen-i-i-i' = widen-i-i-i*

declare *widen.equation* [*code del*]

lemmas *widen-i-i-i'-equation* [*code*] = *widen.equation*[*folded widen-i-i-i'-def*]

lemma *widen-i-i-i-code* [*code*]:

widen-i-i-i P T T' = (if P ⊢ T ≤ T' then Predicate.single () else bot)

by(*auto intro!*: *pred-eqI intro: widen-i-i-iI elim: widen-i-i-iE*)

code-pred

(*modes: i ⇒ i ⇒ i ⇒ bool, i ⇒ i ⇒ o ⇒ bool*)

Methods

.

code-pred

(*modes: i ⇒ i ⇒ i ⇒ o ⇒ o ⇒ o ⇒ o ⇒ bool, i ⇒ i ⇒ i ⇒ o ⇒ o ⇒ o ⇒ i ⇒ bool*)

[*inductify*]

Method

.

code-pred

(*modes: i ⇒ i ⇒ i ⇒ bool*)

[*inductify*]

has-method

.

declare *fun-upd-def* [*code-pred-inline*]

code-pred

(*modes: i ⇒ i ⇒ o ⇒ bool*)

Fields

.

code-pred

(*modes: i ⇒ i ⇒ i ⇒ o ⇒ o ⇒ i ⇒ bool*)

[*inductify, skip-proof*]

has-field

.

code-pred

(*modes: i ⇒ i ⇒ i ⇒ o ⇒ o ⇒ o ⇒ bool, i ⇒ i ⇒ i ⇒ o ⇒ o ⇒ i ⇒ bool*)

[*inductify, skip-proof*]

sees-field

.

lemma *eval-Method-i-i-i-o-o-o-o-conv*:

Predicate.eval (Method-i-i-i-o-o-o-o P C M) = (λ(Ts, T, m, D). P ⊢ C sees M:Ts→T=m in D)

by(*auto intro: Method-i-i-i-o-o-o-oI elim: Method-i-i-i-o-o-o-oE intro!: ext*)

lemma *method-code* [*code*]:

method P C M =

```

    Predicate.the (Predicate.bind (Method-i-i-i-o-o-o P C M) ( $\lambda(Ts, T, m, D).$  Predicate.single (D, Ts,
    T, m)))
  apply (rule sym, rule the-eqI)
  apply (simp add: method-def eval-Method-i-i-i-o-o-o-conv)
  apply (rule arg-cong [where f=The])
  apply (auto simp add: Sup-fun-def Sup-bool-def fun-eq-iff)
  done

```

lemma *eval-sees-field-i-i-i-o-o-conv*:

```

    Predicate.eval (sees-field-i-i-i-o-o P C F) = ( $\lambda(T, fm, D).$  P  $\vdash$  C sees F:T (fm) in D)
  by(auto intro!: ext intro: sees-field-i-i-i-o-o-I elim: sees-field-i-i-i-o-o-E)

```

lemma *eval-sees-field-i-i-i-o-i-conv*:

```

    Predicate.eval (sees-field-i-i-i-o-i P C F D) = ( $\lambda(T, fm).$  P  $\vdash$  C sees F:T (fm) in D)
  by(auto intro!: ext intro: sees-field-i-i-i-o-i-I elim: sees-field-i-i-i-o-i-E)

```

lemma *field-code* [code]:

```

    field P C F = Predicate.the (Predicate.bind (sees-field-i-i-i-o-o P C F) ( $\lambda(T, fm, D).$  Predi-
    cate.single (D, T, fm)))
  apply (rule sym, rule the-eqI)
  apply (simp add: field-def eval-sees-field-i-i-i-o-o-conv)
  apply (rule arg-cong [where f=The])
  apply (auto simp add: Sup-fun-def Sup-bool-def fun-eq-iff)
  done

```

lemma *eval-Fields-conv*:

```

    Predicate.eval (Fields-i-i-o P C) = ( $\lambda FDTs.$  P  $\vdash$  C has-fields FDTs)
  by(auto intro: Fields-i-i-o-I elim: Fields-i-i-o-E intro!: ext)

```

lemma *fields-code* [code]:

```

    fields P C = Predicate.the (Fields-i-i-o P C)
  by(simp add: fields-def Predicate.the-def eval-Fields-conv)

```

code-identifier

```

  code-module TypeRel  $\rightarrow$ 
    (SML) TypeRel and (Haskell) TypeRel and (OCaml) TypeRel
| code-module Decl  $\rightarrow$ 
    (SML) TypeRel and (Haskell) TypeRel and (OCaml) TypeRel

```

end

3.4 Jinja Values

theory *Value*

imports

```

    TypeRel
    HOL-Library.Word

```

begin

unbundle *no floor-ceiling-syntax*

type-synonym *word32 = 32 word*

datatype *'addr val*
 = *Unit* — dummy result value of void expressions
 | *Null* — null reference
 | *Bool bool* — Boolean value
 | *Intg word32* — integer value
 | *Addr 'addr* — addresses of objects, arrays and threads in the heap

primrec *default-val* :: *ty* \Rightarrow *'addr val* — default value for all types

where

default-val Void = *Unit*
 | *default-val Boolean* = *Bool False*
 | *default-val Integer* = *Intg 0*
 | *default-val NT* = *Null*
 | *default-val (Class C)* = *Null*
 | *default-val (Array A)* = *Null*

lemma *default-val-not-Addr*: *default-val T* \neq *Addr a*
by(*cases T*)(*simp-all*)

lemma *Addr-not-default-val*: *Addr a* \neq *default-val T*
by(*cases T*)(*simp-all*)

primrec *the-Intg* :: *'addr val* \Rightarrow *word32*

where

the-Intg (Intg i) = *i*

primrec *the-Addr* :: *'addr val* \Rightarrow *'addr*

where

the-Addr (Addr a) = *a*

fun *is-Addr* :: *'addr val* \Rightarrow *bool*

where

is-Addr (Addr a) = *True*
 | *is-Addr -* = *False*

lemma *is-AddrE* [*elim!*]:

$\llbracket \text{is-Addr } v; \bigwedge a. v = \text{Addr } a \implies \text{thesis} \rrbracket \implies \text{thesis}$
by(*cases v, auto*)

fun *is-Intg* :: *'addr val* \Rightarrow *bool*

where

is-Intg (Intg i) = *True*
 | *is-Intg -* = *False*

lemma *is-IntgE* [*elim!*]:

$\llbracket \text{is-Intg } v; \bigwedge i. v = \text{Intg } i \implies \text{thesis} \rrbracket \implies \text{thesis}$
by(*cases v, auto*)

fun *is-Bool* :: *'addr val* \Rightarrow *bool*

where

is-Bool (Bool b) = *True*
 | *is-Bool -* = *False*

lemma *is-BoolE* [*elim!*]:

$\llbracket \text{is-Bool } v; \bigwedge a. v = \text{Bool } a \implies \text{thesis} \rrbracket \implies \text{thesis}$
by(*cases v, auto*)

definition *is-Ref* :: 'addr val \Rightarrow bool
where *is-Ref* v \equiv v = Null \vee *is-Addr* v

lemma *is-Ref-def2*:
is-Ref v = (v = Null \vee (\exists a. v = Addr a))
by (*cases v*) (*auto simp add: is-Ref-def*)

lemma [*iff*]: *is-Ref* Null **by** (*simp add: is-Ref-def2*)

definition *undefined-value* :: 'addr val **where** *undefined-value* = Unit

lemma *undefined-value-not-Addr*:
undefined-value \neq Addr a Addr a \neq *undefined-value*
by(*simp-all add: undefined-value-def*)

class *addr* =
fixes *hash-addr* :: 'a \Rightarrow int
and *monitor-funfun-to-list* :: ('a \Rightarrow f nat) \Rightarrow 'a list
assumes *set* (*monitor-funfun-to-list* f) = Collect ((\$) (*funfun-dom* f))

locale *addr-base* =
fixes *addr2thread-id* :: 'addr \Rightarrow 'thread-id
and *thread-id2addr* :: 'thread-id \Rightarrow 'addr

end

3.5 Exceptions

theory *Exceptions*
imports
Value
begin

definition *NullPointer* :: cname
where [*code-unfold*]: *NullPointer* = STR "java/lang/NullPointerException"

definition *ClassCast* :: cname
where [*code-unfold*]: *ClassCast* = STR "java/lang/ClassCastException"

definition *OutOfMemory* :: cname
where [*code-unfold*]: *OutOfMemory* = STR "java/lang/OutOfMemoryError"

definition *ArrayIndexOutOfBounds* :: cname
where [*code-unfold*]: *ArrayIndexOutOfBounds* = STR "java/lang/ArrayIndexOutOfBoundsException"

definition *ArrayStore* :: cname
where [*code-unfold*]: *ArrayStore* = STR "java/lang/ArrayStoreException"

definition *NegativeArraySize* :: cname
where [*code-unfold*]: *NegativeArraySize* = STR "java/lang/NegativeArraySizeException"

definition *ArithmeticException* :: *cname*

where [*code-unfold*]: *ArithmeticException* = *STR "java/lang/ArithmeticException"*

definition *IllegalMonitorState* :: *cname*

where [*code-unfold*]: *IllegalMonitorState* = *STR "java/lang/IllegalMonitorStateException"*

definition *IllegalThreadState* :: *cname*

where [*code-unfold*]: *IllegalThreadState* = *STR "java/lang/IllegalThreadStateException"*

definition *InterruptedException* :: *cname*

where [*code-unfold*]: *InterruptedException* = *STR "java/lang/InterruptedException"*

definition *sys-xcpts-list* :: *cname list*

where

sys-xcpts-list =
 [*NullPointer*, *ClassCast*, *OutOfMemory*, *ArrayIndexOutOfBounds*, *ArrayStore*, *NegativeArraySize*,
ArithmeticException,
IllegalMonitorState, *IllegalThreadState*, *InterruptedException*]

definition *sys-xcpts* :: *cname set*

where [*code-unfold*]: *sys-xcpts* = *set sys-xcpts-list*

definition *wf-syscls* :: '*m prog* ⇒ *bool*

where *wf-syscls* *P* ≡ (∀ *C* ∈ {*Object*, *Throwable*, *Thread*}. *is-class P C*) ∧ (∀ *C* ∈ *sys-xcpts*. *P* ⊢ *C* ≲* *Throwable*)

3.5.1 System exceptions

lemma [*simp*]:

NullPointer ∈ *sys-xcpts* ∧
OutOfMemory ∈ *sys-xcpts* ∧
ClassCast ∈ *sys-xcpts* ∧
ArrayIndexOutOfBounds ∈ *sys-xcpts* ∧
ArrayStore ∈ *sys-xcpts* ∧
NegativeArraySize ∈ *sys-xcpts* ∧
IllegalMonitorState ∈ *sys-xcpts* ∧
IllegalThreadState ∈ *sys-xcpts* ∧
InterruptedException ∈ *sys-xcpts* ∧
ArithmeticException ∈ *sys-xcpts*
by(*simp add: sys-xcpts-def sys-xcpts-list-def*)

lemma *sys-xcpts-cases* [*consumes 1, cases set*]:

[[*C* ∈ *sys-xcpts*; *P NullPointer*; *P OutOfMemory*; *P ClassCast*;
P ArrayIndexOutOfBounds; *P ArrayStore*; *P NegativeArraySize*;
P ArithmeticException;
P IllegalMonitorState; *P IllegalThreadState*; *P InterruptedException*]]
 ⇒ *P C*

by (*auto simp add: sys-xcpts-def sys-xcpts-list-def*)

lemma *OutOfMemory-not-Object*[*simp*]: *OutOfMemory* ≠ *Object*

by(*simp add: OutOfMemory-def Object-def*)

lemma *ClassCast-not-Object*[*simp*]: *ClassCast* ≠ *Object*

by(*simp add: ClassCast-def Object-def*)

lemma *NullPointerException-not-Object[simp]: NullPointerException ≠ Object*

by(*simp add: NullPointerException-def Object-def*)

lemma *ArrayIndexOutOfBounds-not-Object[simp]: ArrayIndexOutOfBounds ≠ Object*

by(*simp add: ArrayIndexOutOfBounds-def Object-def*)

lemma *ArrayStore-not-Object[simp]: ArrayStore ≠ Object*

by(*simp add: ArrayStore-def Object-def*)

lemma *NegativeArraySize-not-Object[simp]: NegativeArraySize ≠ Object*

by(*simp add: NegativeArraySize-def Object-def*)

lemma *ArithmeticException-not-Object[simp]: ArithmeticException ≠ Object*

by(*simp add: ArithmeticException-def Object-def*)

lemma *IllegalMonitorState-not-Object[simp]: IllegalMonitorState ≠ Object*

by(*simp add: IllegalMonitorState-def Object-def*)

lemma *IllegalThreadState-not-Object[simp]: IllegalThreadState ≠ Object*

by(*simp add: IllegalThreadState-def Object-def*)

lemma *InterruptedException-not-Object[simp]: InterruptedException ≠ Object*

by(*simp add: InterruptedException-def Object-def*)

lemma *sys-xcpts-neqs-aux:*

*NullPointerException ≠ ClassCast NullPointerException ≠ OutOfMemory NullPointerException ≠ ArrayIndexOutOfBounds
 NullPointerException ≠ ArrayStore NullPointerException ≠ NegativeArraySize NullPointerException ≠ IllegalMonitorState
 NullPointerException ≠ IllegalThreadState NullPointerException ≠ InterruptedException NullPointerException ≠ ArithmeticEx-
 ception*

ClassCast ≠ OutOfMemory ClassCast ≠ ArrayIndexOutOfBounds

ClassCast ≠ ArrayStore ClassCast ≠ NegativeArraySize ClassCast ≠ IllegalMonitorState

ClassCast ≠ IllegalThreadState ClassCast ≠ InterruptedException ClassCast ≠ ArithmeticException

OutOfMemory ≠ ArrayIndexOutOfBounds

*OutOfMemory ≠ ArrayStore OutOfMemory ≠ NegativeArraySize OutOfMemory ≠ IllegalMoni-
 torState*

OutOfMemory ≠ IllegalThreadState OutOfMemory ≠ InterruptedException

OutOfMemory ≠ ArithmeticException

*ArrayIndexOutOfBounds ≠ ArrayStore ArrayIndexOutOfBounds ≠ NegativeArraySize ArrayIndex-
 OutOfBounds ≠ IllegalMonitorState*

*ArrayIndexOutOfBounds ≠ IllegalThreadState ArrayIndexOutOfBounds ≠ InterruptedException Ar-
 rayIndexOutOfBounds ≠ ArithmeticException*

ArrayStore ≠ NegativeArraySize ArrayStore ≠ IllegalMonitorState

ArrayStore ≠ IllegalThreadState ArrayStore ≠ InterruptedException

ArrayStore ≠ ArithmeticException

NegativeArraySize ≠ IllegalMonitorState

NegativeArraySize ≠ IllegalThreadState NegativeArraySize ≠ InterruptedException

NegativeArraySize ≠ ArithmeticException

IllegalMonitorState ≠ IllegalThreadState IllegalMonitorState ≠ InterruptedException

IllegalMonitorState ≠ ArithmeticException

IllegalThreadState ≠ InterruptedException

IllegalThreadState ≠ ArithmeticException

InterruptedException ≠ ArithmeticException

by(*simp-all add: NullPointerException-def ClassCast-def OutOfMemory-def ArrayIndexOutOfBounds-def ArrayStore-def NegativeArraySize-def IllegalMonitorState-def IllegalThreadState-def InterruptedException-def ArithmeticException-def*)

lemmas *sys-xcpts-neqs = sys-xcpts-neqs-aux sys-xcpts-neqs-aux[symmetric]*

lemma *Thread-neq-sys-xcpts-aux:*

Thread \neq *NullPointerException*

Thread \neq *ClassCast*

Thread \neq *OutOfMemory*

Thread \neq *ArrayIndexOutOfBounds*

Thread \neq *ArrayStore*

Thread \neq *NegativeArraySize*

Thread \neq *ArithmeticException*

Thread \neq *IllegalMonitorState*

Thread \neq *IllegalThreadState*

Thread \neq *InterruptedException*

by(*simp-all add: Thread-def NullPointerException-def ClassCast-def OutOfMemory-def ArrayIndexOutOfBounds-def ArrayStore-def NegativeArraySize-def IllegalMonitorState-def IllegalThreadState-def InterruptedException-def ArithmeticException-def*)

lemmas *Thread-neq-sys-xcpts = Thread-neq-sys-xcpts-aux Thread-neq-sys-xcpts-aux[symmetric]*

3.5.2 Well-formedness for system classes and exceptions

lemma

assumes *wf-syscls P*

shows *wf-syscls-class-Object: $\exists C fs ms. class P Object = Some (C,fs,ms)$*

and *wf-syscls-class-Thread: $\exists C fs ms. class P Thread = Some (C,fs,ms)$*

using *assms*

by(*auto simp: map-of-SomeI wf-syscls-def is-class-def*)

lemma [*simp*]:

assumes *wf-syscls P*

shows *wf-syscls-is-class-Object: is-class P Object*

and *wf-syscls-is-class-Thread: is-class P Thread*

using *assms by(simp-all add: is-class-def wf-syscls-class-Object wf-syscls-class-Thread)*

lemma *wf-syscls-xcpt-subcls-Throwable:*

$\llbracket C \in sys-xcpts; wf-syscls P \rrbracket \implies P \vdash C \preceq^* Throwable$

by(*simp add: wf-syscls-def is-class-def class-def*)

lemma *wf-syscls-is-class-Throwable:*

wf-syscls P \implies is-class P Throwable

by(*auto simp add: wf-syscls-def is-class-def class-def map-of-SomeI*)

lemma *wf-syscls-is-class-sub-Throwable:*

$\llbracket wf-syscls P; P \vdash C \preceq^* Throwable \rrbracket \implies is-class P C$

by(*erule subcls-is-classI*)(*erule wf-syscls-is-class-Throwable*)

lemma *wf-syscls-is-class-xcpt:*

$\llbracket C \in sys-xcpts; wf-syscls P \rrbracket \implies is-class P C$

by(*blast intro: wf-syscls-is-class-sub-Throwable wf-syscls-xcpt-subcls-Throwable*)

```

lemma wf-syscls-code [code]:
  wf-syscls P  $\longleftrightarrow$ 
    ( $\forall C \in \text{set } [Object, Throwable, Thread]. \text{is-class } P C$ )  $\wedge$  ( $\forall C \in \text{sys-xcpts}. P \vdash C \preceq^* Throwable$ )
by(simp only: wf-syscls-def) simp

end

```

3.6 System Classes

```

theory SystemClasses

```

```

imports

```

```

  Exceptions

```

```

begin

```

This theory provides definitions for the *Object* class, and the system exceptions.

Inline *SystemClasses* definition because they are polymorphic values that violate ML's value restriction.

Object has actually superclass, but we set it to the empty string for code generation. Any other class name (like *undefined*) would do as well except for code generation.

```

definition ObjectC :: 'm cdecl

```

```

where [code-unfold]:

```

```

  ObjectC =
    (Object, (STR "", []),
     [(wait, [], Void, Native),
      (notify, [], Void, Native),
      (notifyAll, [], Void, Native),
      (hashCode, [], Integer, Native),
      (clone, [], Class Object, Native),
      (print, [Integer], Void, Native),
      (currentThread, [], Class Thread, Native),
      (interrupted, [], Boolean, Native),
      (yield, [], Void, Native)
     ])

```

```

definition ThrowableC :: 'm cdecl

```

```

where [code-unfold]: ThrowableC  $\equiv$  (Throwable, (Object, [], []))

```

```

definition NullPointerException :: 'm cdecl

```

```

where [code-unfold]: NullPointerException  $\equiv$  (NullPointerException, (Throwable, [], []))

```

```

definition ClassCastC :: 'm cdecl

```

```

where [code-unfold]: ClassCastC  $\equiv$  (ClassCast, (Throwable, [], []))

```

```

definition OutOfMemoryC :: 'm cdecl

```

```

where [code-unfold]: OutOfMemoryC  $\equiv$  (OutOfMemory, (Throwable, [], []))

```

```

definition ArrayIndexOutOfBoundsC :: 'm cdecl

```

```

where [code-unfold]: ArrayIndexOutOfBoundsC  $\equiv$  (ArrayIndexOutOfBounds, (Throwable, [], []))

```

```

definition ArrayStoreC :: 'm cdecl

```

```

where [code-unfold]: ArrayStoreC  $\equiv$  (ArrayStore, (Throwable, [], []))

```

```

definition NegativeArraySizeC :: 'm cdecl

```

```

where [code-unfold]: NegativeArraySizeC ≡ (NegativeArraySize, (Throwable,[],[]))

definition ArithmeticExceptionC :: 'm cdecl
where [code-unfold]: ArithmeticExceptionC ≡ (ArithmeticException, (Throwable,[],[]))

definition IllegalMonitorStateC :: 'm cdecl
where [code-unfold]: IllegalMonitorStateC ≡ (IllegalMonitorState, (Throwable,[],[]))

definition IllegalThreadStateC :: 'm cdecl
where [code-unfold]: IllegalThreadStateC ≡ (IllegalThreadState, (Throwable,[],[]))

definition InterruptedExceptionC :: 'm cdecl
where [code-unfold]: InterruptedExceptionC ≡ (InterruptedException, (Throwable,[],[]))

definition SystemClasses :: 'm cdecl list
where [code-unfold]:
  SystemClasses ≡
  [ObjectC, ThrowableC, NullPointerException, ClassCastC, OutOfMemoryC,
   ArrayIndexOutOfBoundsException, ArrayStoreC, NegativeArraySizeC,
   ArithmeticExceptionC,
   IllegalMonitorStateC, IllegalThreadStateC, InterruptedExceptionC]

end

```

3.7 An abstract heap model

```

theory Heap
imports
  Value
begin

primrec typeof :: 'addr val → ty
where
  typeof Unit    = Some Void
| typeof Null    = Some NT
| typeof (Bool b) = Some Boolean
| typeof (Intg i) = Some Integer
| typeof (Addr a) = None

datatype addr-loc =
  CField cname vname
| ACell nat

lemma rec-addr-loc [simp]: rec-addr-loc = case-addr-loc
by(auto simp add: fun-eq-iff split: addr-loc.splits)

primrec is-volatile :: 'm prog ⇒ addr-loc ⇒ bool
where
  is-volatile P (ACell n) = False
| is-volatile P (CField D F) = volatile (snd (snd (field P D F)))

locale heap-base =
  addr-base addr2thread-id thread-id2addr

```

```

for addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
+
fixes spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
begin

```

```

fun typeof-h :: 'heap ⇒ 'addr val ⇒ ty option (⟨typeof-⟩)
where
  typeofh (Addr a) = map-option ty-of-htype (typeof-addr h a)
| typeofh v = typeof v

```

```

definition cname-of :: 'heap ⇒ 'addr ⇒ cname
where cname-of h a = the-Class (ty-of-htype (the (typeof-addr h a)))

```

```

definition hext :: 'heap ⇒ 'heap ⇒ bool (⟨- ⊆ -⟩ [51,51] 50)
where
  h ⊆ h' ≡ typeof-addr h ⊆m typeof-addr h'

```

```

context
  notes [[inductive-internals]]
begin

```

```

inductive addr-loc-type :: 'm prog ⇒ 'heap ⇒ 'addr ⇒ addr-loc ⇒ ty ⇒ bool
  (⟨-, - ⊢ -@- : -⟩ [50, 50, 50, 50, 50] 51)

```

```

for P :: 'm prog and h :: 'heap and a :: 'addr
where

```

```

  addr-loc-type-field:
  [[ typeof-addr h a = [U]; P ⊢ class-type-of U has F:T (fm) in D ]
  ⇒⇒ P, h ⊢ a@CField D F : T

```

```

| addr-loc-type-cell:
  [[ typeof-addr h a = [Array-type T n']; n < n' ]
  ⇒⇒ P, h ⊢ a@ACell n : T

```

```

end

```

```

definition typeof-addr-loc :: 'm prog ⇒ 'heap ⇒ 'addr ⇒ addr-loc ⇒ ty
where typeof-addr-loc P h a al = (THE T. P, h ⊢ a@al : T)

```

```

definition deterministic-heap-ops :: bool
where

```

```

  deterministic-heap-ops ⇔
  (∀ h ad al v v'. heap-read h ad al v → heap-read h ad al v' → v = v') ∧
  (∀ h ad al v h' h''. heap-write h ad al v h' → heap-write h ad al v h'' → h' = h'') ∧
  (∀ h hT h' a h'' a'. (h', a) ∈ allocate h hT → (h'', a') ∈ allocate h hT → h' = h'' ∧ a = a') ∧
  ¬ spurious-wakeups

```

```

end

```

lemma *typeof-lit-eq-Boolean* [simp]: (typeof v = Some Boolean) = ($\exists b. v = \text{Bool } b$)
by(cases v)(auto)

lemma *typeof-lit-eq-Integer* [simp]: (typeof v = Some Integer) = ($\exists i. v = \text{Intg } i$)
by(cases v)(auto)

lemma *typeof-lit-eq-NT* [simp]: (typeof v = Some NT) = (v = Null)
by(cases v)(auto)

lemma *typeof-lit-eq-Void* [simp]: typeof v = Some Void \longleftrightarrow v = Unit
by(cases v)(auto)

lemma *typeof-lit-neq-Class* [simp]: typeof v \neq Some (Class C)
by(cases v) auto

lemma *typeof-lit-neq-Array* [simp]: typeof v \neq Some (Array T)
by(cases v) auto

lemma *typeof-NoneD* [simp,dest]:
 typeof v = Some x \implies \neg is-Addr v
by (cases v) auto

lemma *typeof-lit-is-type*:
 typeof v = Some T \implies is-type P T
by(cases v) auto

context heap-base **begin**

lemma *typeof-h-eq-Boolean* [simp]: (typeof_h v = Some Boolean) = ($\exists b. v = \text{Bool } b$)
by(cases v)(auto)

lemma *typeof-h-eq-Integer* [simp]: (typeof_h v = Some Integer) = ($\exists i. v = \text{Intg } i$)
by(cases v)(auto)

lemma *typeof-h-eq-NT* [simp]: (typeof_h v = Some NT) = (v = Null)
by(cases v)(auto)

lemma *hextI*:

$\llbracket \bigwedge a C. \text{typeof-addr } h a = \lfloor \text{Class-type } C \rfloor \implies \text{typeof-addr } h' a = \lfloor \text{Class-type } C \rfloor;$
 $\bigwedge a T n. \text{typeof-addr } h a = \lfloor \text{Array-type } T n \rfloor \implies \text{typeof-addr } h' a = \lfloor \text{Array-type } T n \rfloor \rrbracket$
 $\implies h \trianglelefteq h'$

unfolding *hext-def*

by(rule map-leI)(case-tac v, simp-all)

lemma *hext-objD*:

assumes $h \trianglelefteq h'$

and typeof-addr h a = $\lfloor \text{Class-type } C \rfloor$

shows typeof-addr h' a = $\lfloor \text{Class-type } C \rfloor$

using *assms* **unfolding** *hext-def* **by**(auto dest: map-le-SomeD)

lemma *hext-arrD*:

assumes $h \trianglelefteq h'$ typeof-addr h a = $\lfloor \text{Array-type } T n \rfloor$

shows typeof-addr h' a = $\lfloor \text{Array-type } T n \rfloor$

using *assms* **unfolding** *heax-def* **by**(*blast dest: map-le-SomeD*)

lemma *heax-refl* [*iff*]: $h \trianglelefteq h$
by (*rule heaxI*) *blast+*

lemma *heax-trans* [*trans*]: $\llbracket h \trianglelefteq h'; h' \trianglelefteq h'' \rrbracket \Longrightarrow h \trianglelefteq h''$
unfolding *heax-def* **by**(*rule map-le-trans*)

lemma *typeof-lit-typeof*:
 $typeof\ v = \lfloor T \rfloor \Longrightarrow typeof_h\ v = \lfloor T \rfloor$
by(*cases v*)(*simp-all*)

lemma *addr-loc-type-fun*:
 $\llbracket P, h \vdash a@al : T; P, h \vdash a@al : T' \rrbracket \Longrightarrow T = T'$
by(*auto elim!*: *addr-loc-type.cases dest: has-field-fun*)

lemma *THE-addr-loc-type*:
 $P, h \vdash a@al : T \Longrightarrow (THE\ T.\ P, h \vdash a@al : T) = T$
by(*rule the-equality*)(*auto dest: addr-loc-type-fun*)

lemma *typeof-addr-locI* [*simp*]:
 $P, h \vdash a@al : T \Longrightarrow typeof\text{-addr-loc}\ P\ h\ a\ al = T$
by(*auto simp add: typeof-addr-loc-def dest: addr-loc-type-fun*)

lemma *deterministic-heap-opsI*:
 $\llbracket \bigwedge h\ ad\ al\ v\ v'. \llbracket heap\text{-read}\ h\ ad\ al\ v; heap\text{-read}\ h\ ad\ al\ v' \rrbracket \Longrightarrow v = v';$
 $\bigwedge h\ ad\ al\ v\ h'\ h''. \llbracket heap\text{-write}\ h\ ad\ al\ v\ h'; heap\text{-write}\ h\ ad\ al\ v\ h'' \rrbracket \Longrightarrow h' = h'';$
 $\bigwedge h\ hT\ h'\ a\ h''\ a'. \llbracket (h', a) \in allocate\ h\ hT; (h'', a') \in allocate\ h\ hT \rrbracket \Longrightarrow h' = h'' \wedge a = a';$
 $\neg\ spurious\text{-wakeups} \rrbracket$
 $\Longrightarrow deterministic\text{-heap-ops}$
unfolding *deterministic-heap-ops-def* **by** *blast*

lemma *deterministic-heap-ops-readD*:
 $\llbracket deterministic\text{-heap-ops}; heap\text{-read}\ h\ ad\ al\ v; heap\text{-read}\ h\ ad\ al\ v' \rrbracket \Longrightarrow v = v'$
unfolding *deterministic-heap-ops-def* **by** *blast*

lemma *deterministic-heap-ops-writeD*:
 $\llbracket deterministic\text{-heap-ops}; heap\text{-write}\ h\ ad\ al\ v\ h'; heap\text{-write}\ h\ ad\ al\ v\ h'' \rrbracket \Longrightarrow h' = h''$
unfolding *deterministic-heap-ops-def* **by** *blast*

lemma *deterministic-heap-ops-allocateD*:
 $\llbracket deterministic\text{-heap-ops}; (h', a) \in allocate\ h\ hT; (h'', a') \in allocate\ h\ hT \rrbracket \Longrightarrow h' = h'' \wedge a = a'$
unfolding *deterministic-heap-ops-def* **by** *blast*

lemma *deterministic-heap-ops-no-spurious-wakeups*:
 $deterministic\text{-heap-ops} \Longrightarrow \neg\ spurious\text{-wakeups}$
unfolding *deterministic-heap-ops-def* **by** *blast*

end

locale *addr-conv* =
heap-base
addr2thread-id thread-id2addr
spurious-wakeups

```

    empty-heap allocate typeof-addr heap-read heap-write
+
prog P
for addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and P :: 'm prog
+
assumes addr2thread-id-inverse:
[[ typeof-addr h a = [Class-type C]; P ⊢ C ≤* Thread ]] ⇒ thread-id2addr (addr2thread-id a) = a
begin

lemma typeof-addr-thread-id2-addr-addr2thread-id [simp]:
[[ typeof-addr h a = [Class-type C]; P ⊢ C ≤* Thread ]] ⇒ typeof-addr h (thread-id2addr (addr2thread-id
a)) = [Class-type C]
by(simp add: addr2thread-id-inverse)

end

locale heap =
  addr-conv
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  P
for addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and P :: 'm prog
+
assumes allocate-SomeD: [[ (h', a) ∈ allocate h hT; is-htype P hT ]] ⇒ typeof-addr h' a = Some
hT

and hext-allocate: ∧a. (h', a) ∈ allocate h hT ⇒ h ≤ h'

and hext-heap-write:
heap-write h a al v h' ⇒ h ≤ h'

begin

lemmas hext-heap-ops = hext-allocate hext-heap-write

lemma typeof-addr-hext-mono:
[[ h ≤ h'; typeof-addr h a = [hT] ]] ⇒ typeof-addr h' a = [hT]

```

unfolding *hext-def* **by**(*rule map-le-SomeD*)

lemma *hext-typeof-mono*:

$\llbracket h \trianglelefteq h'; \text{typeof}_h v = \text{Some } T \rrbracket \implies \text{typeof}_{h'} v = \text{Some } T$
by (*cases v*)(*auto intro: typeof-addr-hext-mono*)

lemma *addr-loc-type-hext-mono*:

$\llbracket P, h \vdash a@al : T; h \trianglelefteq h' \rrbracket \implies P, h' \vdash a@al : T$
by(*force elim!*: *addr-loc-type.cases intro: addr-loc-type.intros elim: typeof-addr-hext-mono dest: hext-arrD*)

lemma *type-of-hext-type-of*: — FIXME: What's this rule good for?

$\llbracket \text{typeof}_h w = \lfloor T \rfloor; \text{hext } h \ h' \rrbracket \implies \text{typeof}_{h'} w = \lfloor T \rfloor$
by(*rule hext-typeof-mono*)

lemma *hext-None*: $\llbracket h \trianglelefteq h'; \text{typeof-addr } h' \ a = \text{None} \rrbracket \implies \text{typeof-addr } h \ a = \text{None}$

by(*rule ccontr*)(*auto dest: typeof-addr-hext-mono*)

lemma *map-typeof-hext-mono*:

$\llbracket \text{map } \text{typeof}_h \ vs = \text{map } \text{Some } Ts; h \trianglelefteq h' \rrbracket \implies \text{map } \text{typeof}_{h'} \ vs = \text{map } \text{Some } Ts$
apply(*induct vs arbitrary: Ts*)
apply(*auto simp add: Cons-eq-map-conv intro: hext-typeof-mono*)
done

lemma *hext-typeof-addr-map-le*:

$h \trianglelefteq h' \implies \text{typeof-addr } h \subseteq_m \text{typeof-addr } h'$
by(*auto simp add: map-le-def dest: typeof-addr-hext-mono*)

lemma *hext-dom-typeof-addr-subset*:

$h \trianglelefteq h' \implies \text{dom } (\text{typeof-addr } h) \subseteq \text{dom } (\text{typeof-addr } h')$
by (*metis hext-typeof-addr-map-le map-le-implies-dom-le*)

end

declare *heap-base.typeof-h.simps* [*code*]

declare *heap-base.cname-of-def* [*code*]

end

3.8 Observable events in JinjaThreads

theory *Observable-Events*

imports

Heap

../Framework/FWState

begin

datatype (*'addr, 'thread-id*) *obs-event* =

ExternalCall 'addr mname 'addr val list 'addr val
| *ReadMem 'addr addr-loc 'addr val*
| *WriteMem 'addr addr-loc 'addr val*
| *NewHeapElem 'addr htype*
| *ThreadStart 'thread-id*
| *ThreadJoin 'thread-id*


```

| SyncLock 'addr
| SyncUnlock 'addr
| ObsInterrupt 'thread-id
| ObsInterrupted 'thread-id

```

instance *obs-event* :: (*type*, *type*) *obs-action*
proof **qed**

type-synonym

```

('addr, 'thread-id, 'x, 'heap) Jinja-thread-action =
  ('addr, 'thread-id, 'x, 'heap, 'addr, ('addr, 'thread-id) obs-event) thread-action

```

print-translation <

```

let
  fun tr'
    [ a1, t1, x, h, a2
    , Const (@{type-syntax obs-event}, -) $ a3 $ t2] =
    if a1 = a2 andalso a2 = a3 andalso t1 = t2 then Syntax.const @{type-syntax Jinja-thread-action}
$ a1 $ t1 $ x $ h
    else raise Match;
  in [(@{type-syntax thread-action}, K tr')]
end
>
typ ('addr, 'thread-id, 'x, 'heap) Jinja-thread-action

```

lemma *range-ty-of-htype*: $\text{range ty-of-htype} \subseteq \text{range Class} \cup \text{range Array}$

```

apply(rule subsetI)
apply(erule rangeE)
apply(rename-tac ht)
apply(case-tac ht)
apply auto
done

```

lemma *some-choice*: $(\exists a. \forall b. P b (a b)) \longleftrightarrow (\forall b. \exists a. P b a)$
by *metis*

definition *convert-RA* :: '*addr* *released-locks* \Rightarrow ('*addr* :: *addr*, '*thread-id*) *obs-event list*

where $\bigwedge ln. \text{convert-RA } ln = \text{concat} (\text{map} (\lambda ad. \text{replicate} (ln \$ ad) (\text{SyncLock } ad)) (\text{monitor-funfun-to-list } ln))$

lemma *set-convert-RA-not-New* [*simp*]:

```

 $\bigwedge ln. \text{NewHeapElem } a \text{ CTn} \notin \text{set} (\text{convert-RA } ln)$ 
by(auto simp add: convert-RA-def)

```

lemma *set-convert-RA-not-Read* [*simp*]:

```

 $\bigwedge ln. \text{ReadMem } ad \text{ al } v \notin \text{set} (\text{convert-RA } ln)$ 
by(auto simp add: convert-RA-def)

```

end

3.9 The initial configuration

theory *StartConfig*

imports

Exceptions

Observable-Events

begin

definition *initialization-list* :: *cname list*

where

initialization-list = *Thread # sys-xcpts-list*

context *heap-base* **begin**

definition *create-initial-object* :: *'heap × 'addr list × bool ⇒ cname ⇒ 'heap × 'addr list × bool*

where

create-initial-object =

$(\lambda(h, ads, b) C.$

if *b*

then let *HA* = *allocate h (Class-type C)*

in if *HA* = $\{\}$ *then* $(h, ads, False)$

else let (h', a') = *SOME* *ha*. *ha* ∈ *HA* *in* $(h', ads @ [a'], True)$

else $(h, ads, False)$)

definition *start-heap-data* :: *'heap × 'addr list × bool*

where

start-heap-data = *foldl create-initial-object (empty-heap, [], True) initialization-list*

definition *start-heap* :: *'heap*

where *start-heap* = *fst start-heap-data*

definition *start-heap-ok* :: *bool*

where *start-heap-ok* = *snd (snd (start-heap-data))*

definition *start-heap-obs* :: *('addr, 'thread-id) obs-event list*

where

start-heap-obs =

map $(\lambda(C, a). NewHeapElem a (Class-type C)) (zip\ initialization-list\ (fst\ (snd\ start-heap-data)))$

definition *start-addr*s :: *'addr list*

where *start-addr*s = *fst (snd start-heap-data)*

definition *addr-of-sys-xcpt* :: *cname ⇒ 'addr*

where *addr-of-sys-xcpt* *C* = *the (map-of (zip initialization-list start-addr*s) *C)*

definition *start-tid* :: *'thread-id*

where *start-tid* = *addr2thread-id (hd start-addr*s)

definition *start-state* :: $(cname \Rightarrow mname \Rightarrow ty\ list \Rightarrow ty \Rightarrow 'm \Rightarrow 'addr\ val\ list \Rightarrow 'x) \Rightarrow 'm\ prog \Rightarrow cname \Rightarrow mname \Rightarrow 'addr\ val\ list \Rightarrow ('addr, 'thread-id, 'x, 'heap, 'addr)\ state$

where

start-state *f P C M vs* ≡

let (D, Ts, T, m) = *method P C M*

in $(K\$ None, ([start-tid \mapsto (f\ D\ M\ Ts\ T\ (the\ m)\ vs, no-wait-locks)], start-heap), Map.empty, \{\})$

lemma *create-initial-object-simps*:

```
create-initial-object (h, ads, b) C =
  (if b
   then let HA = allocate h (Class-type C)
        in if HA = {} then (h, ads, False)
           else let (h', a'') = SOME ha. ha ∈ HA in (h', ads @ [a''], True)
   else (h, ads, False))
```

unfolding *create-initial-object-def* **by** *simp*

lemma *create-initial-object-False* [*simp*]:

```
create-initial-object (h, ads, False) C = (h, ads, False)
```

by(*simp add: create-initial-object-simps*)

lemma *foldl-create-initial-object-False* [*simp*]:

```
foldl create-initial-object (h, ads, False) Cs = (h, ads, False)
```

by(*induct Cs*) *simp-all*

lemma *NewHeapElem-start-heap-obs-start-addrD*:

```
NewHeapElem a CTn ∈ set start-heap-obs ⇒ a ∈ set start-addr
```

unfolding *start-heap-obs-def start-addr-def*

by(*auto dest: set-zip-rightD*)

lemma *shr-start-state*: *shr (start-state f P C M vs) = start-heap*

by(*simp add: start-state-def split-beta*)

lemma *start-heap-obs-not-Read*:

```
ReadMem ad al v ∉ set start-heap-obs
```

unfolding *start-heap-obs-def* **by** *auto*

lemma *length-initialization-list-le-length-start-addr*:

```
length initialization-list ≥ length start-addr
```

proof –

```
{ fix h ads xs
  have length (fst (snd (foldl create-initial-object (h, ads, True) xs))) ≤ length ads + length xs
  proof (induct xs arbitrary: h ads)
    case Nil thus ?case by simp
  next
    case (Cons x xs)
    from this[of fst (SOME ha. ha ∈ allocate h (Class-type x)) ads @ [snd (SOME ha. ha ∈ allocate
h (Class-type x))]]
    show ?case by (clarsimp simp add: create-initial-object-simps split-beta)
  qed }
from this[of empty-heap [] initialization-list]
show ?thesis unfolding start-heap-def start-addr-def start-heap-data-def by simp
qed
```

lemma (**in** –) *distinct-initialization-list*:

```
distinct initialization-list
```

by(*simp add: initialization-list-def sys-xcpts-list-def sys-xcpts-neqs Thread-neq-sys-xcpts*)

lemma (**in** –) *wf-syscls-initialization-list-is-class*:

```
[[ wf-syscls P; C ∈ set initialization-list ]] ⇒ is-class P C
```

by(*auto simp add: initialization-list-def sys-xcpts-list-def wf-syscls-is-class-xcpt*)

lemma *start-addr-HeapElem-start-heap-obsD*:

$a \in \text{set start-addr} \implies \exists CTn. \text{NewHeapElem } a \text{ } CTn \in \text{set start-heap-obs}$

using *length-initialization-list-le-length-start-addr*

unfolding *start-heap-obs-def start-addr-def*

by(*force simp add: set-zip in-set-conv-nth intro: rev-image-eqI*)

lemma *in-set-start-addr-conv-NewHeapElem*:

$a \in \text{set start-addr} \iff (\exists CTn. \text{NewHeapElem } a \text{ } CTn \in \text{set start-heap-obs})$

by(*blast dest: start-addr-NewHeapElem-start-heap-obsD intro: NewHeapElem-start-heap-obs-start-addrD*)

3.9.1 preallocated

definition *preallocated* :: 'heap \Rightarrow bool

where *preallocated* $h \equiv \forall C \in \text{sys-xcpts}. \text{typeof-addr } h \text{ (addr-of-sys-xcpt } C) = \lfloor \text{Class-type } C \rfloor$

lemma *typeof-addr-sys-xcp*:

$\llbracket \text{preallocated } h; C \in \text{sys-xcpts} \rrbracket \implies \text{typeof-addr } h \text{ (addr-of-sys-xcpt } C) = \lfloor \text{Class-type } C \rfloor$

by(*simp add: preallocated-def*)

lemma *typeof-sys-xcp*:

$\llbracket \text{preallocated } h; C \in \text{sys-xcpts} \rrbracket \implies \text{typeof}_h \text{ (Addr (addr-of-sys-xcpt } C)) = \lfloor \text{Class } C \rfloor$

by(*simp add: typeof-addr-sys-xcp*)

lemma *addr-of-sys-xcpt-start-addr*:

$\llbracket \text{start-heap-ok}; C \in \text{sys-xcpts} \rrbracket \implies \text{addr-of-sys-xcpt } C \in \text{set start-addr}$

unfolding *start-heap-ok-def start-heap-data-def initialization-list-def sys-xcpts-list-def preallocated-def start-heap-def start-addr-def*

apply(*simp split: prod.split-asm if-split-asm add: create-initial-object-simps*)

apply(*erule sys-xcpts-cases*)

apply(*simp-all add: addr-of-sys-xcpt-def start-addr-def start-heap-data-def initialization-list-def sys-xcpts-list-def create-initial-object-simps*)

done

lemma [*simp*]:

assumes *preallocated* h

shows *typeof-ClassCast*: $\text{typeof-addr } h \text{ (addr-of-sys-xcpt ClassCast)} = \text{Some}(\text{Class-type ClassCast})$

and *typeof-OutOfMemory*: $\text{typeof-addr } h \text{ (addr-of-sys-xcpt OutOfMemory)} = \text{Some}(\text{Class-type OutOfMemory})$

and *typeof-NullPointer*: $\text{typeof-addr } h \text{ (addr-of-sys-xcpt NullPointer)} = \text{Some}(\text{Class-type NullPointer})$

and *typeof-ArrayIndexOutOfBounds*:

$\text{typeof-addr } h \text{ (addr-of-sys-xcpt ArrayIndexOutOfBounds)} = \text{Some}(\text{Class-type ArrayIndexOutOfBounds})$

and *typeof-ArrayStore*: $\text{typeof-addr } h \text{ (addr-of-sys-xcpt ArrayStore)} = \text{Some}(\text{Class-type ArrayStore})$

and *typeof-NegativeArraySize*: $\text{typeof-addr } h \text{ (addr-of-sys-xcpt NegativeArraySize)} = \text{Some}(\text{Class-type NegativeArraySize})$

and *typeof-ArithmeticException*: $\text{typeof-addr } h \text{ (addr-of-sys-xcpt ArithmeticException)} = \text{Some}(\text{Class-type ArithmeticException})$

and *typeof-IllegalMonitorState*: $\text{typeof-addr } h \text{ (addr-of-sys-xcpt IllegalMonitorState)} = \text{Some}(\text{Class-type IllegalMonitorState})$

and *typeof-IllegalThreadState*: $\text{typeof-addr } h \text{ (addr-of-sys-xcpt IllegalThreadState)} = \text{Some}(\text{Class-type IllegalThreadState})$

and *typeof-InterruptedException*: $\text{typeof-addr } h \text{ (addr-of-sys-xcpt InterruptedException)} = \text{Some}(\text{Class-type InterruptedException})$

InterruptedException)

using *assms*

by(*simp-all add: typeof-addr-sys-xcp*)

lemma *cname-of-xcp* [*simp*]:

$\llbracket \text{preallocated } h; C \in \text{sys-xcpts} \rrbracket \implies \text{cname-of } h \text{ (addr-of-sys-xcpt } C) = C$

by(*drule (1) typeof-addr-sys-xcp*)(*simp add: cname-of-def*)

lemma *preallocated-hext*:

$\llbracket \text{preallocated } h; h \leq h' \rrbracket \implies \text{preallocated } h'$

by(*auto simp add: preallocated-def dest: hext-objD*)

end

context *heap begin*

lemma *preallocated-heap-ops*:

assumes *preallocated h*

shows *preallocated-allocate*: $\bigwedge a. (h', a) \in \text{allocate } h \text{ hT} \implies \text{preallocated } h'$

and *preallocated-write-field*: *heap-write h a al v h' \implies preallocated h'*

using *preallocated-hext[OF assms, of h']*

by(*blast intro: hext-heap-ops*)**+**

lemma *not-empty-pairE*: $\llbracket A \neq \{\}; \bigwedge a b. (a, b) \in A \implies \text{thesis} \rrbracket \implies \text{thesis}$

by *auto*

lemma *allocate-not-emptyI*: $(h', a) \in \text{allocate } h \text{ hT} \implies \text{allocate } h \text{ hT} \neq \{\}$

by *auto*

lemma *allocate-Eps*:

$\llbracket (h'', a'') \in \text{allocate } h \text{ hT}; (\text{SOME } ha. ha \in \text{allocate } h \text{ hT}) = (h', a') \rrbracket \implies (h', a') \in \text{allocate } h \text{ hT}$

by(*drule sym*)(*auto intro: someI*)

lemma *preallocated-start-heap*:

$\llbracket \text{start-heap-ok}; \text{wf-syscls } P \rrbracket \implies \text{preallocated start-heap}$

unfolding *start-heap-ok-def start-heap-data-def initialization-list-def sys-xcpts-list-def preallocated-def start-heap-def start-addr-def*

apply(*clarsimp split: prod.split-asm if-split-asm simp add: create-initial-object-simps*)

apply(*erule not-empty-pairE*)**+**

apply(*drule (1) allocate-Eps*)

apply(*drule (1) allocate-Eps*)

apply(*drule (1) allocate-Eps*)

apply(*drule (1) allocate-Eps*)

apply(*drule (1) allocate-Eps*)

apply(*drule (1) allocate-Eps*)

apply(*drule (1) allocate-Eps*)

apply(*drule (1) allocate-Eps*)

apply(*drule (1) allocate-Eps*)

apply(*drule (1) allocate-Eps*)

apply(*drule (1) allocate-Eps*)

apply(*rotate-tac 13*)

apply(*frule allocate-SomeD, simp add: wf-syscls-is-class-xcpt, frule hext-allocate, rotate-tac 1*)

apply(*frule allocate-SomeD, simp add: wf-syscls-is-class-xcpt, frule hext-allocate, rotate-tac 1*)

apply(*frule allocate-SomeD, simp add: wf-syscls-is-class-xcpt, frule hext-allocate, rotate-tac 1*)

```

apply(frule allocate-SomeD, simp add: wf-syscls-is-class-xcpt, frule hext-allocate, rotate-tac 1)
apply(frule allocate-SomeD, simp add: wf-syscls-is-class-xcpt, frule hext-allocate, rotate-tac 1)
apply(frule allocate-SomeD, simp add: wf-syscls-is-class-xcpt, frule hext-allocate, rotate-tac 1)
apply(frule allocate-SomeD, simp add: wf-syscls-is-class-xcpt, frule hext-allocate, rotate-tac 1)
apply(frule allocate-SomeD, simp add: wf-syscls-is-class-xcpt, frule hext-allocate, rotate-tac 1)
apply(frule allocate-SomeD, simp add: wf-syscls-is-class-xcpt, frule hext-allocate, rotate-tac 1)
apply(frule allocate-SomeD, simp add: wf-syscls-is-class-xcpt, frule hext-allocate, rotate-tac 1)
apply(frule allocate-SomeD, simp add: wf-syscls-is-class-xcpt, frule hext-allocate, rotate-tac 1)
apply(erule sys-xcpts-cases)
apply(simp-all add: addr-of-sys-xcpt-def initialization-list-def sys-xcpts-list-def sys-xcpts-neqs Thread-neq-sys-xcpts
start-heap-data-def start-addr-def create-initial-object-simps allocate-not-emptyI split del: if-split)
apply(assumption|erule typeof-addr-hext-mono)+
done

```

lemma start-tid-start-addr:

```

  [[ wf-syscls P; start-heap-ok ] => thread-id2addr start-tid ∈ set start-addr
unfolding start-heap-ok-def start-heap-data-def initialization-list-def sys-xcpts-list-def
  preallocated-def start-heap-def start-addr-def
apply(simp split: prod.split-asm if-split-asm add: create-initial-object-simps addr-of-sys-xcpt-def start-addr-def
start-tid-def start-heap-data-def initialization-list-def sys-xcpts-list-def)
apply(erule not-empty-pairE)+
apply(drule (1) allocate-Eps)
apply(rotate-tac -1)
apply(drule allocate-SomeD, simp)
apply(auto intro: addr2thread-id-inverse)
done

```

lemma

```

  assumes wf-syscls P
  shows dom-typeof-addr-start-heap: set start-addr ⊆ dom (typeof-addr start-heap)
  and distinct-start-addr: distinct start-addr
proof -
  { fix h ads b and Cs xs :: cname list
    assume set ads ⊆ dom (typeof-addr h) and distinct (Cs @ xs) and length Cs = length ads
      and  $\bigwedge C a. (C, a) \in \text{set } (\text{zip } Cs \text{ ads}) \implies \text{typeof-addr } h \ a = \lfloor \text{Class-type } C \rfloor$ 
      and  $\bigwedge C. C \in \text{set } xs \implies \text{is-class } P \ C$ 
    hence set (fst (snd (foldl create-initial-object (h, ads, b) xs))) ⊆
      dom (typeof-addr (fst (foldl create-initial-object (h, ads, b) xs)))  $\wedge$ 
      (distinct ads  $\longrightarrow$  distinct (fst (snd (foldl create-initial-object (h, ads, b) xs))))
    (is ?concl xs h ads b Cs)
    proof(induct xs arbitrary: h ads b Cs)
      case Nil thus ?case by auto
    next
      case (Cons x xs)
      note ads =  $\langle \text{set ads} \subseteq \text{dom } (\text{typeof-addr } h) \rangle$ 
      note dist =  $\langle \text{distinct } (Cs @ x \# xs) \rangle$ 
      note len =  $\langle \text{length } Cs = \text{length ads} \rangle$ 
      note type =  $\langle \bigwedge C a. (C, a) \in \text{set } (\text{zip } Cs \text{ ads}) \implies \text{typeof-addr } h \ a = \lfloor \text{Class-type } C \rfloor \rangle$ 
      note is-class =  $\langle \bigwedge C. C \in \text{set } (x \# xs) \implies \text{is-class } P \ C \rangle$ 
      show ?case
      proof(cases b  $\wedge$  allocate h (Class-type x)  $\neq$  {})
        case False thus ?thesis
        using ads len by(auto simp add: create-initial-object-simps zip-append1)
      next

```

```

case [simp]: True
obtain h' a' where h'a': (SOME ha. ha ∈ allocate h (Class-type x)) = (h', a')
  by(cases SOME ha. ha ∈ allocate h (Class-type x))
with True have new-obj: (h', a') ∈ allocate h (Class-type x)
  by(auto simp del: True intro: allocate-Eps)
hence hext: h ≤ h' by(rule hext-allocate)
with ads new-obj have ads': set ads ⊆ dom (typeof-addr h')
  by(auto dest: typeof-addr-hext-mono[OF hext-allocate])
moreover {
  from new-obj ads' is-class[of x]
  have set (ads @ [a']) ⊆ dom (typeof-addr h')
    by(auto dest: allocate-SomeD)
  moreover from dist have distinct ((Cs @ [x]) @ xs) by simp
  moreover have length (Cs @ [x]) = length (ads @ [a']) using len by simp
  moreover {
    fix C a
    assume (C, a) ∈ set (zip (Cs @ [x]) (ads @ [a']))
    hence typeof-addr h' a = [Class-type C]
      using hext new-obj type[of C a] len is-class
      by(auto dest: allocate-SomeD hext-objD) }
  note type' = this
  moreover have is-class': ∧C. C ∈ set xs ⇒ is-class P C using is-class by simp
  ultimately have ?concl xs h' (ads @ [a']) True (Cs @ [x]) by(rule Cons)
  moreover have a' ∉ set ads
  proof
    assume a': a' ∈ set ads
    then obtain C where (C, a') ∈ set (zip Cs ads) C ∈ set Cs
      using len unfolding set-zip in-set-conv-nth by auto
    hence typeof-addr h a' = [Class-type C] by-(rule type)
    with hext have typeof-addr h' a' = [Class-type C] by(rule typeof-addr-hext-mono)
    moreover from new-obj is-class
    have typeof-addr h' a' = [Class-type x] by(auto dest: allocate-SomeD)
    ultimately have C = x by simp
    with dist ⟨C ∈ set Cs⟩ show False by simp
  qed
  moreover note calculation }
ultimately show ?thesis by(simp add: create-initial-object-simps new-obj h'a')
qed
qed }
from this[of [] empty-heap [] initialization-list True]
  distinct-initialization-list wf-syscls-initialization-list-is-class[OF assms]
show set start-addr ⊆ dom (typeof-addr start-heap)
  and distinct start-addr
  unfolding start-heap-def start-addr-def start-heap-data-def by auto
qed

lemma NewHeapElem-start-heap-obsD:
  assumes wf-syscls P
  and NewHeapElem a hT ∈ set start-heap-obs
  shows typeof-addr start-heap a = [hT]
proof -
  show ?thesis
  proof(cases hT)
    case (Class-type C)

```

```

{ fix  $h$   $ads$   $b$   $xs$   $Cs$ 
  assume  $(C, a) \in \text{set } (\text{zip } (Cs @ xs) (\text{fst } (\text{snd } (\text{foldl } \text{create-initial-object } (h, ads, b) xs))))$ 
    and  $\forall (C, a) \in \text{set } (\text{zip } Cs ads). \text{typeof-addr } h a = \lfloor \text{Class-type } C \rfloor$ 
    and  $\text{length } Cs = \text{length } ads$ 
    and  $\forall C \in \text{set } xs. \text{is-class } P C$ 
  hence  $\text{typeof-addr } (\text{fst } (\text{foldl } \text{create-initial-object } (h, ads, b) xs)) a = \lfloor \text{Class-type } C \rfloor$ 
  proof(induct xs arbitrary: h ads b Cs)
    case Nil thus ?case by auto
  next
    case  $(\text{Cons } x xs)$ 
    note  $inv = \langle \forall (C, a) \in \text{set } (\text{zip } Cs ads). \text{typeof-addr } h a = \lfloor \text{Class-type } C \rfloor \rangle$ 
      and  $Ca = \langle (C, a) \in \text{set } (\text{zip } (Cs @ x \# xs) (\text{fst } (\text{snd } (\text{foldl } \text{create-initial-object } (h, ads, b) (x \# xs)))))) \rangle$ 
      and  $len = \langle \text{length } Cs = \text{length } ads \rangle$ 
      and  $is-class = \langle \forall C \in \text{set } (x \# xs). \text{is-class } P C \rangle$ 
    show ?case
    proof(cases b  $\wedge$  allocate h (Class-type x)  $\neq$  {})
      case False thus ?thesis
        using  $inv$   $Ca$   $len$  by(auto simp add: create-initial-object-simps zip-append1 split: if-split-asm)
    next
      case [simp]:  $True$ 
      obtain  $h' a'$  where  $h'a': (\text{SOME } ha. ha \in \text{allocate } h (\text{Class-type } x)) = (h', a')$ 
        by(cases SOME ha. ha  $\in$  allocate h (Class-type x))
      with  $True$  have  $new\text{-obj}: (h', a') \in \text{allocate } h (\text{Class-type } x)$ 
        by(auto simp del: True intro: allocate-Eps)
      hence  $hext: h \trianglelefteq h'$  by(rule hext-allocate)

      have  $(C, a) \in \text{set } (\text{zip } ((Cs @ [x]) @ xs) (\text{fst } (\text{snd } (\text{foldl } \text{create-initial-object } (h', ads @ [a'], \text{True}) xs))))$ 
        using  $Ca$   $new\text{-obj}$  by(simp add: create-initial-object-simps h'a')
      moreover have  $\forall (C, a) \in \text{set } (\text{zip } (Cs @ [x]) (ads @ [a'])). \text{typeof-addr } h' a = \lfloor \text{Class-type } C \rfloor$ 
      proof(clarify)
        fix  $C a$ 
        assume  $(C, a) \in \text{set } (\text{zip } (Cs @ [x]) (ads @ [a']))$ 
        thus  $\text{typeof-addr } h' a = \lfloor \text{Class-type } C \rfloor$ 
          using  $inv$   $len$   $hext$   $new\text{-obj}$   $is-class$  by(auto dest: allocate-SomeD typeof-addr-hext-mono)
      qed
      moreover have  $\text{length } (Cs @ [x]) = \text{length } (ads @ [a'])$  using  $len$  by simp
      moreover have  $\forall C \in \text{set } xs. \text{is-class } P C$  using  $is-class$  by simp
      ultimately have  $\text{typeof-addr } (\text{fst } (\text{foldl } \text{create-initial-object } (h', ads @ [a'], \text{True}) xs)) a = \lfloor \text{Class-type } C \rfloor$ 
        by(rule Cons)
      thus ?thesis using  $new\text{-obj}$  by(simp add: create-initial-object-simps h'a')
    qed
  qed }
from this[of [] initialization-list empty-heap [] True] assms wf-syscls-initialization-list-is-class[of P]

  show ?thesis by(auto simp add: start-heap-obs-def start-heap-data-def start-heap-def Class-type)
next
  case Array-type thus ?thesis using assms
    by(auto simp add: start-heap-obs-def start-heap-data-def start-heap-def)
  qed
qed

```


end

3.9.2 Code generation

definition *pick-addr* :: ('heap × 'addr) set ⇒ 'heap × 'addr
where *pick-addr* HA = (SOME ha. ha ∈ HA)

lemma *pick-addr-code* [code]:
pick-addr (set [ha]) = ha
by(*simp* add: *pick-addr-def*)

lemma (**in** *heap-base*) *start-heap-data-code*:
start-heap-data =
 (let
 (h, ads, b) = *foldl*
 (λ(h, ads, b) C.
 if b then
 let HA = *allocate* h (Class-type C)
 in if HA = {} then (h, ads, False)
 else let (h', a'') = *pick-addr* HA in (h', a'' # ads, True)
 else (h, ads, False))
 (*empty-heap*, [], True)
initialization-list
 in (h, rev ads, b))

unfolding *start-heap-data-def* *create-initial-object-def* *pick-addr-def*
by(*rule* *rev-induct*)(*simp-all* add: *split-beta*)

lemmas [code] =
heap-base.start-heap-data-code
heap-base.start-heap-def
heap-base.start-heap-ok-def
heap-base.start-heap-obs-def
heap-base.start-addr-def
heap-base.addr-of-sys-xcpt-def
heap-base.start-tid-def
heap-base.start-state-def

end

3.10 Conformance Relations for Type Soundness Proofs

theory *Conform*

imports

StartConfig

begin

context *heap-base* **begin**

definition *conf* :: 'm prog ⇒ 'heap ⇒ 'addr val ⇒ ty ⇒ bool (⟨-, - ⟩ - :≤ -⟩ [51,51,51,51] 50)
where $P, h \vdash v : \leq T \equiv \exists T'. \text{typeof}_h v = \text{Some } T' \wedge P \vdash T' \leq T$

definition *lconf* :: 'm prog ⇒ 'heap ⇒ (vname → 'addr val) ⇒ (vname → ty) ⇒ bool (⟨-, - ⟩ - '(≤)')
 -> [51,51,51,51] 50)
where $P, h \vdash l (: \leq) E \equiv \forall V v. l V = \text{Some } v \longrightarrow (\exists T. E V = \text{Some } T \wedge P, h \vdash v : \leq T)$

abbreviation $conf s :: 'm\ prog \Rightarrow 'heap \Rightarrow 'addr\ val\ list \Rightarrow ty\ list \Rightarrow bool$ ($\langle -, - \vdash - \rangle [:\leq] \rightarrow [51, 51, 51, 51]$ 50)

where $P, h \vdash vs [:\leq] Ts == list-all2 (conf P h) vs Ts$

definition $tconf :: 'm\ prog \Rightarrow 'heap \Rightarrow 'thread-id \Rightarrow bool$ ($\langle -, - \vdash - \rangle \sqrt{t} [51, 51, 51] 50$)

where $P, h \vdash t \sqrt{t} \equiv \exists C. typeof-addr\ h (thread-id2addr\ t) = [Class-type\ C] \wedge P \vdash C \preceq^* Thread$

end

locale $heap-conf-base =$

$heap-base +$

constrains $addr2thread-id :: ('addr :: addr) \Rightarrow 'thread-id$

and $thread-id2addr :: 'thread-id \Rightarrow 'addr$

and $spurious-wakeups :: bool$

and $empty-heap :: 'heap$

and $allocate :: 'heap \Rightarrow htype \Rightarrow ('heap \times 'addr)\ set$

and $typeof-addr :: 'heap \Rightarrow 'addr \rightarrow htype$

and $heap-read :: 'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr\ val \Rightarrow bool$

and $heap-write :: 'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr\ val \Rightarrow 'heap \Rightarrow bool$

fixes $hconf :: 'heap \Rightarrow bool$

and $P :: 'm\ prog$

sublocale $heap-conf-base < prog\ P .$

locale $heap-conf =$

$heap$

$addr2thread-id\ thread-id2addr$

$spurious-wakeups$

$empty-heap\ allocate\ typeof-addr\ heap-read\ heap-write$

P

$+$

$heap-conf-base$

$addr2thread-id\ thread-id2addr$

$spurious-wakeups$

$empty-heap\ allocate\ typeof-addr\ heap-read\ heap-write$

$hconf\ P$

for $addr2thread-id :: ('addr :: addr) \Rightarrow 'thread-id$

and $thread-id2addr :: 'thread-id \Rightarrow 'addr$

and $spurious-wakeups :: bool$

and $empty-heap :: 'heap$

and $allocate :: 'heap \Rightarrow htype \Rightarrow ('heap \times 'addr)\ set$

and $typeof-addr :: 'heap \Rightarrow 'addr \rightarrow htype$

and $heap-read :: 'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr\ val \Rightarrow bool$

and $heap-write :: 'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr\ val \Rightarrow 'heap \Rightarrow bool$

and $hconf :: 'heap \Rightarrow bool$

and $P :: 'm\ prog$

$+$

assumes $hconf-empty\ [iff]: hconf\ empty-heap$

and $typeof-addr-is-type: \llbracket typeof-addr\ h\ a = [hT]; hconf\ h \rrbracket \Longrightarrow is-type\ P (ty-of-htype\ hT)$

and $hconf-allocate-mono: \bigwedge a. \llbracket (h', a) \in allocate\ h\ hT; hconf\ h; is-htype\ P\ hT \rrbracket \Longrightarrow hconf\ h'$

and $hconf-heap-write-mono:$

$\bigwedge T. \llbracket heap-write\ h\ a\ al\ v\ h'; hconf\ h; P, h \vdash a@al : T; P, h \vdash v : \leq T \rrbracket \Longrightarrow hconf\ h'$

```

locale heap-progress =
  heap-conf
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  hconf P
for addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype
and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
and hconf :: 'heap  $\Rightarrow$  bool
and P :: 'm prog
+
assumes heap-read-total:  $\llbracket$  hconf h; P, h  $\vdash$  a@al : T  $\rrbracket \Longrightarrow \exists v. \text{heap-read } h \ a \ al \ v \wedge P, h \vdash v : \leq T$ 
and heap-write-total:  $\llbracket$  hconf h; P, h  $\vdash$  a@al : T; P, h  $\vdash$  v :  $\leq$  T  $\rrbracket \Longrightarrow \exists h'. \text{heap-write } h \ a \ al \ v \ h'$ 

```

```

locale heap-conf-read =
  heap-conf
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  hconf P
for addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype
and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
and hconf :: 'heap  $\Rightarrow$  bool
and P :: 'm prog
+
assumes heap-read-conf:  $\llbracket$  heap-read h a al v; P, h  $\vdash$  a@al : T; hconf h  $\rrbracket \Longrightarrow P, h \vdash v : \leq T$ 

```

```

locale heap-typesafe =
  heap-conf-read +
  heap-progress +
constrains addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype
and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
and hconf :: 'heap  $\Rightarrow$  bool
and P :: 'm prog

```

```

context heap-conf begin

```

lemmas *hconf-heap-ops-mono* =
hconf-allocate-mono
hconf-heap-write-mono

end

3.10.1 Value conformance $:\leq$

context *heap-base* **begin**

lemma *conf-Null* [*simp*]: $P, h \vdash \text{Null} :\leq T = P \vdash NT \leq T$
unfolding *conf-def* **by** (*simp* (*no-asm*))

lemma *typeof-conf* [*simp*]: $\text{typeof}_h v = \text{Some } T \implies P, h \vdash v :\leq T$
unfolding *conf-def* **by** (*cases* *v*) *auto*

lemma *typeof-lit-conf* [*simp*]: $\text{typeof } v = \text{Some } T \implies P, h \vdash v :\leq T$
by (*rule* *typeof-conf* [*OF* *typeof-lit-typeof*])

lemma *defval-conf* [*simp*]: $P, h \vdash \text{default-val } T :\leq T$
unfolding *conf-def* **by** (*cases* *T*) *auto*

lemma *conf-widen*: $P, h \vdash v :\leq T \implies P \vdash T \leq T' \implies P, h \vdash v :\leq T'$
unfolding *conf-def* **by** (*cases* *v*) (*auto* *intro*: *widen-trans*)

lemma *conf-sys-xcpt*:
 $\llbracket \text{preallocated } h; C \in \text{sys-xcpts} \rrbracket \implies P, h \vdash \text{Addr } (\text{addr-of-sys-xcpt } C) :\leq \text{Class } C$
by (*simp* *add*: *conf-def* *typeof-addr-sys-xcp*)

lemma *conf-NT* [*iff*]: $P, h \vdash v :\leq NT = (v = \text{Null})$
by (*auto* *simp* *add*: *conf-def*)

lemma *is-IntgI*: $P, h \vdash v :\leq \text{Integer} \implies \text{is-Intg } v$
by (*unfold* *conf-def*) *auto*

lemma *is-BoolI*: $P, h \vdash v :\leq \text{Boolean} \implies \text{is-Bool } v$
by (*unfold* *conf-def*) *auto*

lemma *is-RefI*: $P, h \vdash v :\leq T \implies \text{is-refT } T \implies \text{is-Ref } v$
by (*cases* *v*) (*auto* *elim*: *is-refT.cases* *simp* *add*: *conf-def* *is-Ref-def*)

lemma *non-npD*:
 $\llbracket v \neq \text{Null}; P, h \vdash v :\leq \text{Class } C; C \neq \text{Object} \rrbracket$
 $\implies \exists a C'. v = \text{Addr } a \wedge \text{typeof-addr } h a = \llbracket \text{Class-type } C' \rrbracket \wedge P \vdash C' \preceq^* C$
by (*cases* *v*) (*auto* *simp* *add*: *conf-def* *widen-Class*)

lemma *non-npD2*:
 $\llbracket v \neq \text{Null}; P, h \vdash v :\leq \text{Class } C \rrbracket$
 $\implies \exists a hT. v = \text{Addr } a \wedge \text{typeof-addr } h a = \llbracket hT \rrbracket \wedge P \vdash \text{class-type-of } hT \preceq^* C$
by (*cases* *v*) (*auto* *simp* *add*: *conf-def* *widen-Class*)

end

context *heap* **begin**

lemma *conf-heap*: $\llbracket h \sqsubseteq h'; P, h \vdash v : \leq T \rrbracket \Longrightarrow P, h' \vdash v : \leq T$
unfolding *conf-def* **by**(*cases v*)(*auto dest: typeof-addr-heap-mono*)

lemma *conf-heap-ops-mono*:

assumes $P, h \vdash v : \leq T$

shows *conf-allocate-mono*: $(h', a) \in \text{allocate } h \ hT \Longrightarrow P, h' \vdash v : \leq T$

and *conf-heap-write-mono*: $\text{heap-write } h \ a \ al \ v' \ h' \Longrightarrow P, h' \vdash v : \leq T$

using *assms*

by(*auto intro: conf-heap dest: heap-ops*)

end

3.10.2 Value list conformance $[:\leq]$

context *heap-base* **begin**

lemma *confs-widens* [*trans*]: $\llbracket P, h \vdash vs : [\leq] Ts; P \vdash Ts [\leq] Ts' \rrbracket \Longrightarrow P, h \vdash vs : [\leq] Ts'$
by (*rule list-all2-trans*)(*rule conf-widen*)

lemma *confs-rev*: $P, h \vdash \text{rev } s : [\leq] t = (P, h \vdash s : [\leq] \text{rev } t)$

by(*rule list-all2-rev1*)

lemma *confs-conv-map*:

$P, h \vdash vs : [\leq] Ts' = (\exists Ts. \text{map } \text{typeof}_h \ vs = \text{map } \text{Some } Ts \wedge P \vdash Ts [\leq] Ts')$

apply(*induct vs arbitrary: Ts'*)

apply *simp*

apply(*case-tac Ts'*)

apply(*auto simp add: conf-def*)

apply(*rule-tac x=Ts' # Ts in exI*)

apply(*simp add: fun-of-def*)

done

lemma *confs-Cons2*: $P, h \vdash xs : [\leq] y \# ys = (\exists z \ zs. xs = z \# zs \wedge P, h \vdash z : \leq y \wedge P, h \vdash zs : [\leq] ys)$

by (*rule list-all2-Cons2*)

end

context *heap* **begin**

lemma *confs-heap*: $P, h \vdash vs : [\leq] Ts \Longrightarrow h \sqsubseteq h' \Longrightarrow P, h' \vdash vs : [\leq] Ts$

by (*erule list-all2-mono, erule conf-heap, assumption*)

end

3.10.3 Local variable conformance

context *heap-base* **begin**

lemma *lconf-upd*:

$\llbracket P, h \vdash l : (\leq) E; P, h \vdash v : \leq T; E \ V = \text{Some } T \rrbracket \Longrightarrow P, h \vdash l(V \mapsto v) : (\leq) E$

unfolding *lconf-def* **by** *auto*

lemma *lconf-empty* [iff]: $P, h \vdash \text{Map.empty } (:\leq) E$
by(*simp add:lconf-def*)

lemma *lconf-upd2*: $\llbracket P, h \vdash l \text{ } (:\leq) E; P, h \vdash v \text{ } :\leq T \rrbracket \implies P, h \vdash l(V \mapsto v) \text{ } (:\leq) E(V \mapsto T)$
by(*simp add:lconf-def*)

end

context *heap* **begin**

lemma *lconf-heap*: $\llbracket P, h \vdash l \text{ } (:\leq) E; h \sqsubseteq h' \rrbracket \implies P, h' \vdash l \text{ } (:\leq) E$
unfolding *lconf-def* **by**(*fast elim: conf-heap*)

end

3.10.4 Thread object conformance

context *heap-base* **begin**

lemma *tconfI*: $\llbracket \text{typeof-addr } h \text{ } (\text{thread-id2addr } t) = \lfloor \text{Class-type } C \rfloor; P \vdash C \preceq^* \text{Thread} \rrbracket \implies P, h \vdash t \sqrt{t}$
by(*simp add: tconf-def*)

lemma *tconfD*: $P, h \vdash t \sqrt{t} \implies \exists C. \text{typeof-addr } h \text{ } (\text{thread-id2addr } t) = \lfloor \text{Class-type } C \rfloor \wedge P \vdash C \preceq^* \text{Thread}$
by(*auto simp add: tconf-def*)

end

context *heap* **begin**

lemma *tconf-heap-mono*: $\llbracket P, h \vdash t \sqrt{t}; h \sqsubseteq h' \rrbracket \implies P, h' \vdash t \sqrt{t}$
by(*auto simp add: tconf-def dest: typeof-addr-heap-mono*)

lemma *tconf-heap-ops-mono*:
assumes $P, h \vdash t \sqrt{t}$
shows *tconf-allocate-mono*: $(h', a) \in \text{allocate } h \text{ } hT \implies P, h' \vdash t \sqrt{t}$
and *tconf-heap-write-mono*: $\text{heap-write } h \text{ } a \text{ } al \text{ } v \text{ } h' \implies P, h' \vdash t \sqrt{t}$
using *tconf-heap-mono*[*OF* *assms*, *of* h']
by(*blast intro: heap-ops*)**+**

lemma *tconf-start-heap-start-tid*:

$\llbracket \text{start-heap-ok}; \text{wf-syscls } P \rrbracket \implies P, \text{start-heap} \vdash \text{start-tid} \sqrt{t}$

unfolding *start-tid-def start-heap-def start-heap-ok-def start-heap-data-def initialization-list-def addr-of-sys-xcpt-def start-addr-def sys-xcpts-list-def*

apply(*clarsimp split: prod.split-asm simp add: create-initial-object-simps split: if-split-asm*)

apply(*erule not-empty-pairE*)**+**

apply(*drule* (1) *allocate-Eps*)

apply(*drule* (1) *allocate-Eps*)

apply(*drule* (1) *allocate-Eps*)

apply(*drule* (1) *allocate-Eps*)

apply(*drule* (1) *allocate-Eps*)

apply(*drule* (1) *allocate-Eps*)

apply(*drule* (1) *allocate-Eps*)

```

apply(drule (1) allocate-Eps)
apply(drule (1) allocate-Eps)
apply(drule (1) allocate-Eps)
apply(drule (1) allocate-Eps)
apply(drule allocate-SomeD[where hT=Class-type Thread])
  apply simp
apply(rule tconfI)
  apply(erule typeof-addr-heap-mono[OF heap-allocate])+
  apply simp
apply blast
done

```

```

lemma start-heap-write-typeable:
  assumes WriteMem ad al v ∈ set start-heap-obs
  shows  $\exists T. P, \text{start-heap} \vdash \text{ad}@al : T \wedge P, \text{start-heap} \vdash v : \leq T$ 
using assms
unfolding start-heap-obs-def start-heap-def
by clarsimp

```

end

3.10.5 Well-formed start state

```

context heap-base begin

```

```

inductive wf-start-state :: 'm prog  $\Rightarrow$  cname  $\Rightarrow$  mname  $\Rightarrow$  'addr val list  $\Rightarrow$  bool
for P :: 'm prog and C :: cname and M :: mname and vs :: 'addr val list
where
  wf-start-state:
  [ P  $\vdash$  C sees M:Ts $\rightarrow$ T = [meth] in D; start-heap-ok; P, start-heap  $\vdash$  vs [ $:\leq$ ] Ts ]
   $\Longrightarrow$  wf-start-state P C M vs

```

end

end

3.11 Semantics of method calls that cannot be defined inside JinjaThreads

```

theory ExternalCall

```

```

imports

```

```

  ../Framework/FWSemantics

```

```

  Conform

```

```

begin

```

```

type-synonym

```

```

  ('addr, 'thread-id, 'heap) external-thread-action = ('addr, 'thread-id, cname  $\times$  mname  $\times$  'addr, 'heap)
  Jinja-thread-action

```

```

print-translation <

```

```

  let

```

```

    fun tr'

```

```

    [a1, t
    , Const (@{type-syntax prod}, -) $ Const (@{type-syntax String.literal}, -) $
      (Const (@{type-syntax prod}, -) $ Const (@{type-syntax String.literal}, -) $ a2)
    , h] =
    if a1 = a2 then Syntax.const @{type-syntax external-thread-action} $ a1 $ t $ h
    else raise Match;
    in [(@{type-syntax Jinja-thread-action}, K tr')]
  end
>
typ ('addr,'thread-id,'heap) external-thread-action

```

3.11.1 Typing of external calls

inductive *external-WT-defs* :: *cname* \Rightarrow *mname* \Rightarrow *ty list* \Rightarrow *ty* \Rightarrow *bool* ($\langle \dots \rangle$:: $\rightarrow [50, 0, 0, 50]$
60)

where

```

  Thread.start([]) :: Void
| Thread.join([]) :: Void
| Thread.interrupt([]) :: Void
| Thread.isInterrupted([]) :: Boolean
| Object.wait([]) :: Void
| Object.notify([]) :: Void
| Object.notifyAll([]) :: Void
| Object.clone([]) :: Class Object
| Object.hashCode([]) :: Integer
| Object.print([Integer]) :: Void
| Object.currentThread([]) :: Class Thread
| Object.interrupted([]) :: Boolean
| Object.yield([]) :: Void

```

inductive-cases *external-WT-defs-cases*:

```

  a.start(vs) :: T
  a.join(vs) :: T
  a.interrupt(vs) :: T
  a.isInterrupted(vs) :: T
  a.wait(vs) :: T
  a.notify(vs) :: T
  a.notifyAll(vs) :: T
  a.clone(vs) :: T
  a.hashCode(vs) :: T
  a.print(vs) :: T
  a.currentThread(vs) :: T
  a.interrupted([]) :: T
  a.yield(vs) :: T

```

inductive *is-native* :: '*m prog* \Rightarrow *hT* \Rightarrow *mname* \Rightarrow *bool*

for *P* :: '*m prog* **and** *hT* :: *hT* **and** *M* :: *mname*

where $\llbracket P \vdash \text{class-type-of } hT \text{ sees } M:Ts \rightarrow T = \text{Native in } D; D \cdot M(Ts) :: T \rrbracket \Longrightarrow \text{is-native } P \ hT \ M$

lemma *is-nativeD*: *is-native* *P hT M* $\Longrightarrow \exists Ts \ T \ D. P \vdash \text{class-type-of } hT \text{ sees } M:Ts \rightarrow T = \text{Native in } D \wedge D \cdot M(Ts) :: T$

by (*simp add: is-native.simps*)

inductive (**in** *heap-base*) *external-WT'* :: '*m prog* \Rightarrow '*heap* \Rightarrow '*addr* \Rightarrow *mname* \Rightarrow '*addr val list* \Rightarrow *ty*

$\Rightarrow \text{bool}$
 $(\langle -, - \rangle \vdash (\dots'(-')) : \rightarrow [50, 0, 0, 0, 50] 60)$
for $P :: 'm \text{ prog}$ **and** $h :: 'heap$ **and** $a :: 'addr$ **and** $M :: mname$ **and** $vs :: 'addr \text{ val list}$ **and** $U :: ty$
where
 $\llbracket \text{typeof-addr } h \ a = \lfloor hT \rfloor; \text{map typeof}_h \ vs = \text{map Some } Ts; P \vdash \text{class-type-of } hT \text{ sees } M:Ts' \rightarrow U = \text{Native in } D;$
 $P \vdash Ts \llbracket \leq \rrbracket Ts'$
 $\implies P, h \vdash a \cdot M(vs) : U$

context *heap-base* **begin**

lemma *external-WT'-iff*:

$P, h \vdash a \cdot M(vs) : U \iff$
 $(\exists hT \ Ts \ Ts' \ D. \text{typeof-addr } h \ a = \lfloor hT \rfloor \wedge \text{map typeof}_h \ vs = \text{map Some } Ts \wedge P \vdash \text{class-type-of } hT \text{ sees } M:Ts' \rightarrow U = \text{Native in } D \wedge P \vdash Ts \llbracket \leq \rrbracket Ts')$
by(*simp add: external-WT'.simps*)

end

context *heap* **begin**

lemma *external-WT'-heqt-mono*:

$\llbracket P, h \vdash a \cdot M(vs) : T; h \leq h' \rrbracket \implies P, h' \vdash a \cdot M(vs) : T$
by(*auto 5 2 simp add: external-WT'-iff dest: typeof-addr-heqt-mono map-typeof-heqt-mono*)

end

3.11.2 Semantics of external calls

datatype *'addr extCallRet* =

$\text{RetVal } 'addr \text{ val}$
 $| \text{RetExc } 'addr$
 $| \text{RetStaySame}$

lemma *rec-extCallRet [simp]*: *rec-extCallRet = case-extCallRet*
by(*auto simp add: fun-eq-iff split: extCallRet.split*)

context *heap-base* **begin**

abbreviation *RetEXC* :: *cname* \Rightarrow *'addr extCallRet*

where *RetEXC* $C \equiv \text{RetExc (addr-of-sys-xcpt } C)$

inductive *heap-copy-loc* :: *'addr* \Rightarrow *'addr* \Rightarrow *addr-loc* \Rightarrow *'heap* \Rightarrow (*'addr*, *'thread-id*) *obs-event list* \Rightarrow *'heap* \Rightarrow *bool*

for $a :: 'addr$ **and** $a' :: 'addr$ **and** $al :: \text{addr-loc}$ **and** $h :: 'heap$

where

$\llbracket \text{heap-read } h \ a \ al \ v; \text{heap-write } h \ a' \ al \ v \ h' \rrbracket$
 $\implies \text{heap-copy-loc } a \ a' \ al \ h \ ([\text{ReadMem } a \ al \ v, \text{WriteMem } a' \ al \ v]) \ h'$

inductive *heap-copies* :: *'addr* \Rightarrow *'addr* \Rightarrow *addr-loc list* \Rightarrow *'heap* \Rightarrow (*'addr*, *'thread-id*) *obs-event list* \Rightarrow *'heap* \Rightarrow *bool*

for $a :: 'addr$ **and** $a' :: 'addr$

where

Nil: *heap-copies* $a \ a' \ [] \ h \ [] \ h$

| *Cons*:
 $\llbracket \text{heap-copy-loc } a \ a' \ \text{al } h \ \text{ob } h'; \text{ heap-copies } a \ a' \ \text{als } h' \ \text{obs } h'' \rrbracket$
 $\implies \text{heap-copies } a \ a' \ (\text{al } \# \ \text{als}) \ h \ (\text{ob } @ \ \text{obs}) \ h''$

inductive-cases *heap-copies-cases*:

heap-copies $a \ a' \ [] \ h \ \text{ops } h'$
heap-copies $a \ a' \ (\text{al}\#\text{als}) \ h \ \text{ops } h'$

Contrary to Sun's JVM 1.6.0_07, cloning an interrupted thread does not yield an interrupted thread, because the interrupt flag is not stored inside the thread object. Starting a clone of a started thread with Sun JVM 1.6.0_07 raises an illegal thread state exception, we just start another thread. The thread at <http://mail.openjdk.java.net/pipermail/core-libs-dev/2010-August/004715.html> discusses the general problem of thread cloning and argues against that. The bug report http://bugs.sun.com/bugdatabase/view_bug.do?bug_id=6968584 changes the Thread class implementation such that `Object.clone()` can no longer be accessed for Thread and subclasses in Java 7.

Array cells are never volatile themselves.

inductive *heap-clone* $:: 'm \ \text{prog} \Rightarrow 'heap \Rightarrow 'addr \Rightarrow 'heap \Rightarrow (('addr, 'thread-id) \ \text{obs-event list} \times 'addr) \ \text{option} \Rightarrow \text{bool}$

for $P :: 'm \ \text{prog}$ **and** $h :: 'heap$ **and** $a :: 'addr$

where

CloneFail:

$\llbracket \text{typeof-addr } h \ a = [hT]; \text{ allocate } h \ hT = \{\} \rrbracket$
 $\implies \text{heap-clone } P \ h \ a \ h \ \text{None}$

| *ObjClone*:

$\llbracket \text{typeof-addr } h \ a = [\text{Class-type } C]; (h', a') \in \text{allocate } h \ (\text{Class-type } C);$
 $P \vdash C \ \text{has-fields } \text{FDTs}; \text{ heap-copies } a \ a' \ (\text{map } (\lambda((F, D), \text{Tfm}). \text{CField } D \ F) \ \text{FDTs}) \ h' \ \text{obs } h'' \rrbracket$
 $\implies \text{heap-clone } P \ h \ a \ h'' \ [(\text{NewHeapElem } a' \ (\text{Class-type } C) \ \# \ \text{obs}, a')]$

| *ArrClone*:

$\llbracket \text{typeof-addr } h \ a = [\text{Array-type } T \ n]; (h', a') \in \text{allocate } h \ (\text{Array-type } T \ n); P \vdash \text{Object has-fields } \text{FDTs};$

$\text{heap-copies } a \ a' \ (\text{map } (\lambda((F, D), \text{Tfm}). \text{CField } D \ F) \ \text{FDTs} \ @ \ \text{map } \text{ACell } [0..<n]) \ h' \ \text{obs } h'' \rrbracket$
 $\implies \text{heap-clone } P \ h \ a \ h'' \ [(\text{NewHeapElem } a' \ (\text{Array-type } T \ n) \ \# \ \text{obs}, a')]$

inductive *red-external* $::$

$'m \ \text{prog} \Rightarrow 'thread-id \Rightarrow 'heap \Rightarrow 'addr \Rightarrow \text{mname} \Rightarrow 'addr \ \text{val list}$
 $\Rightarrow ('addr, 'thread-id, 'heap) \ \text{external-thread-action} \Rightarrow 'addr \ \text{extCallRet} \Rightarrow 'heap \Rightarrow \text{bool}$

and *red-external-syntax* $::$

$'m \ \text{prog} \Rightarrow 'thread-id \Rightarrow 'addr \Rightarrow \text{mname} \Rightarrow 'addr \ \text{val list} \Rightarrow 'heap$
 $\Rightarrow ('addr, 'thread-id, 'heap) \ \text{external-thread-action} \Rightarrow 'addr \ \text{extCallRet} \Rightarrow 'heap \Rightarrow \text{bool}$
 $(\langle -, - \vdash ((\dots'(-)'),/-) \rangle \dashrightarrow \text{ext } (\langle (-), /(-) \rangle) \rangle [50, 0, 0, 0, 0, 0, 0, 0] \ 51)$

for $P :: 'm \ \text{prog}$ **and** $t :: 'thread-id$ **and** $h :: 'heap$ **and** $a :: 'addr$

where

$P, t \vdash \langle a \cdot M(vs), h \rangle \dashrightarrow \text{ext } \langle va, h' \rangle \equiv \text{red-external } P \ t \ h \ a \ M \ vs \ ta \ va \ h'$

| *RedNewThread*:

$\llbracket \text{typeof-addr } h \ a = [\text{Class-type } C]; P \vdash C \preceq^* \text{Thread} \rrbracket$
 $\implies P, t \vdash \langle a \cdot \text{start}([], h) \rangle \dashrightarrow \text{ext } \langle \text{NewThread } (\text{addr}2\text{thread-id } a) \ (C, \text{run}, a) \ h, \text{ThreadStart } (\text{addr}2\text{thread-id } a) \rangle \dashrightarrow \text{ext } \langle \text{RetVal } \text{Unit}, h \rangle$

| *RedNewThreadFail*:

$\llbracket \text{typeof-addr } h \ a = [\text{Class-type } C]; P \vdash C \preceq^* \text{Thread} \rrbracket$
 $\implies P, t \vdash \langle a \cdot \text{start}([], h) \rangle \dashrightarrow \text{ext } \langle \text{ThreadExists } (\text{addr}2\text{thread-id } a) \ \text{True} \rangle \dashrightarrow \text{ext } \langle \text{RetEXC } \text{IllegalThreadState},$

$h\rangle$

| *RedJoin*:

$\llbracket \text{typeof-addr } h \ a = \lfloor \text{Class-type } C \rfloor; P \vdash C \preceq^* \text{Thread} \rrbracket$
 $\implies P, t \vdash \langle a.\text{join}(\square), h \rangle - \{ \text{Join}(\text{addr2thread-id } a), \text{IsInterrupted } t \ \text{False}, \text{ThreadJoin}(\text{addr2thread-id } a) \} \rightarrow \text{ext} \langle \text{RetVal } \text{Unit}, h \rangle$

| *RedJoinInterrupt*:

$\llbracket \text{typeof-addr } h \ a = \lfloor \text{Class-type } C \rfloor; P \vdash C \preceq^* \text{Thread} \rrbracket$
 $\implies P, t \vdash \langle a.\text{join}(\square), h \rangle - \{ \text{IsInterrupted } t \ \text{True}, \text{ClearInterrupt } t, \text{ObsInterrupted } t \} \rightarrow \text{ext} \langle \text{RetEXC } \text{InterruptedException}, h \rangle$

— Interruption should produce inter-thread actions (JLS 17.4.4) for the synchronizes-with order. They should synchronize with the inter-thread actions that determine whether a thread has been interrupted. Hence, interruption generates an *ObsInterrupt* action.

Although *WakeUp* causes the interrupted thread to raise an *InterruptedException* independent of the interrupt status, the interrupt flag must be set with *Interrupt* such that other threads observe the interrupted thread as interrupted while it competes for the monitor lock again.

Interrupting a thread which has not yet been started does not set the interrupt flag (tested with Sun HotSpot JVM 1.6.0_07).

| *RedInterrupt*:

$\llbracket \text{typeof-addr } h \ a = \lfloor \text{Class-type } C \rfloor; P \vdash C \preceq^* \text{Thread} \rrbracket$
 $\implies P, t \vdash \langle a.\text{interrupt}(\square), h \rangle$
 $- \{ \text{ThreadExists}(\text{addr2thread-id } a) \ \text{True}, \text{WakeUp}(\text{addr2thread-id } a),$
 $\text{Interrupt}(\text{addr2thread-id } a), \text{ObsInterrupt}(\text{addr2thread-id } a) \} \rightarrow \text{ext}$
 $\langle \text{RetVal } \text{Unit}, h \rangle$

| *RedInterruptInexist*:

$\llbracket \text{typeof-addr } h \ a = \lfloor \text{Class-type } C \rfloor; P \vdash C \preceq^* \text{Thread} \rrbracket$
 $\implies P, t \vdash \langle a.\text{interrupt}(\square), h \rangle$
 $- \{ \text{ThreadExists}(\text{addr2thread-id } a) \ \text{False} \} \rightarrow \text{ext}$
 $\langle \text{RetVal } \text{Unit}, h \rangle$

| *RedIsInterruptedTrue*:

$\llbracket \text{typeof-addr } h \ a = \lfloor \text{Class-type } C \rfloor; P \vdash C \preceq^* \text{Thread} \rrbracket$
 $\implies P, t \vdash \langle a.\text{isInterrupted}(\square), h \rangle - \{ \text{IsInterrupted}(\text{addr2thread-id } a) \ \text{True}, \text{ObsInterrupted}(\text{addr2thread-id } a) \} \rightarrow \text{ext}$
 $\langle \text{RetVal } (\text{Bool } \text{True}), h \rangle$

| *RedIsInterruptedFalse*:

$\llbracket \text{typeof-addr } h \ a = \lfloor \text{Class-type } C \rfloor; P \vdash C \preceq^* \text{Thread} \rrbracket$
 $\implies P, t \vdash \langle a.\text{isInterrupted}(\square), h \rangle - \{ \text{IsInterrupted}(\text{addr2thread-id } a) \ \text{False} \} \rightarrow \text{ext} \langle \text{RetVal } (\text{Bool } \text{False}), h \rangle$

— The JLS leaves unspecified whether *wait* first checks for the monitor state (whether the thread holds a lock on the monitor) or for the interrupt flag of the current thread. Sun Hotspot JVM 1.6.0_07 seems to check for the monitor state first, so we do it here, too.

| *RedWaitInterrupt*:

$P, t \vdash \langle a.\text{wait}(\square), h \rangle - \{ \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a, \text{IsInterrupted } t \ \text{True}, \text{ClearInterrupt } t, \text{ObsInterrupted } t \} \rightarrow \text{ext}$
 $\langle \text{RetEXC } \text{InterruptedException}, h \rangle$

| *RedWait*:

$P, t \vdash \langle a \cdot \text{wait}(\square), h \rangle - \{\text{Suspend } a, \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a, \text{ReleaseAcquire} \rightarrow a, \text{IsInterrupted } t \text{ False}, \text{SyncUnlock } a\} \rightarrow \text{ext} \langle \text{RetStaySame}, h \rangle$

| *RedWaitFail*:

$P, t \vdash \langle a \cdot \text{wait}(\square), h \rangle - \{\text{UnlockFail} \rightarrow a\} \rightarrow \text{ext} \langle \text{RetEXC IllegalMonitorState}, h \rangle$

| *RedWaitNotified*:

$P, t \vdash \langle a \cdot \text{wait}(\square), h \rangle - \{\text{Notified}\} \rightarrow \text{ext} \langle \text{RetVal Unit}, h \rangle$

— This rule does NOT check that the interrupted flag is set, but still clears it. The semantics will be that only the executing thread clears its interrupt.

| *RedWaitInterrupted*:

$P, t \vdash \langle a \cdot \text{wait}(\square), h \rangle - \{\text{WokenUp}, \text{ClearInterrupt } t, \text{ObsInterrupted } t\} \rightarrow \text{ext} \langle \text{RetEXC InterruptedException}, h \rangle$

— Calls to wait may decide to immediately wake up spuriously. This is indistinguishable from waking up spuriously any time before being notified or interrupted. Spurious wakeups are configured by the *spurious-wakeup* parameter of the *heap-base* locale.

| *RedWaitSpurious*:

$\text{spurious-wakeups} \implies$
 $P, t \vdash \langle a \cdot \text{wait}(\square), h \rangle - \{\text{Unlock} \rightarrow a, \text{Lock} \rightarrow a, \text{ReleaseAcquire} \rightarrow a, \text{IsInterrupted } t \text{ False}, \text{SyncUnlock } a\} \rightarrow \text{ext} \langle \text{RetVal Unit}, h \rangle$

— *notify* and *notifyAll* do not perform synchronization inter-thread actions because they only tests whether the thread holds a lock, but do not change the lock state.

| *RedNotify*:

$P, t \vdash \langle a \cdot \text{notify}(\square), h \rangle - \{\text{Notify } a, \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a\} \rightarrow \text{ext} \langle \text{RetVal Unit}, h \rangle$

| *RedNotifyFail*:

$P, t \vdash \langle a \cdot \text{notify}(\square), h \rangle - \{\text{UnlockFail} \rightarrow a\} \rightarrow \text{ext} \langle \text{RetEXC IllegalMonitorState}, h \rangle$

| *RedNotifyAll*:

$P, t \vdash \langle a \cdot \text{notifyAll}(\square), h \rangle - \{\text{NotifyAll } a, \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a\} \rightarrow \text{ext} \langle \text{RetVal Unit}, h \rangle$

| *RedNotifyAllFail*:

$P, t \vdash \langle a \cdot \text{notifyAll}(\square), h \rangle - \{\text{UnlockFail} \rightarrow a\} \rightarrow \text{ext} \langle \text{RetEXC IllegalMonitorState}, h \rangle$

| *RedClone*:

$\text{heap-clone } P \ h \ a \ h' \ [(obs, a')]$
 $\implies P, t \vdash \langle a \cdot \text{clone}(\square), h \rangle - (K\$ \square, \square, \square, \square, \square, obs) \rightarrow \text{ext} \langle \text{RetVal (Addr } a'), h' \rangle$

| *RedCloneFail*:

$\text{heap-clone } P \ h \ a \ h' \ \text{None} \implies P, t \vdash \langle a \cdot \text{clone}(\square), h \rangle - \varepsilon \rightarrow \text{ext} \langle \text{RetEXC OutOfMemory}, h' \rangle$

| *RedHashcode*:

$P, t \vdash \langle a \cdot \text{hashcode}(\square), h \rangle - \{\} \rightarrow \text{ext} \langle \text{RetVal (Intg (word-of-int (hash-addr } a))}, h \rangle$

| *RedPrint*:

$P, t \vdash \langle a \cdot \text{print}(vs), h \rangle - \{\text{ExternalCall } a \ \text{print } vs \ \text{Unit}\} \rightarrow \text{ext} \langle \text{RetVal Unit}, h \rangle$

| *RedCurrentThread*:

$P, t \vdash \langle a \cdot \text{currentThread}(\square), h \rangle - \{\!\{ \} \!\} \rightarrow \text{ext} \langle \text{RetVal} (\text{Addr} (\text{thread-id2addr } t)), h \rangle$

| *RedInterruptedTrue:*

$P, t \vdash \langle a \cdot \text{interrupted}(\square), h \rangle - \{\!\{ \text{IsInterrupted } t \text{ True}, \text{ClearInterrupt } t, \text{ObsInterrupted } t \}\!\} \rightarrow \text{ext} \langle \text{RetVal} (\text{Bool True}), h \rangle$

| *RedInterruptedFalse:*

$P, t \vdash \langle a \cdot \text{interrupted}(\square), h \rangle - \{\!\{ \text{IsInterrupted } t \text{ False} \}\!\} \rightarrow \text{ext} \langle \text{RetVal} (\text{Bool False}), h \rangle$

| *RedYield:*

$P, t \vdash \langle a \cdot \text{yield}(\square), h \rangle - \{\!\{ \text{Yield} \}\!\} \rightarrow \text{ext} \langle \text{RetVal Unit}, h \rangle$

3.11.3 Aggressive formulation for external calls

definition *red-external-aggr* ::

$'m \text{ prog} \Rightarrow 't \text{ thread-id} \Rightarrow 'a \text{ addr} \Rightarrow m \text{ name} \Rightarrow 'a \text{ addr val list} \Rightarrow 'h \text{ heap} \Rightarrow$
 $(('a \text{ addr}, 't \text{ thread-id}, 'h \text{ heap}) \text{ external-thread-action} \times 'a \text{ addr extCallRet} \times 'h \text{ heap}) \text{ set}$

where

$\text{red-external-aggr } P \ t \ a \ M \ \text{vs } h =$

(if $M = \text{wait}$ then

let $ad-t = \text{thread-id2addr } t$

in $\{\!\{ \{\!\{ \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a, \text{IsInterrupted } t \text{ True}, \text{ClearInterrupt } t, \text{ObsInterrupted } t \}\!\}, \text{RetEXC InterruptedException}, h \},$

$\{\!\{ \{\!\{ \text{Suspend } a, \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a, \text{ReleaseAcquire} \rightarrow a, \text{IsInterrupted } t \text{ False}, \text{SyncUnlock } a \}\!\},$
 $\text{RetStaySame}, h \},$

$\{\!\{ \{\!\{ \text{UnlockFail} \rightarrow a \}\!\}, \text{RetEXC IllegalMonitorState}, h \},$

$\{\!\{ \{\!\{ \text{Notified} \}\!\}, \text{RetVal Unit}, h \},$

$\{\!\{ \{\!\{ \text{WokenUp}, \text{ClearInterrupt } t, \text{ObsInterrupted } t \}\!\}, \text{RetEXC InterruptedException}, h \}\!\} \cup$

$\{\!\{ \text{if spurious-wakeups then } \{\!\{ \{\!\{ \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a, \text{ReleaseAcquire} \rightarrow a, \text{IsInterrupted } t \text{ False},$
 $\text{SyncUnlock } a \}\!\}, \text{RetVal Unit}, h \}\!\} \text{ else } \{\!\} \}$

else if $M = \text{notify}$ then $\{\!\{ \{\!\{ \{\!\{ \text{Notify } a, \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a \}\!\}, \text{RetVal Unit}, h \},$

$\{\!\{ \{\!\{ \text{UnlockFail} \rightarrow a \}\!\}, \text{RetEXC IllegalMonitorState}, h \}\!\}$

else if $M = \text{notifyAll}$ then $\{\!\{ \{\!\{ \{\!\{ \text{NotifyAll } a, \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a \}\!\}, \text{RetVal Unit}, h \},$

$\{\!\{ \{\!\{ \text{UnlockFail} \rightarrow a \}\!\}, \text{RetEXC IllegalMonitorState}, h \}\!\}$

else if $M = \text{clone}$ then

$\{\!\{ \{\!\{ \{\!\{ (K\$ \square, \square, \square, \square, \square, \text{obs}), \text{RetVal} (\text{Addr } a'), h' \}\!\} | \text{obs } a' \ h'. \text{heap-clone } P \ h \ a \ h' \ [(obs, a')]\!\}\!\}$

$\cup \{\!\{ \{\!\{ \{\!\{ \square, \text{RetEXC OutOfMemory}, h' \}\!\} | h'. \text{heap-clone } P \ h \ a \ h' \ \text{None} \}\!\}\!\}$

else if $M = \text{hashcode}$ then $\{\!\{ \{\!\{ \{\!\{ \square, \text{RetVal} (\text{Intg} (\text{word-of-int} (\text{hash-addr } a))), h \}\!\}\!\}\!\}$

else if $M = \text{print}$ then $\{\!\{ \{\!\{ \{\!\{ \square, \text{RetVal Unit}, h \}\!\}\!\}\!\}$

else if $M = \text{currentThread}$ then $\{\!\{ \{\!\{ \{\!\{ \square, \text{RetVal} (\text{Addr} (\text{thread-id2addr } t)), h \}\!\}\!\}\!\}$

else if $M = \text{interrupted}$ then $\{\!\{ \{\!\{ \{\!\{ \{\!\{ \text{IsInterrupted } t \text{ True}, \text{ClearInterrupt } t, \text{ObsInterrupted } t \}\!\}, \text{RetVal} (\text{Bool True}), h \},$

$\{\!\{ \{\!\{ \{\!\{ \{\!\{ \text{IsInterrupted } t \text{ False} \}\!\}, \text{RetVal} (\text{Bool False}), h \}\!\}\!\}\!\}$

else if $M = \text{yield}$ then $\{\!\{ \{\!\{ \{\!\{ \{\!\{ \text{Yield} \}\!\}, \text{RetVal Unit}, h \}\!\}\!\}\!\}$

else

let $hT = \text{the} (\text{typeof-addr } h \ a)$

in if $P \vdash \text{ty-of-hType } hT \leq \text{Class Thread}$ then

let $t-a = \text{addr2thread-id } a$

in if $M = \text{start}$ then

$\{\!\{ \{\!\{ \{\!\{ \{\!\{ \text{NewThread } t-a (\text{the-Class} (\text{ty-of-hType } hT), \text{run}, a) \ h, \text{ThreadStart } t-a \}\!\}, \text{RetVal Unit}, h \},$

$\{\!\{ \{\!\{ \{\!\{ \{\!\{ \text{ThreadExists } t-a \text{ True} \}\!\}, \text{RetEXC IllegalThreadState}, h \}\!\}\!\}\!\}$

else if $M = \text{join}$ then

$\{\!\{ \{\!\{ \{\!\{ \{\!\{ \text{Join } t-a, \text{IsInterrupted } t \text{ False}, \text{ThreadJoin } t-a \}\!\}, \text{RetVal Unit}, h \},$

$\{ \{ \text{IsInterrupted } t \text{ True, ClearInterrupt } t, \text{ObsInterrupted } t \}, \text{RetEXC InterruptedException, } h \} \}$
 else if $M = \text{interrupt}$ then
 $\{ \{ \{ \text{ThreadExists } t\text{-a True, WakeUp } t\text{-a, Interrupt } t\text{-a, ObsInterrupt } t\text{-a} \}, \text{RetVal Unit, } h \}, \{ \{ \text{ThreadExists } t\text{-a False} \}, \text{RetVal Unit, } h \} \}$
 else if $M = \text{isInterrupted}$ then
 $\{ \{ \{ \text{IsInterrupted } t\text{-a False} \}, \text{RetVal (Bool False), } h \}, \{ \{ \text{IsInterrupted } t\text{-a True, ObsInterrupted } t\text{-a} \}, \text{RetVal (Bool True), } h \} \}$
 else $\{ \{ \}, \text{undefined} \}$
 else $\{ \{ \}, \text{undefined} \}$

lemma *red-external-imp-red-external-aggr*:

$P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle \implies (ta, va, h') \in \text{red-external-aggr } P \ t \ a \ M \ vs \ h$

unfolding *red-external-aggr-def*

by(*auto elim!*: *red-external.cases split del: if-split simp add: split-beta*)

end

context *heap begin*

lemma *hex-heap-copy-loc*:

$\text{heap-copy-loc } a \ a' \ \text{al } h \ \text{obs } h' \implies h \trianglelefteq h'$

by(*blast elim: heap-copy-loc.cases dest: hex-heap-ops*)

lemma *hex-heap-copies*:

assumes *heap-copies* $a \ a' \ \text{als } h \ \text{obs } h'$

shows $h \trianglelefteq h'$

using *assms* **by** *induct(blast intro: hex-heap-copy-loc hex-trans)+*

lemma *hex-heap-clone*:

assumes *heap-clone* $P \ h \ a \ h' \ \text{res}$

shows $h \trianglelefteq h'$

using *assms* **by**(*blast elim: heap-clone.cases dest: hex-heap-ops hex-heap-copies intro: hex-trans*)

theorem *red-external-hext*:

assumes $P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle$

shows *hext* $h \ h'$

using *assms*

by(*cases*)(*blast intro: hex-heap-ops hex-heap-clone*)+

lemma *red-external-preserves-tconf*:

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle; P, h \vdash t' \sqrt{t} \rrbracket \implies P, h' \vdash t' \sqrt{t}$

by(*drule red-external-hext*)(*rule tconf-hext-mono*)

end

context *heap-conf begin*

lemma *typeof-addr-heap-clone*:

assumes *heap-clone* $P \ h \ a \ h' \ \llbracket (\text{obs}, a') \rrbracket$

and *hconf* h

shows *typeof-addr* $h' \ a' = \text{typeof-addr } h \ a$

using *assms*

by *cases* (*auto dest!: allocate-SomeD hex-heap-copies dest: typeof-addr-hext-mono typeof-addr-is-type*)

is-type-ArrayD)

end

context heap-base begin

lemma *red-ext-new-thread-heap*:

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle; NewThread t' \text{ ex } h'' \in set \{ta\}_t \rrbracket \implies h'' = h'$
by(*auto elim: red-external.cases simp add: ta-upd-simps*)

lemma *red-ext-aggr-new-thread-heap*:

$\llbracket (ta, va, h') \in red-external-aggr P t a M vs h; NewThread t' \text{ ex } h'' \in set \{ta\}_t \rrbracket \implies h'' = h'$
by(*auto simp add: red-external-aggr-def is-native.simps split-beta ta-upd-simps split: if-split-asm*)

end

context addr-conv begin

lemma *red-external-new-thread-exists-thread-object*:

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle; NewThread t' \text{ x } h'' \in set \{ta\}_t \rrbracket$
 $\implies \exists C. \text{typeof-addr } h' (\text{thread-id2addr } t') = \lfloor \text{Class-type } C \rfloor \wedge P \vdash C \preceq^* Thread$
by(*auto elim!: red-external.cases dest!: Array-widen simp add: ta-upd-simps*)

lemma *red-external-aggr-new-thread-exists-thread-object*:

$\llbracket (ta, va, h') \in red-external-aggr P t a M vs h; \text{typeof-addr } h a \neq None; NewThread t' \text{ x } h'' \in set \{ta\}_t \rrbracket$
 $\implies \exists C. \text{typeof-addr } h' (\text{thread-id2addr } t') = \lfloor \text{Class-type } C \rfloor \wedge P \vdash C \preceq^* Thread$
by(*auto simp add: red-external-aggr-def is-native.simps split-beta ta-upd-simps widen-Class split: if-split-asm dest!: Array-widen*)

end

context heap begin

lemma *red-external-aggr-hext*:

$\llbracket (ta, va, h') \in red-external-aggr P t a M vs h; is-native P (\text{the } (\text{typeof-addr } h a)) M \rrbracket \implies h \trianglelefteq h'$
apply(*auto simp add: red-external-aggr-def split-beta is-native.simps elim!: external-WT-defs-cases hext-heap-clone split: if-split-asm*)
apply(*auto elim!: external-WT-defs.cases dest!: sees-method-decl-above intro: widen-trans simp add: class-type-of-eq split: htype.split-asm*)
done

lemma *red-external-aggr-preserves-tconf*:

$\llbracket (ta, va, h') \in red-external-aggr P t a M vs h; is-native P (\text{the } (\text{typeof-addr } h a)) M; P, h \vdash t' \surd t \rrbracket$
 $\implies P, h' \vdash t' \surd t$
by(*blast dest: red-external-aggr-hext intro: tconf-hext-mono*)

end

context heap-base begin

lemma *red-external-Wakeup-no-Join-no-Lock-no-Interrupt*:

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle; Notified \in set \{ta\}_w \vee WokenUp \in set \{ta\}_w \rrbracket \implies$
 $collect-locks \{ta\}_l = \{\} \wedge collect-cond-actions \{ta\}_c = \{\} \wedge collect-interrupts \{ta\}_i = \{\}$

by(*auto elim!*: *red-external.cases simp add: ta-upd-simps collect-locks-def collect-interrupts-def*)

lemma *red-external-aggr-Wakeup-no-Join*:

$\llbracket (ta, va, h') \in \text{red-external-aggr } P \ t \ a \ M \ \text{vs } h;$
 $\text{Notified} \in \text{set } \{ta\}_w \vee \text{WokenUp} \in \text{set } \{ta\}_w \rrbracket$
 $\implies \text{collect-locks } \{ta\}_l = \{\} \wedge \text{collect-cond-actions } \{ta\}_c = \{\} \wedge \text{collect-interrupts } \{ta\}_i = \{\}$

by(*auto simp add: red-external-aggr-def split-beta ta-upd-simps collect-locks-def collect-interrupts-def split: if-split-asm*)

lemma *red-external-Suspend-StaySame*:

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow \text{ext } \langle va, h' \rangle; \text{Suspend } w \in \text{set } \{ta\}_w \rrbracket \implies va = \text{RetStaySame}$

by(*auto elim!*: *red-external.cases simp add: ta-upd-simps*)

lemma *red-external-aggr-Suspend-StaySame*:

$\llbracket (ta, va, h') \in \text{red-external-aggr } P \ t \ a \ M \ \text{vs } h; \text{Suspend } w \in \text{set } \{ta\}_w \rrbracket \implies va = \text{RetStaySame}$

by(*auto simp add: red-external-aggr-def split-beta ta-upd-simps split: if-split-asm*)

lemma *red-external-Suspend-waitD*:

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow \text{ext } \langle va, h' \rangle; \text{Suspend } w \in \text{set } \{ta\}_w \rrbracket \implies M = \text{wait}$

by(*auto elim!*: *red-external.cases simp add: ta-upd-simps*)

lemma *red-external-aggr-Suspend-waitD*:

$\llbracket (ta, va, h') \in \text{red-external-aggr } P \ t \ a \ M \ \text{vs } h; \text{Suspend } w \in \text{set } \{ta\}_w \rrbracket \implies M = \text{wait}$

by(*auto simp add: red-external-aggr-def split-beta ta-upd-simps split: if-split-asm*)

lemma *red-external-new-thread-sub-thread*:

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow \text{ext } \langle va, h' \rangle; \text{NewThread } t' \ (C, M', a') \ h'' \in \text{set } \{ta\}_t \rrbracket$
 $\implies \text{typeof-addr } h' \ a' = \lfloor \text{Class-type } C \rfloor \wedge P \vdash C \preceq^* \text{Thread} \wedge M' = \text{run}$

by(*auto elim!*: *red-external.cases simp add: widen-Class ta-upd-simps*)

lemma *red-external-aggr-new-thread-sub-thread*:

$\llbracket (ta, va, h') \in \text{red-external-aggr } P \ t \ a \ M \ \text{vs } h; \text{typeof-addr } h \ a \neq \text{None};$

$\text{NewThread } t' \ (C, M', a') \ h'' \in \text{set } \{ta\}_t \rrbracket$

$\implies \text{typeof-addr } h' \ a' = \lfloor \text{Class-type } C \rfloor \wedge P \vdash C \preceq^* \text{Thread} \wedge M' = \text{run}$

by(*auto simp add: red-external-aggr-def split-beta ta-upd-simps widen-Class split: if-split-asm dest!: Array-widen*)

lemma *heap-copy-loc-length*:

assumes *heap-copy-loc* *a a' al h obs h'*

shows *length obs = 2*

using *assms* **by**(*cases*) *simp*

lemma *heap-copies-length*:

assumes *heap-copies* *a a' als h obs h'*

shows *length obs = 2 * length als*

using *assms* **by**(*induct*)(*auto dest!: heap-copy-loc-length*)

end

3.11.4 τ -moves

inductive τ *external-defs* :: *cname* \Rightarrow *mname* \Rightarrow *bool*

where

$\tau external-defs$ Object hashCode
 | $\tau external-defs$ Object currentThread

definition $\tau external :: 'm prog \Rightarrow htype \Rightarrow mname \Rightarrow bool$
where $\tau external P hT M \longleftrightarrow (\exists Ts Tr D. P \vdash class-type-of hT sees M:Ts \rightarrow Tr = Native \text{ in } D \wedge \tau external-defs D M)$

context heap-base **begin**

definition $\tau external' :: 'm prog \Rightarrow 'heap \Rightarrow 'addr \Rightarrow mname \Rightarrow bool$
where $\tau external' P h a M \longleftrightarrow (\exists hT. typeof-addr h a = Some hT \wedge \tau external P hT M)$

lemma $\tau external'-red-external-heap-unchanged:$

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle; \tau external' P h a M \rrbracket \Longrightarrow h' = h$
by(auto elim!: red-external.cases $\tau external-defs.cases$ simp add: $\tau external-def \tau external'-def$)

lemma $\tau external'-red-external-aggr-heap-unchanged:$

$\llbracket (ta, va, h') \in red-external-aggr P t a M vs h; \tau external' P h a M \rrbracket \Longrightarrow h' = h$
by(auto elim!: $\tau external-defs.cases$ simp add: $\tau external-def \tau external'-def red-external-aggr-def$)

lemma $\tau external'-red-external-TA-empty:$

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle; \tau external' P h a M \rrbracket \Longrightarrow ta = \varepsilon$
by(auto elim!: red-external.cases $\tau external-defs.cases$ simp add: $\tau external-def \tau external'-def$)

lemma $\tau external'-red-external-aggr-TA-empty:$

$\llbracket (ta, va, h') \in red-external-aggr P t a M vs h; \tau external' P h a M \rrbracket \Longrightarrow ta = \varepsilon$
by(auto elim!: $\tau external-defs.cases$ simp add: $\tau external-def \tau external'-def red-external-aggr-def$)

lemma $red-external-new-thread-addr-conf:$

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle; NewThread t (C, M, a') h'' \in set \{ta\}_t \rrbracket$
 $\Longrightarrow P, h' \vdash Addr a \leq Class Thread$
by(auto elim!: red-external.cases simp add: conf-def ta-upd-simps)

lemma $\tau external-red-external-aggr-heap-unchanged:$

$\llbracket (ta, va, h') \in red-external-aggr P t a M vs h; \tau external P (the (typeof-addr h a)) M \rrbracket \Longrightarrow h' = h$
by(auto elim!: $\tau external-defs.cases$ simp add: $\tau external-def red-external-aggr-def$)

lemma $\tau external-red-external-aggr-TA-empty:$

$\llbracket (ta, va, h') \in red-external-aggr P t a M vs h; \tau external P (the (typeof-addr h a)) M \rrbracket \Longrightarrow ta = \varepsilon$
by(auto elim!: $\tau external-defs.cases$ simp add: $\tau external-def red-external-aggr-def$)

end

3.11.5 Code generation

code-pred

(modes:

$i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow bool,$
 $i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow bool,$
 $i \Rightarrow i \Rightarrow o \Rightarrow o \Rightarrow bool,$
 $o \Rightarrow i \Rightarrow o \Rightarrow o \Rightarrow bool)$

external-WT-defs

.

code-pred

(modes: $i \Rightarrow i \Rightarrow i \Rightarrow \text{bool}$)
 [inductify, skip-proof]
 is-native

.

declare *heap-base.heap-copy-loc.intros* [code-pred-intro]

code-pred

(modes: $(i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow (i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow o \Rightarrow \text{bool}$)

heap-base.heap-copy-loc

proof –

case *heap-copy-loc*

from *heap-copy-loc.premis* **show** *thesis*

by(rule *heap-base.heap-copy-loc.cases*)(rule *heap-copy-loc.that*[OF refl refl refl refl refl refl])

qed

declare *heap-base.heap-copies.intros* [code-pred-intro]

code-pred

(modes: $(i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow (i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow o \Rightarrow \text{bool}$)

heap-base.heap-copies

proof –

case *heap-copies*

from *heap-copies.premis* **show** *thesis*

by(rule *heap-base.heap-copies.cases*)(erule (3) *heap-copies.that*[OF refl refl refl refl]|*assumption*)+

qed

declare *heap-base.heap-clone.intros* [folded Predicate-Compile.contains-def, code-pred-intro]

code-pred

(modes: $i \Rightarrow i \Rightarrow (i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow (i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow o \Rightarrow \text{bool}$)

heap-base.heap-clone

proof –

case *heap-clone*

from *heap-clone.premis* **show** *thesis*

by(rule *heap-base.heap-clone.cases*[folded Predicate-Compile.contains-def])(erule (3) *heap-clone.that*[OF refl refl refl refl refl refl]|*assumption*)+

qed

code_pred in Isabelle2012 cannot handle boolean parameters as premises properly, so this replacement rule explicitly tests for *True*

lemma (in *heap-base*) *RedWaitSpurious-Code*:

spurious-wakeups = *True* \implies

$P, t \vdash \langle a \cdot \text{wait}([], h) \rangle - \{ \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a, \text{ReleaseAcquire} \rightarrow a, \text{IsInterrupted } t \text{ False}, \text{SyncUnlock } a \} \rightarrow \text{ext} \langle \text{RetVal } \text{Unit}, h \rangle$

by(rule *RedWaitSpurious*) *simp*

lemmas [code-pred-intro] =

heap-base.RedNewThread *heap-base.RedNewThreadFail*

heap-base.RedJoin *heap-base.RedJoinInterrupt*

```

heap-base.RedInterrupt heap-base.RedInterruptInexist heap-base.RedIsInterruptedTrue heap-base.RedIsInterruptedFalse
heap-base.RedWaitInterrupt heap-base.RedWait heap-base.RedWaitFail heap-base.RedWaitNotified heap-base.RedWaitInterrupt
declare heap-base.RedWaitSpurious-Code [code-pred-intro RedWaitSpurious]
lemmas [code-pred-intro] =
  heap-base.RedNotify heap-base.RedNotifyFail heap-base.RedNotifyAll heap-base.RedNotifyAllFail
  heap-base.RedClone heap-base.RedCloneFail
  heap-base.RedHashcode heap-base.RedPrint heap-base.RedCurrentThread
  heap-base.RedInterruptedTrue heap-base.RedInterruptedFalse
  heap-base.RedYield

code-pred
  (modes:  $i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow (i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow (i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow i$ 
 $\Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow o \Rightarrow o \Rightarrow \text{bool}$ )
  heap-base.red-external
proof –
  case red-external
  from red-external.premis show ?thesis
  apply(rule heap-base.red-external.cases)
  apply(erule (4) red-external.that[OF refl refl refl refl refl refl refl refl refl refl refl refl])|assumption|erule
  eqTrueI)+
  done
qed

end

```

3.12 Generic Well-formedness of programs

```

theory WellForm
imports
  SystemClasses
  ExternalCall
begin

```

This theory defines global well-formedness conditions for programs but does not look inside method bodies. Hence it works for both Jinja and JVM programs. Well-typing of expressions is defined elsewhere (in theory *WellType*).

Because JinjaThreads does not have method overloading, its policy for method overriding is the classical one: *covariant in the result type but contravariant in the argument types*. This means the result type of the overriding method becomes more specific, the argument types become more general.

```

type-synonym 'm wf-mdecl-test = 'm prog  $\Rightarrow$  cname  $\Rightarrow$  'm mdecl  $\Rightarrow$  bool

```

```

definition wf-fdecl :: 'm prog  $\Rightarrow$  fdecl  $\Rightarrow$  bool
where wf-fdecl P  $\equiv$   $\lambda(F, T, fm).$  is-type P T

```

```

definition wf-mdecl :: 'm wf-mdecl-test  $\Rightarrow$  'm prog  $\Rightarrow$  cname  $\Rightarrow$  'm mdecl'  $\Rightarrow$  bool where
  wf-mdecl wf-md P C  $\equiv$ 
   $\lambda(M, Ts, T, m).$  ( $\forall T \in \text{set } Ts.$  is-type P T)  $\wedge$  is-type P T  $\wedge$ 
  (case m of Native  $\Rightarrow$  C·M(Ts) :: T | [mb]  $\Rightarrow$  wf-md P C (M, Ts, T, mb))

```

```

fun wf-overriding :: 'm prog  $\Rightarrow$  cname  $\Rightarrow$  'm mdecl'  $\Rightarrow$  bool
where
  wf-overriding P D (M, Ts, T, m) =

```

$$(\forall D' Ts' T' m'. P \vdash D \text{ sees } M:Ts' \rightarrow T' = m' \text{ in } D' \longrightarrow P \vdash Ts' [\leq] Ts \wedge P \vdash T \leq T')$$

definition *wf-cdecl* :: 'm wf-mdecl-test \Rightarrow 'm prog \Rightarrow 'm cdecl \Rightarrow bool

where

$$\begin{aligned} \text{wf-cdecl wf-md } P &\equiv \lambda(C,(D,fs,ms)). \\ &(\forall f \in \text{set } fs. \text{wf-fdecl } P f) \wedge \text{distinct-fst } fs \wedge \\ &(\forall m \in \text{set } ms. \text{wf-mdecl wf-md } P C m) \wedge \\ &\text{distinct-fst } ms \wedge \\ &(C \neq \text{Object} \longrightarrow \\ &\text{is-class } P D \wedge \neg P \vdash D \preceq^* C \wedge \\ &(\forall m \in \text{set } ms. \text{wf-overriding } P D m)) \wedge \\ &(C = \text{Thread} \longrightarrow (\exists m. (\text{run}, [], \text{Void}, m) \in \text{set } ms)) \end{aligned}$$

definition *wf-prog* :: 'm wf-mdecl-test \Rightarrow 'm prog \Rightarrow bool

where

$$\text{wf-prog wf-md } P \longleftarrow \text{wf-syscls } P \wedge \text{distinct-fst } (\text{classes } P) \wedge (\forall c \in \text{set } (\text{classes } P). \text{wf-cdecl wf-md } P c)$$

lemma *wf-prog-def2*:

$$\text{wf-prog wf-md } P \longleftarrow \text{wf-syscls } P \wedge (\forall C \text{ rest. class } P C = [\text{rest}] \longrightarrow \text{wf-cdecl wf-md } P (C, \text{rest}))$$

$\wedge \text{distinct-fst } (\text{classes } P)$

by(cases P)(auto simp add: wf-prog-def dest: map-of-SomeD map-of-SomeI)

3.12.1 Well-formedness lemmas

lemma *wf-prog-wf-syscls*: wf-prog wf-md P \Longrightarrow wf-syscls P

by(simp add: wf-prog-def)

lemma *class-wf*:

$$\llbracket \text{class } P C = \text{Some } c; \text{wf-prog wf-md } P \rrbracket \Longrightarrow \text{wf-cdecl wf-md } P (C, c)$$

by (cases P) (fastforce dest: map-of-SomeD simp add: wf-prog-def)

lemma [simp]:

assumes wf-prog wf-md P

shows class-Object: $\exists C fs ms. \text{class } P \text{ Object} = \text{Some } (C, fs, ms)$

and class-Thread: $\exists C fs ms. \text{class } P \text{ Thread} = \text{Some } (C, fs, ms)$

using wf-prog-wf-syscls[OF assms]

by(rule wf-syscls-class-Object wf-syscls-class-Thread)+

lemma [simp]:

assumes wf-prog wf-md P

shows is-class-Object: is-class P Object

and is-class-Thread: is-class P Thread

using wf-prog-wf-syscls[OF assms] **by** simp-all

lemma *xcpt-subcls-Throwable*:

$$\llbracket C \in \text{sys-xcpts}; \text{wf-prog wf-md } P \rrbracket \Longrightarrow P \vdash C \preceq^* \text{Throwable}$$

by(simp add: wf-prog-wf-syscls wf-syscls-xcpt-subcls-Throwable)

lemma *is-class-Throwable*:

$$\text{wf-prog wf-md } P \Longrightarrow \text{is-class } P \text{ Throwable}$$

by(rule wf-prog-wf-syscls wf-syscls-is-class-Throwable)+

lemma *is-class-sub-Throwable*:

$\llbracket \text{wf-prog wf-md } P; P \vdash C \preceq^* \text{Throwable} \rrbracket \implies \text{is-class } P \ C$
by(rule wf-syscls-is-class-sub-Throwable[OF wf-prog-wf-syscls])

lemma is-class-xcpt:

$\llbracket C \in \text{sys-xcpts}; \text{wf-prog wf-md } P \rrbracket \implies \text{is-class } P \ C$
by(rule wf-syscls-is-class-xcpt[OF - wf-prog-wf-syscls])

context heap-base begin

lemma wf-preallocatedE:

assumes wf-prog wf-md P

and preallocated h

and $C \in \text{sys-xcpts}$

obtains typeof-addr h (addr-of-sys-xcpt C) = $\lfloor \text{Class-type } C \rfloor P \vdash C \preceq^* \text{Throwable}$

proof –

from $\langle \text{preallocated } h \rangle \langle C \in \text{sys-xcpts} \rangle$ **have** typeof-addr h (addr-of-sys-xcpt C) = $\lfloor \text{Class-type } C \rfloor$

by(rule typeof-addr-sys-xcp)

moreover from $\langle C \in \text{sys-xcpts} \rangle \langle \text{wf-prog wf-md } P \rangle$ **have** $P \vdash C \preceq^* \text{Throwable}$ **by**(rule xcpt-subcls-Throwable)

ultimately show thesis by(rule that)

qed

lemma wf-preallocatedD:

assumes wf-prog wf-md P

and preallocated h

and $C \in \text{sys-xcpts}$

shows typeof-addr h (addr-of-sys-xcpt C) = $\lfloor \text{Class-type } C \rfloor \wedge P \vdash C \preceq^* \text{Throwable}$

using *assms*

by(rule wf-preallocatedE) *blast*

end

lemma (in heap-conf) hconf-start-heap:

$\text{wf-prog wf-md } P \implies \text{hconf start-heap}$

unfolding start-heap-def start-heap-data-def initialization-list-def sys-xcpts-list-def

using *hconf-empty*

by –(*simp add: create-initial-object-simps del: hconf-empty, clarsimp split: prod.split elim!: not-empty-pairE simp del: hconf-empty, drule (1) allocate-Eps, drule (1) hconf-allocate-mono, simp add: is-class-xcpt*)+

lemma subcls1-wfD:

$\llbracket P \vdash C \prec^1 D; \text{wf-prog wf-md } P \rrbracket \implies D \neq C \wedge \neg (\text{subcls1 } P)^{++} D \ C$

apply(*frule tranclp.r-into-trancl*[**where** $r=\text{subcls1 } P$])

apply(*drule subcls1D*)

apply(*clarify*)

apply(*drule (1) class-wf*)

apply(*unfold wf-cdecl-def*)

apply(*rule conjI*)

apply(*force*)

apply(*unfold reflclp-tranclp*[*symmetric, where* $r=\text{subcls1 } P$])

apply(*blast*)

done

lemma wf-cdecl-supD:

$\llbracket \text{wf-cdecl wf-md } P (C,D,r); C \neq \text{Object} \rrbracket \implies \text{is-class } P \ D$

lemma subcls-asym:

$\llbracket \text{wf-prog wf-md } P; (\text{subcls1 } P)^{++} C D \rrbracket \Longrightarrow \neg (\text{subcls1 } P)^{++} D C$

lemma *subcls-irrefl*:

$\llbracket \text{wf-prog wf-md } P; (\text{subcls1 } P)^{++} C D \rrbracket \Longrightarrow C \neq D$

lemma *acyclicP-def*:

$\text{acyclicP } r \longleftrightarrow (\forall x. \neg r^{\hat{++}} x x)$

unfolding *acyclic-def trancl-def*

by(*auto*)

lemma *acyclic-subcls1*:

$\text{wf-prog wf-md } P \Longrightarrow \text{acyclicP } (\text{subcls1 } P)$

by(*unfold acyclicP-def*)(*fast dest: subcls-irrefl*)

lemma *finite-conversep*: $\text{finite } \{(x, y). r^{-1-1} x y\} = \text{finite } \{(x, y). r x y\}$

by(*subst finite-converse[unfolded converse-unfold, symmetric]*) *simp*

lemma *acyclicP-wf-subcls1*:

$\text{acyclicP } (\text{subcls1 } P) \Longrightarrow \text{wfP } ((\text{subcls1 } P)^{-1-1})$

unfolding *wfp-def*

by(*rule finite-acyclic-wf*)(*simp-all only: finite-conversep finite-subcls1 acyclicP-converse*)

lemma *wf-subcls1*:

$\text{wf-prog wf-md } P \Longrightarrow \text{wfP } ((\text{subcls1 } P)^{-1-1})$

by(*rule acyclicP-wf-subcls1*)(*rule acyclic-subcls1*)

lemma *single-valued-subcls1*:

$\text{wf-prog wf-md } G \Longrightarrow \text{single-valuedp } (\text{subcls1 } G)$

lemma *subcls-induct*:

$\llbracket \text{wf-prog wf-md } P; \bigwedge C. \forall D. (\text{subcls1 } P)^{++} C D \longrightarrow Q D \Longrightarrow Q C \rrbracket \Longrightarrow Q C$

lemma *subcls1-induct-aux*:

$\llbracket \text{is-class } P C; \text{wf-prog wf-md } P; Q \text{ Object};$

$\bigwedge C D \text{ fs ms.}$

$\llbracket C \neq \text{Object}; \text{is-class } P C; \text{class } P C = \text{Some } (D, \text{fs}, \text{ms}) \wedge$

$\text{wf-cdecl wf-md } P (C, D, \text{fs}, \text{ms}) \wedge P \vdash C \prec^1 D \wedge \text{is-class } P D \wedge Q D \rrbracket \Longrightarrow Q C$

$\Longrightarrow Q C$

lemma *subcls1-induct* [*consumes 2, case-names Object Subcls*]:

$\llbracket \text{wf-prog wf-md } P; \text{is-class } P C; Q \text{ Object};$

$\bigwedge C D. \llbracket C \neq \text{Object}; P \vdash C \prec^1 D; \text{is-class } P D; Q D \rrbracket \Longrightarrow Q C$

$\Longrightarrow Q C$

lemma *subcls-C-Object*:

$\llbracket \text{is-class } P C; \text{wf-prog wf-md } P \rrbracket \Longrightarrow P \vdash C \preceq^* \text{Object}$

lemma *converse-subcls-is-class*:

assumes *wf*: $\text{wf-prog wf-md } P$

shows $\llbracket P \vdash C \preceq^* D; \text{is-class } P C \rrbracket \Longrightarrow \text{is-class } P D$

proof(*induct rule: rtranclp-induct*)

assume *is-class* $P C$

thus *is-class* $P C$.

next

fix $D E$

assume *PDE*: $P \vdash D \prec^1 E$

and *IH*: $\text{is-class } P C \Longrightarrow \text{is-class } P D$

```

  and iPC: is-class P C
  have is-class P D by (rule IH[OF iPC])
  with PDE obtain fsD MsD where classD: class P D = [(E, fsD, MsD)]
  by(auto simp add: is-class-def elim!: subcls1.cases)
  thus is-class P E using wf PDE
  by(auto elim!: subcls1.cases dest: class-wf simp: wf-cdecl-def)
qed

```

lemma *is-class-is-subcls*:

wf-prog *m P* \implies *is-class* *P C* = *P* \vdash *C* \preceq^* *Object*

lemma *subcls-antisym*:

$\llbracket \text{wf-prog } m P; P \vdash C \preceq^* D; P \vdash D \preceq^* C \rrbracket \implies C = D$

apply(*drule acyclic-subcls1*)

apply(*drule acyclic-impl-antisym-rtrancl*)

apply(*drule antisymD*)

apply(*unfold Transitive-Closure.rtrancl-def*)

apply(*auto*)

done

lemma *is-type-pTs*:

$\llbracket \text{wf-prog wf-md } P; \text{class } P C = [(S, fs, ms)]; (M, Ts, T, m) \in \text{set } ms \rrbracket \implies \text{set } Ts \subseteq \text{types } P$
by(*fastforce dest: class-wf simp add: wf-cdecl-def wf-mdecl-def*)

lemma *widen-asm-1*:

assumes *wfP*: *wf-prog* *wf-md* *P*

shows *P* \vdash *C* \leq *D* \implies *C* = *D* \vee \neg (*P* \vdash *D* \leq *C*)

proof (*erule widen.induct*)

fix *T*

show *T* = *T* \vee \neg (*P* \vdash *T* \leq *T*) **by** *simp*

next

fix *C D*

assume *CscD*: *P* \vdash *C* \preceq^* *D*

then have *CpscD*: *C* = *D* \vee (*C* \neq *D* \wedge (*subcls1* *P*)⁺⁺ *C D*) **by** (*simp add: rtranclpD*)

{ **assume** *P* \vdash *D* \preceq^* *C*

then have *DpscC*: *D* = *C* \vee (*D* \neq *C* \wedge (*subcls1* *P*)⁺⁺ *D C*) **by** (*simp add: rtranclpD*)

{ **assume** (*subcls1* *P*)⁺⁺ *D C*

with *wfP* **have** *CnscD*: \neg (*subcls1* *P*)⁺⁺ *C D* **by** (*rule subcls-asm*)

with *CpscD* **have** *C* = *D* **by** *simp*

}

with *DpscC* **have** *C* = *D* **by** *blast*

}

hence *Class* *C* = *Class* *D* \vee \neg (*P* \vdash *D* \preceq^* *C*) **by** *blast*

thus *Class* *C* = *Class* *D* \vee \neg *P* \vdash *Class* *D* \leq *Class* *C* **by** *simp*

next

fix *C*

show *NT* = *Class* *C* \vee \neg *P* \vdash *Class* *C* \leq *NT* **by** *simp*

next

fix *A*

{ **assume** *P* \vdash *A*[] \leq *NT*

hence *A*[] = *NT* **by** *fastforce*

hence *False* **by** *simp* }

hence \neg (*P* \vdash *A*[] \leq *NT*) **by** *blast*

thus *NT* = *A*[] \vee \neg *P* \vdash *A*[] \leq *NT* **by** *simp*

next

```

fix A
show A[] = Class Object  $\vee \neg P \vdash$  Class Object  $\leq$  A[]
  by(auto dest: Object-widen)
next
fix A B
assume AsU:  $P \vdash A \leq B$  and BnpscA:  $A = B \vee \neg P \vdash B \leq A$ 
{ assume  $P \vdash B[] \leq A[]$ 
  hence  $P \vdash B \leq A$  by (auto dest: Array-Array-widen)
  with BnpscA have  $A = B$  by blast
  hence  $A[] = B[]$  by simp }
thus  $A[] = B[] \vee \neg P \vdash B[] \leq A[]$  by blast
qed

```

lemma *widen-asym*: $\llbracket \text{wf-prog wf-md } P; P \vdash C \leq D; C \neq D \rrbracket \implies \neg (P \vdash D \leq C)$

proof –

```

assume wfP: wf-prog wf-md P and CsD:  $P \vdash C \leq D$  and CneqD:  $C \neq D$ 
from wfP CsD have  $C = D \vee \neg (P \vdash D \leq C)$  by (rule widen-asym-1)
with CneqD show ?thesis by simp

```

qed

lemma *widen-antisym*:

$\llbracket \text{wf-prog } m P; P \vdash T \leq U; P \vdash U \leq T \rrbracket \implies T = U$

by(*auto dest: widen-asym*)

lemma *widen-C-Object*: $\llbracket \text{wf-prog wf-md } P; \text{is-class } P C \rrbracket \implies P \vdash \text{Class } C \leq \text{Class Object}$
by(*simp add: subcls-C-Object*)

lemma *is-refType-widen-Object*:

assumes *wfP*: *wf-prog wfmc P*

shows $\llbracket \text{is-type } P A; \text{is-refT } A \rrbracket \implies P \vdash A \leq \text{Class Object}$

by(*induct A*)(*auto elim: refTE intro: subcls-C-Object[OF - wfP] widen-array-object*)

lemma *is-lub-unique*:

assumes *wf*: *wf-prog wf-md P*

shows $\llbracket P \vdash \text{lub}(U, V) = T; P \vdash \text{lub}(U, V) = T' \rrbracket \implies T = T'$

by(*auto elim!: is-lub.cases intro: widen-antisym[OF wf]*)

3.12.2 Well-formedness and method lookup

lemma *sees-wf-mdecl*:

$\llbracket \text{wf-prog wf-md } P; P \vdash C \text{ sees } M:Ts \rightarrow T = m \text{ in } D \rrbracket \implies \text{wf-mdecl wf-md } P D (M, Ts, T, m)$

lemma *sees-method-mono* [*rule-format (no-asm)*]:

$\llbracket P \vdash C' \leq^* C; \text{wf-prog wf-md } P \rrbracket \implies$

$\forall D Ts T m. P \vdash C \text{ sees } M:Ts \rightarrow T = m \text{ in } D \longrightarrow$

$(\exists D' Ts' T' m'. P \vdash C' \text{ sees } M:Ts' \rightarrow T' = m' \text{ in } D' \wedge P \vdash Ts \leq Ts' \wedge P \vdash T' \leq T)$

apply(*drule rtranclpD*)

apply(*erule disjE*)

apply(*fastforce intro: widen-refl widens-refl*)

apply(*erule conjE*)

apply(*erule tranclp-trans-induct*)

prefer 2

apply(*clarify*)

apply(*drule spec, drule spec, drule spec, drule spec, erule (1) impE*)


```

apply clarify
apply( fast elim: widen-trans widens-trans)
apply( clarify)
apply( drule subcls1D)
apply( clarify)
apply(clarsimp simp:Method-def)
apply(frule (2) sees-methods-rec)
apply(rule refl)
apply(case-tac map-of ms M)
apply(rule-tac x = D in exI)
apply(rule-tac x = Ts in exI)
apply(rule-tac x = T in exI)
apply(clarsimp simp add: widens-refl)
apply(rule-tac x = m in exI)
apply(fastforce simp add:map-add-def split:option.split)
apply clarsimp
apply(rename-tac Ts' T' m')
apply( drule (1) class-wf)
apply( unfold wf-cdecl-def Method-def)
apply( frule map-of-SomeD)
apply(clarsimp)
apply(drule (1) bspec)+
apply clarsimp
apply(erule-tac x=D in allE)
apply(erule-tac x=Ts in allE)
apply(rotate-tac -1)
apply(erule-tac x=T in allE)
apply(fastforce simp:map-add-def Method-def split:option.split)
done

```

lemma *sees-method-mono2*:

$$\begin{aligned} & \llbracket P \vdash C' \leq^* C; \text{wf-prog wf-md } P; \\ & \quad P \vdash C \text{ sees } M:Ts \rightarrow T = m \text{ in } D; P \vdash C' \text{ sees } M:Ts' \rightarrow T' = m' \text{ in } D' \rrbracket \\ & \implies P \vdash Ts \llbracket \leq \rrbracket Ts' \wedge P \vdash T' \leq T \end{aligned}$$

lemma *mdecls-visible*:

assumes *wf: wf-prog wf-md P and class: is-class P C*
shows $\bigwedge D \text{ fs ms. class } P \ C = \text{Some}(D, \text{fs}, \text{ms})$
 $\implies \exists Mm. P \vdash C \text{ sees-methods } Mm \wedge (\forall (M, Ts, T, m) \in \text{set ms. } Mm \ M = \text{Some}((Ts, T, m), C))$

using *wf class*

proof (*induct rule:subcls1-induct*)

case *Object*

with *wf* **have** *distinct-fst ms*

by(*auto dest: class-wf simp add: wf-cdecl-def*)

with *Object* **show** *?case* **by**(*fastforce intro!: sees-methods-Object map-of-SomeI*)

next

case *Subcls*

with *wf* **have** *distinct-fst ms*

by(*auto dest: class-wf simp add: wf-cdecl-def*)

with *Subcls* **show** *?case*

by(*fastforce elim:sees-methods-rec dest:subcls1D map-of-SomeI*
simp:is-class-def)

qed

lemma *mdecl-visible*:

assumes *wf*: *wf-prog wf-md P* **and** *C*: *class P C = [(S,fs,ms)]* **and** *m*: $(M, Ts, T, m) \in \text{set } ms$
shows $P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } C$

proof –

from *C* **have** *is-class P C* **by**(*auto simp:is-class-def*)

with *assms* **show** *?thesis*

by(*bestsimp simp:Method-def dest:mdecls-visible*)

qed

lemma *sees-wf-native*:

$\llbracket \text{wf-prog wf-md } P; P \vdash C \text{ sees } M: Ts \rightarrow T = \text{Native in } D \rrbracket \Longrightarrow D \cdot M(Ts) :: T$

apply(*drule (1) sees-wf-mdecl*)

apply(*simp add: wf-mdecl-def*)

done

lemma *Call-lemma*:

$\llbracket P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D; P \vdash C' \preceq^* C; \text{wf-prog wf-md } P \rrbracket$

$\Longrightarrow \exists D' Ts' T' m'.$

$P \vdash C' \text{ sees } M: Ts' \rightarrow T' = m' \text{ in } D' \wedge P \vdash Ts \llbracket \leq \rrbracket Ts' \wedge P \vdash T' \leq T \wedge P \vdash C' \preceq^* D'$

$\wedge \text{is-type } P T' \wedge (\forall T \in \text{set } Ts'. \text{is-type } P T) \wedge (m' \neq \text{Native} \longrightarrow \text{wf-md } P D' (M, Ts', T', \text{the } m'))$

apply(*frule (2) sees-method-mono*)

apply(*fastforce intro:sees-method-decl-above dest:sees-wf-mdecl*
simp: wf-mdecl-def)

done

lemma *sub-Thread-sees-run*:

assumes *wf*: *wf-prog wf-md P*

and *PCThread*: $P \vdash C \preceq^* \text{Thread}$

shows $\exists D \text{ mthd}. P \vdash C \text{ sees run: } [] \rightarrow \text{Void} = \llbracket \text{mthd} \rrbracket \text{ in } D$

proof –

from *class-Thread[OF wf]* **obtain** *T' fsT MsT*

where *classT*: *class P Thread = [(T', fsT, MsT)]* **by** *blast*

hence *wfcThread*: *wf-cdecl wf-md P (Thread, T', fsT, MsT)* **using** *wf* **by**(*rule class-wf*)

then obtain *mrunT* **where** *runThread*: $(\text{run}, [], \text{Void}, \text{mrunT}) \in \text{set } MsT$

by(*auto simp add: wf-cdecl-def*)

moreover have $\exists MmT. P \vdash \text{Thread} \text{ sees-methods } MmT \wedge$

$(\forall (M, Ts, T, m) \in \text{set } MsT. MmT M = \text{Some}((Ts, T, m), \text{Thread}))$

by(*rule mdecls-visible[OF wf is-class-Thread[OF wf] classT]*)

then obtain *MmT* **where** *ThreadMmT*: $P \vdash \text{Thread} \text{ sees-methods } MmT$

and *MmT*: $\forall (M, Ts, T, m) \in \text{set } MsT. MmT M = \text{Some}((Ts, T, m), \text{Thread})$

by *blast*

ultimately obtain *mthd*

where *MmT run* = $\llbracket ([], \text{Void}, \text{mthd}), \text{Thread} \rrbracket$

by(*fastforce*)

with *ThreadMmT* **have** *Tseesrun*: $P \vdash \text{Thread} \text{ sees run: } [] \rightarrow \text{Void} = \text{mthd} \text{ in } \text{Thread}$

by(*auto simp add: Method-def*)

from *sees-method-mono[OF PCThread wf Tseesrun]*

obtain *D' m'* **where** $P \vdash C \text{ sees run: } [] \rightarrow \text{Void} = m' \text{ in } D'$ **by** *auto*

moreover have $m' \neq \text{None}$

proof

assume $m' = \text{None}$

with *wf* $\langle P \vdash C \text{ sees run: } [] \rightarrow \text{Void} = m' \text{ in } D' \rangle$ **have** $D' \cdot \text{run}([]) :: \text{Void}$

```

    by(auto intro: sees-wf-native)
  thus False by cases auto
qed
ultimately show ?thesis by auto
qed

```

lemma *wf-prog-lift*:

```

assumes wf: wf-prog ( $\lambda P C$  bd.  $A P C$  bd)  $P$ 
and rule:
 $\bigwedge$  wf-md  $C M Ts C T m$ .
   $\llbracket$  wf-prog wf-md  $P$ ;  $P \vdash C$  sees  $M:Ts \rightarrow T = [m]$  in  $C$ ; is-class  $P C$ ; set  $Ts \subseteq$  types  $P$ ;  $A P C$ 
( $M, Ts, T, m$ )  $\rrbracket$ 
 $\implies B P C (M, Ts, T, m)$ 
shows wf-prog ( $\lambda P C$  bd.  $B P C$  bd)  $P$ 
proof(cases  $P$ )
case (Program  $P'$ )
thus ?thesis using wf
  apply(clarsimp simp add: wf-prog-def wf-cdecl-def)
  apply(drule (1) bspec)
  apply(rename-tac  $C D fs ms$ )
  apply(subgoal-tac is-class  $P C$ )
  prefer 2
  apply(simp add: is-class-def)
  apply(drule weak-map-of-SomeI)
  apply(simp add: Program)
  apply(clarsimp simp add: Program wf-mdecl-def split del: option.split)
  apply(drule (1) bspec)
  apply clarsimp
  apply(rule conjI)
  apply clarsimp
  apply clarsimp
  apply(frule (1) map-of-SomeI)
  apply(rule rule[OF wf, unfolded Program])
  apply(clarsimp simp add: is-class-def)
  apply(rule mdecl-visible[OF wf[unfolded Program]])
  apply(fastforce intro: is-type-pTs [OF wf, unfolded Program])+
done
qed

```

3.12.3 Well-formedness and field lookup

lemma *wf-Fields-Ex*:

\llbracket wf-prog wf-md P ; is-class $P C$ $\rrbracket \implies \exists$ FDTs. $P \vdash C$ has-fields FDTs

lemma *has-fields-types*:

$\llbracket P \vdash C$ has-fields FDTs; $(FD, T, fm) \in$ set FDTs; wf-prog wf-md P $\rrbracket \implies$ is-type $P T$

lemma *sees-field-is-type*:

$\llbracket P \vdash C$ sees $F:T (fm)$ in D ; wf-prog wf-md P $\rrbracket \implies$ is-type $P T$

by(fastforce simp: sees-field-def

elim: has-fields-types map-of-SomeD[OF map-of-remap-SomeD])

lemma *wf-has-field-mono2*:

assumes wf: wf-prog wf-md P

and $has: P \vdash C \text{ has } F:T (fm) \text{ in } E$
shows $\llbracket P \vdash C \preceq^* D; P \vdash D \preceq^* E \rrbracket \implies P \vdash D \text{ has } F:T (fm) \text{ in } E$
proof(*induct rule: rtranclp-induct*)
case base show *?case using has .*
next
case (*step D D'*)
note $DsubD' = \langle P \vdash D \prec^1 D' \rangle$
from $DsubD'$ **obtain** $rest$ **where** $classD: class P D = \llbracket (D', rest) \rrbracket$
and $DObj: D \neq Object$ **by**(*auto elim!: subcls1.cases*)
from $DsubD' \langle P \vdash D' \preceq^* E \rangle$ **have** $DsubE: P \vdash D \preceq^* E$ **and** $DsubE2: (subcls1 P)^{++} D E$
by(*rule converse-rtranclp-into-rtranclp rtranclp-into-tranclp2*)
from $wf DsubE2$ **have** $DnE: D \neq E$ **by**(*rule subcls-irrefl*)
from $DsubE$ **have** $hasD: P \vdash D \text{ has } F:T (fm) \text{ in } E$ **by**(*rule \langle P \vdash D \preceq^* E \implies P \vdash D \text{ has } F:T (fm) \text{ in } E \rangle*)
then obtain $FDTs$ **where** $hasf: P \vdash D \text{ has-fields } FDTs$ **and** $FE: map-of FDTs (F, E) = \llbracket (T, fm) \rrbracket$
unfolding *has-field-def* **by** *blast*
from $hasf$ **show** *?case*
proof *cases*
case *has-fields-Object with DObj show ?thesis by simp*
next
case (*has-fields-rec DD' fs ms FDTs'*)
with $classD$ **have** [*simp*]: $DD' = D' \text{ rest} = (fs, ms)$
and $hasf': P \vdash D' \text{ has-fields } FDTs'$
and $FDTs: FDTs = map (\lambda(F, Tm). ((F, D), Tm)) fs @ FDTs'$ **by** *auto*
from $FDTs FE DnE hasf'$ **show** *?thesis by (auto dest: map-of-SomeD simp add: has-field-def)*
qed
qed

lemma *wf-has-field-idemp:*

$\llbracket wf-prog wf-md P; P \vdash C \text{ has } F:T (fm) \text{ in } D \rrbracket \implies P \vdash D \text{ has } F:T (fm) \text{ in } D$
apply(*frule has-field-decl-above*)
apply(*erule (2) wf-has-field-mono2*)
apply(*rule rtranclp.rtrancl-refl*)
done

lemma *map-of-remap-conv:*

$\llbracket distinct-fst fs; map-of (map (\lambda(F, y). ((F, D), y)) fs) (F, D) = \llbracket T \rrbracket \rrbracket$
 $\implies map-of (map (\lambda((F, D), T). (F, D, T)) (map (\lambda(F, y). ((F, D), y)) fs)) F = \llbracket (D, T) \rrbracket$
apply(*induct fs*)
apply *auto*
done

lemma *has-field-idemp-sees-field:*

assumes $wf: wf-prog wf-md P$
and $has: P \vdash D \text{ has } F:T (fm) \text{ in } D$
shows $P \vdash D \text{ sees } F:T (fm) \text{ in } D$

proof –

from has **obtain** $FDTs$ **where** $hasf: P \vdash D \text{ has-fields } FDTs$
and $FD: map-of FDTs (F, D) = \llbracket (T, fm) \rrbracket$ **unfolding** *has-field-def* **by** *blast*
from $hasf$ **have** $map-of (map (\lambda((F, D), T). (F, D, T)) FDTs) F = \llbracket (D, T, fm) \rrbracket$
proof *cases*
case (*has-fields-Object D' fs ms*)
from $\langle class P Object = \llbracket (D', fs, ms) \rrbracket \rangle wf$
have $wf-cdecl wf-md P (Object, D', fs, ms)$ **by**(*rule class-wf*)

```

hence distinct-fst fs by(simp add: wf-cdecl-def)
with FD has-fields-Object show ?thesis by(auto intro: map-of-remap-conv simp del: map-map)
next
case (has-fields-rec D' fs ms FDTs')
hence [simp]: FDTs = map ( $\lambda(F, Tm). ((F, D), Tm)$ ) fs @ FDTs'
  and classD: class P D = [(D', fs, ms)] and DnObj: D ≠ Object
  and hasf': P ⊢ D' has-fields FDTs' by auto
from  $\langle \text{class } P \ D = [(D', fs, ms)] \rangle$  wf
have wf-cdecl wf-md P (D, D', fs, ms) by(rule class-wf)
hence distinct-fst fs by(simp add: wf-cdecl-def)
moreover have map-of FDTs' (F, D) = None
proof(rule ccontr)
  assume map-of FDTs' (F, D) ≠ None
  then obtain T' fm' where map-of FDTs' (F, D) = [(T', fm')] by(auto)
  with hasf' have P ⊢ D' ≼* D by(auto dest!: map-of-SomeD intro: has-fields-decl-above)
  with classD DnObj have (subcls1 P) ^++ D D
    by(auto intro: subcls1.intros rtranclp-into-tranclp2)
  with wf show False by(auto dest: subcls-irrefl)
qed
ultimately show ?thesis using FD hasf'
  by(auto simp add: map-add-Some-iff intro: map-of-remap-conv simp del: map-map)
qed
with hasf show ?thesis unfolding sees-field-def by blast
qed

```

lemma *has-fields-distinct*:

```

assumes wf: wf-prog wf-md P
and P ⊢ C has-fields FDTs
shows distinct (map fst FDTs)
using  $\langle P ⊢ C \text{ has-fields } FDTs \rangle$ 
proof(induct)
case (has-fields-Object D fs ms FDTs)
have eq: map (fst ∘ (λ(F, y). ((F, Object), y))) fs = map ((λF. (F, Object)) ∘ fst) fs by(auto)
from  $\langle \text{class } P \ \text{Object} = [(D, fs, ms)] \rangle$  wf
have wf-cdecl wf-md P (Object, D, fs, ms) by(rule class-wf)
hence distinct (map fst fs) by(simp add: wf-cdecl-def distinct-fst-def)
hence distinct (map (fst ∘ (λ(F, y). ((F, Object), y))) fs)
  unfolding eq distinct-map by(auto intro: comp-inj-on inj-onI)
thus ?case using  $\langle FDTs = \text{map } (\lambda(F, T). ((F, \text{Object}), T)) \text{ fs} \rangle$  by(simp)

```

next

```

case (has-fields-rec C D fs ms FDTs FDTs')
have eq: map (fst ∘ (λ(F, y). ((F, C), y))) fs = map ((λF. (F, C)) ∘ fst) fs by(auto)
from  $\langle \text{class } P \ C = [(D, fs, ms)] \rangle$  wf
have wf-cdecl wf-md P (C, D, fs, ms) by(rule class-wf)
hence distinct (map fst fs) by(simp add: wf-cdecl-def distinct-fst-def)
hence distinct (map (fst ∘ (λ(F, y). ((F, C), y))) fs)
  unfolding eq distinct-map by(auto intro: comp-inj-on inj-onI)
moreover from  $\langle \text{class } P \ C = [(D, fs, ms)] \rangle$   $\langle C \neq \text{Object} \rangle$ 
have P ⊢ C ≺1 D by(rule subcls1.intros)
with  $\langle P ⊢ D \text{ has-fields } FDTs \rangle$ 
have  $(\text{fst} \circ (\lambda(F, y). ((F, C), y))) \text{ ' set } fs \cap \text{fst ' set } FDTs = \{\}$ 
  by(auto dest: subcls-notin-has-fields)
ultimately show ?case using  $\langle FDTs' = \text{map } (\lambda(F, T). ((F, C), T)) \text{ fs @ FDTs} \rangle$   $\langle \text{distinct (map } \text{fst } FDTs) \rangle$  by simp

```

qed

3.12.4 Code generation

code-pred

(*modes*: $i \Rightarrow i \Rightarrow i \Rightarrow \text{bool}$)
 [inductify]
 wf-overriding

.

Separate subclass acyclicity from class declaration check. Otherwise, cyclic class hierarchies might lead to non-termination as *Methods* recurses over the class hierarchy.

definition *acyclic-class-hierarchy* :: '*m prog* \Rightarrow bool

where

acyclic-class-hierarchy $P \longleftrightarrow$
 $(\forall (C, D, fs, ml) \in \text{set } (\text{classes } P). C \neq \text{Object} \longrightarrow \neg P \vdash D \preceq^* C)$

definition *wf-cdecl'* :: '*m wf-mdecl-test* \Rightarrow '*m prog* \Rightarrow '*m cdecl* \Rightarrow bool

where

wf-cdecl' *wf-md* $P = (\lambda(C, (D, fs, ms)).$
 $(\forall f \in \text{set } fs. \text{wf-fdecl } P f) \wedge \text{distinct-fst } fs \wedge$
 $(\forall m \in \text{set } ms. \text{wf-mdecl } \text{wf-md } P C m) \wedge$
 $\text{distinct-fst } ms \wedge$
 $(C \neq \text{Object} \longrightarrow \text{is-class } P D \wedge (\forall m \in \text{set } ms. \text{wf-overriding } P D m)) \wedge$
 $(C = \text{Thread} \longrightarrow (\exists m. (\text{run}, [], \text{Void}, m) \in \text{set } ms)))$

lemma *acyclic-class-hierarchy-code* [code]:

acyclic-class-hierarchy $P \longleftrightarrow (\forall (C, D, fs, ml) \in \text{set } (\text{classes } P). C \neq \text{Object} \longrightarrow \neg \text{subcls}' P D C)$

by(*simp add: acyclic-class-hierarchy-def subcls'-def*)

lemma *wf-cdecl'-code* [code]:

wf-cdecl' *wf-md* $P = (\lambda(C, (D, fs, ms)).$
 $(\forall f \in \text{set } fs. \text{wf-fdecl } P f) \wedge \text{distinct-fst } fs \wedge$
 $(\forall m \in \text{set } ms. \text{wf-mdecl } \text{wf-md } P C m) \wedge$
 $\text{distinct-fst } ms \wedge$
 $(C \neq \text{Object} \longrightarrow \text{is-class } P D \wedge (\forall m \in \text{set } ms. \text{wf-overriding } P D m)) \wedge$
 $(C = \text{Thread} \longrightarrow ((\text{run}, [], \text{Void}) \in \text{set } (\text{map } (\lambda(M, Ts, T, b). (M, Ts, T)) ms))))$

by(*auto simp add: wf-cdecl'-def intro!: ext intro: rev-image-eqI*)

declare *set-append* [symmetric, code-unfold]

lemma *wf-prog-code* [code]:

wf-prog *wf-md* $P \longleftrightarrow$
acyclic-class-hierarchy $P \wedge$
wf-syscls $P \wedge \text{distinct-fst } (\text{classes } P) \wedge$
 $(\forall c \in \text{set } (\text{classes } P). \text{wf-cdecl}' \text{wf-md } P c)$

unfolding *wf-prog-def wf-cdecl-def wf-cdecl'-def acyclic-class-hierarchy-def split-def*

by *blast*

end

3.13 Properties of external calls in well-formed programs

theory *ExternalCallWF*

```

imports
  WellForm
  ../Framework/FWSemantics
begin

lemma external-WT-defs-is-type:
  assumes wf-prog wf-md P and C.M(Ts) :: T
  shows is-class P C and is-type P T set Ts ⊆ types P
using assms by(auto elim: external-WT-defs.cases)

context heap-base begin

lemma WT-red-external-aggr-imp-red-external:
   $\llbracket \text{wf-prog wf-md } P; (ta, va, h^\wedge) \in \text{red-external-aggr } P \text{ t a } M \text{ vs } h; P, h \vdash a \cdot M(vs) : U; P, h \vdash t \sqrt{t} \rrbracket$ 
   $\implies P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow \text{ext } \langle va, h^\wedge \rangle$ 
apply(drule tconfD)
apply(erule external-WT'.cases)
apply(clarsimp)
apply(drule (1) sees-wf-native)
apply(erule external-WT-defs.cases)
apply(case-tac [!] hT)
apply(auto 4 4 simp add: red-external-aggr-def widen-Class intro: red-external.intros heap-base.red-external.intros where
addr2thread-id=addr2thread-id and thread-id2addr=thread-id2addr and spurious-wakeups=True and
empty-heap=empty-heap and allocate=allocate and typeof-addr=typeof-addr and heap-read=heap-read
and heap-write=heap-write) heap-base.red-external.intros where addr2thread-id=addr2thread-id and
thread-id2addr=thread-id2addr and spurious-wakeups=False and empty-heap=empty-heap and al-
locate=allocate and typeof-addr=typeof-addr and heap-read=heap-read and heap-write=heap-write)
split: if-split-asm dest: sees-method-decl-above)
done

lemma WT-red-external-list-conv:
   $\llbracket \text{wf-prog wf-md } P; P, h \vdash a \cdot M(vs) : U; P, h \vdash t \sqrt{t} \rrbracket$ 
   $\implies P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow \text{ext } \langle va, h^\wedge \rangle \longleftrightarrow (ta, va, h^\wedge) \in \text{red-external-aggr } P \text{ t a } M \text{ vs } h$ 
by(blast intro: WT-red-external-aggr-imp-red-external red-external-imp-red-external-aggr)

lemma red-external-new-thread-sees:
   $\llbracket \text{wf-prog wf-md } P; P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow \text{ext } \langle va, h^\wedge \rangle; \text{NewThread } t' (C, M', a') h'' \in \text{set } \{ta\}_t \rrbracket$ 
   $\implies \text{typeof-addr } h' a' = \lfloor \text{Class-type } C \rfloor \wedge (\exists T \text{ meth } D. P \vdash C \text{ sees } M':[] \rightarrow T = \lfloor \text{meth} \rfloor \text{ in } D)$ 
by(fastforce elim!: red-external.cases simp add: widen-Class ta-upd-simps dest: sub-Thread-sees-run)

end

```

3.13.1 Preservation of heap conformance

```

context heap-conf-read begin

```

```

lemma hconf-heap-copy-loc-mono:
  assumes heap-copy-loc a a' al h obs h'
  and hconf h
  and  $P, h \vdash a @ al : T$   $P, h \vdash a' @ al : T$ 
  shows hconf h'
proof –
  from  $\langle \text{heap-copy-loc } a \ a' \ \text{al } h \ \text{obs } h' \rangle$  obtain v
  where read: heap-read h a al v

```

and *write*: *heap-write* h a' *al* v h' **by** *cases auto*
from *read* $\langle P, h \vdash a@al : T \rangle$ *hconf* h **have** $P, h \vdash v : \leq T$
by(*rule heap-read-conf*)
with *write* $\langle hconf\ h \rangle$ $\langle P, h \vdash a'@al : T \rangle$ **show** *?thesis*
by(*rule hconf-heap-write-mono*)
qed

lemma *hconf-heap-copies-mono*:
assumes *heap-copies* a a' *als* h *obs* h'
and *hconf* h
and *list-all2* $(\lambda al\ T.\ P, h \vdash a@al : T)$ *als* Ts
and *list-all2* $(\lambda al\ T.\ P, h \vdash a'@al : T)$ *als* Ts
shows *hconf* h'

using *assms*
proof(*induct arbitrary: Ts*)
case *Nil* **thus** *?case* **by** *simp*
next
case (*Cons* al h *ob* h' *als* *obs* h'')
note *step* = $\langle heap-copy-loc\ a\ a'\ al\ h\ ob\ h' \rangle$
from $\langle list-all2\ (\lambda al\ T.\ P, h \vdash a@al : T)\ (al\ \#)\ als \rangle Ts$
obtain $T\ Ts'$ **where** [*simp*]: $Ts = T\ \#\ Ts'$
and $P, h \vdash a@al : T$ *list-all2* $(\lambda al\ T.\ P, h \vdash a@al : T)$ *als* Ts'
by(*auto simp add: list-all2-Cons1*)
from $\langle list-all2\ (\lambda al\ T.\ P, h \vdash a'@al : T)\ (al\ \#)\ als \rangle Ts$
have $P, h \vdash a'@al : T$ *list-all2* $(\lambda al\ T.\ P, h \vdash a'@al : T)$ *als* Ts' **by** *simp-all*
from *step* $\langle hconf\ h \rangle$ $\langle P, h \vdash a@al : T \rangle$ $\langle P, h \vdash a'@al : T \rangle$
have *hconf* h' **by**(*rule hconf-heap-copy-loc-mono*)
moreover from *step* **have** $h \sqsubseteq h'$ **by**(*rule hext-heap-copy-loc*)
from $\langle list-all2\ (\lambda al\ T.\ P, h \vdash a@al : T)\ als\ Ts' \rangle$
have *list-all2* $(\lambda al\ T.\ P, h' \vdash a@al : T)$ *als* Ts'
by(*rule list-all2-mono*)(*rule addr-loc-type-hext-mono*[*OF* - $\langle h \sqsubseteq h' \rangle$])
moreover from $\langle list-all2\ (\lambda al\ T.\ P, h \vdash a'@al : T)\ als\ Ts' \rangle$
have *list-all2* $(\lambda al\ T.\ P, h' \vdash a'@al : T)$ *als* Ts'
by(*rule list-all2-mono*)(*rule addr-loc-type-hext-mono*[*OF* - $\langle h \sqsubseteq h' \rangle$])
ultimately show *?case* **by**(*rule Cons*)
qed

lemma *hconf-heap-clone-mono*:
assumes *heap-clone* P h a h' *res*
and *hconf* h
shows *hconf* h'

using $\langle heap-clone\ P\ h\ a\ h'\ res \rangle$
proof *cases*
case *CloneFail* **thus** *?thesis* **using** $\langle hconf\ h \rangle$
by(*fastforce intro: hconf-heap-ops-mono dest: typeof-addr-is-type*)
next
case (*ObjClone* C h'' a' *FDTs* *obs*)
note *FDTs* = $\langle P \vdash C\ has-fields\ FDTs \rangle$
let $?als = map\ (\lambda((F, D),\ Tfm).\ CField\ D\ F)\ FDTs$
let $?Ts = map\ (\lambda(FD,\ T).\ fst\ (the\ (map-of\ FDTs\ FD)))\ FDTs$
note $\langle heap-copies\ a\ a'\ ?als\ h''\ obs\ h' \rangle$
moreover from $\langle typeof-addr\ h\ a = \lfloor Class-type\ C \rfloor \rangle$ $\langle hconf\ h \rangle$ **have** *is-class* $P\ C$
by(*auto dest: typeof-addr-is-type*)
from $\langle (h'', a') \in allocate\ h\ (Class-type\ C) \rangle$ **have** $h \sqsubseteq h''$ *hconf* h''


```

  by(rule heat-heap-ops hconf-allocate-mono)+(simp-all add: ⟨hconf h⟩ ⟨is-class P C⟩)
note ⟨hconf h'⟩
moreover
from ⟨typeof-addr h a = [Class-type C]⟩ FDTs
have list-all2 (λal T. P, h ⊢ a@al : T) ?als ?Ts
  unfolding list-all2-map1 list-all2-map2 list-all2-same
  by(fastforce intro: addr-loc-type.intros simp add: has-field-def dest: weak-map-of-SomeI)
hence list-all2 (λal T. P, h' ⊢ a@al : T) ?als ?Ts
  by(rule list-all2-mono)(rule addr-loc-type-heat-mono[OF - ⟨h ≤ h'⟩])
moreover from ⟨(h'', a') ∈ allocate h (Class-type C)⟩ ⟨is-class P C⟩
have typeof-addr h'' a' = [Class-type C] by(auto dest: allocate-SomeD)
with FDTs have list-all2 (λal T. P, h'' ⊢ a'@al : T) ?als ?Ts
  unfolding list-all2-map1 list-all2-map2 list-all2-same
  by(fastforce intro: addr-loc-type.intros simp add: has-field-def dest: weak-map-of-SomeI)
ultimately have hconf h' by(rule hconf-heap-copies-mono)
thus ?thesis using ObjClone by simp
next
case (ArrClone T n h'' a' FDTs obs)
let ?als = map (λ((F, D), Tfm). CField D F) FDTs @ map ACell [0..<n]
let ?Ts = map (λ(FD, T). fst (the (map-of FDTs FD))) FDTs @ replicate n T
note ⟨heap-copies a a' ?als h'' obs h'⟩
moreover from ⟨typeof-addr h a = [Array-type T n]⟩ ⟨hconf h⟩ have is-type P (T[])
  by(auto dest: typeof-addr-is-type)
from ⟨(h'', a') ∈ allocate h (Array-type T n)⟩ have h ≤ h'' hconf h''
  by(rule heat-heap-ops hconf-allocate-mono)+(simp-all add: ⟨hconf h⟩ ⟨is-type P (T[])⟩[simplified])
note ⟨hconf h''⟩
moreover from ⟨h ≤ h''⟩ ⟨typeof-addr h a = [Array-type T n]⟩
have type'a: typeof-addr h'' a = [Array-type T n] by(auto intro: heat-arrD)
note FDTs = ⟨P ⊢ Object has-fields FDTs⟩
from type'a FDTs have list-all2 (λal T. P, h'' ⊢ a@al : T) ?als ?Ts
  by(fastforce intro: list-all2-all-nthI addr-loc-type.intros simp add: has-field-def distinct-fst-def list-all2-append
list-all2-map1 list-all2-map2 list-all2-same dest: weak-map-of-SomeI)
  moreover from ⟨(h'', a') ∈ allocate h (Array-type T n)⟩ ⟨is-type P (T[])⟩
  have typeof-addr h'' a' = [Array-type T n] by(auto dest: allocate-SomeD)
  hence list-all2 (λal T. P, h'' ⊢ a'@al : T) ?als ?Ts using FDTs
  by(fastforce intro: list-all2-all-nthI addr-loc-type.intros simp add: has-field-def distinct-fst-def list-all2-append
list-all2-map1 list-all2-map2 list-all2-same dest: weak-map-of-SomeI)
  ultimately have hconf h' by(rule hconf-heap-copies-mono)
  thus ?thesis using ArrClone by simp
qed

theorem external-call-hconf:
  assumes major: P, t ⊢ ⟨a.M(vs), h⟩ -ta→ext ⟨va, h'⟩
  and minor: P, h ⊢ a.M(vs) : U hconf h
  shows hconf h'
using major minor
by cases(fastforce intro: hconf-heap-clone-mono)+

end

context heap-base begin

primrec conf-extRet :: 'm prog ⇒ 'heap ⇒ 'addr extCallRet ⇒ ty ⇒ bool where
  conf-extRet P h (RetVal v) T = (P, h ⊢ v : ≤ T)

```

| *conf-extRet* $P\ h\ (RetExc\ a)\ T = (P, h \vdash Addr\ a : \leq Class\ Throwable)$
 | *conf-extRet* $P\ h\ RetStaySame\ T = True$

end

context *heap-conf* **begin**

lemma *red-external-conf-extRet*:

assumes *wf*: *wf-prog wf-md P*

shows $\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle; P, h \vdash a \cdot M(vs) : U; hconf\ h; preallocated\ h; P, h \vdash t \sqrt{t} \rrbracket$

$\implies conf-extRet\ P\ h'\ va\ U$

using *wf* **apply** –

apply(*frule red-external-heap*)

apply(*drule (1) preallocated-heap*)

apply(*auto elim!*: *red-external.cases external-WT'.cases external-WT-defs-cases dest!*: *sees-wf-native[OF wf]*)

apply(*auto simp add*: *conf-def tconf-def intro*: *xcpt-subcls-Throwable dest!*: *heap-heap-write*)

apply(*case-tac hT*)

apply(*auto* λ λ *dest!*: *typeof-addr-heap-clone dest*: *typeof-addr-is-type intro*: *widen-array-object subcls-C-Object*)

done

end

3.13.2 Progress theorems for external calls

context *heap-progress* **begin**

lemma *heap-copy-loc-progress*:

assumes *hconf*: *hconf h*

and *alconfa*: $P, h \vdash a @ al : T$

and *alconfa'*: $P, h \vdash a' @ al : T$

shows $\exists v\ h'. heap-copy-loc\ a\ a'\ al\ h\ ([ReadMem\ a\ al\ v,\ WriteMem\ a'\ al\ v])\ h' \wedge P, h \vdash v : \leq T \wedge hconf\ h'$

proof –

from *heap-read-total[OF hconfalconfa]*

obtain *v* **where** *heap-read h a al v P, h* $\vdash v : \leq T$ **by** *blast*

moreover from *heap-write-total[OF hconfalconfa' <P, h* $\vdash v : \leq T$] **obtain** *h'* **where** *heap-write h a' al v h' ..*

moreover hence *hconf h'* **using** *hconfalconfa' <P, h* $\vdash v : \leq T$ **by**(*rule hconf-heap-write-mono*)

ultimately show *?thesis* **by**(*blast intro*: *heap-copy-loc.intros*)

qed

lemma *heap-copies-progress*:

assumes *hconf* *h*

and *list-all2* $(\lambda al\ T.\ P, h \vdash a @ al : T)\ als\ Ts$

and *list-all2* $(\lambda al\ T.\ P, h \vdash a' @ al : T)\ als\ Ts$

shows $\exists vs\ h'. heap-copies\ a\ a'\ als\ h\ (concat\ (map\ (\lambda(al,\ v).\ [ReadMem\ a\ al\ v,\ WriteMem\ a'\ al\ v])\ (zip\ als\ vs)))\ h' \wedge hconf\ h'$

using *assms*

proof(*induct als arbitrary*: *h Ts*)

case *Nil* **thus** *?case* **by**(*auto intro*: *heap-copies.Nil*)

next

```

case (Cons al als)
from ⟨list-all2 (λal T. P,h ⊢ a@al : T) (al # als) Ts⟩
obtain T' Ts' where [simp]: Ts = T' # Ts'
  and P,h ⊢ a@al : T' list-all2 (λal T. P,h ⊢ a@al : T) als Ts'
  by(auto simp add: list-all2-Cons1)
from ⟨list-all2 (λal T. P,h ⊢ a'@al : T) (al # als) Ts⟩
have P,h ⊢ a'@al : T' and list-all2 (λal T. P,h ⊢ a'@al : T) als Ts' by simp-all
from ⟨hconf h⟩ ⟨P,h ⊢ a@al : T'⟩ ⟨P,h ⊢ a'@al : T'⟩
obtain v h' where heap-copy-loc a a' al h [ReadMem a al v, WriteMem a' al v] h'
  and hconf h' by(fastforce dest: heap-copy-loc-progress)
moreover hence h ≤ h' by-(rule hext-heap-copy-loc)
{
  note ⟨hconf h'⟩
  moreover from ⟨list-all2 (λal T. P,h ⊢ a@al : T) als Ts'⟩
  have list-all2 (λal T. P,h' ⊢ a@al : T) als Ts'
    by(rule list-all2-mono)(rule addr-loc-type-hext-mono[OF - ⟨h ≤ h'⟩])
  moreover from ⟨list-all2 (λal T. P,h ⊢ a'@al : T) als Ts'⟩
  have list-all2 (λal T. P,h' ⊢ a'@al : T) als Ts'
    by(rule list-all2-mono)(rule addr-loc-type-hext-mono[OF - ⟨h ≤ h'⟩])
  ultimately have ∃ vs h''. heap-copies a a' als h' (concat (map (λ(al, v). [ReadMem a al v, WriteMem
a' al v]) (zip als vs))) h'' ∧ hconf h''
    by(rule Cons) }
  then obtain vs h''
    where heap-copies a a' als h' (concat (map (λ(al, v). [ReadMem a al v, WriteMem a' al v]) (zip
als vs))) h''
    and hconf h'' by blast
  ultimately
  have heap-copies a a' (al # als) h ([ReadMem a al v, WriteMem a' al v] @ (concat (map (λ(al, v).
[ReadMem a al v, WriteMem a' al v]) (zip als vs)))) h''
    by-(rule heap-copies.Cons)
  also have [ReadMem a al v, WriteMem a' al v] @ (concat (map (λ(al, v). [ReadMem a al v, WriteMem
a' al v]) (zip als vs))) =
    (concat (map (λ(al, v). [ReadMem a al v, WriteMem a' al v]) (zip (al # als) (v # vs)))) by
simp
  finally show ?case using ⟨hconf h'⟩ by blast
qed

```

lemma heap-clone-progress:

```

assumes wf: wf-prog wf-md P
and typea: typeof-addr h a = [hT]
and hconf: hconf h
shows ∃ h' res. heap-clone P h a h' res
proof -
from typea hconf have is-htype P hT by(rule typeof-addr-is-type)
show ?thesis
proof(cases allocate h hT = {})
  case True
    with typea CloneFail[of h a hT P]
    show ?thesis by auto
  next
    case False
    then obtain h' a' where new: (h', a') ∈ allocate h hT by(rule not-empty-pairE)
    hence h ≤ h' by(rule hext-allocate)
    have hconf h' using new hconf ⟨is-htype P hT⟩ by(rule hconf-allocate-mono)

```

```

show ?thesis
proof(cases hT)
  case [simp]: (Class-type C)
  from ⟨is-htype P hT⟩ have is-class P C by simp
  from wf-Fields-Ex[OF wf this]
  obtain FDTs where FDTs: P ⊢ C has-fields FDTs ..
  let ?als = map (λ((F, D), Tfm). CField D F) FDTs
  let ?Ts = map (λ(FD, T). fst (the (map-of FDTs FD))) FDTs
  from typea FDTs have list-all2 (λal T. P, h ⊢ a@al : T) ?als ?Ts
    unfolding list-all2-map1 list-all2-map2 list-all2-same
    by(fastforce intro: addr-loc-type.intros simp add: has-field-def dest: weak-map-of-SomeI)
  hence list-all2 (λal T. P, h' ⊢ a@al : T) ?als ?Ts
    by(rule list-all2-mono)(simp add: addr-loc-type-hext-mono[OF - ⟨h ≤ h'⟩] split-def)
  moreover from new ⟨is-class P C⟩
  have typeof-addr h' a' = [Class-type C] by(auto dest: allocate-SomeD)
  with FDTs have list-all2 (λal T. P, h' ⊢ a'@al : T) ?als ?Ts
    unfolding list-all2-map1 list-all2-map2 list-all2-same
  by(fastforce intro: addr-loc-type.intros map-of-SomeI simp add: has-field-def dest: weak-map-of-SomeI)
  ultimately obtain obs h'' where heap-copies a a' ?als h' obs h'' hconf h''
    by(blast dest: heap-copies-progress[OF ⟨hconf h'⟩])
  with typea new FDTs ObjClone[of h a C h' a' P FDTs obs h'']
  show ?thesis by auto
next
case [simp]: (Array-type T n)
from wf obtain FDTs where FDTs: P ⊢ Object has-fields FDTs
  by(blast dest: wf-Fields-Ex is-class-Object)
let ?als = map (λ((F, D), Tfm). CField D F) FDTs @ map ACell [0..<n]
let ?Ts = map (λ(FD, T). fst (the (map-of FDTs FD))) FDTs @ replicate n T
from ⟨h ≤ h'⟩ typea have type'a: typeof-addr h' a = [Array-type T n]
  by(auto intro: hext-arrD)
from type'a FDTs have list-all2 (λal T. P, h' ⊢ a@al : T) ?als ?Ts
  by(fastforce intro: list-all2-all-nthI addr-loc-type.intros simp add: has-field-def list-all2-append
list-all2-map1 list-all2-map2 list-all2-same dest: weak-map-of-SomeI)
moreover from new ⟨is-htype P hT⟩
have typeof-addr h' a' = [Array-type T n]
  by(auto dest: allocate-SomeD)
hence list-all2 (λal T. P, h' ⊢ a'@al : T) ?als ?Ts using FDTs
  by(fastforce intro: list-all2-all-nthI addr-loc-type.intros simp add: has-field-def list-all2-append
list-all2-map1 list-all2-map2 list-all2-same dest: weak-map-of-SomeI)
ultimately obtain obs h'' where heap-copies a a' ?als h' obs h'' hconf h''
  by(blast dest: heap-copies-progress[OF ⟨hconf h'⟩])
with typea new FDTs ArrClone[of h a T n h' a' P FDTs obs h'']
show ?thesis by auto
qed
qed
qed

```

```

theorem external-call-progress:
  assumes wf: wf-prog wf-md P
  and wt: P, h ⊢ a·M(vs) : U
  and hconf: hconf h
  shows ∃ ta va h'. P, t ⊢ ⟨a·M(vs), h⟩ -ta→ext ⟨va, h'⟩
proof -
  note [simp del] = split-paired-Ex

```

```

from wt obtain hT Ts Ts' D
  where T: typeof-addr h a = [hT] and Ts: map typeofh vs = map Some Ts
  and P ⊢ class-type-of hT sees M:Ts'→U = Native in D and subTs: P ⊢ Ts [≤] Ts'
  unfolding external-WT'-iff by blast
from wf ⟨P ⊢ class-type-of hT sees M:Ts'→U = Native in D⟩
have D·M(Ts') :: U by(rule sees-wf-native)
moreover from ⟨P ⊢ class-type-of hT sees M:Ts'→U = Native in D⟩
have P ⊢ ty-of-htype hT ≤ Class D
  by(cases hT)(auto dest: sees-method-decl-above intro: widen-trans widen-array-object)
ultimately show ?thesis using T Ts subTs
proof cases
  assume [simp]: D = Object M = clone Ts' = [] U = Class Object
  from heap-clone-progress[OF wf T hconf] obtain h' res where heap-clone P h a h' res by blast
  thus ?thesis using subTs Ts by(cases res)(auto intro: red-external.intros)
qed(auto simp add: widen-Class intro: red-external.intros)
qed
end

```

3.13.3 Lemmas for preservation of deadlocked threads

context *heap-progress* **begin**

lemma *red-external-wt-hconf-hext*:

```

assumes wf: wf-prog wf-md P
and red: P, t ⊢ ⟨a·M(vs), h⟩ -ta→ext ⟨va, h⟩
and hext: h'' ≤ h
and wt: P, h'' ⊢ a·M(vs) : U
and tconf: P, h'' ⊢ t √t
and hconf: hconf h''
shows ∃ ta' va' h'''. P, t ⊢ ⟨a·M(vs), h''⟩ -ta'→ext ⟨va', h'''⟩ ∧
  collect-locks {ta}l = collect-locks {ta'}l ∧
  collect-cond-actions {ta}c = collect-cond-actions {ta'}c ∧
  collect-interrupts {ta}i = collect-interrupts {ta'}i

```

using *red wt hext*

proof cases

case (*RedClone obs a'*)

from *wt* **obtain** *hT C Ts Ts' D*

where *T*: *typeof-addr h'' a = [hT]*

unfolding *external-WT'-iff* **by** *blast*

from *heap-clone-progress[OF wf T hconf]*

obtain *h''' res* **where** *heap-clone P h'' a h''' res* **by** *blast*

thus *?thesis* **using** *RedClone*

by(*cases res*)(*fastforce intro: red-external.intros*)+

next

case *RedCloneFail*

from *wt* **obtain** *hT Ts Ts'*

where *T*: *typeof-addr h'' a = [hT]*

unfolding *external-WT'-iff* **by** *blast*

from *heap-clone-progress[OF wf T hconf]*

obtain *h''' res* **where** *heap-clone P h'' a h''' res* **by** *blast*

thus *?thesis* **using** *RedCloneFail*

by(*cases res*)(*fastforce intro: red-external.intros*)+

qed(*fastforce simp add: ta-upd-simps elim!: external-WT'.cases intro: red-external.intros[simplified]*)

dest: typeof-addr-hext-mono)+

lemma *red-external-wf-red*:

assumes *wf*: *wf-prog wf-md P*

and *red*: $P, t \vdash \langle a \cdot M(vs), h \rangle - ta \rightarrow ext \langle va, h' \rangle$

and *tconf*: $P, h \vdash t \sqrt{t}$

and *hconf*: *hconf h*

and *wst*: $wset\ s\ t = None \vee (M = wait \wedge (\exists w. wset\ s\ t = [PostWS\ w]))$

obtains *ta' va' h''*

where $P, t \vdash \langle a \cdot M(vs), h \rangle - ta' \rightarrow ext \langle va', h'' \rangle$

and *final-thread.actions-ok final s t ta' \vee final-thread.actions-ok' s t ta' \wedge final-thread.actions-subset ta' ta*

proof(*atomize-elim*)

let *?a-t* = *thread-id2addr t*

let *?t-a* = *addr2thread-id a*

from *tconf* **obtain** *C* **where** *ht*: *typeof-addr h ?a-t* = $[Class\text{-}type\ C]$

and *sub*: $P \vdash C \preceq^* Thread$ **by**(*fastforce dest: tconfD*)

show $\exists ta' va' h'. P, t \vdash \langle a \cdot M(vs), h \rangle - ta' \rightarrow ext \langle va', h' \rangle \wedge (final\text{-}thread.actions\text{-}ok\ final\ s\ t\ ta' \vee final\text{-}thread.actions\text{-}ok'\ s\ t\ ta' \wedge final\text{-}thread.actions\text{-}subset\ ta'\ ta)$

proof(*cases final-thread.actions-ok' s t ta*)

case *True*

have *final-thread.actions-subset ta ta* **by**(*rule final-thread.actions-subset-refl*)

with *True red* **show** *?thesis* **by** *blast*

next

case *False*

note $[simp] = final\text{-}thread.actions\text{-}ok'\text{-}iff\ lock\text{-}ok\text{-}las'\text{-}def\ final\text{-}thread.cond\text{-}action\text{-}oks'\text{-}subset\text{-}Join\ final\text{-}thread.actions\text{-}subset\text{-}iff\ ta\text{-}upd\text{-}simps\ collect\text{-}cond\text{-}actions\text{-}def\ collect\text{-}interrupts\text{-}def$

note $[rule\ del] = subsetI$

note $[intro] = collect\text{-}locks'\text{-}subset\text{-}collect\text{-}locks\ red\text{-}external.intros[simplified]$

show *?thesis*

proof(*cases wset s t*)

case $[simp]: (Some\ w)$

with *wst* **obtain** *w'* **where** $[simp]: w = PostWS\ w'\ M = wait$ **by** *auto*

from *red* **have** $[simp]: vs = []$ **by**(*auto elim: red-external.cases*)

show *?thesis*

proof(*cases w'*)

case $[simp]: WSWokenUp$

let $?ta' = \{\{WokenUp, ClearInterrupt\ t, ObsInterrupted\ t\}$

have *final-thread.actions-ok' s t ?ta'* **by**(*simp add: wset-actions-ok-def*)

moreover **have** *final-thread.actions-subset ?ta' ta*

by(*auto simp add: collect-locks'-def finfun-upd-apply*)

moreover **from** *RedWaitInterrupted*

have $\exists va\ h'. P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow ext \langle va, h' \rangle$ **by** *auto*

ultimately **show** *?thesis* **by** *blast*

next

case $[simp]: WSNotified$

let $?ta' = \{\{Notified\}$

have *final-thread.actions-ok' s t ?ta'* **by**(*simp add: wset-actions-ok-def*)

moreover **have** *final-thread.actions-subset ?ta' ta*

by(*auto simp add: collect-locks'-def finfun-upd-apply*)

moreover **from** *RedWaitNotified*

```

have  $\exists va\ h'. P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow ext \langle va, h' \rangle$  by auto
ultimately show ?thesis by blast
qed
next
case None

from red False show ?thesis
proof cases
  case (RedNewThread C)
    note  $ta = \langle ta = \{\{NewThread\ ?t-a\ (C, run, a)\ h, ThreadStart\ ?t-a\}\} \rangle$ 
    let  $?ta' = \{\{ThreadExists\ ?t-a\ True\}\}$ 
    from  $ta\ False\ None$  have final-thread.actions-ok'  $s\ t\ ?ta'$  by(auto)
    moreover from  $ta$  have final-thread.actions-subset  $?ta'\ ta$  by(auto)
    ultimately show ?thesis using RedNewThread by(fastforce)
  next
    case RedNewThreadFail
    then obtain  $va'\ h'\ x$  where  $P, t \vdash \langle a \cdot M(vs), h \rangle - \{\{NewThread\ ?t-a\ x\ h', ThreadStart\ ?t-a\}\} \rightarrow ext$ 
 $\langle va', h' \rangle$ 
      by(fastforce)
    moreover from  $\langle ta = \{\{ThreadExists\ ?t-a\ True\}\} \rangle\ False\ None$ 
    have final-thread.actions-ok'  $s\ t\ \{\{NewThread\ ?t-a\ x\ h', ThreadStart\ ?t-a\}\}$  by(auto)
    moreover from  $\langle ta = \{\{ThreadExists\ ?t-a\ True\}\} \rangle$ 
    have final-thread.actions-subset  $\{\{NewThread\ ?t-a\ x\ h', ThreadStart\ ?t-a\}\}\ ta$  by(auto)
    ultimately show ?thesis by blast
  next
    case RedJoin
    let  $?ta = \{\{IsInterrupted\ t\ True, ClearInterrupt\ t, ObsInterrupted\ t\}\}$ 
    from  $\langle ta = \{\{Join\ (addr2thread-id\ a), IsInterrupted\ t\ False, ThreadJoin\ (addr2thread-id\ a)\}\} \rangle$ 
None False
    have  $t \in interrupts\ s$  by(auto)
    hence final-thread.actions-ok final  $s\ t\ ?ta$ 
      using None by(auto simp add: final-thread.actions-ok-iff final-thread.cond-action-oks.simps)
    moreover obtain  $va\ h'$  where  $P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta \rightarrow ext \langle va, h' \rangle$  using RedJoinInterrupt
RedJoin by auto
    ultimately show ?thesis by blast
  next
    case RedJoinInterrupt
    hence False using False None by(auto)
    thus ?thesis ..
  next
    case RedInterrupt
    let  $?ta = \{\{ThreadExists\ (addr2thread-id\ a)\ False\}\}$ 
    from RedInterrupt None False
    have free-thread-id (thr s) (addr2thread-id a) by(auto simp add: wset-actions-ok-def)
    hence final-thread.actions-ok final  $s\ t\ ?ta$  using None
      by(auto simp add: final-thread.actions-ok-iff final-thread.cond-action-oks.simps)
    moreover obtain  $va\ h'$  where  $P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta \rightarrow ext \langle va, h' \rangle$  using RedInterruptInexist
RedInterrupt by auto
    ultimately show ?thesis by blast
  next
    case RedInterruptInexist
    let  $?ta = \{\{ThreadExists\ (addr2thread-id\ a)\ True, WakeUp\ (addr2thread-id\ a), Interrupt$ 
 $(addr2thread-id\ a), ObsInterrupt\ (addr2thread-id\ a)\}\}$ 
    from RedInterruptInexist None False

```

```

have  $\neg$  free-thread-id (thr s) (addr2thread-id a) by(auto simp add: wset-actions-ok-def)
hence final-thread.actions-ok final s t ?ta using None
by(auto simp add: final-thread.actions-ok-iff final-thread.cond-action-oks.simps wset-actions-ok-def)
moreover obtain va h' where  $P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta \rightarrow ext \langle va, h' \rangle$  using RedInterruptInexist
RedInterrupt by auto
  ultimately show ?thesis by blast
next
  case (RedIsInterruptedTrue C)
  let ?ta' =  $\{ \{ IsInterrupted ?t-a \} False \}$ 
  from RedIsInterruptedTrue False None have ?t-a  $\notin$  interrupts s by(auto)
  hence final-thread.actions-ok' s t ?ta' using None by auto
  moreover from RedIsInterruptedTrue have final-thread.actions-subset ?ta' ta by auto
  moreover from RedIsInterruptedTrue RedIsInterruptedFalse obtain va h'
    where  $P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow ext \langle va, h' \rangle$  by auto
  ultimately show ?thesis by blast
next
  case (RedIsInterruptedFalse C)
  let ?ta' =  $\{ \{ IsInterrupted ?t-a \} True, \{ ObsInterrupted ?t-a \} \}$ 
  from RedIsInterruptedFalse have ?t-a  $\in$  interrupts s
    using False None by(auto)
  hence final-thread.actions-ok final s t ?ta'
    using None by(auto simp add: final-thread.actions-ok-iff final-thread.cond-action-oks.simps)
  moreover obtain va h' where  $P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow ext \langle va, h' \rangle$ 
    using RedIsInterruptedFalse RedIsInterruptedTrue by auto
  ultimately show ?thesis by blast
next
  case RedWaitInterrupt
  note ta =  $\langle ta = \{ \{ Unlock \rightarrow a, Lock \rightarrow a, IsInterrupted t \} True, \{ ClearInterrupt t, ObsInterrupted t \} \} \rangle$ 
  from ta False None have hli:  $\neg$  has-lock (locks s $ a) t  $\vee$  t  $\notin$  interrupts s
  by(fastforce simp add: lock-actions-ok'-iff finfun-upd-apply split: if-split-asm dest: may-lock-t-may-lock-unlock
dest: has-lock-may-lock)
  show ?thesis
  proof(cases has-lock (locks s $ a) t)
    case True
  let ?ta' =  $\{ \{ Suspend a, Unlock \rightarrow a, Lock \rightarrow a, ReleaseAcquire \rightarrow a, IsInterrupted t \} False, \{ SyncUnlock a \} \}$ 
  from True hli have t  $\notin$  interrupts s by simp
  with True False have final-thread.actions-ok' s t ?ta' using None
  by(auto simp add: lock-actions-ok'-iff finfun-upd-apply wset-actions-ok-def Cons-eq-append-conv)
  moreover from ta have final-thread.actions-subset ?ta' ta
    by(auto simp add: collect-locks'-def finfun-upd-apply)
  moreover from RedWait RedWaitInterrupt obtain va h' where  $P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow ext \langle va, h' \rangle$ 
  by auto
  ultimately show ?thesis by blast
next
  case False
  let ?ta' =  $\{ \{ UnlockFail \rightarrow a \} \}$ 
  from False have final-thread.actions-ok' s t ?ta' using None
    by(auto simp add: lock-actions-ok'-iff finfun-upd-apply)
  moreover from ta have final-thread.actions-subset ?ta' ta
    by(auto simp add: collect-locks'-def finfun-upd-apply)
  moreover from RedWaitInterrupt obtain va h' where  $P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow ext \langle va, h' \rangle$ 
  by(fastforce)

```



```

    ultimately show ?thesis by blast
qed
next
case RedWait
  note ta = ⟨ta = {Suspend a, Unlock→a, Lock→a, ReleaseAcquire→a, IsInterrupted t False,
SyncUnlock a}⟩

  from ta False None have hli: ¬ has-lock (locks s $ a) t ∨ t ∈ interrupts s
  by(auto simp add: lock-actions-ok'-iff finfun-upd-apply wset-actions-ok-def Cons-eq-append-conv
split: if-split-asm dest: may-lock-t-may-lock-unlock-lock-t dest: has-lock-may-lock)
  show ?thesis
  proof(cases has-lock (locks s $ a) t)
    case True
    let ?ta' = {Unlock→a, Lock→a, IsInterrupted t True, ClearInterrupt t, ObsInterrupted t}
    from True hli have t ∈ interrupts s by simp
    with True False have final-thread.actions-ok final s t ?ta' using None
    by(auto simp add: final-thread.actions-ok-iff final-thread.cond-action-oks.simps lock-ok-las-def
finfun-upd-apply has-lock-may-lock)
    moreover from RedWait RedWaitInterrupt obtain va h' where P,t ⊢ ⟨a·M(vs),h⟩ - ?ta'→ext
⟨va,h^⟩ by auto
    ultimately show ?thesis by blast
  next
  case False
  let ?ta' = {UnlockFail→a}
  from False have final-thread.actions-ok' s t ?ta' using None
  by(auto simp add: lock-actions-ok'-iff finfun-upd-apply)
  moreover from ta have final-thread.actions-subset ?ta' ta
  by(auto simp add: collect-locks'-def finfun-upd-apply)
  moreover from RedWait RedWaitFail obtain va h' where P,t ⊢ ⟨a·M(vs),h⟩ - ?ta'→ext
⟨va,h^⟩ by(fastforce)
  ultimately show ?thesis by blast
qed
next
case RedWaitFail
  note ta = ⟨ta = {UnlockFail→a}⟩
  let ?ta' = if t ∈ interrupts s
    then {Unlock→a, Lock→a, IsInterrupted t True, ClearInterrupt t, ObsInterrupted t}
    else {Suspend a, Unlock→a, Lock→a, ReleaseAcquire→a, IsInterrupted t False,
SyncUnlock a }
  from ta False None have has-lock (locks s $ a) t
  by(auto simp add: finfun-upd-apply split: if-split-asm)
  hence final-thread.actions-ok final s t ?ta' using None
  by(auto simp add: final-thread.actions-ok-iff final-thread.cond-action-oks.simps lock-ok-las-def
finfun-upd-apply has-lock-may-lock wset-actions-ok-def)
  moreover from RedWaitFail RedWait RedWaitInterrupt
  obtain va h' where P,t ⊢ ⟨a·M(vs),h⟩ - ?ta'→ext ⟨va,h^⟩
  by(cases t ∈ interrupts s) (auto)
  ultimately show ?thesis by blast
next
case RedWaitNotified
  note ta = ⟨ta = {Notified}⟩
  let ?ta' = if has-lock (locks s $ a) t
    then (if t ∈ interrupts s
    then {Unlock→a, Lock→a, IsInterrupted t True, ClearInterrupt t, ObsInterrupted

```

$t\}$
 $\text{SyncUnlock } a \}$
 $\text{else } \{\{ \text{Suspend } a, \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a, \text{ReleaseAcquire} \rightarrow a, \text{IsInterrupted } t \text{ False},$
 $\text{UnlockFail} \rightarrow a \}$
have $\text{final-thread.actions-ok final } s \ t \ ?ta' \text{ using None}$
by $(\text{auto simp add: final-thread.actions-ok-iff final-thread.cond-action-oks.simps lock-ok-las-def}$
 $\text{finfun-upd-apply has-lock-may-lock wset-actions-ok-def})$
moreover from $\text{RedWaitNotified RedWait RedWaitInterrupt RedWaitFail}$
have $\exists va \ h'. P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow \text{ext } \langle va, h' \rangle$ **by auto**
ultimately show $?thesis$ **by blast**
next
case $\text{RedWaitInterrupted}$
note $ta = \langle ta = \{\{ \text{WokenUp}, \text{ClearInterrupt } t, \text{ObsInterrupted } t \}\rangle$
let $?ta' = \text{if has-lock (locks } s \ \$ \ a) \ t$
 $\text{then (if } t \in \text{interrupts } s$
 $\text{then } \{\{ \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a, \text{IsInterrupted } t \ \text{True}, \text{ClearInterrupt } t, \text{ObsInterrupted}$
 $t \}$
 $\text{else } \{\{ \text{Suspend } a, \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a, \text{ReleaseAcquire} \rightarrow a, \text{IsInterrupted } t \ \text{False},$
 $\text{SyncUnlock } a \}$
 $\text{UnlockFail} \rightarrow a \}$
have $\text{final-thread.actions-ok final } s \ t \ ?ta' \text{ using None}$
by $(\text{auto simp add: final-thread.actions-ok-iff final-thread.cond-action-oks.simps lock-ok-las-def}$
 $\text{finfun-upd-apply has-lock-may-lock wset-actions-ok-def})$
moreover from $\text{RedWaitInterrupted RedWait RedWaitInterrupt RedWaitFail}$
have $\exists va \ h'. P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow \text{ext } \langle va, h' \rangle$ **by auto**
ultimately show $?thesis$ **by blast**
next
case RedWaitSpurious
note $ta = \langle ta = \{\{ \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a, \text{ReleaseAcquire} \rightarrow a, \text{IsInterrupted } t \ \text{False}, \text{SyncUnlock}$
 $a \}\rangle$
from $ta \ \text{False None}$ **have** $\text{hli: } \neg \text{has-lock (locks } s \ \$ \ a) \ t \vee t \in \text{interrupts } s$
by $(\text{auto simp add: lock-actions-ok'-iff finfun-upd-apply wset-actions-ok-def Cons-eq-append-conv}$
 $\text{split: if-split-asm dest: may-lock-t-may-lock-unlock-lock-t dest: has-lock-may-lock})$
show $?thesis$
proof $(\text{cases has-lock (locks } s \ \$ \ a) \ t)$
case True
let $?ta' = \{\{ \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a, \text{IsInterrupted } t \ \text{True}, \text{ClearInterrupt } t, \text{ObsInterrupted } t \}\}$
from True hli **have** $t \in \text{interrupts } s$ **by simp**
with True False **have** $\text{final-thread.actions-ok final } s \ t \ ?ta' \text{ using None}$
by $(\text{auto simp add: final-thread.actions-ok-iff final-thread.cond-action-oks.simps lock-ok-las-def}$
 $\text{finfun-upd-apply has-lock-may-lock})$
moreover from $\text{RedWaitInterrupt RedWaitSpurious(1-5)}$
obtain $va \ h' \text{ where } P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow \text{ext } \langle va, h' \rangle$ **by auto**
ultimately show $?thesis$ **by blast**
next
case False
let $?ta' = \{\{ \text{UnlockFail} \rightarrow a \}\}$
from False **have** $\text{final-thread.actions-ok}' \ s \ t \ ?ta' \text{ using None}$
by $(\text{auto simp add: lock-actions-ok'-iff finfun-upd-apply})$
moreover from ta **have** $\text{final-thread.actions-subset } ?ta' \ ta$
by $(\text{auto simp add: collect-locks'-def finfun-upd-apply})$
moreover from $\text{RedWaitSpurious(1-5) RedWaitFail}$
obtain $va \ h' \text{ where } P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow \text{ext } \langle va, h' \rangle$ **by (fastforce)**
ultimately show $?thesis$ **by blast**

qed

next

case *RedNotify*

note $ta = \langle ta = \{\{Notify\ a, Unlock \rightarrow a, Lock \rightarrow a\}\} \rangle$

let $?ta' = \{\{UnlockFail \rightarrow a\}\}$

from ta *False None* have \neg has-lock (locks s $\$$ a) t

by(*fastforce simp add: lock-actions-ok'-iff finfun-upd-apply wset-actions-ok-def Cons-eq-append-conv*

split: if-split-asm dest: may-lock-t-may-lock-unlock-lock-t has-lock-may-lock)

hence *final-thread.actions-ok'* s t $?ta'$ using *None*

by(*auto simp add: lock-actions-ok'-iff finfun-upd-apply*)

moreover from ta have *final-thread.actions-subset* $?ta'$ ta

by(*auto simp add: collect-locks'-def finfun-upd-apply*)

moreover from *RedNotify* obtain va h' where $P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow ext \langle va, h' \rangle$

by(*fastforce*)

ultimately show *?thesis* by *blast*

next

case *RedNotifyFail*

note $ta = \langle ta = \{\{UnlockFail \rightarrow a\}\} \rangle$

let $?ta' = \{\{Notify\ a, Unlock \rightarrow a, Lock \rightarrow a\}\}$

from ta *False None* have has-lock (locks s $\$$ a) t

by(*auto simp add: finfun-upd-apply split: if-split-asm*)

hence *final-thread.actions-ok'* s t $?ta'$ using *None*

by(*auto simp add: finfun-upd-apply simp add: wset-actions-ok-def intro: has-lock-may-lock*)

moreover from ta have *final-thread.actions-subset* $?ta'$ ta

by(*auto simp add: collect-locks'-def finfun-upd-apply*)

moreover from *RedNotifyFail* obtain va h' where $P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow ext \langle va, h' \rangle$

by(*fastforce*)

ultimately show *?thesis* by *blast*

next

case *RedNotifyAll*

note $ta = \langle ta = \{\{NotifyAll\ a, Unlock \rightarrow a, Lock \rightarrow a\}\} \rangle$

let $?ta' = \{\{UnlockFail \rightarrow a\}\}$

from ta *False None* have \neg has-lock (locks s $\$$ a) t

by(*auto simp add: lock-actions-ok'-iff finfun-upd-apply wset-actions-ok-def Cons-eq-append-conv*

split: if-split-asm dest: may-lock-t-may-lock-unlock-lock-t)

hence *final-thread.actions-ok'* s t $?ta'$ using *None*

by(*auto simp add: lock-actions-ok'-iff finfun-upd-apply*)

moreover from ta have *final-thread.actions-subset* $?ta'$ ta

by(*auto simp add: collect-locks'-def finfun-upd-apply*)

moreover from *RedNotifyAll* obtain va h' where $P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow ext \langle va, h' \rangle$

by(*fastforce*)

ultimately show *?thesis* by *blast*

next

case *RedNotifyAllFail*

note $ta = \langle ta = \{\{UnlockFail \rightarrow a\}\} \rangle$

let $?ta' = \{\{NotifyAll\ a, Unlock \rightarrow a, Lock \rightarrow a\}\}$

from ta *False None* have has-lock (locks s $\$$ a) t

by(*auto simp add: finfun-upd-apply split: if-split-asm*)

hence *final-thread.actions-ok'* s t $?ta'$ using *None*

by(*auto simp add: finfun-upd-apply wset-actions-ok-def intro: has-lock-may-lock*)

moreover from ta have *final-thread.actions-subset* $?ta'$ ta

by(*auto simp add: collect-locks'-def finfun-upd-apply*)

moreover from *RedNotifyAllFail* obtain va h' where $P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow ext \langle va, h' \rangle$

```

by(fastforce)
  ultimately show ?thesis by blast
next
  case RedInterruptedTrue
  let ?ta' = {IsInterrupted t False}
  from RedInterruptedTrue have final-thread.actions-ok final s t ?ta'
  using None False by(auto simp add: final-thread.actions-ok-iff final-thread.cond-action-oks.simps)
  moreover obtain va h' where  $P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow ext \langle va, h' \rangle$ 
    using RedInterruptedFalse RedInterruptedTrue by auto
  ultimately show ?thesis by blast
next
  case RedInterruptedFalse
  let ?ta' = {IsInterrupted t True, ClearInterrupt t, ObsInterrupted t}
  from RedInterruptedFalse have final-thread.actions-ok final s t ?ta'
    using None False
  by(auto simp add: final-thread.actions-ok-iff final-thread.cond-action-oks.simps)
  moreover obtain va h' where  $P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow ext \langle va, h' \rangle$ 
    using RedInterruptedFalse RedInterruptedTrue by auto
  ultimately show ?thesis by blast
qed(auto simp add: None)
qed
qed
qed
end

```

context *heap-base* begin

lemma *red-external-ta-satisfiable*:

```

  fixes final
  assumes  $P, t \vdash \langle a \cdot M(vs), h \rangle - ta \rightarrow ext \langle va, h' \rangle$ 
  shows  $\exists s. final\text{-thread.actions-ok final s t ta$ 
proof -
  note [simp] =
    final-thread.actions-ok-iff final-thread.cond-action-oks.simps final-thread.cond-action-ok.simps
    lock-ok-las-def finfun-upd-apply wset-actions-ok-def has-lock-may-lock
  and [intro] =
    free-thread-id.intros
  and [cong] = conj-cong

  from assms show ?thesis by cases(fastforce intro: exI[where x=(K$ None)(a $:= [(t, 0)])])
exI[where x=(K$ None)]+
qed

```

lemma *red-external-aggr-ta-satisfiable*:

```

  fixes final
  assumes  $(ta, va, h') \in red\text{-external-aggr } P \ t \ a \ M \ vs \ h$ 
  shows  $\exists s. final\text{-thread.actions-ok final s t ta$ 
proof -
  note [simp] =
    final-thread.actions-ok-iff final-thread.cond-action-oks.simps final-thread.cond-action-ok.simps
    lock-ok-las-def finfun-upd-apply wset-actions-ok-def has-lock-may-lock
  and [intro] =
    free-thread-id.intros

```

and [cong] = conj-cong

from *assms* show *?thesis*

by(*fastforce simp add: red-external-aggr-def split-beta ta-upd-simps split: if-split-asm intro: exI*[**where** $x=Map.empty$] *exI*[**where** $x=(K\$ None)(a \$:= [(t, 0)])$] *exI*[**where** $x=K\$ None$])

qed

end

3.13.4 Determinism

context *heap-base* begin

lemma *heap-copy-loc-deterministic*:

assumes *det: deterministic-heap-ops*

and *copy: heap-copy-loc a a' al h ops h' heap-copy-loc a a' al h ops' h''*

shows $ops = ops' \wedge h' = h''$

using *copy*

by(*auto elim!: heap-copy-loc.cases dest: deterministic-heap-ops-readD*[*OF det*] *deterministic-heap-ops-writeD*[*OF det*])

lemma *heap-copies-deterministic*:

assumes *det: deterministic-heap-ops*

and *copy: heap-copies a a' als h ops h' heap-copies a a' als h ops' h''*

shows $ops = ops' \wedge h' = h''$

using *copy*

apply(*induct arbitrary: ops' h''*)

apply(*fastforce elim!: heap-copies-cases*)

apply(*erule heap-copies-cases*)

apply *clarify*

apply(*drule* (1) *heap-copy-loc-deterministic*[*OF det*])

apply *clarify*

apply(*unfold same-append-eq*)

apply *blast*

done

lemma *heap-clone-deterministic*:

assumes *det: deterministic-heap-ops*

and *clone: heap-clone P h a h' obs heap-clone P h a h'' obs'*

shows $h' = h'' \wedge obs = obs'$

using *clone*

by(*auto 4 4 elim!: heap-clone.cases dest: heap-copies-deterministic*[*OF det*] *deterministic-heap-ops-allocateD*[*OF det*] *has-fields-fun*)

lemma *red-external-deterministic*:

fixes *final*

assumes *det: deterministic-heap-ops*

and *red: P,t ⊢ ⟨a·M(vs), (shr s)⟩ -ta→ext ⟨va, h'⟩ P,t ⊢ ⟨a·M(vs), (shr s)⟩ -ta'→ext ⟨va', h''⟩*

and *aok: final-thread.actions-ok final s t ta final-thread.actions-ok final s t ta'*

shows $ta = ta' \wedge va = va' \wedge h' = h''$

using *red aok*

apply(*simp add: final-thread.actions-ok-iff lock-ok-las-def*)

apply(*erule red-external.cases*)

apply(*erule-tac* [!] *red-external.cases*)

```

apply simp-all
apply(auto simp add: finfun-upd-apply wset-actions-ok-def dest: heap-clone-deterministic[OF det] split: if-split-asm)
using deterministic-heap-ops-no-spurious-wakeups[OF det]
apply simp-all
done

end

end

```

3.14 Conformance for threads

```

theory ConformThreaded
imports
  ../Framework/FWLifting
  ../Framework/FWWellform
  Conform
begin

```

Every thread must be represented as an object whose address is its thread ID

```
context heap-base begin
```

```
abbreviation thread-conf :: 'm prog  $\Rightarrow$  ('addr, 'thread-id, 'x) thread-info  $\Rightarrow$  'heap  $\Rightarrow$  bool
where thread-conf  $P \equiv ts\text{-ok} (\lambda t x m. P, m \vdash t \sqrt{t})$ 
```

```
lemma thread-confI:
  ( $\bigwedge t xln. ts\ t = \lfloor xln \rfloor \implies P, h \vdash t \sqrt{t} \implies thread\text{-conf}\ P\ ts\ h$ )
by(blast intro: ts-okI)
```

```
lemma thread-confD:
  assumes thread-conf  $P\ ts\ h\ ts\ t = \lfloor xln \rfloor$ 
  shows  $P, h \vdash t \sqrt{t}$ 
using assms by(cases xln)(auto dest: ts-okD)
```

```
lemma thread-conf-ts-upd-eq [simp]:
  assumes tst:  $ts\ t = \lfloor xln \rfloor$ 
  shows thread-conf  $P\ (ts(t \mapsto xln'))\ h \longleftrightarrow thread\text{-conf}\ P\ ts\ h$ 
```

```
proof
  assume tc: thread-conf  $P\ (ts(t \mapsto xln'))\ h$ 
  show thread-conf  $P\ ts\ h$ 
  proof(rule thread-confI)
    fix  $T\ XLN$ 
    assume  $ts\ T = \lfloor XLN \rfloor$ 
    with tc show  $P, h \vdash T \sqrt{t}$ 
    by(cases T = t)(auto dest: thread-confD)
  qed

```

```
next
  assume tc: thread-conf  $P\ ts\ h$ 
  show thread-conf  $P\ (ts(t \mapsto xln'))\ h$ 
  proof(rule thread-confI)
    fix  $T\ XLN$ 
    assume  $(ts(t \mapsto xln'))\ T = \lfloor XLN \rfloor$ 
    with tst obtain  $XLN'$  where  $ts\ T = \lfloor XLN' \rfloor$ 

```

```

    by(cases T = t)(auto)
  with tc show P, h ⊢ T √t
    by(auto dest: thread-confD)
qed
qed

end

context heap begin

lemma thread-conf-heat:
  [[ thread-conf P ts h; h ≤ h' ]] ⇒ thread-conf P ts h'
by(blast intro: thread-confI tconf-heat-mono dest: thread-confD)

lemma thread-conf-start-state:
  [[ start-heap-ok; wf-syscls P ]] ⇒ thread-conf P (thr (start-state f P C M vs)) (shr (start-state f P C M vs))
by(auto intro!: thread-confI simp add: start-state-def split-beta split: if-split-asm intro: tconf-start-heap-start-tid)

end

context heap-base begin

lemma lock-thread-ok-start-state:
  lock-thread-ok (locks (start-state f P C M vs)) (thr (start-state f P C M vs))
by(rule lock-thread-okI)(simp add: start-state-def split-beta)

lemma wset-thread-ok-start-state:
  wset-thread-ok (wset (start-state f P C M vs)) (thr (start-state f P C M vs))
by(auto simp add: wset-thread-ok-def start-state-def split-beta)

lemma wset-final-ok-start-state:
  final-thread.wset-final-ok final (wset (start-state f P C M vs)) (thr (start-state f P C M vs))
by(rule final-thread.wset-final-okI)(simp add: start-state-def split-beta)

end

end

```

3.15 Binary Operators

```

theory BinOp
imports
  WellForm Word-Lib.Bit-Shifts-Infix-Syntax
begin

datatype bop = — names of binary operations
  | Eq
  | NotEq
  | LessThan
  | LessOrEqual
  | GreaterThan
  | GreaterOrEqual

```

```

| Add
| Subtract
| Mult
| Div
| Mod
| BinAnd
| BinOr
| BinXor
| ShiftLeft
| ShiftRightZeros
| ShiftRightSigned

```

3.15.1 The semantics of binary operators

context

includes *bit-operations-syntax*

begin

type-synonym *'addr binop-ret* = *'addr val* + *'addr* — a value or the address of an exception

fun *binop-LessThan* :: *'addr val* ⇒ *'addr val* ⇒ *'addr binop-ret option*

where

binop-LessThan (*Intg i1*) (*Intg i2*) = *Some (Inl (Bool (i1 < s i2)))*

| *binop-LessThan v1 v2* = *None*

fun *binop-LessOrEqual* :: *'addr val* ⇒ *'addr val* ⇒ *'addr binop-ret option*

where

binop-LessOrEqual (*Intg i1*) (*Intg i2*) = *Some (Inl (Bool (i1 <= s i2)))*

| *binop-LessOrEqual v1 v2* = *None*

fun *binop-GreaterThan* :: *'addr val* ⇒ *'addr val* ⇒ *'addr binop-ret option*

where

binop-GreaterThan (*Intg i1*) (*Intg i2*) = *Some (Inl (Bool (i2 < s i1)))*

| *binop-GreaterThan v1 v2* = *None*

fun *binop-GreaterOrEqual* :: *'addr val* ⇒ *'addr val* ⇒ *'addr binop-ret option*

where

binop-GreaterOrEqual (*Intg i1*) (*Intg i2*) = *Some (Inl (Bool (i2 <= s i1)))*

| *binop-GreaterOrEqual v1 v2* = *None*

fun *binop-Add* :: *'addr val* ⇒ *'addr val* ⇒ *'addr binop-ret option*

where

binop-Add (*Intg i1*) (*Intg i2*) = *Some (Inl (Intg (i1 + i2)))*

| *binop-Add v1 v2* = *None*

fun *binop-Subtract* :: *'addr val* ⇒ *'addr val* ⇒ *'addr binop-ret option*

where

binop-Subtract (*Intg i1*) (*Intg i2*) = *Some (Inl (Intg (i1 - i2)))*

| *binop-Subtract v1 v2* = *None*

fun *binop-Mult* :: *'addr val* ⇒ *'addr val* ⇒ *'addr binop-ret option*

where

binop-Mult (*Intg i1*) (*Intg i2*) = *Some (Inl (Intg (i1 * i2)))*

| *binop-Mult v1 v2* = *None*

fun *binop-BinAnd* :: 'addr val ⇒ 'addr val ⇒ 'addr binop-ret option

where

binop-BinAnd (Intg i1) (Intg i2) = Some (Inl (Intg (i1 AND i2)))
| *binop-BinAnd* (Bool b1) (Bool b2) = Some (Inl (Bool (b1 ∧ b2)))
| *binop-BinAnd* v1 v2 = None

fun *binop-BinOr* :: 'addr val ⇒ 'addr val ⇒ 'addr binop-ret option

where

binop-BinOr (Intg i1) (Intg i2) = Some (Inl (Intg (i1 OR i2)))
| *binop-BinOr* (Bool b1) (Bool b2) = Some (Inl (Bool (b1 ∨ b2)))
| *binop-BinOr* v1 v2 = None

fun *binop-BinXor* :: 'addr val ⇒ 'addr val ⇒ 'addr binop-ret option

where

binop-BinXor (Intg i1) (Intg i2) = Some (Inl (Intg (i1 XOR i2)))
| *binop-BinXor* (Bool b1) (Bool b2) = Some (Inl (Bool (b1 ≠ b2)))
| *binop-BinXor* v1 v2 = None

fun *binop-ShiftLeft* :: 'addr val ⇒ 'addr val ⇒ 'addr binop-ret option

where

binop-ShiftLeft (Intg i1) (Intg i2) = Some (Inl (Intg (i1 << unat (i2 AND 0x1f))))
| *binop-ShiftLeft* v1 v2 = None

fun *binop-ShiftRightZeros* :: 'addr val ⇒ 'addr val ⇒ 'addr binop-ret option

where

binop-ShiftRightZeros (Intg i1) (Intg i2) = Some (Inl (Intg (i1 >> unat (i2 AND 0x1f))))
| *binop-ShiftRightZeros* v1 v2 = None

fun *binop-ShiftRightSigned* :: 'addr val ⇒ 'addr val ⇒ 'addr binop-ret option

where

binop-ShiftRightSigned (Intg i1) (Intg i2) = Some (Inl (Intg (i1 >>> unat (i2 AND 0x1f))))
| *binop-ShiftRightSigned* v1 v2 = None

Division on 'a word is unsigned, but JLS specifies signed division.

definition *word-sdiv* :: 'a :: len word ⇒ 'a word ⇒ 'a word (**infixl** <sdiv> 70)

where [code]:

x sdiv y =
(let x' = sint x; y' = sint y;
negative = (x' < 0) ≠ (y' < 0);
result = abs x' div abs y'
in word-of-int (if negative then -result else result))

definition *word-smod* :: 'a :: len word ⇒ 'a word ⇒ 'a word (**infixl** <smod> 70)

where [code]:

x smod y =
(let x' = sint x; y' = sint y;
negative = (x' < 0);
result = abs x' mod abs y'
in word-of-int (if negative then -result else result))

declare *word-sdiv-def* [simp] *word-smod-def* [simp]

lemma *sdiv-smod-id*: (a sdiv b) * b + (a smod b) = a

proof –

have $F5: \forall u::'a \text{ word. } - (- u) = u$

by *simp*

have $F7: \forall v u::'a \text{ word. } u + v = v + u$

by (*simp add: ac-simps*)

have $F8: \forall (w::'a \text{ word}) (v::\text{int}) u::\text{int. } \text{word-of-int } u + \text{word-of-int } v * w = \text{word-of-int } (u + v * \text{sint } w)$

by *simp*

have $\exists u. u = - \text{sint } b \wedge \text{word-of-int } (\text{sint } a \text{ mod } u + - (- u * (\text{sint } a \text{ div } u))) = a$

using $F5$ **by** *simp*

hence $\text{word-of-int } (\text{sint } a \text{ mod } - \text{sint } b + - (\text{sint } b * (\text{sint } a \text{ div } - \text{sint } b))) = a$

by (*metis equation-minus-iff*)

hence $\text{word-of-int } (\text{sint } a \text{ mod } - \text{sint } b) + \text{word-of-int } (- (\text{sint } a \text{ div } - \text{sint } b)) * b = a$

using $F8$ **by** (*simp add: ac-simps*)

hence $\text{eq: } \text{word-of-int } (- (\text{sint } a \text{ div } - \text{sint } b)) * b + \text{word-of-int } (\text{sint } a \text{ mod } - \text{sint } b) = a$

using $F7$ **by** *simp*

show *?thesis*

proof(*cases sint a < 0*)

case *True* **note** $a = \text{this}$

show *?thesis*

proof(*cases sint b < 0*)

case *True*

with a **show** *?thesis*

by *simp (metis F7 F8 eq minus-equation-iff minus-mult-minus mod-div-mult-eq)*

next

case *False*

from eq **have** $\text{word-of-int } (- (- \text{sint } a \text{ div } \text{sint } b)) * b + \text{word-of-int } (- (- \text{sint } a \text{ mod } \text{sint } b))$

$= a$

by (*metis div-minus-right mod-minus-right*)

with a *False* **show** *?thesis* **by** *simp*

qed

next

case *False* **note** $a = \text{this}$

show *?thesis*

proof(*cases sint b < 0*)

case *True*

with a *eq* **show** *?thesis* **by** *simp*

next

case *False* **with** a **show** *?thesis*

by (*simp add: F7 F8*)

qed

qed

qed

end

notepad begin

have $5 \text{ sdiv } (3 :: \text{word32}) = 1$

and $5 \text{ smod } (3 :: \text{word32}) = 2$

and $5 \text{ sdiv } (-3 :: \text{word32}) = -1$

and $5 \text{ smod } (-3 :: \text{word32}) = 2$

and $(-5) \text{ sdiv } (3 :: \text{word32}) = -1$

and $(-5) \text{ smod } (3 :: \text{word32}) = -2$

```

and (-5) sdiv (-3 :: word32) = 1
and (-5) smod (-3 :: word32) = -2
and -2147483648 sdiv 1 = (-2147483648 :: word32)
by eval+
end

context heap-base
begin

fun binop-Mod :: 'addr val ⇒ 'addr val ⇒ 'addr binop-ret option
where
  binop-Mod (Intg i1) (Intg i2) =
    Some (if i2 = 0 then Inr (addr-of-sys-xcpt ArithmeticException) else Inl (Intg (i1 smod i2)))
| binop-Mod v1 v2 = None

fun binop-Div :: 'addr val ⇒ 'addr val ⇒ 'addr binop-ret option
where
  binop-Div (Intg i1) (Intg i2) =
    Some (if i2 = 0 then Inr (addr-of-sys-xcpt ArithmeticException) else Inl (Intg (i1 sdiv i2)))
| binop-Div v1 v2 = None

primrec binop :: bop ⇒ 'addr val ⇒ 'addr val ⇒ 'addr binop-ret option
where
  binop Eq v1 v2 = Some (Inl (Bool (v1 = v2)))
| binop NotEq v1 v2 = Some (Inl (Bool (v1 ≠ v2)))
| binop LessThan = binop-LessThan
| binop LessOrEqual = binop-LessOrEqual
| binop GreaterThan = binop-GreaterThan
| binop GreaterOrEqual = binop-GreaterOrEqual
| binop Add = binop-Add
| binop Subtract = binop-Subtract
| binop Mult = binop-Mult
| binop Mod = binop-Mod
| binop Div = binop-Div
| binop BinAnd = binop-BinAnd
| binop BinOr = binop-BinOr
| binop BinXor = binop-BinXor
| binop ShiftLeft = binop-ShiftLeft
| binop ShiftRightZeros = binop-ShiftRightZeros
| binop ShiftRightSigned = binop-ShiftRightSigned

end

context
  includes bit-operations-syntax
begin

lemma [simp]:
  (binop-LessThan v1 v2 = Some va) ⟷
  (∃ i1 i2. v1 = Intg i1 ∧ v2 = Intg i2 ∧ va = Inl (Bool (i1 < i2)))
by(cases (v1, v2) rule: binop-LessThan.cases) auto

lemma [simp]:
  (binop-LessOrEqual v1 v2 = Some va) ⟷

```

$(\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Bool } (i1 \leq_s i2)))$
by(cases (v1, v2) rule: binop-LessOrEqual.cases) auto

lemma [simp]:
 $(\text{binop-GreaterThan } v1\ v2 = \text{Some } va) \longleftrightarrow$
 $(\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Bool } (i2 <_s i1)))$
by(cases (v1, v2) rule: binop-GreaterThan.cases) auto

lemma [simp]:
 $(\text{binop-GreaterOrEqual } v1\ v2 = \text{Some } va) \longleftrightarrow$
 $(\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Bool } (i2 \leq_s i1)))$
by(cases (v1, v2) rule: binop-GreaterOrEqual.cases) auto

lemma [simp]:
 $(\text{binop-Add } v1\ v2 = \text{Some } va) \longleftrightarrow$
 $(\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Intg } (i1 + i2)))$
by(cases (v1, v2) rule: binop-Add.cases) auto

lemma [simp]:
 $(\text{binop-Subtract } v1\ v2 = \text{Some } va) \longleftrightarrow$
 $(\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Intg } (i1 - i2)))$
by(cases (v1, v2) rule: binop-Subtract.cases) auto

lemma [simp]:
 $(\text{binop-Mult } v1\ v2 = \text{Some } va) \longleftrightarrow$
 $(\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Intg } (i1 * i2)))$
by(cases (v1, v2) rule: binop-Mult.cases) auto

lemma [simp]:
 $(\text{binop-BinAnd } v1\ v2 = \text{Some } va) \longleftrightarrow$
 $(\exists b1\ b2. v1 = \text{Bool } b1 \wedge v2 = \text{Bool } b2 \wedge va = \text{Inl } (\text{Bool } (b1 \wedge b2))) \vee$
 $(\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Intg } (i1 \text{ AND } i2)))$
by(cases (v1, v2) rule: binop-BinAnd.cases) auto

lemma [simp]:
 $(\text{binop-BinOr } v1\ v2 = \text{Some } va) \longleftrightarrow$
 $(\exists b1\ b2. v1 = \text{Bool } b1 \wedge v2 = \text{Bool } b2 \wedge va = \text{Inl } (\text{Bool } (b1 \vee b2))) \vee$
 $(\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Intg } (i1 \text{ OR } i2)))$
by(cases (v1, v2) rule: binop-BinOr.cases) auto

lemma [simp]:
 $(\text{binop-BinXor } v1\ v2 = \text{Some } va) \longleftrightarrow$
 $(\exists b1\ b2. v1 = \text{Bool } b1 \wedge v2 = \text{Bool } b2 \wedge va = \text{Inl } (\text{Bool } (b1 \neq b2))) \vee$
 $(\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Intg } (i1 \text{ XOR } i2)))$
by(cases (v1, v2) rule: binop-BinXor.cases) auto

lemma [simp]:
 $(\text{binop-ShiftLeft } v1\ v2 = \text{Some } va) \longleftrightarrow$
 $(\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Intg } (i1 \ll \text{unat } (i2 \text{ AND } 0xf))))$
by(cases (v1, v2) rule: binop-ShiftLeft.cases) auto

lemma [simp]:
 $(\text{binop-ShiftRightZeros } v1\ v2 = \text{Some } va) \longleftrightarrow$
 $(\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Intg } (i1 \gg \text{unat } (i2 \text{ AND } 0xf))))$

by(cases (v1, v2) rule: binop-ShiftRightZeros.cases) auto

lemma [simp]:

(binop-ShiftRightSigned v1 v2 = Some va) \longleftrightarrow
 $(\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Intg } (i1 \gg \gg \text{unat } (i2 \text{ AND } 0x1f))))$)

by(cases (v1, v2) rule: binop-ShiftRightSigned.cases) auto

end

context heap-base

begin

lemma [simp]:

(binop-Mod v1 v2 = Some va) \longleftrightarrow
 $(\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge$
 $va = (\text{if } i2 = 0 \text{ then } \text{Inr } (\text{addr-of-sys-xcpt } \text{ArithmeticException}) \text{ else } \text{Inl } (\text{Intg } (i1 \text{ smod } i2))))$)

by(cases (v1, v2) rule: binop-Mod.cases) auto

lemma [simp]:

(binop-Div v1 v2 = Some va) \longleftrightarrow
 $(\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge$
 $va = (\text{if } i2 = 0 \text{ then } \text{Inr } (\text{addr-of-sys-xcpt } \text{ArithmeticException}) \text{ else } \text{Inl } (\text{Intg } (i1 \text{ sdiv } i2))))$)

by(cases (v1, v2) rule: binop-Div.cases) auto

end

3.15.2 Typing for binary operators

inductive WT-binop :: 'm prog \Rightarrow ty \Rightarrow bop \Rightarrow ty \Rightarrow ty \Rightarrow bool ($\langle \cdot \vdash \text{-}\langle \text{-} \rangle \text{-} \cdot \rangle \rightarrow [51, 0, 0, 0, 51]$ 50)

where

WT-binop-Eq:

$P \vdash T1 \leq T2 \vee P \vdash T2 \leq T1 \implies P \vdash T1 \langle \text{Eq} \rangle T2 :: \text{Boolean}$

| WT-binop-NotEq:

$P \vdash T1 \leq T2 \vee P \vdash T2 \leq T1 \implies P \vdash T1 \langle \text{NotEq} \rangle T2 :: \text{Boolean}$

| WT-binop-LessThan:

$P \vdash \text{Integer} \langle \text{LessThan} \rangle \text{Integer} :: \text{Boolean}$

| WT-binop-LessOrEqual:

$P \vdash \text{Integer} \langle \text{LessOrEqual} \rangle \text{Integer} :: \text{Boolean}$

| WT-binop-GreaterThan:

$P \vdash \text{Integer} \langle \text{GreaterThan} \rangle \text{Integer} :: \text{Boolean}$

| WT-binop-GreaterOrEqual:

$P \vdash \text{Integer} \langle \text{GreaterOrEqual} \rangle \text{Integer} :: \text{Boolean}$

| WT-binop-Add:

$P \vdash \text{Integer} \langle \text{Add} \rangle \text{Integer} :: \text{Integer}$

| WT-binop-Subtract:

$P \vdash \text{Integer} \langle \text{Subtract} \rangle \text{Integer} :: \text{Integer}$

| *WT-binop-Mult*:
 $P \vdash \text{Integer} \langle \text{Mult} \rangle \text{Integer} :: \text{Integer}$

| *WT-binop-Div*:
 $P \vdash \text{Integer} \langle \text{Div} \rangle \text{Integer} :: \text{Integer}$

| *WT-binop-Mod*:
 $P \vdash \text{Integer} \langle \text{Mod} \rangle \text{Integer} :: \text{Integer}$

| *WT-binop-BinAnd-Bool*:
 $P \vdash \text{Boolean} \langle \text{BinAnd} \rangle \text{Boolean} :: \text{Boolean}$

| *WT-binop-BinAnd-Int*:
 $P \vdash \text{Integer} \langle \text{BinAnd} \rangle \text{Integer} :: \text{Integer}$

| *WT-binop-BinOr-Bool*:
 $P \vdash \text{Boolean} \langle \text{BinOr} \rangle \text{Boolean} :: \text{Boolean}$

| *WT-binop-BinOr-Int*:
 $P \vdash \text{Integer} \langle \text{BinOr} \rangle \text{Integer} :: \text{Integer}$

| *WT-binop-BinXor-Bool*:
 $P \vdash \text{Boolean} \langle \text{BinXor} \rangle \text{Boolean} :: \text{Boolean}$

| *WT-binop-BinXor-Int*:
 $P \vdash \text{Integer} \langle \text{BinXor} \rangle \text{Integer} :: \text{Integer}$

| *WT-binop-ShiftLeft*:
 $P \vdash \text{Integer} \langle \text{ShiftLeft} \rangle \text{Integer} :: \text{Integer}$

| *WT-binop-ShiftRightZeros*:
 $P \vdash \text{Integer} \langle \text{ShiftRightZeros} \rangle \text{Integer} :: \text{Integer}$

| *WT-binop-ShiftRightSigned*:
 $P \vdash \text{Integer} \langle \text{ShiftRightSigned} \rangle \text{Integer} :: \text{Integer}$

lemma *WT-binopI* [*intro*]:

$P \vdash T1 \leq T2 \vee P \vdash T2 \leq T1 \implies P \vdash T1 \langle \text{Eq} \rangle T2 :: \text{Boolean}$
 $P \vdash T1 \leq T2 \vee P \vdash T2 \leq T1 \implies P \vdash T1 \langle \text{NotEq} \rangle T2 :: \text{Boolean}$
 $\text{bop} = \text{Add} \vee \text{bop} = \text{Subtract} \vee \text{bop} = \text{Mult} \vee \text{bop} = \text{Mod} \vee \text{bop} = \text{Div} \vee \text{bop} = \text{BinAnd} \vee \text{bop} =$
 $\text{BinOr} \vee \text{bop} = \text{BinXor} \vee$
 $\text{bop} = \text{ShiftLeft} \vee \text{bop} = \text{ShiftRightZeros} \vee \text{bop} = \text{ShiftRightSigned}$
 $\implies P \vdash \text{Integer} \langle \text{bop} \rangle \text{Integer} :: \text{Integer}$
 $\text{bop} = \text{LessThan} \vee \text{bop} = \text{LessOrEqual} \vee \text{bop} = \text{GreaterThan} \vee \text{bop} = \text{GreaterOrEqual} \implies P \vdash$
 $\text{Integer} \langle \text{bop} \rangle \text{Integer} :: \text{Boolean}$
 $\text{bop} = \text{BinAnd} \vee \text{bop} = \text{BinOr} \vee \text{bop} = \text{BinXor} \implies P \vdash \text{Boolean} \langle \text{bop} \rangle \text{Boolean} :: \text{Boolean}$

by(*auto intro: WT-binop.intros*)

inductive-cases [*elim*]:

$P \vdash T1 \langle \text{Eq} \rangle T2 :: T$
 $P \vdash T1 \langle \text{NotEq} \rangle T2 :: T$
 $P \vdash T1 \langle \text{LessThan} \rangle T2 :: T$
 $P \vdash T1 \langle \text{LessOrEqual} \rangle T2 :: T$
 $P \vdash T1 \langle \text{GreaterThan} \rangle T2 :: T$

$P \vdash T1 \llbracket \text{GreaterOrEqual} \rrbracket T2 :: T$
 $P \vdash T1 \llbracket \text{Add} \rrbracket T2 :: T$
 $P \vdash T1 \llbracket \text{Subtract} \rrbracket T2 :: T$
 $P \vdash T1 \llbracket \text{Mult} \rrbracket T2 :: T$
 $P \vdash T1 \llbracket \text{Div} \rrbracket T2 :: T$
 $P \vdash T1 \llbracket \text{Mod} \rrbracket T2 :: T$
 $P \vdash T1 \llbracket \text{BinAnd} \rrbracket T2 :: T$
 $P \vdash T1 \llbracket \text{BinOr} \rrbracket T2 :: T$
 $P \vdash T1 \llbracket \text{BinXor} \rrbracket T2 :: T$
 $P \vdash T1 \llbracket \text{ShiftLeft} \rrbracket T2 :: T$
 $P \vdash T1 \llbracket \text{ShiftRightZeros} \rrbracket T2 :: T$
 $P \vdash T1 \llbracket \text{ShiftRightSigned} \rrbracket T2 :: T$

lemma *WT-binop-fun*: $\llbracket P \vdash T1 \llbracket \text{bop} \rrbracket T2 :: T; P \vdash T1 \llbracket \text{bop} \rrbracket T2 :: T' \rrbracket \implies T = T'$
by(cases bop)(auto)

lemma *WT-binop-is-type*:
 $\llbracket P \vdash T1 \llbracket \text{bop} \rrbracket T2 :: T; \text{is-type } P \ T1; \text{is-type } P \ T2 \rrbracket \implies \text{is-type } P \ T$
by(cases bop) auto

inductive *WTrt-binop* :: 'm prog \Rightarrow ty \Rightarrow bop \Rightarrow ty \Rightarrow ty \Rightarrow bool ($\langle \vdash \text{-}\langle \text{-} \rangle \text{-} : \rightarrow [51,0,0,0,51] \ 50$)
where

WTrt-binop-Eq:
 $P \vdash T1 \llbracket \text{Eq} \rrbracket T2 : \text{Boolean}$

| *WTrt-binop-NotEq*:
 $P \vdash T1 \llbracket \text{NotEq} \rrbracket T2 : \text{Boolean}$

| *WTrt-binop-LessThan*:
 $P \vdash \text{Integer} \llbracket \text{LessThan} \rrbracket \text{Integer} : \text{Boolean}$

| *WTrt-binop-LessOrEqual*:
 $P \vdash \text{Integer} \llbracket \text{LessOrEqual} \rrbracket \text{Integer} : \text{Boolean}$

| *WTrt-binop-GreaterThan*:
 $P \vdash \text{Integer} \llbracket \text{GreaterThan} \rrbracket \text{Integer} : \text{Boolean}$

| *WTrt-binop-GreaterOrEqual*:
 $P \vdash \text{Integer} \llbracket \text{GreaterOrEqual} \rrbracket \text{Integer} : \text{Boolean}$

| *WTrt-binop-Add*:
 $P \vdash \text{Integer} \llbracket \text{Add} \rrbracket \text{Integer} : \text{Integer}$

| *WTrt-binop-Subtract*:
 $P \vdash \text{Integer} \llbracket \text{Subtract} \rrbracket \text{Integer} : \text{Integer}$

| *WTrt-binop-Mult*:
 $P \vdash \text{Integer} \llbracket \text{Mult} \rrbracket \text{Integer} : \text{Integer}$

| *WTrt-binop-Div*:
 $P \vdash \text{Integer} \llbracket \text{Div} \rrbracket \text{Integer} : \text{Integer}$

| *WTrt-binop-Mod*:
 $P \vdash \text{Integer} \llbracket \text{Mod} \rrbracket \text{Integer} : \text{Integer}$

| *WTrt-binop-BinAnd-Bool*:
 $P \vdash \text{Boolean} \langle \text{BinAnd} \rangle \text{Boolean} : \text{Boolean}$

| *WTrt-binop-BinAnd-Int*:
 $P \vdash \text{Integer} \langle \text{BinAnd} \rangle \text{Integer} : \text{Integer}$

| *WTrt-binop-BinOr-Bool*:
 $P \vdash \text{Boolean} \langle \text{BinOr} \rangle \text{Boolean} : \text{Boolean}$

| *WTrt-binop-BinOr-Int*:
 $P \vdash \text{Integer} \langle \text{BinOr} \rangle \text{Integer} : \text{Integer}$

| *WTrt-binop-BinXor-Bool*:
 $P \vdash \text{Boolean} \langle \text{BinXor} \rangle \text{Boolean} : \text{Boolean}$

| *WTrt-binop-BinXor-Int*:
 $P \vdash \text{Integer} \langle \text{BinXor} \rangle \text{Integer} : \text{Integer}$

| *WTrt-binop-ShiftLeft*:
 $P \vdash \text{Integer} \langle \text{ShiftLeft} \rangle \text{Integer} : \text{Integer}$

| *WTrt-binop-ShiftRightZeros*:
 $P \vdash \text{Integer} \langle \text{ShiftRightZeros} \rangle \text{Integer} : \text{Integer}$

| *WTrt-binop-ShiftRightSigned*:
 $P \vdash \text{Integer} \langle \text{ShiftRightSigned} \rangle \text{Integer} : \text{Integer}$

lemma *WTrt-binopI* [*intro*]:

$P \vdash T1 \langle \text{Eq} \rangle T2 : \text{Boolean}$
 $P \vdash T1 \langle \text{NotEq} \rangle T2 : \text{Boolean}$
 $\text{bop} = \text{Add} \vee \text{bop} = \text{Subtract} \vee \text{bop} = \text{Mult} \vee \text{bop} = \text{Div} \vee \text{bop} = \text{Mod} \vee \text{bop} = \text{BinAnd} \vee \text{bop} = \text{BinOr} \vee \text{bop} = \text{BinXor} \vee$
 $\text{bop} = \text{ShiftLeft} \vee \text{bop} = \text{ShiftRightZeros} \vee \text{bop} = \text{ShiftRightSigned}$
 $\implies P \vdash \text{Integer} \langle \text{bop} \rangle \text{Integer} : \text{Integer}$
 $\text{bop} = \text{LessThan} \vee \text{bop} = \text{LessOrEqual} \vee \text{bop} = \text{GreaterThan} \vee \text{bop} = \text{GreaterOrEqual} \implies P \vdash$
 $\text{Integer} \langle \text{bop} \rangle \text{Integer} : \text{Boolean}$
 $\text{bop} = \text{BinAnd} \vee \text{bop} = \text{BinOr} \vee \text{bop} = \text{BinXor} \implies P \vdash \text{Boolean} \langle \text{bop} \rangle \text{Boolean} : \text{Boolean}$

by(*auto intro: WTrt-binop.intros*)

inductive-cases *WTrt-binop-cases* [*elim*]:

$P \vdash T1 \langle \text{Eq} \rangle T2 : T$
 $P \vdash T1 \langle \text{NotEq} \rangle T2 : T$
 $P \vdash T1 \langle \text{LessThan} \rangle T2 : T$
 $P \vdash T1 \langle \text{LessOrEqual} \rangle T2 : T$
 $P \vdash T1 \langle \text{GreaterThan} \rangle T2 : T$
 $P \vdash T1 \langle \text{GreaterOrEqual} \rangle T2 : T$
 $P \vdash T1 \langle \text{Add} \rangle T2 : T$
 $P \vdash T1 \langle \text{Subtract} \rangle T2 : T$
 $P \vdash T1 \langle \text{Mult} \rangle T2 : T$
 $P \vdash T1 \langle \text{Div} \rangle T2 : T$
 $P \vdash T1 \langle \text{Mod} \rangle T2 : T$
 $P \vdash T1 \langle \text{BinAnd} \rangle T2 : T$
 $P \vdash T1 \langle \text{BinOr} \rangle T2 : T$

$P \vdash T1 \ll \text{BinXor} \gg T2 : T$
 $P \vdash T1 \ll \text{ShiftLeft} \gg T2 : T$
 $P \vdash T1 \ll \text{ShiftRightZeros} \gg T2 : T$
 $P \vdash T1 \ll \text{ShiftRightSigned} \gg T2 : T$

inductive-simps *WTrt-binop-simps* [*simp*]:

$P \vdash T1 \ll \text{Eq} \gg T2 : T$
 $P \vdash T1 \ll \text{NotEq} \gg T2 : T$
 $P \vdash T1 \ll \text{LessThan} \gg T2 : T$
 $P \vdash T1 \ll \text{LessOrEqual} \gg T2 : T$
 $P \vdash T1 \ll \text{GreaterThan} \gg T2 : T$
 $P \vdash T1 \ll \text{GreaterOrEqual} \gg T2 : T$
 $P \vdash T1 \ll \text{Add} \gg T2 : T$
 $P \vdash T1 \ll \text{Subtract} \gg T2 : T$
 $P \vdash T1 \ll \text{Mult} \gg T2 : T$
 $P \vdash T1 \ll \text{Div} \gg T2 : T$
 $P \vdash T1 \ll \text{Mod} \gg T2 : T$
 $P \vdash T1 \ll \text{BinAnd} \gg T2 : T$
 $P \vdash T1 \ll \text{BinOr} \gg T2 : T$
 $P \vdash T1 \ll \text{BinXor} \gg T2 : T$
 $P \vdash T1 \ll \text{ShiftLeft} \gg T2 : T$
 $P \vdash T1 \ll \text{ShiftRightZeros} \gg T2 : T$
 $P \vdash T1 \ll \text{ShiftRightSigned} \gg T2 : T$

fun *binop-relevant-class* :: *bop* \Rightarrow *'m prog* \Rightarrow *cname* \Rightarrow *bool*

where

$\text{binop-relevant-class } \text{Div} = (\lambda P C. P \vdash \text{ArithmeticException} \preceq^* C)$
 $\text{binop-relevant-class } \text{Mod} = (\lambda P C. P \vdash \text{ArithmeticException} \preceq^* C)$
 $\text{binop-relevant-class } - = (\lambda P C. \text{False})$

lemma *WT-binop-WTrt-binop*:

$P \vdash T1 \ll \text{bop} \gg T2 :: T \Longrightarrow P \vdash T1 \ll \text{bop} \gg T2 : T$

by(*auto elim*: *WT-binop.cases*)

context *heap begin*

lemma *binop-progress*:

$\llbracket \text{typeof}_h v1 = \llbracket T1 \rrbracket; \text{typeof}_h v2 = \llbracket T2 \rrbracket; P \vdash T1 \ll \text{bop} \gg T2 : T \rrbracket$
 $\Longrightarrow \exists va. \text{binop } \text{bop } v1 v2 = \llbracket va \rrbracket$

by(*cases bop*)(*auto del*: *disjCI split del*: *if-split*)

lemma *binop-type*:

assumes *wf*: *wf-prog wf-md P*

and pre: *preallocated h*

and type: $\text{typeof}_h v1 = \llbracket T1 \rrbracket \text{typeof}_h v2 = \llbracket T2 \rrbracket P \vdash T1 \ll \text{bop} \gg T2 : T$

shows $\text{binop } \text{bop } v1 v2 = \llbracket \text{Inl } v \rrbracket \Longrightarrow P, h \vdash v : \leq T$

and $\text{binop } \text{bop } v1 v2 = \llbracket \text{Inr } a \rrbracket \Longrightarrow P, h \vdash \text{Addr } a : \leq \text{Class Throwable}$

using *type*

apply(*case-tac* [!] *bop*)

apply(*auto split*: *if-split-asm simp add*: *conf-def wf-preallocatedD[OF wf pre]*)

done

lemma *binop-relevant-class*:

assumes *wf*: *wf-prog wf-md P*

and *pre*: *preallocated h*
and *bop*: *binop bop v1 v2 = [Inr a]*
and *sup*: $P \vdash \text{cname-of } h \ a \preceq^* C$
shows *binop-relevant-class bop P C*
using *assms*
by(*cases bop*)(*auto split: if-split-asm*)

end

lemma *WTrt-binop-fun*: $\llbracket P \vdash T1 \llbracket \text{bop} \rrbracket T2 : T; P \vdash T1 \llbracket \text{bop} \rrbracket T2 : T' \rrbracket \implies T = T'$
by(*cases bop*)(*auto*)

lemma *WTrt-binop-THE [simp]*: $P \vdash T1 \llbracket \text{bop} \rrbracket T2 : T \implies \text{The } (WTrt\text{-binop } P \ T1 \ \text{bop} \ T2) = T$
by(*auto dest: WTrt-binop-fun*)

lemma *WTrt-binop-widen-mono*:
 $\llbracket P \vdash T1 \llbracket \text{bop} \rrbracket T2 : T; P \vdash T1' \leq T1; P \vdash T2' \leq T2 \rrbracket \implies \exists T'. P \vdash T1' \llbracket \text{bop} \rrbracket T2' : T' \wedge P \vdash T' \leq T$
by(*cases bop*)(*auto elim!: WTrt-binop-cases*)

lemma *WTrt-binop-is-type*:
 $\llbracket P \vdash T1 \llbracket \text{bop} \rrbracket T2 : T; \text{is-type } P \ T1; \text{is-type } P \ T2 \rrbracket \implies \text{is-type } P \ T$
by(*cases bop*) *auto*

3.15.3 Code generator setup

lemmas [*code*] =
heap-base.binop-Div.simps
heap-base.binop-Mod.simps
heap-base.binop.simps

code-pred

(modes: i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow bool, i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow bool)
WTrt-binop

.

code-pred

(modes: i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow bool, i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow bool)
WTrt-binop

.

lemma *eval-WTrt-binop-i-i-i-i-o*:

Predicate.eval (WTrt-binop-i-i-i-i-o P T1 bop T2) T \longleftrightarrow P \vdash T1 $\llbracket \text{bop} \rrbracket$ T2 : T
by(*auto elim: WTrt-binop-i-i-i-i-oE intro: WTrt-binop-i-i-i-i-oI*)

lemma *the-WTrt-binop-code*:

(THE T. P \vdash T1 $\llbracket \text{bop} \rrbracket$ T2 : T) = Predicate.the (WTrt-binop-i-i-i-i-o P T1 bop T2)
by(*simp add: Predicate.the-def eval-WTrt-binop-i-i-i-i-o*)

end

3.16 The Jinja Type System as a Semilattice

theory *SemiType*

imports

WellForm
../DFA/Semilattices

begin**inductive-set**

$widen1 :: 'a\ prog \Rightarrow (ty \times ty)\ set$
and $widen1\text{-syntax} :: 'a\ prog \Rightarrow ty \Rightarrow ty \Rightarrow bool\ (\langle\!-\!| - \langle^1 \!-\! \rangle [71,71,71]\ 70)$
for $P :: 'a\ prog$

where

$P \vdash C \langle^1 D \equiv (C, D) \in widen1\ P$

| *widen1-Array-Object*:

$P \vdash Array\ (Class\ Object) \langle^1\ Class\ Object$

| *widen1-Array-Integer*:

$P \vdash Array\ Integer \langle^1\ Class\ Object$

| *widen1-Array-Boolean*:

$P \vdash Array\ Boolean \langle^1\ Class\ Object$

| *widen1-Array-Void*:

$P \vdash Array\ Void \langle^1\ Class\ Object$

| *widen1-Class*:

$P \vdash C \langle^1 D \Longrightarrow P \vdash Class\ C \langle^1\ Class\ D$

| *widen1-Array-Array*:

$\llbracket P \vdash T \langle^1 U; \neg\ is\ NT\text{-}Array\ T \rrbracket \Longrightarrow P \vdash Array\ T \langle^1\ Array\ U$

abbreviation $widen1\text{-trancl} :: 'a\ prog \Rightarrow ty \Rightarrow ty \Rightarrow bool\ (\langle\!-\!| - \langle^+ \!-\! \rangle [71,71,71]\ 70)$ **where**

$P \vdash T \langle^+ U \equiv (T, U) \in trancl\ (widen1\ P)$

abbreviation $widen1\text{-rtrancl} :: 'a\ prog \Rightarrow ty \Rightarrow ty \Rightarrow bool\ (\langle\!-\!| - \langle^* \!-\! \rangle [71,71,71]\ 70)$ **where**

$P \vdash T \langle^* U \equiv (T, U) \in rtrancl\ (widen1\ P)$

inductive-simps $widen1\text{-simps1}\ [simp]$:

$P \vdash Integer \langle^1 T$
 $P \vdash Boolean \langle^1 T$
 $P \vdash Void \langle^1 T$
 $P \vdash Class\ Object \langle^1 T$
 $P \vdash NT \langle^1 U$

inductive-simps $widen1\text{-simps}\ [simp]$:

$P \vdash Array\ (Class\ Object) \langle^1 T$
 $P \vdash Array\ Integer \langle^1 T$
 $P \vdash Array\ Boolean \langle^1 T$
 $P \vdash Array\ Void \langle^1 T$
 $P \vdash Class\ C \langle^1 T$
 $P \vdash T \langle^1 Array\ U$

lemma *is-type-widen1*:

assumes $icO: is\ class\ P\ Object$
shows $P \vdash T \langle^1 U \Longrightarrow is\ type\ P\ T$

by(*induct rule: widen1.induct*)(*auto intro: subcls-is-class icO split: ty.split dest: is-type-ground-type*)

lemma *widen1-NT-Array*:

assumes *is-NT-Array T*

shows $\neg P \vdash T[] <^1 U$

proof

assume $P \vdash T[] <^1 U$ **thus** *False* **using** *assms*

by(*induct T[] U arbitrary: T*) *auto*

qed

lemma *widen1-is-type*:

assumes *wfP: wf-prog wfmd P*

shows $(A, B) \in \text{widen1 } P \implies \text{is-type } P B$

proof(*induct rule: widen1.induct*)

case (*widen1-Class C D*)

hence *is-class P C is-class P D*

by(*auto intro: subcls-is-class converse-subcls-is-class[OF wfP]*)

thus *?case* **by** *simp*

next

case (*widen1-Array-Array T U*)

thus *?case* **by**(*cases U*)(*auto elim: widen1.cases*)

qed(*insert wfP, auto*)

lemma *widen1-trancl-is-type*:

assumes *wfP: wf-prog wfmd P*

shows $(A, B) \in (\text{widen1 } P)^+ \implies \text{is-type } P B$

apply(*induct rule: trancl-induct*)

apply(*auto intro: widen1-is-type[OF wfP]*)

done

lemma *single-valued-widen1*:

assumes *wf: wf-prog wf-md P*

shows *single-valued (widen1 P)*

proof(*rule single-valuedI*)

fix *x y z*

assume $P \vdash x <^1 y$ $P \vdash x <^1 z$

thus $y = z$

proof(*induct arbitrary: z rule: widen1.induct*)

case *widen1-Class*

with *single-valued-subcls1[OF wf]* **show** *?case*

by(*auto dest: single-valuedpD*)

next

case (*widen1-Array-Array T U z*)

from $\langle P \vdash T[] <^1 z \rangle \langle P \vdash T <^1 U \rangle \langle \neg \text{is-NT-Array } T \rangle$

obtain z' **where** $z': z = z'[]$ **and** $Tz': P \vdash T <^1 z'$

by(*auto elim: widen1.cases*)

with $\langle P \vdash T <^1 z' \implies U = z' \rangle$ **have** $U = z'$ **by** *blast*

with z' **show** *?case* **by** *simp*

qed *simp-all*

qed

function *inheritance-level* :: *'a prog* \Rightarrow *cname* \Rightarrow *nat* **where**

inheritance-level P C =

(*if acyclicP (subcls1 P) \wedge is-class P C \wedge C \neq Object*

```

    then Suc (inheritance-level P (fst (the (class P C))))
    else 0)
by(pat-completeness, auto)
termination
proof(relation same-fst (λP. acyclicP (subcls1 P)) (λP. {(C, C'). (subcls1 P)-1-1 C C'}))
  show wf (same-fst (λP. acyclicP (subcls1 P)) (λP. {(C, C'). (subcls1 P)-1-1 C C'}))
    by(rule wf-same-fst)(rule acyclicP-wf-subcls1 [unfolded wfp-def])
qed(auto simp add: is-class-def intro: subcls1I)

fun subtype-measure :: 'a prog ⇒ ty ⇒ nat where
  subtype-measure P (Class C) = inheritance-level P C
| subtype-measure P (Array T) = 1 + subtype-measure P T
| subtype-measure P T = 0

lemma subtype-measure-measure:
  assumes acyclic: acyclicP (subcls1 P)
  and widen1: P ⊢ x <1 y
  shows subtype-measure P y < subtype-measure P x
using widen1
proof(induct rule: widen1.induct)
  case (widen1-Class C D)
  then obtain rest where is-class P C C ≠ Object class P C = [(D, rest)]
    by(auto elim!: subcls1.cases simp: is-class-def)
  thus ?case using acyclic by(simp)
qed(simp-all)

lemma wf-converse-widen1:
  assumes wfP: wf-prog wfmc P
  shows wf ((widen1 P)^-1)
proof(rule wf-subset)
  from wfP have acyclicP (subcls1 P) by(rule acyclic-subcls1)
  thus (widen1 P)-1 ⊆ measure (subtype-measure P)
    by(auto dest: subtype-measure-measure)
qed simp

fun super :: 'a prog ⇒ ty ⇒ ty
where
  super P (Array Integer) = Class Object
| super P (Array Boolean) = Class Object
| super P (Array Void) = Class Object
| super P (Array (Class C)) = (if C = Object then Class Object else Array (super P (Class C)))
| super P (Array (Array T)) = Array (super P (Array T))
| super P (Class C) = Class (fst (the (class P C)))

lemma superI:
  P ⊢ T <1 U ⇒ super P T = U
proof(induct rule: widen1.induct)
  case (widen1-Array-Array T U)
  thus ?case by(cases T) auto
qed(auto dest: subcls1D)

lemma Class-widen1-super:
  P ⊢ Class C' <1 U' ⇔ is-class P C' ∧ C' ≠ Object ∧ U' = super P (Class C')
  (is ?lhs ⇔ ?rhs)

```

```

proof(rule iffI)
  assume ?lhs thus ?rhs
  by(auto intro: subcls-is-class simp add: superI simp del: super.simps)
next
  assume ?rhs thus ?lhs
  by(auto simp add: is-class-def intro: subcls1.intros)
qed

```

lemma *super-widen1*:

assumes *icO*: *is-class P Object*

shows $P \vdash T <^1 U \iff \text{is-type } P \ T \wedge (\text{case } T \text{ of Class } C \Rightarrow (C \neq \text{Object} \wedge U = \text{super } P \ T)$
 $\quad \quad \quad | \text{Array } T' \Rightarrow U = \text{super } P \ T$
 $\quad \quad \quad | - \Rightarrow \text{False})$

proof(*induct T arbitrary: U*)

case *Class* **thus** ?case **using** *Class-widen1-super* **by**(*simp*)

next

case (*Array T' U'*)

note *IH* = *this*

have $P \vdash T'[] <^1 U' = (\text{is-type } P \ (T'[]) \wedge U' = \text{super } P \ (T'[]))$

proof(rule iffI)

assume *wd*: $P \vdash T'[] <^1 U'$

with *icO* **have** *is-type P (T'[])* **by**(rule *is-type-widen1*)

moreover from *wd* **have** $\text{super } P \ (T'[]) = U'$ **by**(rule *superI*)

ultimately show $\text{is-type } P \ (T'[]) \wedge U' = \text{super } P \ (T'[])$ **by** *simp*

next

assume $\text{is-type } P \ (T'[]) \wedge U' = \text{super } P \ (T'[])$

then obtain *is-type P (T'[])* **and** *U'*: $U' = \text{super } P \ (T'[])$..

thus $P \vdash T'[] <^1 U'$

proof(*cases T'*)

case (*Class D*)

thus ?*thesis* **using** *U' icO <is-type P (T'[])>*

by(*cases D = Object*)(auto simp add: *is-class-def* intro: *subcls1.intros*)

next

case *Array* **thus** ?*thesis*

using *IH <is-type P (T'[])> U'* **by**(auto simp add: *ty.split-asm*)

qed *simp-all*

qed

thus ?case **by**(*simp*)

qed(*simp-all*)

definition *sup* :: '*c prog* \Rightarrow *ty* \Rightarrow *ty* \Rightarrow *ty* *err* **where**

sup P T U \equiv

if is-refT T \wedge *is-refT U*

then OK (if U = NT then T

else if T = NT then U

else exec-lub (widen1 P) (super P) T U)

else if (T = U) then OK T else Err

lemma *sup-def'*:

sup P = $(\lambda T \ U.$

if is-refT T \wedge *is-refT U*

then OK (if U = NT then T

else if T = NT then U

else exec-lub (widen1 P) (super P) T U)

else if (T = U) then OK T else Err)
 by (simp add: fun-eq-iff sup-def)

definition *esl* :: 'm prog \Rightarrow ty *esl*

where

esl P = (types P, widen P, sup P)

lemma *order-widen* [intro,simp]:

wf-prog m P \Longrightarrow order (widen P)

unfolding *Semilat.order-def* *lesub-def*

by (auto intro: widen-trans widen-antisym)

lemma *subcls1-trancl-widen1-trancl*:

(*subcls1* P)⁺⁺ C D \Longrightarrow P \vdash Class C $<^+$ Class D

by(*induct* rule: tranclp-induct[consumes 1, case-names base step])

(auto intro: trancl-into-trancl)

lemma *subcls-into-widen1-rtrancl*:

P \vdash C \preceq^* D \Longrightarrow P \vdash Class C $<^*$ Class D

by(*induct* rule: rtranclp-induct)(auto intro: rtrancl-into-rtrancl)

lemma *not-widen1-NT-Array*:

P \vdash U $<^1$ T \Longrightarrow \neg *is-NT-Array* T

by(*induct* rule: widen1.induct)(auto)

lemma *widen1-trancl-into-Array-widen1-trancl*:

$\llbracket P \vdash A <^+ B; \neg \text{is-NT-Array } A \rrbracket \Longrightarrow P \vdash A[] <^+ B[]$

by(*induct* rule: converse-trancl-induct)

(auto intro: trancl-into-trancl2 widen1-Array-Array dest: not-widen1-NT-Array)

lemma *widen1-rtrancl-into-Array-widen1-rtrancl*:

$\llbracket P \vdash A <^* B; \neg \text{is-NT-Array } A \rrbracket \Longrightarrow P \vdash A[] <^* B[]$

by(blast elim: rtranclE intro: trancl-into-rtrancl widen1-trancl-into-Array-widen1-trancl rtrancl-into-trancl1)

lemma *Array-Object-widen1-trancl*:

assumes *wf*: *wf-prog* *wmdc* P

and *itA*: *is-type* P (A[])

shows P \vdash A[] $<^+$ Class Object

using *itA*

proof(*induction* A)

case (Class C)

hence *is-class* P C **by** *simp*

hence P \vdash C \preceq^* Object **by**(rule *subcls-C-Object*[OF - *wf*])

hence P \vdash Class C $<^*$ Class Object **by**(rule *subcls-into-widen1-rtrancl*)

hence P \vdash Class C[] $<^*$ Class Object[]

by(rule *widen1-rtrancl-into-Array-widen1-rtrancl*) *simp*

thus ?case **by**(rule *rtrancl-into-trancl1*) *simp*

next

case (Array A)

from $\langle \text{is-type } P (A[][][]) \rangle$ **have** *is-type* P (A[]) **by**(rule *is-type-ArrayD*)

hence P \vdash A[] $<^+$ Class Object **by**(rule *Array.IH*)

moreover from $\langle \text{is-type } P (A[][][]) \rangle$ **have** $\neg \text{is-NT-Array } (A[])$ **by** *auto*

ultimately have P \vdash A[][] $<^+$ Class Object[]

by(rule *widen1-trancl-into-Array-widen1-trancl*)

thus ?case **by**(rule trancl-into-trancl) simp
qed auto

lemma widen-into-widen1-trancl:

assumes wf: wf-prog wfmd P

shows $\llbracket P \vdash A \leq B; A \neq B; A \neq NT; \text{is-type } P A \rrbracket \implies P \vdash A <^+ B$

proof(induct rule: widen.induct)

case (widen-subcls C D)

from $\langle \text{Class } C \neq \text{Class } D \rangle \langle P \vdash C \preceq^* D \rangle$ **have** (subcls1 P)⁺⁺ C D

by(auto elim: rtranclp.cases intro: rtranclp-into-tranclp1)

thus ?case **by**(rule subcls1-trancl-widen1-trancl)

next

case widen-array-object **thus** ?case **by**(auto intro: Array-Object-widen1-trancl[OF wf])

next

case (widen-array-array A B)

hence $P \vdash A <^+ B$ **by**(cases A) auto

with $\langle \text{is-type } P (A[]) \rangle$ **show** ?case **by**(auto intro: widen1-trancl-into-Array-widen1-trancl)

qed(auto)

lemma wf-prog-impl-acc-widen:

assumes wfP: wf-prog wfmd P

shows acc (types P) (widen P)

proof –

from wf-converse-widen1[OF wfP]

have wf $((\text{widen1 } P)^{-1})^+ \text{ by (rule wf-trancl)}$

hence wfw1t: $\bigwedge M T. T \in M \implies (\exists z \in M. \forall y. (y, z) \in ((\text{widen1 } P)^{-1})^+ \longrightarrow y \notin M)$

by(auto simp only: wf-eq-minimal)

have wf $\{(y, x). \text{is-type } P x \wedge \text{is-type } P y \wedge \text{widen } P x y \wedge x \neq y\}$

unfolding wf-eq-minimal

proof(intro strip)

fix M and T :: ty

assume TM: $T \in M$

show $\exists z \in M. \forall y. (y, z) \in \{(y, T). \text{is-type } P T \wedge \text{is-type } P y \wedge \text{widen } P T y \wedge T \neq y\} \longrightarrow y \notin M$

proof(cases $(\exists C. \text{Class } C \in M \wedge \text{is-class } P C) \vee (\exists U. U[] \in M \wedge \text{is-type } P (U[]))$)

case True

have BNTthesis: $\bigwedge B. \llbracket B \in (M \cap \text{types } P) - \{NT\} \rrbracket \implies \text{?thesis}$

proof –

fix B

assume BM: $B \in M \cap \text{types } P - \{NT\}$

from wfw1t[OF BM] **obtain** z

where zM: $z \in M$

and znnt: $z \neq NT$

and itz: $\text{is-type } P z$

and y: $\bigwedge y. (y, z) \in ((\text{widen1 } P)^{-1})^+ \implies y \notin M \cap \text{types } P - \{NT\}$ **by** blast

show ?thesis B

proof(rule beXI[OF - zM], rule allI, rule impI)

fix y

assume $(y, z) \in \{(y, T). \text{is-type } P T \wedge \text{is-type } P y \wedge \text{widen } P T y \wedge T \neq y\}$

hence Pzy: $P \vdash z \leq y$ **and** zy: $z \neq y$ **and** $\text{is-type } P y$ **by** auto

hence $P \vdash z <^+ y$ **using** znnt itz

by $-(\text{rule widen-into-widen1-trancl}[OF wfP])$

hence ynM: $y \notin M \cap \text{types } P - \{NT\}$

by $-(\text{rule } y, \text{simp add: trancl-converse})$


```

    thus  $y \notin M$  using  $Pzy$  znnt  $\langle is\text{-}type\ P\ y \rangle$  by auto
  qed
qed
from True show ?thesis by(fastforce intro: BNTthesis)
next
case False

hence not-is-class:  $\bigwedge C. Class\ C \in M \implies \neg is\text{-}class\ P\ C$ 
and not-is-array:  $\bigwedge U. U[] \in M \implies \neg is\text{-}type\ P\ (U[])$  by simp-all

show ?thesis
proof(cases  $\exists C. Class\ C \in M$ )
  case True
  then obtain  $C$  where  $Class\ C \in M$  ..
  with not-is-class[of  $C$ ] show ?thesis
    by(blast dest: rtranclD subcls-is-class Class-widen)
  next
  case False
  show ?thesis
  proof(cases  $\exists T. Array\ T \in M$ )
    case True
    then obtain  $U$  where  $U: Array\ U \in M$  ..
    hence  $\neg is\text{-}type\ P\ (U[])$  by(rule not-is-array)
    thus ?thesis using  $U$  by(auto simp del: is-type.simps)
  next
  case False
  with  $\langle \neg (\exists C. Class\ C \in M) \rangle TM$ 
  have  $\forall y. P \vdash T \leq y \wedge T \neq y \longrightarrow y \notin M$ 
    by(cases  $T$ )(fastforce simp add: NT-widen)+
  thus ?thesis using  $TM$  by blast
  qed
  qed
  qed
  qed
  thus ?thesis by(simp add: Semilat.acc-def lesssub-def lesub-def)
qed

```

lemmas $wf\text{-}widen\text{-}acc = wf\text{-}prog\text{-}impl\text{-}acc\text{-}widen$

declare $wf\text{-}widen\text{-}acc$ [intro, simp]

lemma *acyclic-widen1*:

$wf\text{-}prog\ wfmc\ P \implies acyclic\ (widen1\ P)$

by(auto dest: wf-converse-widen1 wf-acyclic simp add: acyclic-converse)

lemma *widen1-into-widen*:

$(A, B) \in widen1\ P \implies P \vdash A \leq B$

by(induct rule: widen1.induct)(auto intro: widen.intros)

lemma *widen1-rtrancl-into-widen*:

$P \vdash A <^* B \implies P \vdash A \leq B$

by(induct rule: rtrancl-induct)(auto dest!: widen1-into-widen elim: widen-trans)

lemma *widen-eq-widen1-trancl*:

$\llbracket wf\text{-}prog\ wf\text{-}md\ P; T \neq NT; T \neq U; is\text{-}type\ P\ T \rrbracket \implies P \vdash T \leq U \longleftrightarrow P \vdash T <^+ U$

by(*blast intro: widen-into-widen1-trancl widen1-rtrancl-into-widen trancl-into-rtrancl*)

lemma *sup-is-type*:

assumes *wf: wf-prog wf-md P*

and *itA: is-type P A*

and *itB: is-type P B*

and *sup: sup P A B = OK T*

shows *is-type P T*

proof –

{ assume *ANT: A ≠ NT*

and *BNT: B ≠ NT*

and *AnB: A ≠ B*

and *RTA: is-refT A*

and *RTB: is-refT B*

with *itA itB* have *AObject: P ⊢ A ≤ Class Object*

and *BObject: P ⊢ B ≤ Class Object*

by(*auto intro: is-refType-widen-Object[OF wf]*)

have *is-type P (exec-lub (widen1 P) (super P) A B)*

proof(*cases A = Class Object ∨ B = Class Object*)

case *True*

hence *exec-lub (widen1 P) (super P) A B = Class Object*

proof(*rule disjE*)

assume *A: A = Class Object*

moreover

from *BObject BNT itB* have *P ⊢ B <* Class Object*

by(*cases B = Class Object*)(*auto intro: trancl-into-rtrancl widen-into-widen1-trancl[OF wf]*)

hence *is-ub ((widen1 P)*) (Class Object) B (Class Object)*

by(*auto intro: is-ubI*)

hence *is-lub ((widen1 P)*) (Class Object) B (Class Object)*

by(*auto simp add: is-lub-def dest: is-ubD*)

with *acyclic-widen1[OF wf]*

have *exec-lub (widen1 P) (super P) (Class Object) B = Class Object*

by(*auto intro: exec-lub-conv superI*)

ultimately show *exec-lub (widen1 P) (super P) A B = Class Object* by *simp*

next

assume *B: B = Class Object*

moreover

from *AObject ANT itA*

have *(A, Class Object) ∈ (widen1 P)**

by(*cases A = Class Object, auto intro: trancl-into-rtrancl widen-into-widen1-trancl[OF wf]*)

hence *is-ub ((widen1 P)*) (Class Object) A (Class Object)*

by(*auto intro: is-ubI*)

hence *is-lub ((widen1 P)*) (Class Object) A (Class Object)*

by(*auto simp add: is-lub-def dest: is-ubD*)

with *acyclic-widen1[OF wf]*

have *exec-lub (widen1 P) (super P) A (Class Object) = Class Object*

by(*auto intro: exec-lub-conv superI*)

ultimately show *exec-lub (widen1 P) (super P) A B = Class Object* by *simp*

qed

with *wf* show *?thesis* by(*simp*)

next

case *False*

hence *AnObject: A ≠ Class Object*

and *BnObject: B ≠ Class Object* by *auto*

from *widen-into-widen1-trancl*[*OF wf AObject AnObject ANT itA*]
have $P \vdash A <^* \text{Class Object}$ **by**(*rule trancl-into-rtrancl*)
moreover from *widen-into-widen1-trancl*[*OF wf BObject BnObject BNT itB*]
have $P \vdash B <^* \text{Class Object}$ **by**(*rule trancl-into-rtrancl*)
ultimately have *is-lub* $((\text{widen1 } P)^*) A B$ (*exec-lub* $(\text{widen1 } P)$ (*super* P) $A B$)
by(*rule is-lub-exec-lub*[*OF single-valued-widen1* [*OF wf*] *acyclic-widen1* [*OF wf*]])(*auto intro*:
superI)
hence *Aew1*: $P \vdash A <^* \text{exec-lub} (\text{widen1 } P) (\text{super } P) A B$
by(*auto simp add: is-lub-def dest!: is-ubD*)
thus ?thesis
proof(*rule rtranclE*)
assume $A = \text{exec-lub} (\text{widen1 } P) (\text{super } P) A B$
with *itA* **show** ?thesis **by** *simp*
next
fix A'
assume $P \vdash A' <^1 \text{exec-lub} (\text{widen1 } P) (\text{super } P) A B$
thus ?thesis **by**(*rule widen1-is-type*[*OF wf*])
qed
qed }
with *is-class-Object*[*OF wf*] *sup itA itB* **show** ?thesis **unfolding** *sup-def*
by(*cases A = B*)(*auto split: if-split-asm simp add: exec-lub-refl*)
qed

lemma *closed-err-types*:

assumes *wfP*: *wf-prog wf-mb P*
shows *closed* (*err* (*types P*)) (*lift2* (*sup P*))
proof –
{ **fix** $A B$
assume *it*: *is-type P A is-type P B*
and $A \neq NT B \neq NT A \neq B$
and *is-refT A is-refT B*
hence *is-type P* (*exec-lub* $(\text{widen1 } P) (\text{super } P) A B$)
using *sup-is-type*[*OF wfP it*] **by**(*simp add: sup-def*) }
with *is-class-Object*[*OF wfP*] **show** ?thesis
unfolding *closed-def plussub-def lift2-def sup-def'*
by(*auto split: err.split ty.splits*)(*auto simp add: exec-lub-refl*)
qed

lemma *widen-into-widen1-rtrancl*:

$\llbracket \text{wf-prog wfmd } P; \text{widen } P A B; A \neq NT; \text{is-type } P A \rrbracket \implies (A, B) \in (\text{widen1 } P)^*$
by(*cases A = B*)(*auto intro: trancl-into-rtrancl widen-into-widen1-trancl*)

lemma *sup-widen-greater*:

assumes *wfP*: *wf-prog wf-mb P*
and *it1*: *is-type P t1*
and *it2*: *is-type P t2*
and *sup*: *sup P t1 t2 = OK s*
shows *widen P t1 s* \wedge *widen P t2 s*
proof –
{ **assume** *t1*: *is-refT t1*
and *t2*: *is-refT t2*
and *t1NT*: $t1 \neq NT$
and *t2NT*: $t2 \neq NT$

with $it1\ it2\ wfP$ **have** $P \vdash t1 \leq \text{Class Object } P \vdash t2 \leq \text{Class Object}$
by(*auto intro: is-refType-widen-Object*)
with $t1NT\ t2NT\ it1\ it2$
have $P \vdash t1 <^* \text{Class Object } P \vdash t2 <^* \text{Class Object}$
by(*auto intro: widen-into-widen1-rtrancl[OF wfP]*)
with $\text{single-valued-widen1}[OF\ wfP]$
obtain u **where** $\text{is-lub } ((\text{widen1 } P)^{\wedge*})\ t1\ t2\ u$
by (*blast dest: single-valued-has-lubs*)
hence $P \vdash t1 \leq \text{exec-lub } (\text{widen1 } P)\ (\text{super } P)\ t1\ t2 \wedge$
 $P \vdash t2 \leq \text{exec-lub } (\text{widen1 } P)\ (\text{super } P)\ t1\ t2$
using $\text{acyclic-widen1}[OF\ wfP]\ \text{superI}[of\ -\ -\ P]$
by(*simp add: exec-lub-conv*)(*blast dest: is-lubD is-ubD intro: widen1-rtrancl-into-widen*) }
with $it1\ it2\ sup$ **show** *?thesis*
by (*cases s*) (*auto simp add: sup-def split: if-split-asm elim: refTE*)
qed

lemma *sup-widen-smallest:*

assumes $wfP: wf\text{-prog } wf\text{-mb } P$
and $itT: is\text{-type } P\ T$
and $itU: is\text{-type } P\ U$
and $TwV: P \vdash T \leq V$
and $UwV: P \vdash U \leq V$
and $sup: sup\ P\ T\ U = OK\ W$
shows $widen\ P\ W\ V$

proof –

{ **assume** $rT: is\text{-refT } T$
and $rU: is\text{-refT } U$
and $UNT: U \neq NT$
and $TNT: T \neq NT$
and $W: \text{exec-lub } (\text{widen1 } P)\ (\text{super } P)\ T\ U = W$
from $itU\ itT\ rT\ rU\ UNT\ TNT$ **have** $P \vdash T \leq \text{Class Object } P \vdash U \leq \text{Class Object}$
by(*auto intro: is-refType-widen-Object[OF wfP]*)
with $UNT\ TNT\ itT\ itU$
have $P \vdash T <^* \text{Class Object } P \vdash U <^* \text{Class Object}$
by(*auto intro: widen-into-widen1-rtrancl[OF wfP]*)
with $\text{single-valued-widen1}[OF\ wfP]$
obtain X **where** $\text{lub: is-lub } ((\text{widen1 } P)^{\wedge*})\ T\ U\ X$
by (*blast dest: single-valued-has-lubs*)
with $\text{acyclic-widen1}[OF\ wfP]$
have $\text{exec-lub } (\text{widen1 } P)\ (\text{super } P)\ T\ U = X$
by (*blast intro: superI exec-lub-conv*)
also from $TwV\ TNT\ UwV\ UNT\ itT\ itU$ **have** $P \vdash T <^* V\ P \vdash U <^* V$
by(*auto intro: widen-into-widen1-rtrancl[OF wfP]*)
with lub **have** $P \vdash X <^* V$
by (*clarsimp simp add: is-lub-def is-ub-def*)
finally have $P \vdash \text{exec-lub } (\text{widen1 } P)\ (\text{super } P)\ T\ U \leq V$
by(*rule widen1-rtrancl-into-widen*)
with W **have** $P \vdash W \leq V$ **by** *simp* }
with $sup\ itT\ itU\ TwV\ UwV$ **show** *?thesis*
by(*simp add: sup-def split: if-split-asm*)

qed

lemma *sup-exists:*

$\llbracket \text{widen } P\ a\ c; \text{widen } P\ b\ c \rrbracket \implies \exists T. \text{sup } P\ a\ b = OK\ T$

by(cases b a rule: ty.exhaust[case-product ty.exhaust])(auto simp add: sup-def)

lemma err-semilat-JType-esl:

assumes wf-prog: wf-prog wf-mb P

shows err-semilat (esl P)

proof –

from wf-prog have order (widen P) ..

moreover from wf-prog

have closed (err (types P)) (lift2 (sup P))

by (rule closed-err-types)

moreover

from wf-prog have

$(\forall x \in \text{err } (\text{types } P). \forall y \in \text{err } (\text{types } P). x \sqsubseteq_{\text{Err.le}} (\text{widen } P) x \sqcup_{\text{lift2}} (\text{sup } P) y) \wedge$

$(\forall x \in \text{err } (\text{types } P). \forall y \in \text{err } (\text{types } P). y \sqsubseteq_{\text{Err.le}} (\text{widen } P) x \sqcup_{\text{lift2}} (\text{sup } P) y)$

by(auto simp add: lesub-def plussub-def Err.le-def lift2-def sup-widen-greater split: err.split)

moreover from wf-prog have

$\forall x \in \text{err } (\text{types } P). \forall y \in \text{err } (\text{types } P). \forall z \in \text{err } (\text{types } P).$

$x \sqsubseteq_{\text{Err.le}} (\text{widen } P) z \wedge y \sqsubseteq_{\text{Err.le}} (\text{widen } P) z \longrightarrow x \sqcup_{\text{lift2}} (\text{sup } P) y \sqsubseteq_{\text{Err.le}} (\text{widen } P) z$

unfolding lift2-def plussub-def lesub-def Err.le-def

by(auto intro: sup-widen-smallest dest:sup-exists simp add: split: err.split)

ultimately show ?thesis by (simp add: esl-def semilat-def sl-def Err.sl-def)

qed

3.16.1 Relation between $\text{SemiType.sup } P \ T \ U = \text{OK } V$ and $P \vdash \text{lub}(T, U) = V$

lemma sup-is-lubI:

assumes wf: wf-prog wf-md P

and it: is-type P T is-type P U

and sup: sup P T U = OK V

shows $P \vdash \text{lub}(T, U) = V$

proof

from sup-widen-greater[OF wf it sup]

show $P \vdash T \leq V \ P \vdash U \leq V$ by blast+

next

fix T'

assume $P \vdash T \leq T' \ P \vdash U \leq T'$

thus $P \vdash V \leq T'$ using sup by(rule sup-widen-smallest[OF wf it])

qed

lemma is-lub-subD:

assumes wf: wf-prog wf-md P

and it: is-type P T is-type P U

and lub: $P \vdash \text{lub}(T, U) = V$

shows sup P T U = OK V

proof –

from lub have $P \vdash T \leq V \ P \vdash U \leq V$ by(blast dest: is-lub-upper)+

from sup-exists[OF this] obtain W where sup P T U = OK W by blast

moreover

with wf it have $P \vdash \text{lub}(T, U) = W$ by(rule sup-is-lubI)

with lub have $V = W$ by(auto dest: is-lub-unique[OF wf])

ultimately show ?thesis by simp

qed

lemma is-lub-is-type:

$\llbracket \text{wf-prog } \text{wf-md } P; \text{ is-type } P \ T; \text{ is-type } P \ U; P \vdash \text{lub}(T, U) = V \rrbracket \implies \text{is-type } P \ V$
by(*frule* (3) *is-lub-subD*)(*erule* (3) *sup-is-type*)

3.16.2 Code generator setup

code-pred *widen1p* .

lemmas [*code*] = *widen1-def*

lemma *eval-widen1p-i-i-o-conv*:

Predicate.eval (*widen1p-i-i-o* *P* *T*) = ($\lambda U. P \vdash T <^1 U$)

by(*auto elim*: *widen1p-i-i-oE* *intro*: *widen1p-i-i-oI simp add*: *widen1-def fun-eq-iff*)

lemma *rtrancl-widen1-code* [*code-unfold*]:

$(\text{widen1 } P)^{\hat{*}} = \{(a, b). \text{Predicate.holds } (\text{rtrancl-tab-FioB-i-i-i } (\text{widen1p-i-i-o } P) \ [] \ a \ b)\}$

by(*auto simp add*: *fun-eq-iff Predicate.holds-eq widen1-def rtrancl-def rtranclp-eq-rtrancl-tab-nil eval-widen1p-i-i-o-conv*
intro!: *rtrancl-tab-FioB-i-i-iE elim!*: *rtrancl-tab-FioB-i-i-iE*)

declare *exec-lub-def* [*code-unfold*]

end

theory *Common-Main*

imports

../Basic/Auxiliary
../Framework/FWProgress
../Framework/FWBisimDeadlock
../Framework/FWBisimLift
../DFA/Abstract-BV
ExternalCallWF
ConformThreaded
BinOp
SemiType

begin

end

Chapter 4

JinjaThreads source language

4.1 Program State

```
theory State
imports
  ../Common/Heap
begin

type-synonym
  'addr locals = vname  $\rightarrow$  'addr val    — local vars, incl. params and “this”
type-synonym
  ('addr, 'heap) Jstate = 'heap  $\times$  'addr locals    — the heap and the local vars

definition hp :: 'heap  $\times$  'x  $\Rightarrow$  'heap where hp  $\equiv$  fst

definition lcl :: 'heap  $\times$  'x  $\Rightarrow$  'x where lcl  $\equiv$  snd

lemma hp-conv [simp]: hp (h, l) = h
by(simp add: hp-def)

lemma lcl-conv [simp]: lcl (h, l) = l
by(simp add: lcl-def)

end
```

4.2 Expressions

```
theory Expr
imports
  ../Common/BinOp
begin

datatype (dead 'a, dead 'b, dead 'addr) exp
  = new cname    — class instance creation
  | newArray ty ('a,'b,'addr) exp (⟨newA -[-]⟩ [99,0] 90)    — array instance creation: type, size in
  outermost dimension
  | Cast ty ('a,'b,'addr) exp    — type cast
  | InstanceOf ('a,'b,'addr) exp ty (⟨instanceof -> [99, 99] 90) — instance of
```

| *Val* 'addr val — value
 | *BinOp* ('a,'b,'addr) exp bop ('a,'b,'addr) exp (⟨- «-» -⟩ [80,0,81] 80) — binary operation
 | *Var* 'a — local variable (incl. parameter)
 | *LAss* 'a ('a,'b,'addr) exp (⟨-:=⟩ [90,90]90) — local assignment
 | *AAcc* ('a,'b,'addr) exp ('a,'b,'addr) exp (⟨-[-]⟩ [99,0] 90) — array cell read
 | *AAss* ('a,'b,'addr) exp ('a,'b,'addr) exp ('a,'b,'addr) exp (⟨-[-] := -⟩ [10,99,90] 90) — array cell assignment
 | *ALen* ('a,'b,'addr) exp (⟨-length⟩ [10] 90) — array length
 | *FAcc* ('a,'b,'addr) exp vname cname (⟨--{-}⟩ [10,90,99]90) — field access
 | *FAss* ('a,'b,'addr) exp vname cname ('a,'b,'addr) exp (⟨--{-} := -⟩ [10,90,99,90]90) — field assignment
 | *CompareAndSwap* ('a,'b,'addr) exp cname vname ('a,'b,'addr) exp ('a,'b,'addr) exp (⟨-compareAndSwap('(--, -, -)⟩ [10,90,90,90,90] 90) — compare and swap
 | *Call* ('a,'b,'addr) exp mname ('a,'b,'addr) exp list (⟨--'(-)⟩ [90,99,0] 90) — method call
 | *Block* 'a ty 'addr val option ('a,'b,'addr) exp (⟨'{-:=; -}⟩)
 | *Synchronized* 'b ('a,'b,'addr) exp ('a,'b,'addr) exp (⟨sync- '(-) -⟩ [99,99,90] 90)
 | *InSynchronized* 'b 'addr ('a,'b,'addr) exp (⟨insync- '(-) -⟩ [99,99,90] 90)
 | *Seq* ('a,'b,'addr) exp ('a,'b,'addr) exp (⟨-;;/ -⟩ [61,60]60)
 | *Cond* ('a,'b,'addr) exp ('a,'b,'addr) exp ('a,'b,'addr) exp (⟨if '(-) -/ else -⟩ [80,79,79]70)
 | *While* ('a,'b,'addr) exp ('a,'b,'addr) exp (⟨while '(-) -⟩ [80,79]70)
 | *throw* ('a,'b,'addr) exp
 | *TryCatch* ('a,'b,'addr) exp cname 'a ('a,'b,'addr) exp (⟨try -/ catch'(- -) -⟩ [0,99,80,79] 70)

type-synonym

'addr expr = (vname, unit, 'addr) exp — Jinja expression

type-synonym

'addr J-mb = vname list × 'addr expr — Jinja method body: parameter names and expression

type-synonym

'addr J-prog = 'addr J-mb prog — Jinja program

translations

(type) 'addr expr ≤ (type) (String.literal, unit, 'addr) exp

(type) 'addr J-prog ≤ (type) (String.literal list × 'addr expr) prog

4.2.1 Syntactic sugar

abbreviation unit :: ('a,'b,'addr) exp

where unit ≡ Val Unit

abbreviation null :: ('a,'b,'addr) exp

where null ≡ Val Null

abbreviation addr :: 'addr ⇒ ('a,'b,'addr) exp

where addr a == Val (Addr a)

abbreviation true :: ('a,'b,'addr) exp

where true == Val (Bool True)

abbreviation false :: ('a,'b,'addr) exp

where false == Val (Bool False)

abbreviation Throw :: 'addr ⇒ ('a,'b,'addr) exp

where Throw a == throw (Val (Addr a))

abbreviation (in *heap-base*) *THROW* :: *cname* \Rightarrow ('a,'b,'addr) *exp*
where *THROW* *xc* == *Throw* (*addr-of-sys-xcpt* *xc*)

abbreviation *sync-unit-syntax* :: ('a,unit,'addr) *exp* \Rightarrow ('a,unit,'addr) *exp* \Rightarrow ('a,unit,'addr) *exp*
(*sync*'(-) -> [99,90] 90)
where *sync*(*e1*) *e2* \equiv *sync*₍₎ (*e1*) *e2*

abbreviation *insync-unit-syntax* :: 'addr \Rightarrow ('a,unit,'addr) *exp* \Rightarrow ('a,unit,'addr) *exp* (*insync*'(-) -> [99,90] 90)
where *insync*(*a*) *e2* \equiv *insync*₍₎ (*a*) *e2*

Java syntax for binary operators

abbreviation *BinOp-Eq* :: ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp*
(*«==»* -> [80,81] 80)
where *e* *«==»* *e'* \equiv *e* *«Eq»* *e'*

abbreviation *BinOp-NotEq* :: ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp*
(*«!=»* -> [80,81] 80)
where *e* *«!=»* *e'* \equiv *e* *«NotEq»* *e'*

abbreviation *BinOp-LessThan* :: ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp*
(*«<»* -> [80,81] 80)
where *e* *«<»* *e'* \equiv *e* *«LessThan»* *e'*

abbreviation *BinOp-LessOrEqual* :: ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp*
(*«<=»* -> [80,81] 80)
where *e* *«<=»* *e'* \equiv *e* *«LessOrEqual»* *e'*

abbreviation *BinOp-GreaterThan* :: ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp*
(*«>»* -> [80,81] 80)
where *e* *«>»* *e'* \equiv *e* *«GreaterThan»* *e'*

abbreviation *BinOp-GreaterOrEqual* :: ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp*
(*«>=»* -> [80,81] 80)
where *e* *«>=»* *e'* \equiv *e* *«GreaterOrEqual»* *e'*

abbreviation *BinOp-Add* :: ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp*
(*«+»* -> [80,81] 80)
where *e* *«+»* *e'* \equiv *e* *«Add»* *e'*

abbreviation *BinOp-Subtract* :: ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp*
(*«-»* -> [80,81] 80)
where *e* *«-»* *e'* \equiv *e* *«Subtract»* *e'*

abbreviation *BinOp-Mult* :: ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp*
(*«*»* -> [80,81] 80)
where *e* *«*»* *e'* \equiv *e* *«Mult»* *e'*

abbreviation *BinOp-Div* :: ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp*
(*«/»* -> [80,81] 80)
where *e* *«/»* *e'* \equiv *e* *«Div»* *e'*

abbreviation *BinOp-Mod* :: ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp* \Rightarrow ('a, 'b, 'c) *exp*

($\leftarrow \langle \% \rangle \rightarrow [80,81] 80$)
where $e \langle \% \rangle e' \equiv e \langle Mod \rangle e'$

abbreviation *BinOp-BinAnd* :: ($'a, 'b, 'c$) *exp* \Rightarrow ($'a, 'b, 'c$) *exp* \Rightarrow ($'a, 'b, 'c$) *exp*
($\leftarrow \langle \& \rangle \rightarrow [80,81] 80$)
where $e \langle \& \rangle e' \equiv e \langle BinAnd \rangle e'$

abbreviation *BinOp-BinOr* :: ($'a, 'b, 'c$) *exp* \Rightarrow ($'a, 'b, 'c$) *exp* \Rightarrow ($'a, 'b, 'c$) *exp*
($\leftarrow \langle | \rangle \rightarrow [80,81] 80$)
where $e \langle | \rangle e' \equiv e \langle BinOr \rangle e'$

abbreviation *BinOp-BinXor* :: ($'a, 'b, 'c$) *exp* \Rightarrow ($'a, 'b, 'c$) *exp* \Rightarrow ($'a, 'b, 'c$) *exp*
($\leftarrow \langle \wedge \rangle \rightarrow [80,81] 80$)
where $e \langle \wedge \rangle e' \equiv e \langle BinXor \rangle e'$

abbreviation *BinOp-ShiftLeft* :: ($'a, 'b, 'c$) *exp* \Rightarrow ($'a, 'b, 'c$) *exp* \Rightarrow ($'a, 'b, 'c$) *exp*
($\leftarrow \langle \ll \rangle \rightarrow [80,81] 80$)
where $e \langle \ll \rangle e' \equiv e \langle ShiftLeft \rangle e'$

abbreviation *BinOp-ShiftRightZeros* :: ($'a, 'b, 'c$) *exp* \Rightarrow ($'a, 'b, 'c$) *exp* \Rightarrow ($'a, 'b, 'c$) *exp*
($\leftarrow \langle \gg \rangle \rightarrow [80,81] 80$)
where $e \langle \gg \rangle e' \equiv e \langle ShiftRightZeros \rangle e'$

abbreviation *BinOp-ShiftRightSigned* :: ($'a, 'b, 'c$) *exp* \Rightarrow ($'a, 'b, 'c$) *exp* \Rightarrow ($'a, 'b, 'c$) *exp*
($\leftarrow \langle \gg \rangle \rightarrow [80,81] 80$)
where $e \langle \gg \rangle e' \equiv e \langle ShiftRightSigned \rangle e'$

abbreviation *BinOp-CondAnd* :: ($'a, 'b, 'c$) *exp* \Rightarrow ($'a, 'b, 'c$) *exp* \Rightarrow ($'a, 'b, 'c$) *exp*
($\leftarrow \langle \&\& \rangle \rightarrow [80,81] 80$)
where $e \langle \&\& \rangle e' \equiv \text{if } (e) e' \text{ else false}$

abbreviation *BinOp-CondOr* :: ($'a, 'b, 'c$) *exp* \Rightarrow ($'a, 'b, 'c$) *exp* \Rightarrow ($'a, 'b, 'c$) *exp*
($\leftarrow \langle || \rangle \rightarrow [80,81] 80$)
where $e \langle || \rangle e' \equiv \text{if } (e) \text{ true else } e'$

lemma *inj-Val* [*simp*]: *inj Val*
by(*rule inj-onI*)(*simp*)

lemma *expr-ineqs* [*simp*]: *Val v* ;; $e \neq e$ *if* ($e1$) e *else* $e2 \neq e$ *if* ($e1$) $e2$ *else* $e \neq e$
by(*induct e*) *auto*

4.2.2 Free Variables

primrec *fv* :: ($'a, 'b, 'addr$) *exp* \Rightarrow $'a$ *set*
and *fvs* :: ($'a, 'b, 'addr$) *exp list* \Rightarrow $'a$ *set*
where

$fv(\text{new } C) = \{\}$
 $fv(\text{newA } T[e]) = fv\ e$
 $fv(\text{Cast } C\ e) = fv\ e$
 $fv(e\ \text{instanceof } T) = fv\ e$
 $fv(\text{Val } v) = \{\}$
 $fv(e_1 \langle bop \rangle e_2) = fv\ e_1 \cup fv\ e_2$
 $fv(\text{Var } V) = \{V\}$
 $fv(a[i]) = fv\ a \cup fv\ i$

$| \text{fv}(AAss\ a\ i\ e) = \text{fv}\ a \cup \text{fv}\ i \cup \text{fv}\ e$
 $| \text{fv}(a.\text{length}) = \text{fv}\ a$
 $| \text{fv}(LAss\ V\ e) = \{V\} \cup \text{fv}\ e$
 $| \text{fv}(e.F\{D\}) = \text{fv}\ e$
 $| \text{fv}(FAss\ e_1\ F\ D\ e_2) = \text{fv}\ e_1 \cup \text{fv}\ e_2$
 $| \text{fv}(e_1.\text{compareAndSwap}(D.F, e_2, e_3)) = \text{fv}\ e_1 \cup \text{fv}\ e_2 \cup \text{fv}\ e_3$
 $| \text{fv}(e.M(es)) = \text{fv}\ e \cup \text{fv}\ es$
 $| \text{fv}(\{V:T=vo; e\}) = \text{fv}\ e - \{V\}$
 $| \text{fv}(\text{sync}_V(h)\ e) = \text{fv}\ h \cup \text{fv}\ e$
 $| \text{fv}(\text{insync}_V(a)\ e) = \text{fv}\ e$
 $| \text{fv}(e_1;;e_2) = \text{fv}\ e_1 \cup \text{fv}\ e_2$
 $| \text{fv}(\text{if}(b)\ e_1\ \text{else}\ e_2) = \text{fv}\ b \cup \text{fv}\ e_1 \cup \text{fv}\ e_2$
 $| \text{fv}(\text{while}(b)\ e) = \text{fv}\ b \cup \text{fv}\ e$
 $| \text{fv}(\text{throw}\ e) = \text{fv}\ e$
 $| \text{fv}(\text{try}\ e_1\ \text{catch}(C\ V)\ e_2) = \text{fv}\ e_1 \cup (\text{fv}\ e_2 - \{V\})$

$| \text{fvs}(\[]) = \{\}$
 $| \text{fvs}(e\#es) = \text{fv}\ e \cup \text{fvs}\ es$

lemma [simp]: $\text{fvs}(es\ @\ es') = \text{fvs}\ es \cup \text{fvs}\ es'$
by (induct es) auto

lemma [simp]: $\text{fvs}(\text{map}\ Val\ vs) = \{\}$
by (induct vs) auto

4.2.3 Locks and addresses

primrec $\text{expr-locks} :: ('a, 'b, 'addr)\ \text{exp} \Rightarrow 'addr \Rightarrow \text{nat}$
and $\text{expr-lockss} :: ('a, 'b, 'addr)\ \text{exp}\ \text{list} \Rightarrow 'addr \Rightarrow \text{nat}$

where

$\text{expr-locks}\ (\text{new}\ C) = (\lambda ad.\ 0)$
 $| \text{expr-locks}\ (\text{newA}\ T[e]) = \text{expr-locks}\ e$
 $| \text{expr-locks}\ (\text{Cast}\ T\ e) = \text{expr-locks}\ e$
 $| \text{expr-locks}\ (e\ \text{instanceof}\ T) = \text{expr-locks}\ e$
 $| \text{expr-locks}\ (\text{Val}\ v) = (\lambda ad.\ 0)$
 $| \text{expr-locks}\ (\text{Var}\ v) = (\lambda ad.\ 0)$
 $| \text{expr-locks}\ (e\ \ll\ \text{bop}\ \gg\ e') = (\lambda ad.\ \text{expr-locks}\ e\ ad + \text{expr-locks}\ e'\ ad)$
 $| \text{expr-locks}\ (V := e) = \text{expr-locks}\ e$
 $| \text{expr-locks}\ (a[i]) = (\lambda ad.\ \text{expr-locks}\ a\ ad + \text{expr-locks}\ i\ ad)$
 $| \text{expr-locks}\ (AAss\ a\ i\ e) = (\lambda ad.\ \text{expr-locks}\ a\ ad + \text{expr-locks}\ i\ ad + \text{expr-locks}\ e\ ad)$
 $| \text{expr-locks}\ (a.\text{length}) = \text{expr-locks}\ a$
 $| \text{expr-locks}\ (e.F\{D\}) = \text{expr-locks}\ e$
 $| \text{expr-locks}\ (FAss\ e\ F\ D\ e') = (\lambda ad.\ \text{expr-locks}\ e\ ad + \text{expr-locks}\ e'\ ad)$
 $| \text{expr-locks}\ (e.\text{compareAndSwap}(D.F, e', e'')) = (\lambda ad.\ \text{expr-locks}\ e\ ad + \text{expr-locks}\ e'\ ad + \text{expr-locks}\ e''\ ad)$
 $| \text{expr-locks}\ (e.m(ps)) = (\lambda ad.\ \text{expr-locks}\ e\ ad + \text{expr-lockss}\ ps\ ad)$
 $| \text{expr-locks}\ (\{V : T=vo; e\}) = \text{expr-locks}\ e$
 $| \text{expr-locks}\ (\text{sync}_V(o')\ e) = (\lambda ad.\ \text{expr-locks}\ o'\ ad + \text{expr-locks}\ e\ ad)$
 $| \text{expr-locks}\ (\text{insync}_V(a)\ e) = (\lambda ad.\ \text{if}\ (a = ad)\ \text{then}\ \text{Suc}\ (\text{expr-locks}\ e\ ad)\ \text{else}\ \text{expr-locks}\ e\ ad)$
 $| \text{expr-locks}\ (e;;e') = (\lambda ad.\ \text{expr-locks}\ e\ ad + \text{expr-locks}\ e'\ ad)$
 $| \text{expr-locks}\ (\text{if}(b)\ e\ \text{else}\ e') = (\lambda ad.\ \text{expr-locks}\ b\ ad + \text{expr-locks}\ e\ ad + \text{expr-locks}\ e'\ ad)$
 $| \text{expr-locks}\ (\text{while}(b)\ e) = (\lambda ad.\ \text{expr-locks}\ b\ ad + \text{expr-locks}\ e\ ad)$
 $| \text{expr-locks}\ (\text{throw}\ e) = \text{expr-locks}\ e$
 $| \text{expr-locks}\ (\text{try}\ e\ \text{catch}(C\ v)\ e') = (\lambda ad.\ \text{expr-locks}\ e\ ad + \text{expr-locks}\ e'\ ad)$

| $\text{expr-lockss } [] = (\lambda a. 0)$
| $\text{expr-lockss } (x \# xs) = (\lambda ad. \text{expr-locks } x \text{ } ad + \text{expr-lockss } xs \text{ } ad)$

lemma $\text{expr-lockss-append}$ [simp]:

$\text{expr-lockss } (es @ es') = (\lambda ad. \text{expr-lockss } es \text{ } ad + \text{expr-lockss } es' \text{ } ad)$

by(*induct es*) *auto*

lemma $\text{expr-lockss-map-Val}$ [simp]: $\text{expr-lockss } (\text{map Val } vs) = (\lambda ad. 0)$

by(*induct vs*) *auto*

primrec $\text{contains-insync} :: ('a, 'b, 'addr) \text{exp} \Rightarrow \text{bool}$

and $\text{contains-insyncs} :: ('a, 'b, 'addr) \text{exp list} \Rightarrow \text{bool}$

where

$\text{contains-insync } (\text{new } C) = \text{False}$

| $\text{contains-insync } (\text{newA } T [i]) = \text{contains-insync } i$

| $\text{contains-insync } (\text{Cast } T \text{ } e) = \text{contains-insync } e$

| $\text{contains-insync } (e \text{ instanceof } T) = \text{contains-insync } e$

| $\text{contains-insync } (\text{Val } v) = \text{False}$

| $\text{contains-insync } (\text{Var } v) = \text{False}$

| $\text{contains-insync } (e \llcorner e') = (\text{contains-insync } e \vee \text{contains-insync } e')$

| $\text{contains-insync } (V := e) = \text{contains-insync } e$

| $\text{contains-insync } (a[i]) = (\text{contains-insync } a \vee \text{contains-insync } i)$

| $\text{contains-insync } (\text{AAss } a \text{ } i \text{ } e) = (\text{contains-insync } a \vee \text{contains-insync } i \vee \text{contains-insync } e)$

| $\text{contains-insync } (a \cdot \text{length}) = \text{contains-insync } a$

| $\text{contains-insync } (e \cdot F\{D\}) = \text{contains-insync } e$

| $\text{contains-insync } (\text{FAss } e \text{ } F \text{ } D \text{ } e') = (\text{contains-insync } e \vee \text{contains-insync } e')$

| $\text{contains-insync } (e \cdot \text{compareAndSwap}(D \cdot F, e', e'')) = (\text{contains-insync } e \vee \text{contains-insync } e' \vee \text{contains-insync } e'')$

| $\text{contains-insync } (e \cdot m(pns)) = (\text{contains-insync } e \vee \text{contains-insyncs } pns)$

| $\text{contains-insync } (\{V : T=vo; e\}) = \text{contains-insync } e$

| $\text{contains-insync } (\text{sync}_V (o') \text{ } e) = (\text{contains-insync } o' \vee \text{contains-insync } e)$

| $\text{contains-insync } (\text{insync}_V (a) \text{ } e) = \text{True}$

| $\text{contains-insync } (e; e') = (\text{contains-insync } e \vee \text{contains-insync } e')$

| $\text{contains-insync } (\text{if } (b) \text{ } e \text{ else } e') = (\text{contains-insync } b \vee \text{contains-insync } e \vee \text{contains-insync } e')$

| $\text{contains-insync } (\text{while } (b) \text{ } e) = (\text{contains-insync } b \vee \text{contains-insync } e)$

| $\text{contains-insync } (\text{throw } e) = \text{contains-insync } e$

| $\text{contains-insync } (\text{try } e \text{ catch } (C \text{ } v) \text{ } e') = (\text{contains-insync } e \vee \text{contains-insync } e')$

| $\text{contains-insyncs } [] = \text{False}$

| $\text{contains-insyncs } (x \# xs) = (\text{contains-insync } x \vee \text{contains-insyncs } xs)$

lemma $\text{contains-insyncs-append}$ [simp]:

$\text{contains-insyncs } (es @ es') \longleftrightarrow \text{contains-insyncs } es \vee \text{contains-insyncs } es'$

by(*induct es*, *auto*)

lemma **fixes** $e :: ('a, 'b, 'addr) \text{exp}$

and $es :: ('a, 'b, 'addr) \text{exp list}$

shows $\text{contains-insync-conv}: (\text{contains-insync } e \longleftrightarrow (\exists ad. \text{expr-locks } e \text{ } ad > 0))$

and $\text{contains-insyncs-conv}: (\text{contains-insyncs } es \longleftrightarrow (\exists ad. \text{expr-lockss } es \text{ } ad > 0))$

by(*induct e and es rule: expr-locks.induct expr-lockss.induct*)(*auto*)

lemma $\text{contains-insyncs-map-Val}$ [simp]: $\neg \text{contains-insyncs } (\text{map Val } vs)$

by(*induct vs*) *auto*

4.2.4 Value expressions

inductive *is-val* :: ('a,'b,'addr) exp ⇒ bool **where**
is-val (Val v)

declare *is-val.intros* [simp]
declare *is-val.cases* [elim!]

lemma *is-val-iff*: *is-val* e ⇔ (∃ v. e = Val v)
by(*auto*)

code-pred *is-val* .

fun *is-vals* :: ('a,'b,'addr) exp list ⇒ bool **where**
is-vals [] = True
| *is-vals* (e#es) = (*is-val* e ∧ *is-vals* es)

lemma *is-vals-append* [simp]: *is-vals* (es @ es') ⇔ *is-vals* es ∧ *is-vals* es'
by(*induct* es) *auto*

lemma *is-vals-conv*: *is-vals* es = (∃ vs. es = map Val vs)
by(*induct* es)(*auto simp add: Cons-eq-map-conv*)

lemma *is-vals-map-Val* [simp]: *is-vals* (map Val vs) = True
unfolding *is-vals-conv* **by** *auto*

inductive *is-addr* :: ('a,'b,'addr) exp ⇒ bool
where *is-addr* (addr a)

declare *is-addr.intros*[intro!]
declare *is-addr.cases*[elim!]

lemma [simp]: (*is-addr* e) ⇔ (∃ a. e = addr a)
by *auto*

primrec *the-Val* :: ('a, 'b, 'addr) exp ⇒ 'addr val
where
the-Val (Val v) = v

inductive *is-Throws* :: ('a, 'b, 'addr) exp list ⇒ bool
where
is-Throws (Throw a # es)
| *is-Throws* es ⇒ *is-Throws* (Val v # es)

inductive-simps *is-Throws-simps*:
is-Throws []
is-Throws (e # es)

code-pred *is-Throws* .

lemma *is-Throws-conv*: *is-Throws* es ⇔ (∃ vs a es'. es = map Val vs @ Throw a # es')
(**is** ?lhs ⇔ ?rhs)

proof
assume ?lhs **thus** ?rhs

```

  by(induct)(fastforce simp add: Cons-eq-append-conv Cons-eq-map-conv)+
next
  assume ?rhs thus ?lhs
  by(induct es)(auto simp add: is-Throws-simps Cons-eq-map-conv Cons-eq-append-conv)
qed

```

4.2.5 blocks

```

fun blocks :: 'a list  $\Rightarrow$  ty list  $\Rightarrow$  'addr val list  $\Rightarrow$  ('a,'b,'addr) exp  $\Rightarrow$  ('a,'b,'addr) exp
where
  blocks (V # Vs) (T # Ts) (v # vs) e = {V:T=[v]; blocks Vs Ts vs e}
| blocks [] [] [] e = e

```

lemma [simp]:

```

[[ size vs = size Vs; size Ts = size Vs ]]  $\Longrightarrow$  fv (blocks Vs Ts vs e) = fv e - set Vs
by(induct rule:blocks.induct)(simp-all, blast)

```

lemma expr-locks-blocks:

```

[[ length vs = length pns; length Ts = length pns ]]
 $\Longrightarrow$  expr-locks (blocks pns Ts vs e) = expr-locks e
by(induct pns Ts vs e rule: blocks.induct)(auto)

```

4.2.6 Final expressions

```

inductive final :: ('a,'b,'addr) exp  $\Rightarrow$  bool where
  final (Val v)
| final (Throw a)

```

```

declare final.cases [elim]
declare final.intros[simp]

```

lemmas finalE[consumes 1, case-names Val Throw] = final.cases

```

lemma final-iff: final e  $\longleftrightarrow$  ( $\exists v. e = \text{Val } v$ )  $\vee$  ( $\exists a. e = \text{Throw } a$ )
by(auto)

```

```

lemma final-locks: final e  $\Longrightarrow$  expr-locks e l = 0
by(auto elim: finalE)

```

```

inductive finals :: ('a,'b,'addr) exp list  $\Rightarrow$  bool
where
  finals []
| finals (Throw a # es)
| finals es  $\Longrightarrow$  finals (Val v # es)

```

```

inductive-simps finals-simps:
  finals (e # es)

```

```

lemma [iff]: finals []
by(rule finals.intros)

```

```

lemma [iff]: finals (Val v # es) = finals es
by(simp add: finals-simps)

```

lemma *finals-app-map* [iff]: *finals (map Val vs @ es) = finals es*
by(*induct vs simp-all*)

lemma [iff]: *finals (throw e # es) = (∃ a. e = addr a)*
by(*simp add: finals-simps*)

lemma *not-finals-ConsI*: $\neg \text{final } e \implies \neg \text{finals } (e \# \text{es})$
by(*simp add: finals-simps final-iff*)

lemma *finals-iff*: $\text{finals } es \iff (\exists \text{vs. } es = \text{map Val vs}) \vee (\exists \text{vs } a \text{ es}'. \text{es} = \text{map Val vs @ Throw } a \# \text{es}')$
(is ?lhs \iff ?rhs)

proof

assume *?lhs thus ?rhs*

by *induct(auto simp add: Cons-eq-append-conv Cons-eq-map-conv, metis)*

next

assume *?rhs thus ?lhs by(induct es) auto*

qed

code-pred *final* .

4.2.7 converting results from external calls

primrec *extRet2J* :: ('a, 'b, 'addr) exp \Rightarrow 'addr extCallRet \Rightarrow ('a, 'b, 'addr) exp
where

extRet2J e (RetVal v) = Val v
| *extRet2J e (RetExc a) = Throw a*
| *extRet2J e RetStaySame = e*

lemma *fv-extRet2J* [simp]: $\text{fv } (\text{extRet2J } e \text{ va}) \subseteq \text{fv } e$
by(*cases va simp-all*)

4.2.8 expressions at a call

primrec *call* :: ('a,'b,'addr) exp \Rightarrow ('addr \times mname \times 'addr val list) option
and *calls* :: ('a,'b,'addr) exp list \Rightarrow ('addr \times mname \times 'addr val list) option
where

call (new C) = None
| *call (newA T[e]) = call e*
| *call (Cast C e) = call e*
| *call (e instanceof T) = call e*
| *call (Val v) = None*
| *call (Var V) = None*
| *call (V:=e) = call e*
| *call (e «bop» e') = (if is-val e then call e' else call e)*
| *call (a[i]) = (if is-val a then call i else call a)*
| *call (AAss a i e) = (if is-val a then (if is-val i then call e else call i) else call a)*
| *call (a.length) = call a*
| *call (e.F{D}) = call e*
| *call (FAss e F D e') = (if is-val e then call e' else call e)*
| *call (e.compareAndSwap(D.F, e', e'')) = (if is-val e then if is-val e' then call e'' else call e' else call e)*
| *call (e.M(es)) = (if is-val e then*
(if is-vals es \wedge is-addr e then [(THE a. e = addr a, M, THE vs. es = map Val vs)])

```

else calls es)
  else call e)
| call ({ V:T=vo; e}) = call e
| call (syncV (o') e) = call o'
| call (insyncV (a) e) = call e
| call (e;;e') = call e
| call (if (e) e1 else e2) = call e
| call (while(b) e) = None
| call (throw e) = call e
| call (try e1 catch(C V) e2) = call e1

| calls [] = None
| calls (e#es) = (if is-val e then calls es else call e)

```

lemma *calls-append* [simp]:

calls (es @ es') = (if calls es = None ∧ is-vals es then calls es' else calls es)

by(*induct es*) *auto*

lemma *call-callE* [*consumes 1*, *case-names CallObj CallParams Call*]:

$\llbracket \text{call } (\text{obj} \cdot M(\text{pns})) = \lfloor (a, M', \text{vs}) \rfloor;$

$\text{call obj} = \lfloor (a, M', \text{vs}) \rfloor \implies \text{thesis};$

$\bigwedge v. \llbracket \text{obj} = \text{Val } v; \text{calls pns} = \lfloor (a, M', \text{vs}) \rfloor \rrbracket \implies \text{thesis};$

$\llbracket \text{obj} = \text{addr } a; \text{pns} = \text{map Val vs}; M = M' \rrbracket \implies \text{thesis} \rrbracket \implies \text{thesis}$

by(*auto split: if-split-asm simp add: is-vals-conv*)

lemma *calls-map-Val* [simp]:

calls (map Val vs) = None

by(*induct vs*) *auto*

lemma *call-not-is-val* [*dest*]: *call e = ⌊aMvs⌋ $\implies \neg$ is-val e*

by(*cases e*) *auto*

lemma *is-calls-not-is-vals* [*dest*]: *calls es = ⌊aMvs⌋ $\implies \neg$ is-vals es*

by(*induct es*) *auto*

end

4.3 Abstract heap locales for source code programs

theory *JHeap*

imports

../Common/Conform

Expr

begin

locale *J-heap-base* = *heap-base* +

constrains *addr2thread-id* :: ('addr :: addr) \Rightarrow 'thread-id

and *thread-id2addr* :: 'thread-id \Rightarrow 'addr

and *spurious-wakeups* :: bool

and *empty-heap* :: 'heap

and *allocate* :: 'heap \Rightarrow htype \Rightarrow ('heap \times 'addr) set

and *typeof-addr* :: 'heap \Rightarrow 'addr \rightarrow htype

and *heap-read* :: 'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr val \Rightarrow bool

and *heap-write* :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool

locale *J-heap* = *heap* +
constrains *addr2thread-id* :: ('addr :: addr) ⇒ 'thread-id
and *thread-id2addr* :: 'thread-id ⇒ 'addr
and *spurious-wakeups* :: bool
and *empty-heap* :: 'heap
and *allocate* :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and *typeof-addr* :: 'heap ⇒ 'addr → htype
and *heap-read* :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and *heap-write* :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and *P* :: 'addr *J-prog*

sublocale *J-heap* < *J-heap-base* .

locale *J-heap-conf-base* = *heap-conf-base* +
constrains *addr2thread-id* :: ('addr :: addr) ⇒ 'thread-id
and *thread-id2addr* :: 'thread-id ⇒ 'addr
and *spurious-wakeups* :: bool
and *empty-heap* :: 'heap
and *allocate* :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and *typeof-addr* :: 'heap ⇒ 'addr → htype
and *heap-read* :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and *heap-write* :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and *hconf* :: 'heap ⇒ bool
and *P* :: 'addr *J-prog*

sublocale *J-heap-conf-base* < *J-heap-base* .

locale *J-heap-conf* =
J-heap-conf-base +
heap-conf +
constrains *addr2thread-id* :: ('addr :: addr) ⇒ 'thread-id
and *thread-id2addr* :: 'thread-id ⇒ 'addr
and *spurious-wakeups* :: bool
and *empty-heap* :: 'heap
and *allocate* :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and *typeof-addr* :: 'heap ⇒ 'addr → htype
and *heap-read* :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and *heap-write* :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and *hconf* :: 'heap ⇒ bool
and *P* :: 'addr *J-prog*

sublocale *J-heap-conf* < *J-heap*
by(*unfold-locales*)

locale *J-progress* =
heap-progress +
J-heap-conf-base +
constrains *addr2thread-id* :: ('addr :: addr) ⇒ 'thread-id
and *thread-id2addr* :: 'thread-id ⇒ 'addr
and *spurious-wakeups* :: bool
and *empty-heap* :: 'heap
and *allocate* :: 'heap ⇒ htype ⇒ ('heap × 'addr) set

```

and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype
and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
and hconf :: 'heap  $\Rightarrow$  bool
and P :: 'addr J-prog

```

```

sublocale J-progress < J-heap by(unfold-locales)

```

```

locale J-conf-read =
  heap-conf-read +
  J-heap-conf +
  constrains addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype
and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
and hconf :: 'heap  $\Rightarrow$  bool
and P :: 'addr J-prog

```

```

sublocale J-conf-read < J-heap by(unfold-locales)

```

```

locale J-typesafe =
  heap-typesafe +
  J-conf-read +
  J-progress +
  constrains addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype
and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
and hconf :: 'heap  $\Rightarrow$  bool
and P :: 'addr J-prog

```

```

end

```

4.4 Small Step Semantics

```

theory SmallStep

```

```

imports

```

```

  Expr

```

```

  State

```

```

  JHeap

```

```

begin

```

```

type-synonym

```

```

  ('addr, 'thread-id, 'heap) J-thread-action =

```

```

  ('addr, 'thread-id, 'addr expr  $\times$  'addr locals, 'heap) Jinja-thread-action

```

type-synonym

```

('addr, 'thread-id, 'heap) J-state =
('addr, 'thread-id, 'addr expr × 'addr locals, 'heap, 'addr) state

```

print-translation <

```

let
  fun tr'
    [a1, t
    , Const (@{type-syntax prod}, -) $
      (Const (@{type-syntax exp}, -) $
        Const (@{type-syntax String.literal}, -) $ Const (@{type-syntax unit}, -) $ a2) $
      (Const (@{type-syntax fun}, -) $
        Const (@{type-syntax String.literal}, -) $
        (Const (@{type-syntax option}, -) $
          (Const (@{type-syntax val}, -) $ a3)))
    , h] =
  if a1 = a2 andalso a2 = a3 then Syntax.const @{{type-syntax J-thread-action}} $ a1 $ t $ h
  else raise Match;
  in [(@{{type-syntax Jinja-thread-action}}, K tr')]
end
>
typ ('addr, 'thread-id, 'heap) J-thread-action

```

print-translation <

```

let
  fun tr'
    [a1, t
    , Const (@{type-syntax prod}, -) $
      (Const (@{type-syntax exp}, -) $
        Const (@{type-syntax String.literal}, -) $ Const (@{type-syntax unit}, -) $ a2) $
      (Const (@{type-syntax fun}, -) $
        Const (@{type-syntax String.literal}, -) $
        (Const (@{type-syntax option}, -) $
          (Const (@{type-syntax val}, -) $ a3)))
    , h, a4] =
  if a1 = a2 andalso a2 = a3 andalso a3 = a4 then Syntax.const @{{type-syntax J-state}} $ a1 $ t
  $ h
  else raise Match;
  in [(@{{type-syntax state}}, K tr')]
end
>
typ ('addr, 'thread-id, 'heap) J-state

```

definition *extNTA2J* :: 'addr J-prog ⇒ (cname × mname × 'addr) ⇒ 'addr expr × 'addr locals
where *extNTA2J P* = (λ(C, M, a). let (D, Ts, T, meth) = method P C M; (pns, body) = the meth
 in ({this:Class D=[Addr a]; body}, Map.empty))

abbreviation *J-local-start* ::

```

  cname ⇒ mname ⇒ ty list ⇒ ty ⇒ 'addr J-mb ⇒ 'addr val list
  ⇒ 'addr expr × 'addr locals

```

where

J-local-start \equiv
 $\lambda C M Ts T (pns, body) vs.$
 $(blocks (this \# pns) (Class C \# Ts) (Null \# vs) body, Map.empty)$

abbreviation (in *J-heap-base*)

J-start-state $:: 'addr J\text{-prog} \Rightarrow cname \Rightarrow mname \Rightarrow 'addr\ val\ list \Rightarrow ('addr, 'thread\text{-id}, 'heap) J\text{-state}$

where

J-start-state $\equiv start\text{-state } J\text{-local}\text{-start}$

lemma *extNTA2J-iff* [*simp*]:

extNTA2J $P (C, M, a) = (\{this:Class (fst (method P C M))=[Addr a]; snd (the (snd (snd (snd (method P C M)))))\}, Map.empty)$

by(*simp add: extNTA2J-def split-beta*)

abbreviation *extTA2J* $::$

'addr J-prog $\Rightarrow ('addr, 'thread\text{-id}, 'heap) external\text{-thread}\text{-action} \Rightarrow ('addr, 'thread\text{-id}, 'heap) J\text{-thread}\text{-action}$

where *extTA2J* $P \equiv convert\text{-extTA} (extNTA2J P)$

lemma *extTA2J-ε*: *extTA2J* $P \ \varepsilon = \varepsilon$

by(*simp*)

Locking mechanism: The expression on which the thread is synchronized is evaluated first to a value. If this expression evaluates to null, a null pointer expression is thrown. If this expression evaluates to an address, a lock must be obtained on this address, the sync expression is rewritten to insync. For insync expressions, the body expression may be evaluated. If the body expression is only a value or a thrown exception, the lock is released and the synchronized expression reduces to the body's expression. This is the normal Java semantics, not the one as presented in LNCS 1523, Cenciarelli/Knapp/Reus/Wirsing. There the expression on which the thread synchronized is evaluated except for the last step. If the thread can obtain the lock on the object immediately after the last evaluation step, the evaluation is done and the lock acquired. If the lock cannot be obtained, the evaluation step is discarded. If another thread changes the evaluation result of this last step, the thread then will try to synchronize on the new object.

context *J-heap-base* **begin**

inductive *red* $::$

$(('addr, 'thread\text{-id}, 'heap) external\text{-thread}\text{-action} \Rightarrow ('addr, 'thread\text{-id}, 'x, 'heap) Jinja\text{-thread}\text{-action})$

$\Rightarrow 'addr\ J\text{-prog} \Rightarrow 'thread\text{-id}$

$\Rightarrow 'addr\ expr \Rightarrow ('addr, 'heap) Jstate$

$\Rightarrow ('addr, 'thread\text{-id}, 'x, 'heap) Jinja\text{-thread}\text{-action}$

$\Rightarrow 'addr\ expr \Rightarrow ('addr, 'heap) Jstate \Rightarrow bool$

$(\langle -, - \rangle \vdash ((1 \langle -, - \rangle) \dashrightarrow / (1 \langle -, - \rangle))) \triangleright [51, 51, 0, 0, 0, 0, 0] \ 81)$

and *reds* $::$

$(('addr, 'thread\text{-id}, 'heap) external\text{-thread}\text{-action} \Rightarrow ('addr, 'thread\text{-id}, 'x, 'heap) Jinja\text{-thread}\text{-action})$

$\Rightarrow 'addr\ J\text{-prog} \Rightarrow 'thread\text{-id}$

$\Rightarrow 'addr\ expr\ list \Rightarrow ('addr, 'heap) Jstate$

$\Rightarrow ('addr, 'thread\text{-id}, 'x, 'heap) Jinja\text{-thread}\text{-action}$

$\Rightarrow 'addr\ expr\ list \Rightarrow ('addr, 'heap) Jstate \Rightarrow bool$

$(\langle -, - \rangle \vdash ((1 \langle -, - \rangle) \dashrightarrow / (1 \langle -, - \rangle))) \triangleright [51, 51, 0, 0, 0, 0, 0] \ 81)$

for *extTA* $:: ('addr, 'thread\text{-id}, 'heap) external\text{-thread}\text{-action} \Rightarrow ('addr, 'thread\text{-id}, 'x, 'heap) Jinja\text{-thread}\text{-action}$

and *P* $:: 'addr\ J\text{-prog}$ **and** *t* $:: 'thread\text{-id}$

where

RedNew:

$(h', a) \in \text{allocate } h \text{ (Class-type } C)$
 $\implies \text{extTA}, P, t \vdash \langle \text{new } C, (h, l) \rangle -\{\!\!\{ \text{NewHeapElem } a \text{ (Class-type } C) \}\!\!\} \rightarrow \langle \text{addr } a, (h', l) \rangle$

| *RedNewFail:*

$\text{allocate } h \text{ (Class-type } C) = \{\}$
 $\implies \text{extTA}, P, t \vdash \langle \text{new } C, (h, l) \rangle -\varepsilon \rightarrow \langle \text{THROW OutOfMemory}, (h, l) \rangle$

| *NewArrayRed:*

$\text{extTA}, P, t \vdash \langle e, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle \implies \text{extTA}, P, t \vdash \langle \text{newA } T[e], s \rangle -\text{ta} \rightarrow \langle \text{newA } T[e'], s' \rangle$

| *RedNewArray:*

$\llbracket 0 \leq s \ i; (h', a) \in \text{allocate } h \text{ (Array-type } T \text{ (nat (sint } i)) \rrbracket$
 $\implies \text{extTA}, P, t \vdash \langle \text{newA } T[\text{Val (Intg } i)], (h, l) \rangle -\{\!\!\{ \text{NewHeapElem } a \text{ (Array-type } T \text{ (nat (sint } i)) \}\!\!\} \rightarrow \langle \text{addr } a, (h', l) \rangle$

| *RedNewArrayNegative:*

$i < s \ 0 \implies \text{extTA}, P, t \vdash \langle \text{newA } T[\text{Val (Intg } i)], s \rangle -\varepsilon \rightarrow \langle \text{THROW NegativeArraySize}, s \rangle$

| *RedNewArrayFail:*

$\llbracket 0 \leq s \ i; \text{allocate } h \text{ (Array-type } T \text{ (nat (sint } i)) = \{\} \rrbracket$
 $\implies \text{extTA}, P, t \vdash \langle \text{newA } T[\text{Val (Intg } i)], (h, l) \rangle -\varepsilon \rightarrow \langle \text{THROW OutOfMemory}, (h, l) \rangle$

| *CastRed:*

$\text{extTA}, P, t \vdash \langle e, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle \implies \text{extTA}, P, t \vdash \langle \text{Cast } C \ e, s \rangle -\text{ta} \rightarrow \langle \text{Cast } C \ e', s' \rangle$

| *RedCast:*

$\llbracket \text{typeof}_{hp} \ s \ v = \lfloor U \rfloor; P \vdash U \leq T \rrbracket$
 $\implies \text{extTA}, P, t \vdash \langle \text{Cast } T \text{ (Val } v), s \rangle -\varepsilon \rightarrow \langle \text{Val } v, s \rangle$

| *RedCastFail:*

$\llbracket \text{typeof}_{hp} \ s \ v = \lfloor U \rfloor; \neg P \vdash U \leq T \rrbracket$
 $\implies \text{extTA}, P, t \vdash \langle \text{Cast } T \text{ (Val } v), s \rangle -\varepsilon \rightarrow \langle \text{THROW ClassCast}, s \rangle$

| *InstanceOfRed:*

$\text{extTA}, P, t \vdash \langle e, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle \implies \text{extTA}, P, t \vdash \langle e \text{ instanceof } T, s \rangle -\text{ta} \rightarrow \langle e' \text{ instanceof } T, s' \rangle$

| *RedInstanceOf:*

$\llbracket \text{typeof}_{hp} \ s \ v = \lfloor U \rfloor; b \iff v \neq \text{Null} \wedge P \vdash U \leq T \rrbracket$
 $\implies \text{extTA}, P, t \vdash \langle (\text{Val } v) \text{ instanceof } T, s \rangle -\varepsilon \rightarrow \langle \text{Val (Bool } b), s \rangle$

| *BinOpRed1:*

$\text{extTA}, P, t \vdash \langle e, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle \implies \text{extTA}, P, t \vdash \langle e \text{ «bop» } e2, s \rangle -\text{ta} \rightarrow \langle e' \text{ «bop» } e2, s' \rangle$

| *BinOpRed2:*

$\text{extTA}, P, t \vdash \langle e, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle \implies \text{extTA}, P, t \vdash \langle (\text{Val } v) \text{ «bop» } e, s \rangle -\text{ta} \rightarrow \langle (\text{Val } v) \text{ «bop» } e', s' \rangle$

| *RedBinOp:*

$\text{binop } bop \ v1 \ v2 = \text{Some (Inl } v) \implies$
 $\text{extTA}, P, t \vdash \langle (\text{Val } v1) \text{ «bop» } (\text{Val } v2), s \rangle -\varepsilon \rightarrow \langle \text{Val } v, s \rangle$

| *RedBinOpFail:*

$\text{binop } bop \ v1 \ v2 = \text{Some (Inr } a) \implies$
 $\text{extTA}, P, t \vdash \langle (\text{Val } v1) \text{ «bop» } (\text{Val } v2), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$

- | *RedVar*:
 $lcl\ s\ V = Some\ v \implies$
 $extTA,P,t \vdash \langle Var\ V,\ s \rangle -\varepsilon \rightarrow \langle Val\ v,\ s \rangle$
- | *LAssRed*:
 $extTA,P,t \vdash \langle e,\ s \rangle -ta \rightarrow \langle e',\ s' \rangle \implies extTA,P,t \vdash \langle V:=e,\ s \rangle -ta \rightarrow \langle V:=e',\ s' \rangle$
- | *RedLAss*:
 $extTA,P,t \vdash \langle V:=(Val\ v),\ (h,\ l) \rangle -\varepsilon \rightarrow \langle unit,\ (h,\ l(V \mapsto v)) \rangle$
- | *AAccRed1*:
 $extTA,P,t \vdash \langle a,\ s \rangle -ta \rightarrow \langle a',\ s' \rangle \implies extTA,P,t \vdash \langle a[i],\ s \rangle -ta \rightarrow \langle a'[i],\ s' \rangle$
- | *AAccRed2*:
 $extTA,P,t \vdash \langle i,\ s \rangle -ta \rightarrow \langle i',\ s' \rangle \implies extTA,P,t \vdash \langle (Val\ a)[i],\ s \rangle -ta \rightarrow \langle (Val\ a)[i'],\ s' \rangle$
- | *RedAAccNull*:
 $extTA,P,t \vdash \langle null[Val\ i],\ s \rangle -\varepsilon \rightarrow \langle THROW\ NullPointer,\ s \rangle$
- | *RedAAccBounds*:
 $\llbracket\ typeof\text{-}addr\ (hp\ s)\ a = \lfloor Array\text{-}type\ T\ n \rfloor; i < s\ 0 \vee sint\ i \geq\ int\ n \rrbracket$
 $\implies extTA,P,t \vdash \langle (addr\ a)[Val\ (Intg\ i)],\ s \rangle -\varepsilon \rightarrow \langle THROW\ ArrayIndexOutOfBounds,\ s \rangle$
- | *RedAAcc*:
 $\llbracket\ typeof\text{-}addr\ h\ a = \lfloor Array\text{-}type\ T\ n \rfloor; 0 \leq s\ i; sint\ i < int\ n;$
 $\quad heap\text{-}read\ h\ a\ (ACell\ (nat\ (sint\ i)))\ v \rrbracket$
 $\implies extTA,P,t \vdash \langle (addr\ a)[Val\ (Intg\ i)],\ (h,\ l) \rangle -\{\!| ReadMem\ a\ (ACell\ (nat\ (sint\ i)))\ v \!\} \rightarrow \langle Val\ v,$
 $(h,\ l) \rangle$
- | *AAssRed1*:
 $extTA,P,t \vdash \langle a,\ s \rangle -ta \rightarrow \langle a',\ s' \rangle \implies extTA,P,t \vdash \langle a[i] := e,\ s \rangle -ta \rightarrow \langle a'[i] := e,\ s' \rangle$
- | *AAssRed2*:
 $extTA,P,t \vdash \langle i,\ s \rangle -ta \rightarrow \langle i',\ s' \rangle \implies extTA,P,t \vdash \langle (Val\ a)[i] := e,\ s \rangle -ta \rightarrow \langle (Val\ a)[i'] := e,\ s' \rangle$
- | *AAssRed3*:
 $extTA,P,t \vdash \langle (e::'addr\ expr),\ s \rangle -ta \rightarrow \langle e',\ s' \rangle \implies extTA,P,t \vdash \langle (Val\ a)[Val\ i] := e,\ s \rangle -ta \rightarrow \langle (Val$
 $a)[Val\ i] := e',\ s' \rangle$
- | *RedAAssNull*:
 $extTA,P,t \vdash \langle null[Val\ i] := (Val\ e::'addr\ expr),\ s \rangle -\varepsilon \rightarrow \langle THROW\ NullPointer,\ s \rangle$
- | *RedAAssBounds*:
 $\llbracket\ typeof\text{-}addr\ (hp\ s)\ a = \lfloor Array\text{-}type\ T\ n \rfloor; i < s\ 0 \vee sint\ i \geq\ int\ n \rrbracket$
 $\implies extTA,P,t \vdash \langle (addr\ a)[Val\ (Intg\ i)] := (Val\ e::'addr\ expr),\ s \rangle -\varepsilon \rightarrow \langle THROW\ ArrayIndexOut-$
 $OfBounds,\ s \rangle$
- | *RedAAssStore*:
 $\llbracket\ typeof\text{-}addr\ (hp\ s)\ a = \lfloor Array\text{-}type\ T\ n \rfloor; 0 \leq s\ i; sint\ i < int\ n;$
 $\quad typeof_{hp\ s}\ w = \lfloor U \rfloor; \neg (P \vdash U \leq T) \rrbracket$
 $\implies extTA,P,t \vdash \langle (addr\ a)[Val\ (Intg\ i)] := (Val\ w::'addr\ expr),\ s \rangle -\varepsilon \rightarrow \langle THROW\ ArrayStore,\ s \rangle$
- | *RedAAss*:

$\llbracket \text{typeof-addr } h \ a = \lfloor \text{Array-type } T \ n \rfloor; 0 \leq s \ i; \text{ sint } i < \text{int } n; \text{typeof}_h \ w = \text{Some } U; P \vdash U \leq T;$
 $\text{heap-write } h \ a \ (\text{ACell } (\text{nat } (\text{sint } i))) \ w \ h' \rrbracket$
 $\implies \text{extTA}, P, t \vdash \langle (\text{addr } a) \lfloor \text{Val } (\text{Intg } i) \rfloor := \text{Val } w::'\text{addr } \text{expr}, (h, l) \rangle -\llbracket \text{WriteMem } a \ (\text{ACell } (\text{nat } (\text{sint } i))) \ w \rrbracket \rightarrow \langle \text{unit}, (h', l) \rangle$

| *ALengthRed:*

$\text{extTA}, P, t \vdash \langle a, s \rangle -ta \rightarrow \langle a', s' \rangle \implies \text{extTA}, P, t \vdash \langle a \cdot \text{length}, s \rangle -ta \rightarrow \langle a' \cdot \text{length}, s' \rangle$

| *RedALength:*

$\text{typeof-addr } h \ a = \lfloor \text{Array-type } T \ n \rfloor$
 $\implies \text{extTA}, P, t \vdash \langle \text{addr } a \cdot \text{length}, (h, l) \rangle -\varepsilon \rightarrow \langle \text{Val } (\text{Intg } (\text{word-of-nat } n)), (h, l) \rangle$

| *RedALengthNull:*

$\text{extTA}, P, t \vdash \langle \text{null} \cdot \text{length}, s \rangle -\varepsilon \rightarrow \langle \text{THROW NullPointer}, s \rangle$

| *FAccRed:*

$\text{extTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies \text{extTA}, P, t \vdash \langle e \cdot F\{D\}, s \rangle -ta \rightarrow \langle e' \cdot F\{D\}, s' \rangle$

| *RedFAcc:*

$\text{heap-read } h \ a \ (\text{CField } D \ F) \ v$
 $\implies \text{extTA}, P, t \vdash \langle (\text{addr } a) \cdot F\{D\}, (h, l) \rangle -\llbracket \text{ReadMem } a \ (\text{CField } D \ F) \ v \rrbracket \rightarrow \langle \text{Val } v, (h, l) \rangle$

| *RedFAccNull:*

$\text{extTA}, P, t \vdash \langle \text{null} \cdot F\{D\}, s \rangle -\varepsilon \rightarrow \langle \text{THROW NullPointer}, s \rangle$

| *FAssRed1:*

$\text{extTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies \text{extTA}, P, t \vdash \langle e \cdot F\{D\} := e2, s \rangle -ta \rightarrow \langle e' \cdot F\{D\} := e2, s' \rangle$

| *FAssRed2:*

$\text{extTA}, P, t \vdash \langle (e::'\text{addr } \text{expr}), s \rangle -ta \rightarrow \langle e', s' \rangle \implies \text{extTA}, P, t \vdash \langle \text{Val } v \cdot F\{D\} := e, s \rangle -ta \rightarrow \langle \text{Val } v \cdot F\{D\} := e', s' \rangle$

| *RedFAss:*

$\text{heap-write } h \ a \ (\text{CField } D \ F) \ v \ h' \implies$
 $\text{extTA}, P, t \vdash \langle (\text{addr } a) \cdot F\{D\} := \text{Val } v, (h, l) \rangle -\llbracket \text{WriteMem } a \ (\text{CField } D \ F) \ v \rrbracket \rightarrow \langle \text{unit}, (h', l) \rangle$

| *RedFAssNull:*

$\text{extTA}, P, t \vdash \langle \text{null} \cdot F\{D\} := \text{Val } v::'\text{addr } \text{expr}, s \rangle -\varepsilon \rightarrow \langle \text{THROW NullPointer}, s \rangle$

| *CASRed1:*

$\text{extTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies$
 $\text{extTA}, P, t \vdash \langle e \cdot \text{compareAndSwap}(D \cdot F, e2, e3), s \rangle -ta \rightarrow \langle e' \cdot \text{compareAndSwap}(D \cdot F, e2, e3), s' \rangle$

| *CASRed2:*

$\text{extTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies$
 $\text{extTA}, P, t \vdash \langle \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, e, e3), s \rangle -ta \rightarrow \langle \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, e', e3), s' \rangle$

| *CASRed3:*

$\text{extTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies$
 $\text{extTA}, P, t \vdash \langle \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', e), s \rangle -ta \rightarrow \langle \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', e'), s' \rangle$

| *CASNull:*

$extTA, P, t \vdash \langle null \cdot compareAndSwap(D \cdot F, Val v, Val v'), s \rangle -\varepsilon \rightarrow \langle THROW NullPointer, s \rangle$

| *RedCASSucceed:*

$\llbracket heap-read\ h\ a\ (CField\ D\ F)\ v; heap-write\ h\ a\ (CField\ D\ F)\ v'\ h' \rrbracket \implies$
 $extTA, P, t \vdash \langle addr\ a \cdot compareAndSwap(D \cdot F, Val v, Val v'), (h, l) \rangle$
 $-\llbracket ReadMem\ a\ (CField\ D\ F)\ v, WriteMem\ a\ (CField\ D\ F)\ v' \rrbracket \rightarrow$
 $\langle true, (h', l) \rangle$

| *RedCASFail:*

$\llbracket heap-read\ h\ a\ (CField\ D\ F)\ v''; v \neq v'' \rrbracket \implies$
 $extTA, P, t \vdash \langle addr\ a \cdot compareAndSwap(D \cdot F, Val v, Val v'), (h, l) \rangle$
 $-\llbracket ReadMem\ a\ (CField\ D\ F)\ v'' \rrbracket \rightarrow$
 $\langle false, (h, l) \rangle$

| *CallObj:*

$extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies extTA, P, t \vdash \langle e \cdot M(es), s \rangle -ta \rightarrow \langle e' \cdot M(es), s' \rangle$

| *CallParams:*

$extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies$
 $extTA, P, t \vdash \langle (Val\ v) \cdot M(es), s \rangle -ta \rightarrow \langle (Val\ v) \cdot M(es'), s' \rangle$

| *RedCall:*

$\llbracket typeof-addr\ (hp\ s)\ a = \lfloor hU \rfloor; P \vdash class-type-of\ hU\ sees\ M:Ts \rightarrow T = \lfloor (pns, body) \rfloor\ in\ D;$
 $size\ vs = size\ pns; size\ Ts = size\ pns \rrbracket$
 $\implies extTA, P, t \vdash \langle (addr\ a) \cdot M(map\ Val\ vs), s \rangle -\varepsilon \rightarrow \langle blocks\ (this\ \# \ pns)\ (Class\ D\ \# \ Ts)\ (Addr\ a\ \# \ vs)\ body, s \rangle$

| *RedCallExternal:*

$\llbracket typeof-addr\ (hp\ s)\ a = \lfloor hU \rfloor; P \vdash class-type-of\ hU\ sees\ M:Ts \rightarrow T = Native\ in\ D;$
 $P, t \vdash \langle a \cdot M(vs), hp\ s \rangle -ta \rightarrow ext\ \langle va, h' \rangle;$
 $ta' = extTA\ ta; e' = extRet2J\ ((addr\ a) \cdot M(map\ Val\ vs))\ va; s' = (h', lcl\ s) \rrbracket$
 $\implies extTA, P, t \vdash \langle (addr\ a) \cdot M(map\ Val\ vs), s \rangle -ta' \rightarrow \langle e', s' \rangle$

| *RedCallNull:*

$extTA, P, t \vdash \langle null \cdot M(map\ Val\ vs), s \rangle -\varepsilon \rightarrow \langle THROW\ NullPointer, s \rangle$

| *BlockRed:*

$extTA, P, t \vdash \langle e, (h, l(V := vo)) \rangle -ta \rightarrow \langle e', (h', l') \rangle$
 $\implies extTA, P, t \vdash \langle \{V:T=vo; e\}, (h, l) \rangle -ta \rightarrow \langle \{V:T=l'\ V; e'\}, (h', l'(V := l\ V)) \rangle$

| *RedBlock:*

$extTA, P, t \vdash \langle \{V:T=vo; Val\ u\}, s \rangle -\varepsilon \rightarrow \langle Val\ u, s \rangle$

| *SynchronizedRed1:*

$extTA, P, t \vdash \langle o', s \rangle -ta \rightarrow \langle o'', s' \rangle \implies extTA, P, t \vdash \langle sync(o')\ e, s \rangle -ta \rightarrow \langle sync(o'')\ e, s' \rangle$

| *SynchronizedNull:*

$extTA, P, t \vdash \langle sync(null)\ e, s \rangle -\varepsilon \rightarrow \langle THROW\ NullPointer, s \rangle$

| *LockSynchronized:*

$extTA, P, t \vdash \langle sync(addr\ a)\ e, s \rangle -\llbracket Lock \rightarrow a, SyncLock\ a \rrbracket \rightarrow \langle insync(a)\ e, s \rangle$

| *SynchronizedRed2:*

$extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies extTA, P, t \vdash \langle insync(a)\ e, s \rangle -ta \rightarrow \langle insync(a)\ e', s' \rangle$

- | *UnlockSynchronized*:
 $extTA, P, t \vdash \langle insync(a) (Val v), s \rangle -\{\!| Unlock \rightarrow a, SyncUnlock a \!\! \} \rightarrow \langle Val v, s \rangle$
- | *SeqRed*:
 $extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies extTA, P, t \vdash \langle e;;e2, s \rangle -ta \rightarrow \langle e';e2, s' \rangle$
- | *RedSeq*:
 $extTA, P, t \vdash \langle (Val v);;e, s \rangle -\varepsilon \rightarrow \langle e, s \rangle$
- | *CondRed*:
 $extTA, P, t \vdash \langle b, s \rangle -ta \rightarrow \langle b', s' \rangle \implies extTA, P, t \vdash \langle if (b) e1 else e2, s \rangle -ta \rightarrow \langle if (b') e1 else e2, s' \rangle$
- | *RedCondT*:
 $extTA, P, t \vdash \langle if (true) e1 else e2, s \rangle -\varepsilon \rightarrow \langle e1, s \rangle$
- | *RedCondF*:
 $extTA, P, t \vdash \langle if (false) e1 else e2, s \rangle -\varepsilon \rightarrow \langle e2, s \rangle$
- | *RedWhile*:
 $extTA, P, t \vdash \langle while(b) c, s \rangle -\varepsilon \rightarrow \langle if (b) (c;;while(b) c) else unit, s \rangle$
- | *ThrowRed*:
 $extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies extTA, P, t \vdash \langle throw e, s \rangle -ta \rightarrow \langle throw e', s' \rangle$
- | *RedThrowNull*:
 $extTA, P, t \vdash \langle throw null, s \rangle -\varepsilon \rightarrow \langle THROW NullPointer, s \rangle$
- | *TryRed*:
 $extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies extTA, P, t \vdash \langle try e catch(C V) e2, s \rangle -ta \rightarrow \langle try e' catch(C V) e2, s' \rangle$
- | *RedTry*:
 $extTA, P, t \vdash \langle try (Val v) catch(C V) e2, s \rangle -\varepsilon \rightarrow \langle Val v, s \rangle$
- | *RedTryCatch*:
 $\llbracket \text{typeof-addr } (hp \ s) \ a = \llbracket \text{Class-type } D \rrbracket; P \vdash D \preceq^* C \rrbracket$
 $\implies extTA, P, t \vdash \langle try (Throw a) catch(C V) e2, s \rangle -\varepsilon \rightarrow \langle \{ V:Class C = \llbracket Addr a \rrbracket; e2 \}, s \rangle$
- | *RedTryFail*:
 $\llbracket \text{typeof-addr } (hp \ s) \ a = \llbracket \text{Class-type } D \rrbracket; \neg P \vdash D \preceq^* C \rrbracket$
 $\implies extTA, P, t \vdash \langle try (Throw a) catch(C V) e2, s \rangle -\varepsilon \rightarrow \langle Throw a, s \rangle$
- | *ListRed1*:
 $extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies$
 $extTA, P, t \vdash \langle e \# es, s \rangle [-ta \rightarrow] \langle e' \# es, s' \rangle$
- | *ListRed2*:
 $extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies$
 $extTA, P, t \vdash \langle Val v \# es, s \rangle [-ta \rightarrow] \langle Val v \# es', s' \rangle$
- Exception propagation
- | *NewArrayThrow*: $extTA, P, t \vdash \langle newA \ T \llbracket Throw a \rrbracket, s \rangle -\varepsilon \rightarrow \langle Throw a, s \rangle$

$\text{CastThrow}: \text{extTA}, P, t \vdash \langle \text{Cast } C \text{ (Throw } a), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$
 $\text{InstanceOfThrow}: \text{extTA}, P, t \vdash \langle (\text{Throw } a) \text{ instanceof } T, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$
 $\text{BinOpThrow1}: \text{extTA}, P, t \vdash \langle (\text{Throw } a) \llcorner \text{bop} \llcorner e_2, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$
 $\text{BinOpThrow2}: \text{extTA}, P, t \vdash \langle (\text{Val } v_1) \llcorner \text{bop} \llcorner (\text{Throw } a), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$
 $\text{LAssThrow}: \text{extTA}, P, t \vdash \langle V := (\text{Throw } a), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$
 $\text{AAccThrow1}: \text{extTA}, P, t \vdash \langle (\text{Throw } a)[i], s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$
 $\text{AAccThrow2}: \text{extTA}, P, t \vdash \langle (\text{Val } v)[\text{Throw } a], s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$
 $\text{AAssThrow1}: \text{extTA}, P, t \vdash \langle (\text{Throw } a)[i] := e, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$
 $\text{AAssThrow2}: \text{extTA}, P, t \vdash \langle (\text{Val } v)[\text{Throw } a] := e, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$
 $\text{AAssThrow3}: \text{extTA}, P, t \vdash \langle (\text{Val } v)[\text{Val } i] := \text{Throw } a :: \text{'addr expr}, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$
 $\text{ALengthThrow}: \text{extTA}, P, t \vdash \langle (\text{Throw } a) \cdot \text{length}, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$
 $\text{FAccThrow}: \text{extTA}, P, t \vdash \langle (\text{Throw } a) \cdot F\{D\}, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$
 $\text{FAssThrow1}: \text{extTA}, P, t \vdash \langle (\text{Throw } a) \cdot F\{D\} := e_2, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$
 $\text{FAssThrow2}: \text{extTA}, P, t \vdash \langle \text{Val } v \cdot F\{D\} := (\text{Throw } a :: \text{'addr expr}), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$
 $\text{CASThrow}: \text{extTA}, P, t \vdash \langle \text{Throw } a \cdot \text{compareAndSwap}(D \cdot F, e_2, e_3), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$
 $\text{CASThrow2}: \text{extTA}, P, t \vdash \langle \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Throw } a, e_3), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$
 $\text{CASThrow3}: \text{extTA}, P, t \vdash \langle \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', \text{Throw } a), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$
 $\text{CallThrowObj}: \text{extTA}, P, t \vdash \langle (\text{Throw } a) \cdot M(es), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$
 $\text{CallThrowParams}: \llbracket es = \text{map Val vs } @ \text{ Throw } a \# es' \rrbracket \implies \text{extTA}, P, t \vdash \langle (\text{Val } v) \cdot M(es), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$
 $\text{BlockThrow}: \text{extTA}, P, t \vdash \langle \{ V : T = \text{vo}; \text{Throw } a \}, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$
 $\text{SynchronizedThrow1}: \text{extTA}, P, t \vdash \langle \text{sync}(\text{Throw } a) e, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$
 $\text{SynchronizedThrow2}: \text{extTA}, P, t \vdash \langle \text{insync}(a) \text{ Throw } ad, s \rangle -\{\text{Unlock} \rightarrow a, \text{SyncUnlock } a\} \rightarrow \langle \text{Throw } ad, s \rangle$
 $\text{SeqThrow}: \text{extTA}, P, t \vdash \langle (\text{Throw } a);; e_2, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$
 $\text{CondThrow}: \text{extTA}, P, t \vdash \langle \text{if } (\text{Throw } a) e_1 \text{ else } e_2, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$
 $\text{ThrowThrow}: \text{extTA}, P, t \vdash \langle \text{throw}(\text{Throw } a), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$

inductive-cases red-cases:

$\text{extTA}, P, t \vdash \langle \text{new } C, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle$
 $\text{extTA}, P, t \vdash \langle \text{newA } T[e], s \rangle -\text{ta} \rightarrow \langle e', s' \rangle$
 $\text{extTA}, P, t \vdash \langle \text{Cast } T e, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle$
 $\text{extTA}, P, t \vdash \langle e \text{ instanceof } T, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle$
 $\text{extTA}, P, t \vdash \langle e \llcorner \text{bop} \llcorner e', s \rangle -\text{ta} \rightarrow \langle e'', s' \rangle$
 $\text{extTA}, P, t \vdash \langle \text{Var } V, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle$
 $\text{extTA}, P, t \vdash \langle V := e, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle$
 $\text{extTA}, P, t \vdash \langle a[i], s \rangle -\text{ta} \rightarrow \langle e', s' \rangle$
 $\text{extTA}, P, t \vdash \langle a[i] := e, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle$
 $\text{extTA}, P, t \vdash \langle a \cdot \text{length}, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle$
 $\text{extTA}, P, t \vdash \langle e \cdot F\{D\}, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle$
 $\text{extTA}, P, t \vdash \langle e \cdot F\{D\} := e', s \rangle -\text{ta} \rightarrow \langle e'', s' \rangle$
 $\text{extTA}, P, t \vdash \langle e \cdot \text{compareAndSwap}(D \cdot F, e', e''), s \rangle -\text{ta} \rightarrow \langle e''', s' \rangle$
 $\text{extTA}, P, t \vdash \langle e \cdot M(es), s \rangle -\text{ta} \rightarrow \langle e', s' \rangle$
 $\text{extTA}, P, t \vdash \langle \{ V : T = \text{vo}; e \}, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle$
 $\text{extTA}, P, t \vdash \langle \text{sync}(o') e, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle$
 $\text{extTA}, P, t \vdash \langle \text{insync}(a) e, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle$
 $\text{extTA}, P, t \vdash \langle e;; e', s \rangle -\text{ta} \rightarrow \langle e'', s' \rangle$
 $\text{extTA}, P, t \vdash \langle \text{if } (b) e1 \text{ else } e2, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle$
 $\text{extTA}, P, t \vdash \langle \text{while } (b) e, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle$
 $\text{extTA}, P, t \vdash \langle \text{throw } e, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle$
 $\text{extTA}, P, t \vdash \langle \text{try } e \text{ catch}(C V) e', s \rangle -\text{ta} \rightarrow \langle e'', s' \rangle$

inductive-cases reds-cases:

$\text{extTA}, P, t \vdash \langle e \# es, s \rangle [-\text{ta} \rightarrow] \langle es', s' \rangle$

abbreviation red' ::

'addr J -prog \Rightarrow 'thread-id \Rightarrow 'addr expr \Rightarrow ('heap \times 'addr locals)
 \Rightarrow ('addr, 'thread-id, 'heap) J -thread-action \Rightarrow 'addr expr \Rightarrow ('heap \times 'addr locals) \Rightarrow bool
 $(\langle -, - \vdash ((1 \langle -, / - \rangle) \dashrightarrow / (1 \langle -, / - \rangle)) \rangle [51, 0, 0, 0, 0, 0, 0] 81)$

where $red' P \equiv red (extTA2J P) P$

abbreviation $reds'$::

'addr J -prog \Rightarrow 'thread-id \Rightarrow 'addr expr list \Rightarrow ('heap \times 'addr locals)
 \Rightarrow ('addr, 'thread-id, 'heap) J -thread-action \Rightarrow 'addr expr list \Rightarrow ('heap \times 'addr locals) \Rightarrow bool
 $(\langle -, - \vdash ((1 \langle -, / - \rangle) [-\dashrightarrow] / (1 \langle -, / - \rangle)) \rangle [51, 0, 0, 0, 0, 0, 0] 81)$

where $reds' P \equiv reds (extTA2J P) P$

4.4.1 Some easy lemmas

lemma [iff]:

$\neg extTA, P, t \vdash \langle Val\ v, s \rangle -ta \rightarrow \langle e', s' \rangle$

by(fastforce elim:red.cases)

lemma red-no-val [dest]:

$\llbracket extTA, P, t \vdash \langle e, s \rangle -tas \rightarrow \langle e', s' \rangle; is\text{-val}\ e \rrbracket \Longrightarrow False$

by(auto)

lemma [iff]: $\neg extTA, P, t \vdash \langle Throw\ a, s \rangle -ta \rightarrow \langle e', s' \rangle$

by(fastforce elim:red-cases)

lemma reds-map-Val-Throw:

$extTA, P, t \vdash \langle map\ Val\ vs\ @\ Throw\ a\ \# es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \longleftrightarrow False$

by(induct vs arbitrary: es')(auto elim!: reds-cases)

lemma reds-preserves-len:

$extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \Longrightarrow length\ es' = length\ es$

by(induct es arbitrary: es')(auto elim: reds.cases)

lemma red-lcl-incr: $extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \Longrightarrow dom\ (lcl\ s) \subseteq dom\ (lcl\ s')$

and reds-lcl-incr: $extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \Longrightarrow dom\ (lcl\ s) \subseteq dom\ (lcl\ s')$

apply(induct rule:red-reds.inducts)

apply(auto simp del: fun-upd-apply split: if-split-asm)

done

lemma red-lcl-add-aux:

$extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \Longrightarrow extTA, P, t \vdash \langle e, (hp\ s, l0\ ++\ lcl\ s) \rangle -ta \rightarrow \langle e', (hp\ s', l0\ ++\ lcl\ s') \rangle$

and reds-lcl-add-aux:

$extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \Longrightarrow extTA, P, t \vdash \langle es, (hp\ s, l0\ ++\ lcl\ s) \rangle [-ta \rightarrow] \langle es', (hp\ s', l0\ ++\ lcl\ s') \rangle$

proof (induct arbitrary: l0 and l0 rule:red-reds.inducts)

case (BlockRed $e\ h\ x\ V\ vo\ ta\ e'\ h'\ x'\ T$)

note $IH = \langle \bigwedge l0. extTA, P, t \vdash \langle e, (hp\ (h, x(V := vo)), l0\ ++\ lcl\ (h, x(V := vo))) \rangle -ta \rightarrow \langle e', (hp\ (h', x'), l0\ ++\ lcl\ (h', x')) \rangle \rangle$ [simplified]

have lrew: $\bigwedge x\ x'. x(V := vo) ++ x'(V := vo) = (x ++ x')(V := vo)$

by(simp add:fun-eq-iff map-add-def)

have lrew1: $\bigwedge X\ X'\ X''\ vo. (X(V := vo) ++ X')(V := (X ++ X'')\ V) = X ++ X'(V := X''\ V)$

by(simp add: fun-eq-iff map-add-def)

```

have lrew2:  $\bigwedge X X'. (X(V := None) ++ X') V = X' V$ 
  by(simp add: map-add-def)
show ?case
proof(cases vo)
  case None
  from IH[of l0(V := vo)]
  show ?thesis
    apply(simp del: fun-upd-apply add: lrew)
    apply(drule red-reds.BlockRed)
    by(simp only: lrew1 None lrew2)
next
  case (Some v)
  with  $\langle \text{extTA}, P, t \vdash \langle e, (h, x(V := vo)) \rangle -ta \rightarrow \langle e', (h', x') \rangle \rangle$ 
  have  $x' V \neq None$ 
    by  $-(\text{drule red-lcl-incr}, \text{auto split: if-split-asm})$ 
  with IH[of l0(V := vo)]
  show ?thesis
    apply(clarsimp simp del: fun-upd-apply simp add: lrew)
    apply(drule red-reds.BlockRed)
    by(simp add: lrew1 Some del: fun-upd-apply)
qed
next
  case RedTryFail thus ?case
    by(auto intro: red-reds.RedTryFail)
qed(fastforce intro:red-reds.intros simp del: fun-upd-apply)+

lemma red-lcl-add:  $\text{extTA}, P, t \vdash \langle e, (h, l) \rangle -ta \rightarrow \langle e', (h', l') \rangle \implies \text{extTA}, P, t \vdash \langle e, (h, l0 ++ l) \rangle -ta \rightarrow \langle e', (h', l0 ++ l') \rangle$ 
  and reds-lcl-add:  $\text{extTA}, P, t \vdash \langle es, (h, l) \rangle [-ta \rightarrow] \langle es', (h', l') \rangle \implies \text{extTA}, P, t \vdash \langle es, (h, l0 ++ l) \rangle [-ta \rightarrow] \langle es', (h', l0 ++ l') \rangle$ 
by(auto dest:red-lcl-add-aux reds-lcl-add-aux)

lemma reds-no-val [dest]:
   $\llbracket \text{extTA}, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{is-vals } es \rrbracket \implies \text{False}$ 
apply(induct es arbitrary: s ta es' s')
apply(blast elim: reds.cases)
apply(erule reds.cases)
apply(auto, blast)
done

lemma red-no-Throw [dest!]:
   $\text{extTA}, P, t \vdash \langle \text{Throw } a, s \rangle -ta \rightarrow \langle e', s' \rangle \implies \text{False}$ 
by(auto elim!: red-cases)

lemma red-lcl-sub:
   $\llbracket \text{extTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{fv } e \subseteq W \rrbracket$ 
 $\implies \text{extTA}, P, t \vdash \langle e, (hp\ s, (lcl\ s) | 'W) \rangle -ta \rightarrow \langle e', (hp\ s', (lcl\ s') | 'W) \rangle$ 

  and reds-lcl-sub:
   $\llbracket \text{extTA}, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{fvs } es \subseteq W \rrbracket$ 
 $\implies \text{extTA}, P, t \vdash \langle es, (hp\ s, (lcl\ s) | 'W) \rangle [-ta \rightarrow] \langle es', (hp\ s', (lcl\ s') | 'W) \rangle$ 
proof(induct arbitrary: W and W rule: red-reds.inducts)
  case (RedLAss V v h l W)
  have  $\text{extTA}, P, t \vdash \langle V := \text{Val } v, (h, l | 'W) \rangle -\varepsilon \rightarrow \langle \text{unit}, (h, (l | 'W)(V \mapsto v)) \rangle$ 

```

```

  by(rule red-reds.RedLAss)
with RedLAss show ?case by(simp del: fun-upd-apply)
next
  case (BlockRed e h x V vo ta e' h' x' T)
  have IH:  $\bigwedge W. fv\ e \subseteq W \implies extTA,P,t \vdash \langle e, (hp\ (h, x(V := vo)), lcl\ (h, x(V := vo)) \mid' W) \rangle -ta \rightarrow \langle e', (hp\ (h', x'), lcl\ (h', x') \mid' W) \rangle$  by fact
  from  $\langle fv\ \{V:T=vo; e\} \subseteq W \rangle$  have fve:  $fv\ e \subseteq insert\ V\ W$  by auto
  show ?case
  proof(cases V  $\in$  W)
    case True
    with fve have fv e  $\subseteq$  W by auto
    from True IH[OF this] have extTA,P,t  $\vdash \langle e, (h, (x \mid' W)(V := vo)) \rangle -ta \rightarrow \langle e', (h', x' \mid' W) \rangle$ 
  by(simp)
  with True have extTA,P,t  $\vdash \langle \{V:T=vo; e\}, (h, x \mid' W) \rangle -ta \rightarrow \langle \{V:T=x' V; e'\}, (h', (x' \mid' W)(V := x V)) \rangle$ 
    by  $-(drule\ red-reds.BlockRed[where\ T=T],\ simp)$ 
    with True show ?thesis by(simp del: fun-upd-apply)
  next
  case False
  with IH[OF fve] have extTA,P,t  $\vdash \langle e, (h, (x \mid' W)(V := vo)) \rangle -ta \rightarrow \langle e', (h', x' \mid' insert\ V\ W) \rangle$ 
  by(simp)
  with False have extTA,P,t  $\vdash \langle \{V:T=vo; e\}, (h, x \mid' W) \rangle -ta \rightarrow \langle \{V:T=x' V; e'\}, (h', (x' \mid' W)) \rangle$ 
    by  $-(drule\ red-reds.BlockRed[where\ T=T],\ simp)$ 
    with False show ?thesis by(simp del: fun-upd-apply)
  qed
next
  case RedTryFail thus ?case by(auto intro: red-reds.RedTryFail)
qed(fastforce intro: red-reds.intros)+

```

```

lemma red-notfree-unchanged:  $\llbracket extTA,P,t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; V \notin fv\ e \rrbracket \implies lcl\ s' V = lcl\ s V$ 
  and reds-notfree-unchanged:  $\llbracket extTA,P,t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; V \notin fvs\ es \rrbracket \implies lcl\ s' V = lcl\ s V$ 
apply(induct rule: red-reds.inducts)
apply(fastforce)+
done

```

```

lemma red-dom-lcl:  $extTA,P,t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies dom\ (lcl\ s') \subseteq dom\ (lcl\ s) \cup fv\ e$ 
  and reds-dom-lcl:  $extTA,P,t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies dom\ (lcl\ s') \subseteq dom\ (lcl\ s) \cup fvs\ es$ 

```

```

proof (induct rule:red-reds.inducts)
  case (BlockRed e h x V vo ta e' h' x' T)
  thus ?case by(clarsimp)(fastforce split:if-split-asm)
qed auto

```

lemma red-Suspend-is-call:

```

 $\llbracket convert-extTA\ extNTA,P,t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; Suspend\ w \in set\ \{ta\}_w \rrbracket$ 
 $\implies \exists a\ vs\ hT\ Ts\ Tr\ D. call\ e' = \llbracket (a, wait, vs) \rrbracket \wedge typeof-addr\ (hp\ s)\ a = \llbracket hT \rrbracket \wedge P \vdash class-type-of$ 
 $hT\ sees\ wait:Ts \rightarrow Tr = Native\ in\ D$ 

```

and reds-Suspend-is-calls:

```

 $\llbracket convert-extTA\ extNTA,P,t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; Suspend\ w \in set\ \{ta\}_w \rrbracket$ 
 $\implies \exists a\ vs\ hT\ Ts\ Tr\ D. calls\ es' = \llbracket (a, wait, vs) \rrbracket \wedge typeof-addr\ (hp\ s)\ a = \llbracket hT \rrbracket \wedge P \vdash class-type-of$ 
 $hT\ sees\ wait:Ts \rightarrow Tr = Native\ in\ D$ 

```

```

proof(induct rule: red-reds.inducts)

```

case RedCallExternal

thus ?case

```

  apply clarsimp
  apply (frule red-external-Suspend-StaySame, simp)
  apply (drule red-external-Suspend-waitD, fastforce+)
  done
qed auto

```

end

context *J-heap* begin

lemma *red-heat-incr*: $extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \Longrightarrow hp\ s \leq hp\ s'$
and *reds-heat-incr*: $extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \Longrightarrow hp\ s \leq hp\ s'$
by (*induct rule:red-reds.inducts*) (*auto intro: heat-heap-ops red-external-heat*)

lemma *red-preserves-tconf*: $\llbracket extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; P, hp\ s \vdash t \sqrt{t} \rrbracket \Longrightarrow P, hp\ s' \vdash t \sqrt{t}$
by (*drule red-heat-incr*) (*rule tconf-heat-mono*)

lemma *reds-preserves-tconf*: $\llbracket extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; P, hp\ s \vdash t \sqrt{t} \rrbracket \Longrightarrow P, hp\ s' \vdash t \sqrt{t}$
by (*drule reds-heat-incr*) (*rule tconf-heat-mono*)

end

4.4.2 Code generation

context *J-heap-base* begin

lemma *RedCall-code*:

$\llbracket is\text{-vals}\ es; typeof\text{-addr}\ (hp\ s)\ a = \lfloor hU \rfloor; P \vdash class\text{-type-of}\ hU\ sees\ M:Ts \rightarrow T = \llbracket (pns, body) \rrbracket\ in\ D;$
 $size\ es = size\ pns; size\ Ts = size\ pns \rrbracket$
 $\Longrightarrow extTA, P, t \vdash \langle (addr\ a) \cdot M(es), s \rangle -\varepsilon \rightarrow \langle blocks\ (this\ \# pns)\ (Class\ D\ \# Ts)\ (Addr\ a\ \# map\ the\text{-Val}\ es)\ body, s \rangle$

and *RedCallExternal-code*:

$\llbracket is\text{-vals}\ es; typeof\text{-addr}\ (hp\ s)\ a = \lfloor hU \rfloor; P \vdash class\text{-type-of}\ hU\ sees\ M:Ts \rightarrow T = Native\ in\ D;$
 $P, t \vdash \langle a \cdot M(map\ the\text{-Val}\ es), hp\ s \rangle -ta \rightarrow ext\ \langle va, h' \rangle \rrbracket$
 $\Longrightarrow extTA, P, t \vdash \langle (addr\ a) \cdot M(es), s \rangle -extTA\ ta \rightarrow \langle extRet2J\ ((addr\ a) \cdot M(es))\ va, (h', lcl\ s) \rangle$

and *RedCallNull-code*:

$is\text{-vals}\ es \Longrightarrow extTA, P, t \vdash \langle null \cdot M(es), s \rangle -\varepsilon \rightarrow \langle THROW\ NullPointer, s \rangle$

and *CallThrowParams-code*:

$is\text{-Throws}\ es \Longrightarrow extTA, P, t \vdash \langle (Val\ v) \cdot M(es), s \rangle -\varepsilon \rightarrow \langle hd\ (dropWhile\ is\text{-val}\ es), s \rangle$

apply (*auto simp add: is-vals-conv is-Throws-conv o-def intro: RedCall RedCallExternal RedCallNull simp del: blocks.simps*)

apply (*subst dropWhile-append2*)

apply (*auto intro: CallThrowParams*)

done

end

lemmas [*code-pred-intro*] =

J-heap-base.RedNew[*folded Predicate-Compile.contains-def*] *J-heap-base.RedNewFail* *J-heap-base.NewArrayRed*

J-heap-base.RedNewArray[folded *Predicate-Compile.contains-def*]
J-heap-base.RedNewArrayNegative *J-heap-base.RedNewArrayFail*
J-heap-base.CastRed *J-heap-base.RedCast* *J-heap-base.RedCastFail* *J-heap-base.InstanceOfRed*
J-heap-base.RedInstanceOf *J-heap-base.BinOpRed1* *J-heap-base.BinOpRed2* *J-heap-base.RedBinOp*
J-heap-base.RedBinOpFail
J-heap-base.RedVar *J-heap-base.LAssRed* *J-heap-base.RedLAss*
J-heap-base.AAccRed1 *J-heap-base.AAccRed2* *J-heap-base.RedAAccNull*
J-heap-base.RedAAccBounds *J-heap-base.RedAAcc* *J-heap-base.AAssRed1* *J-heap-base.AAssRed2* *J-heap-base.AAssRed3*
J-heap-base.RedAAssNull *J-heap-base.RedAAssBounds* *J-heap-base.RedAAssStore* *J-heap-base.RedAAss*
J-heap-base.ALengthRed
J-heap-base.RedALength *J-heap-base.RedALengthNull* *J-heap-base.FAccRed* *J-heap-base.RedFAcc* *J-heap-base.RedFAccNull*
J-heap-base.FAssRed1 *J-heap-base.FAssRed2* *J-heap-base.RedFAss* *J-heap-base.RedFAssNull*
J-heap-base.CASRed1 *J-heap-base.CASRed2* *J-heap-base.CASRed3* *J-heap-base.CASNull* *J-heap-base.RedCASSucceed*
J-heap-base.RedCASFail
J-heap-base.CallObj *J-heap-base.CallParams*

declare

J-heap-base.RedCall-code[*code-pred-intro* *RedCall-code*]
J-heap-base.RedCallExternal-code[*code-pred-intro* *RedCallExternal-code*]
J-heap-base.RedCallNull-code[*code-pred-intro* *RedCallNull-code*]

lemmas [*code-pred-intro*] =

J-heap-base.BlockRed *J-heap-base.RedBlock* *J-heap-base.SynchronizedRed1* *J-heap-base.SynchronizedNull*
J-heap-base.LockSynchronized *J-heap-base.SynchronizedRed2* *J-heap-base.UnlockSynchronized*
J-heap-base.SeqRed *J-heap-base.RedSeq* *J-heap-base.CondRed* *J-heap-base.RedCondT* *J-heap-base.RedCondF*
J-heap-base.RedWhile
J-heap-base.ThrowRed

declare

J-heap-base.RedThrowNull[*code-pred-intro* *RedThrowNull*']

lemmas [*code-pred-intro*] =

J-heap-base.TryRed *J-heap-base.RedTry* *J-heap-base.RedTryCatch*
J-heap-base.RedTryFail *J-heap-base.ListRed1* *J-heap-base.ListRed2*
J-heap-base.NewArrayThrow *J-heap-base.CastThrow* *J-heap-base.InstanceOfThrow* *J-heap-base.BinOpThrow1*
J-heap-base.BinOpThrow2
J-heap-base.LAssThrow *J-heap-base.AAccThrow1* *J-heap-base.AAccThrow2* *J-heap-base.AAssThrow1*
J-heap-base.AAssThrow2
J-heap-base.AAssThrow3 *J-heap-base.ALengthThrow* *J-heap-base.FAccThrow* *J-heap-base.FAssThrow1*
J-heap-base.FAssThrow2
J-heap-base.CASThrow *J-heap-base.CASThrow2* *J-heap-base.CASThrow3*
J-heap-base.CallThrowObj

declare

J-heap-base.CallThrowParams-code[*code-pred-intro* *CallThrowParams-code*]

lemmas [*code-pred-intro*] =

J-heap-base.BlockThrow *J-heap-base.SynchronizedThrow1* *J-heap-base.SynchronizedThrow2* *J-heap-base.SeqThrow*
J-heap-base.CondThrow

declare

J-heap-base.ThrowThrow[*code-pred-intro* *ThrowThrow*']

code-pred

(modes:

$$J\text{-heap-base.red: } i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow (i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow (i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow o \Rightarrow o \Rightarrow \text{bool}$$
and

$$J\text{-heap-base.reds: } i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow (i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow (i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow o \Rightarrow o \Rightarrow \text{bool}$$

[detect-switches, skip-proof] — proofs are possible, but take veerry long

*J-heap-base.red***proof** –**case** *red***from** *red.premis* **show** *thesis***proof**(cases rule: *J-heap-base.red.cases*[consumes 1, case-names
RedNew RedNewFail NewArrayRed RedNewArray RedNewArrayNegative RedNewArrayFail CastRed RedCast RedCastFail InstanceOfRed
RedInstanceOf BinOpRed1 BinOpRed2 RedBinOp RedBinOpFail RedVar LAssRed RedLAss
AAccRed1 AAccRed2 RedAAccNull RedAAccBounds RedAAcc
AAssRed1 AAssRed2 AAssRed3 RedAAssNull RedAAssBounds RedAAssStore RedAAss ALengthRed RedALength RedALengthNull FAccRed
RedFAcc RedFAccNull FAssRed1 FAssRed2 RedFAss RedFAssNull CASRed1 CASRed2 CASRed3 RedCASNull RedCASSucceed RedCASFail
CallObj CallParams RedCall RedCallExternal RedCallNull
BlockRed RedBlock SynchronizedRed1 SynchronizedNull LockSynchronized SynchronizedRed2 UnlockSynchronized SeqRed
RedSeq CondRed RedCondT RedCondF RedWhile ThrowRed RedThrowNull TryRed RedTry RedTryCatch RedTryFail
NewArrayThrow CastThrow InstanceOfThrow BinOpThrow1 BinOpThrow2 LAssThrow AAccThrow1 AAccThrow2 AAssThrow1 AAssThrow2
AAssThrow3 ALengthThrow FAccThrow FAssThrow1 FAssThrow2 CASThrow CASThrow2 CASThrow3
CallThrowObj CallThrowParams BlockThrow SynchronizedThrow1
SynchronizedThrow2 SeqThrow CondThrow ThrowThrow)
case (*RedCall* *s a U M Ts T pns body D vs*)**with** *red.RedCall-code*[*OF refl refl refl refl refl refl refl refl refl refl refl*, of a *M* map *Val vs s pns D Ts body U T*]**show** *?thesis* **by**(*simp add: o-def*)**next****case** (*RedCallExternal* *s a U M Ts T D vs ta va h' ta' e' s'*)**with** *red.RedCallExternal-code*[*OF refl refl refl refl refl refl refl refl refl refl refl*, of a *M* map *Val vs s ta va h' U Ts T D*]**show** *?thesis* **by**(*simp add: o-def*)**next****case** (*RedCallNull* *M vs s*)**with** *red.RedCallNull-code*[*OF refl refl refl refl refl refl refl refl refl refl refl*, of *M* map *Val vs s*]**show** *?thesis* **by**(*simp add: o-def*)**next****case** (*CallThrowParams* *es vs a es' v M s*)**with** *red.CallThrowParams-code*[*OF refl refl refl refl refl refl refl refl refl refl refl*, of *v M* map *Val vs @ Throw a # es' s*]**show** *?thesis***apply**(*auto simp add: is-Throws-conv*)**apply**(*erule meta-impE*)**apply**(*subst dropWhile-append2*)


```

    apply auto
  done
next
  case RedThrowNull thus ?thesis
    by-(erule (4) red.RedThrowNull'[OF refl refl refl refl refl refl refl refl refl refl refl])
next
  case ThrowThrow thus ?thesis
    by-(erule (4) red.ThrowThrow'[OF refl refl refl refl refl refl refl refl refl refl refl])
qed(assumption|erule (4) red.that[unfolded Predicate-Compile.contains-def, OF refl refl refl refl
refl refl refl refl refl refl])+)
next
  case reds
  from reds.premis show thesis
  by(rule J-heap-base.reds.cases)(assumption|erule (4) reds.that[OF refl refl refl refl refl refl refl
refl refl refl])+)
qed
end

```

4.5 Weak well-formedness of Jinja programs

```

theory WWellForm
imports
  ../Common/WellForm
  Expr
begin

definition
  wwf-J-mdecl :: 'addr J-prog ⇒ cname ⇒ 'addr J-mb mdecl ⇒ bool
where
  wwf-J-mdecl P C ≡ λ(M, Ts, T, (pns, body)).
    length Ts = length pns ∧ distinct pns ∧ this ∉ set pns ∧ fv body ⊆ {this} ∪ set pns

lemma wwf-J-mdecl[simp]:
  wwf-J-mdecl P C (M, Ts, T, pns, body) =
    (length Ts = length pns ∧ distinct pns ∧ this ∉ set pns ∧ fv body ⊆ {this} ∪ set pns)
abbreviation wwf-J-prog :: 'addr J-prog ⇒ bool
where wwf-J-prog == wf-prog wwf-J-mdecl

end

```

4.6 Well-typedness of Jinja expressions

```

theory WellType
imports
  Expr
  State
  ../Common/ExternalCallWF
  ../Common/WellForm
  ../Common/SemiType
begin

declare Listn.lesub-list-impl-same-size[simp del]

```

declare *listE-length* [*simp del*]

type-synonym

env = *vname* \rightarrow *ty*

inductive

WT :: (*ty* \Rightarrow *ty* \Rightarrow *ty* \Rightarrow *bool*) \Rightarrow 'addr *J-prog* \Rightarrow *env* \Rightarrow 'addr *expr* \Rightarrow *ty* \Rightarrow *bool* ($\langle -, -, - \vdash - :: - \rangle$
[51,51,51,51]50)

and *WTs* :: (*ty* \Rightarrow *ty* \Rightarrow *ty* \Rightarrow *bool*) \Rightarrow 'addr *J-prog* \Rightarrow *env* \Rightarrow 'addr *expr list* \Rightarrow *ty list* \Rightarrow *bool*
($\langle -, -, - \vdash - :: - \rangle$ [51,51,51,51]50)

for *is-lub* :: *ty* \Rightarrow *ty* \Rightarrow *ty* \Rightarrow *bool* ($\langle \vdash \text{lub}'((- / -)' \rangle = \rightarrow$ [51,51,51] 50)

and *P* :: 'addr *J-prog*

where

WTNew:

is-class *P C* \Longrightarrow

is-lub,*P,E* \vdash *new C* :: *Class C*

| *WTNewArray*:

\llbracket *is-lub*,*P,E* \vdash *e* :: *Integer*; *is-type* *P* (*T*[]) $\rrbracket \Longrightarrow$

is-lub,*P,E* \vdash *newA T*[*e*] :: *T*[]

| *WTCast*:

\llbracket *is-lub*,*P,E* \vdash *e* :: *T*; *P* \vdash *U* \leq *T* \vee *P* \vdash *T* \leq *U*; *is-type* *P U* \rrbracket

\Longrightarrow *is-lub*,*P,E* \vdash *Cast U e* :: *U*

| *WTInstanceOf*:

\llbracket *is-lub*,*P,E* \vdash *e* :: *T*; *P* \vdash *U* \leq *T* \vee *P* \vdash *T* \leq *U*; *is-type* *P U*; *is-refT U* \rrbracket

\Longrightarrow *is-lub*,*P,E* \vdash *e instanceof U* :: *Boolean*

| *WTVal*:

typeof v = *Some T* \Longrightarrow

is-lub,*P,E* \vdash *Val v* :: *T*

| *WTVar*:

E V = *Some T* \Longrightarrow

is-lub,*P,E* \vdash *Var V* :: *T*

| *WTBinOp*:

\llbracket *is-lub*,*P,E* \vdash *e1* :: *T1*; *is-lub*,*P,E* \vdash *e2* :: *T2*; *P* \vdash *T1* \llcorner *bop* \llcorner *T2* :: *T* \rrbracket

\Longrightarrow *is-lub*,*P,E* \vdash *e1* \llcorner *bop* \llcorner *e2* :: *T*

| *WTLAss*:

\llbracket *E V* = *Some T*; *is-lub*,*P,E* \vdash *e* :: *T'*; *P* \vdash *T'* \leq *T*; *V* \neq *this* \rrbracket

\Longrightarrow *is-lub*,*P,E* \vdash *V := e* :: *Void*

| *WTAAcc*:

\llbracket *is-lub*,*P,E* \vdash *a* :: *T*[]; *is-lub*,*P,E* \vdash *i* :: *Integer* \rrbracket

\Longrightarrow *is-lub*,*P,E* \vdash *a*[*i*] :: *T*

| *WTAAss*:

\llbracket *is-lub*,*P,E* \vdash *a* :: *T*[]; *is-lub*,*P,E* \vdash *i* :: *Integer*; *is-lub*,*P,E* \vdash *e* :: *T'*; *P* \vdash *T'* \leq *T* \rrbracket

\Longrightarrow *is-lub*,*P,E* \vdash *a*[*i*] := *e* :: *Void*

- | *WTALength*:
 $is-lub, P, E \vdash a :: T[] \implies is-lub, P, E \vdash a.length :: Integer$
- | *WTFAcc*:
 $\llbracket is-lub, P, E \vdash e :: U; class-type-of' U = [C]; P \vdash C \text{ sees } F:T (fm) \text{ in } D \rrbracket$
 $\implies is-lub, P, E \vdash e.F\{D\} :: T$
- | *WTFAss*:
 $\llbracket is-lub, P, E \vdash e_1 :: U; class-type-of' U = [C]; P \vdash C \text{ sees } F:T (fm) \text{ in } D; is-lub, P, E \vdash e_2 :: T'; P \vdash T' \leq T \rrbracket$
 $\implies is-lub, P, E \vdash e_1.F\{D\} := e_2 :: Void$
- | *WTCAS*:
 $\llbracket is-lub, P, E \vdash e_1 :: U; class-type-of' U = [C]; P \vdash C \text{ sees } F:T (fm) \text{ in } D; volatile fm; is-lub, P, E \vdash e_2 :: T'; P \vdash T' \leq T; is-lub, P, E \vdash e_3 :: T''; P \vdash T'' \leq T \rrbracket$
 $\implies is-lub, P, E \vdash e_1.compareAndSwap(D.F, e_2, e_3) :: Boolean$
- | *WTCall*:
 $\llbracket is-lub, P, E \vdash e :: U; class-type-of' U = [C]; P \vdash C \text{ sees } M:Ts \rightarrow T = meth \text{ in } D; is-lub, P, E \vdash es [::] Ts'; P \vdash Ts' [\leq] Ts \rrbracket$
 $\implies is-lub, P, E \vdash e.M(es) :: T$
- | *WTBlock*:
 $\llbracket is-type P T; is-lub, P, E(V \mapsto T) \vdash e :: T'; case\ vo\ of\ None \Rightarrow True \mid [v] \Rightarrow \exists T'. typeof\ v = [T'] \wedge P \vdash T' \leq T \rrbracket$
 $\implies is-lub, P, E \vdash \{V:T=vo; e\} :: T'$
- | *WTSynchronized*:
 $\llbracket is-lub, P, E \vdash o' :: T; is-refT T; T \neq NT; is-lub, P, E \vdash e :: T' \rrbracket$
 $\implies is-lub, P, E \vdash sync(o') e :: T'$
- Note that `insync` is not statically typable.
- | *WTSeq*:
 $\llbracket is-lub, P, E \vdash e_1 :: T_1; is-lub, P, E \vdash e_2 :: T_2 \rrbracket$
 $\implies is-lub, P, E \vdash e_1;;e_2 :: T_2$
- | *WTCond*:
 $\llbracket is-lub, P, E \vdash e :: Boolean; is-lub, P, E \vdash e_1 :: T_1; is-lub, P, E \vdash e_2 :: T_2; lub(T_1, T_2) = T \rrbracket$
 $\implies is-lub, P, E \vdash if (e) e_1 else e_2 :: T$
- | *WTWhile*:
 $\llbracket is-lub, P, E \vdash e :: Boolean; is-lub, P, E \vdash c :: T \rrbracket$
 $\implies is-lub, P, E \vdash while (e) c :: Void$
- | *WTThrow*:
 $\llbracket is-lub, P, E \vdash e :: Class\ C; P \vdash C \preceq^* Throwable \rrbracket \implies$
 $is-lub, P, E \vdash throw\ e :: Void$
- | *WTTry*:
 $\llbracket is-lub, P, E \vdash e_1 :: T; is-lub, P, E(V \mapsto Class\ C) \vdash e_2 :: T; P \vdash C \preceq^* Throwable \rrbracket$
 $\implies is-lub, P, E \vdash try\ e_1\ catch(C\ V)\ e_2 :: T$
- | *WTNil*: $is-lub, P, E \vdash [] [::] []$

| $WTCons: \llbracket is-lub, P, E \vdash e :: T; is-lub, P, E \vdash es \llbracket :: Ts \rrbracket \implies is-lub, P, E \vdash e\#es \llbracket :: T\#Ts \rrbracket$

abbreviation $WT' :: 'addr J-prog \Rightarrow env \Rightarrow 'addr expr \Rightarrow ty \Rightarrow bool (\langle -, - \vdash - :: \rangle \rightarrow [51, 51, 51] 50)$
where $WT' P \equiv WT (TypeRel.is-lub P) P$

abbreviation $WTs' :: 'addr J-prog \Rightarrow env \Rightarrow 'addr expr list \Rightarrow ty list \Rightarrow bool (\langle -, - \vdash - \llbracket :: \rrbracket \rightarrow [51, 51, 51] 50)$

where $WTs' P \equiv WTs (TypeRel.is-lub P) P$

declare $WT-WTs.intros[intro!]$

inductive-simps $WTs-iffs [iff]:$

$is-lub', P, E \vdash \llbracket :: Ts$
 $is-lub', P, E \vdash e\#es \llbracket :: T\#Ts$
 $is-lub', P, E \vdash e\#es \llbracket :: Ts$

lemma $WTs-conv-list-all2:$

fixes $is-lub$

shows $is-lub, P, E \vdash es \llbracket :: Ts = list-all2 (WT is-lub P E) es Ts$

by($induct\ es\ arbitrary: Ts$)($auto\ simp\ add: list-all2-Cons1\ elim: WTs.cases$)

lemma $WTs-append [iff]: \bigwedge is-lub Ts. (is-lub, P, E \vdash es_1 @ es_2 \llbracket :: Ts) =$

$(\exists Ts_1 Ts_2. Ts = Ts_1 @ Ts_2 \wedge is-lub, P, E \vdash es_1 \llbracket :: Ts_1 \wedge is-lub, P, E \vdash es_2 \llbracket :: Ts_2)$

by($auto\ simp\ add: WTs-conv-list-all2\ list-all2-append1\ dest: list-all2-lengthD[symmetric]$)

inductive-simps $WT-iffs [iff]:$

$is-lub', P, E \vdash Val\ v :: T$
 $is-lub', P, E \vdash Var\ V :: T$
 $is-lub', P, E \vdash e_1;;e_2 :: T_2$
 $is-lub', P, E \vdash \{V:T=vo; e\} :: T'$

inductive-cases $WT-elim-cases[elim!]:$

$is-lub', P, E \vdash V := e :: T$
 $is-lub', P, E \vdash sync(o') e :: T$
 $is-lub', P, E \vdash if (e) e_1 else e_2 :: T$
 $is-lub', P, E \vdash while (e) c :: T$
 $is-lub', P, E \vdash throw e :: T$
 $is-lub', P, E \vdash try e_1 catch(C V) e_2 :: T$
 $is-lub', P, E \vdash Cast D e :: T$
 $is-lub', P, E \vdash e instanceof U :: T$
 $is-lub', P, E \vdash a \cdot F\{D\} :: T$
 $is-lub', P, E \vdash a \cdot F\{D\} := v :: T$
 $is-lub', P, E \vdash e \cdot compareAndSwap(D \cdot F, e', e'') :: T$
 $is-lub', P, E \vdash e_1 \llcorner bop \lrcorner e_2 :: T$
 $is-lub', P, E \vdash new C :: T$
 $is-lub', P, E \vdash newA T[e] :: T'$
 $is-lub', P, E \vdash a[i] := e :: T$
 $is-lub', P, E \vdash a[i] :: T$
 $is-lub', P, E \vdash a \cdot length :: T$
 $is-lub', P, E \vdash e \cdot M(ps) :: T$
 $is-lub', P, E \vdash sync(o') e :: T$
 $is-lub', P, E \vdash insync(a) e :: T$

```

lemma fixes is-lub :: ty => ty => ty => bool (⟨+ lub'((- / -)') = -> [51,51,51] 50)
  assumes is-lub-unique:  $\bigwedge T1 T2 T3 T4. \llbracket \vdash \text{lub}(T1, T2) = T3; \vdash \text{lub}(T1, T2) = T4 \rrbracket \implies T3 = T4$ 
  shows WT-unique:  $\llbracket \text{is-lub}, P, E \vdash e :: T; \text{is-lub}, P, E \vdash e :: T' \rrbracket \implies T = T'$ 
  and WTs-unique:  $\llbracket \text{is-lub}, P, E \vdash \text{es} [::] Ts; \text{is-lub}, P, E \vdash \text{es} [::] Ts' \rrbracket \implies Ts = Ts'$ 
apply(induct arbitrary: T' and Ts' rule: WT-WTs.inducts)
apply blast
apply blast
apply blast
apply blast
apply fastforce
apply fastforce
apply(fastforce dest: WT-binop-fun)
apply fastforce
apply fastforce
apply fastforce
apply fastforce
apply(fastforce dest: sees-field-fun)
apply(fastforce dest: sees-field-fun)
apply blast
apply(fastforce dest: sees-method-fun)
apply fastforce
apply fastforce
apply fastforce
apply(blast dest: is-lub-unique)
apply fastforce
apply fastforce
apply blast
apply fastforce
apply fastforce
done

```

lemma fixes *is-lub*

```

  shows wt-env-mono:  $\text{is-lub}, P, E \vdash e :: T \implies (\bigwedge E'. E \subseteq_m E' \implies \text{is-lub}, P, E' \vdash e :: T)$ 
  and wts-env-mono:  $\text{is-lub}, P, E \vdash \text{es} [::] Ts \implies (\bigwedge E'. E \subseteq_m E' \implies \text{is-lub}, P, E' \vdash \text{es} [::] Ts)$ 
apply(induct rule: WT-WTs.inducts)
apply(simp add: WTNew)
apply(simp add: WTNewArray)
apply(fastforce simp: WTCast)
apply(fastforce simp: WTInstanceOf)
apply(fastforce simp: WTVal)
apply(simp add: WTVar map-le-def dom-def)
apply(fastforce simp: WTBinOp)
apply(force simp:map-le-def)
apply(simp add: WTAAcc)
apply(simp add: WTAAss, fastforce)
apply(simp add: WTALength, fastforce)
apply(fastforce simp: WTFAcc)
apply(fastforce simp: WTFAss del: WT-WTs.intros WT-elim-cases)
apply blast
apply(fastforce)
apply(fastforce simp: map-le-def WTBlock)
apply(fastforce simp: WTSynchronized)
apply(fastforce simp: WTSeq)

```

```

apply(fastforce simp: WTCond)
apply(fastforce simp: WTWhile)
apply(fastforce simp: WTThrow)
apply(fastforce simp: WTTry map-le-def dom-def)
apply(fastforce)+
done

```

lemma fixes *is-lub*

```

  shows WT-fv: is-lub,P,E ⊢ e :: T ⇒ fv e ⊆ dom E
  and WT-fvs: is-lub,P,E ⊢ es [::] Ts ⇒ fvs es ⊆ dom E
apply(induct rule: WT-WTs.inducts)
apply(simp-all del: fun-upd-apply)
apply fast+
done

```

lemma fixes *is-lub*

```

  shows WT-expr-locks: is-lub,P,E ⊢ e :: T ⇒ expr-locks e = (λad. 0)
  and WTs-expr-locks: is-lub,P,E ⊢ es [::] Ts ⇒ expr-locks es = (λad. 0)
by(induct rule: WT-WTs.inducts)(auto)

```

lemma

```

  fixes is-lub :: ty ⇒ ty ⇒ ty ⇒ bool (⊢ lub'((-,/ -)') = → [51,51,51] 50)
  assumes is-lub-is-type: ∧ T1 T2 T3. [⊢ lub(T1, T2) = T3; is-type P T1; is-type P T2] ⇒ is-type P T3
  and wf: wf-prog wf-md P
  shows WT-is-type: [ is-lub,P,E ⊢ e :: T; ran E ⊆ types P ] ⇒ is-type P T
  and WTs-is-type: [ is-lub,P,E ⊢ es [::] Ts; ran E ⊆ types P ] ⇒ set Ts ⊆ types P
apply(induct rule: WT-WTs.inducts)
apply simp
apply simp
apply simp
apply simp
apply (simp add: typeof-lit-is-type)
apply (fastforce intro: nth-mem simp add: ran-def)
apply(simp add: WT-binop-is-type)
apply(simp)
apply(simp del: is-type-array add: is-type-ArrayD)
apply(simp)
apply(simp)
apply(simp add: sees-field-is-type[OF - wf])
apply(simp)
apply simp
apply(fastforce dest: sees-wf-mdecl[OF wf] simp: wf-mdecl-def)
apply(fastforce simp add: ran-def split: if-split-asm)
apply(simp add: is-class-Object[OF wf])
apply(simp)
apply(simp)
apply(fastforce intro: is-lub-is-type)
apply(simp)
apply(simp)
apply simp
apply simp
apply simp
done

```

lemma

fixes $is-lub1 :: ty \Rightarrow ty \Rightarrow ty \Rightarrow bool$ ($\langle \vdash 1 \text{ lub}'((- / -)' = \rightarrow [51,51,51] 50) \rangle$)
and $is-lub2 :: ty \Rightarrow ty \Rightarrow ty \Rightarrow bool$ ($\langle \vdash 2 \text{ lub}'((- / -)' = \rightarrow [51,51,51] 50) \rangle$)
assumes $wf: wf-prog \text{ wf-md } P$
and $is-lub1-into-is-lub2: \bigwedge T1 \ T2 \ T3. \llbracket \vdash 1 \text{ lub}(T1, T2) = T3; is-type \ P \ T1; is-type \ P \ T2 \rrbracket \Longrightarrow \vdash 2 \text{ lub}(T1, T2) = T3$
and $is-lub2-is-type: \bigwedge T1 \ T2 \ T3. \llbracket \vdash 2 \text{ lub}(T1, T2) = T3; is-type \ P \ T1; is-type \ P \ T2 \rrbracket \Longrightarrow is-type \ P \ T3$

shows $WT-change-is-lub: \llbracket is-lub1, P, E \vdash e :: T; ran \ E \subseteq types \ P \rrbracket \Longrightarrow is-lub2, P, E \vdash e :: T$
and $WTs-change-is-lub: \llbracket is-lub1, P, E \vdash es \ [::] \ Ts; ran \ E \subseteq types \ P \rrbracket \Longrightarrow is-lub2, P, E \vdash es \ [::] \ Ts$

proof(*induct rule: WT-WTs.inducts*)

case ($WTBlock \ U \ E \ V \ e' \ T \ vo$)

from $\langle is-type \ P \ U \rangle \langle ran \ E \subseteq types \ P \rangle$

have $ran \ (E(V \mapsto U)) \subseteq types \ P$ **by**(*auto simp add: ran-def*)

hence $is-lub2, P, E(V \mapsto U) \vdash e' :: T$ **by**(*rule WTBlock*)

with $\langle is-type \ P \ U \rangle$ **show** *?case*

using $\langle case \ vo \ of \ None \Rightarrow \ True \mid [v] \Rightarrow \exists T'. \text{typeof } v = [T'] \wedge P \vdash T' \leq U \rangle$ **by** *auto*

next

case ($WTCond \ E \ e \ e1 \ T1 \ e2 \ T2 \ T$)

from $\langle ran \ E \subseteq types \ P \rangle$ **have** $is-lub2, P, E \vdash e :: Boolean$ $is-lub2, P, E \vdash e1 :: T1$ $is-lub2, P, E \vdash e2 :: T2$

by(*rule WTCond*)**+**

moreover from $is-lub2-is-type \ wf \ \langle is-lub2, P, E \vdash e1 :: T1 \rangle \langle ran \ E \subseteq types \ P \rangle$

have $is-type \ P \ T1$ **by**(*rule WT-is-type*)

from $is-lub2-is-type \ wf \ \langle is-lub2, P, E \vdash e2 :: T2 \rangle \langle ran \ E \subseteq types \ P \rangle$

have $is-type \ P \ T2$ **by**(*rule WT-is-type*)

with $\langle \vdash 1 \text{ lub}(T1, T2) = T \rangle \langle is-type \ P \ T1 \rangle$

have $\vdash 2 \text{ lub}(T1, T2) = T$ **by**(*rule is-lub1-into-is-lub2*)

ultimately show *?case ..*

next

case ($WTTry \ E \ e1 \ T \ V \ C \ e2$)

from $\langle ran \ E \subseteq types \ P \rangle$ **have** $is-lub2, P, E \vdash e1 :: T$ **by**(*rule WTTry*)

moreover from $\langle P \vdash C \preceq^* \text{Throwable} \rangle$ **have** $is-class \ P \ C$

by(*rule is-class-sub-Throwable[OF wf]*)

with $\langle ran \ E \subseteq types \ P \rangle$ **have** $ran \ (E(V \mapsto Class \ C)) \subseteq types \ P$

by(*auto simp add: ran-def*)

hence $is-lub2, P, E(V \mapsto Class \ C) \vdash e2 :: T$ **by**(*rule WTTry*)

ultimately show *?case using* $\langle P \vdash C \preceq^* \text{Throwable} \rangle$ **..**

qed *auto*

4.6.1 Code generator setup

lemma $WTBlock-code$:

$\bigwedge is-lub. \llbracket is-type \ P \ T; is-lub, P, E(V \mapsto T) \vdash e :: T' \rrbracket$

$case \ vo \ of \ None \Rightarrow \ True \mid [v] \Rightarrow \text{case } \text{typeof } v \text{ of } None \Rightarrow \ False \mid Some \ T' \Rightarrow P \vdash T' \leq T \rrbracket$

$\Longrightarrow is-lub, P, E \vdash \{V:T=vo; e\} :: T'$

by(*auto*)

lemmas [*code-pred-intro*] =

$WTNew \ WTNewArray \ WTCast \ WTInstanceOf \ WTVal \ WTVar \ WTBinOp \ WTLAss \ WTAAcc \ WTAAss$
 $WTALength \ WTFAcc \ WTFAss \ WTCAS \ WTCall$

declare

$WTBlock-code$ [*code-pred-intro* $WTBlock$ ']

lemmas [code-pred-intro] =
 WTSynchronized WTSeq WTCCond WTWhile WTThrow WTTry
 WTNil WTCCons

code-pred

(modes:
 ($i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}$) $\Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}$,
 ($i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}$) $\Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow \text{bool}$)
 [detect-switches, skip-proof]
 WT

proof –

case WT
from WT.premis **show** thesis
proof cases
case (WTBlock T V e vo)
thus thesis **using** WT.WTBlock'[OF refl refl refl, of V T vo e] **by**(auto)
qed(assumption|erule WT.that[OF refl refl refl]|rule refl)+

next

case WTs
from WTs.premis WTs.that **show** thesis **by** cases blast+

qed

inductive is-lub-sup :: 'm prog \Rightarrow ty \Rightarrow ty \Rightarrow ty \Rightarrow bool

for P T1 T2 T3

where

sup P T1 T2 = OK T3 \Longrightarrow is-lub-sup P T1 T2 T3

code-pred

(modes: $i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}$, $i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow \text{bool}$)
 is-lub-sup

.

definition WT-code :: 'addr J-prog \Rightarrow env \Rightarrow 'addr expr \Rightarrow ty \Rightarrow bool ($\langle -, - \vdash - \rangle$::' \rightarrow [51,51,51] 50)

where WT-code P \equiv WT (is-lub-sup P) P

definition WTs-code :: 'addr J-prog \Rightarrow env \Rightarrow 'addr expr list \Rightarrow ty list \Rightarrow bool ($\langle -, - \vdash - \rangle$ [::'] \rightarrow [51,51,51] 50)

where WTs-code P \equiv WTs (is-lub-sup P) P

lemma assumes wf: wf-prog wf-md P

shows WT-code-into-WT:

$\llbracket P, E \vdash e \text{ ::' } T; \text{ ran } E \subseteq \text{types } P \rrbracket \Longrightarrow P, E \vdash e \text{ :: } T$

and WTs-code-into-WTs:

$\llbracket P, E \vdash es \text{ [::'] } Ts; \text{ ran } E \subseteq \text{types } P \rrbracket \Longrightarrow P, E \vdash es \text{ [::] } Ts$

proof –

assume ran: ran E \subseteq types P

{ **assume** wt: P, E \vdash e ::' T

show P, E \vdash e :: T

by(rule WT-change-is-lub[OF wf - - wt[unfolded WT-code-def] ran])(blast elim!: is-lub-sup.cases
 intro: sup-is-lubI[OF wf] is-lub-is-type[OF wf])+ }

{ **assume** wts: P, E \vdash es [::'] Ts

show P, E \vdash es [::] Ts

by(rule WTs-change-is-lub[OF wf - - wts[unfolded WTs-code-def] ran])(blast elim!: is-lub-sup.cases


```
intro: sup-is-lubI[OF wf] is-lub-is-type[OF wf]+ }
qed
```

```
lemma assumes wf: wf-prog wf-md P
```

```
shows WT-into-WT-code:
```

```
[[ P,E ⊢ e :: T; ran E ⊆ types P ]] ⇒ P,E ⊢ e ::' T
```

```
and WT-into-WTs-code-OK:
```

```
[[ P,E ⊢ es [::] Ts; ran E ⊆ types P ]] ⇒ P,E ⊢ es [::'] Ts
```

```
proof -
```

```
assume ran: ran E ⊆ types P
```

```
{ assume wt: P,E ⊢ e :: T
```

```
show P,E ⊢ e ::' T unfolding WT-code-def
```

```
by(rule WT-change-is-lub[OF wf - - wt ran])(blast intro!: is-lub-sup.intros intro: is-lub-subD[OF wf] sup-is-type[OF wf] elim!: is-lub-sup.cases)+ }
```

```
{ assume wts: P,E ⊢ es [::] Ts
```

```
show P,E ⊢ es [::'] Ts unfolding WT-code-def
```

```
by(rule WT-code-change-is-lub[OF wf - - wts ran])(blast intro!: is-lub-sup.intros intro: is-lub-subD[OF wf] sup-is-type[OF wf] elim!: is-lub-sup.cases)+ }
```

```
qed
```

```
theorem WT-eq-WT-code:
```

```
assumes wf-prog wf-md P
```

```
and ran E ⊆ types P
```

```
shows P,E ⊢ e :: T ⇔ P,E ⊢ e ::' T
```

```
using assms by(blast intro: WT-code-into-WT WT-into-WT-code)
```

```
code-pred
```

```
(modes: i ⇒ i ⇒ i ⇒ i ⇒ bool, i ⇒ i ⇒ i ⇒ o ⇒ bool)
```

```
[inductify]
```

```
WT-code
```

```
.
```

```
code-pred
```

```
(modes: i ⇒ i ⇒ i ⇒ i ⇒ bool, i ⇒ i ⇒ i ⇒ o ⇒ bool)
```

```
[inductify]
```

```
WTs-code
```

```
.
```

```
end
```

4.7 Definite assignment

```
theory DefAss
```

```
imports
```

```
Expr
```

```
begin
```

4.7.1 Hypersets

```
type-synonym 'a hyperset = 'a set option
```

```
definition hyperUn :: 'a hyperset ⇒ 'a hyperset ⇒ 'a hyperset (infixl <⊔> 65)
```

```
where
```

$$A \sqcup B \equiv \text{case } A \text{ of } \text{None} \Rightarrow \text{None} \\ | [A] \Rightarrow (\text{case } B \text{ of } \text{None} \Rightarrow \text{None} | [B] \Rightarrow [A \cup B])$$

definition *hyperInt* :: 'a hyperset \Rightarrow 'a hyperset \Rightarrow 'a hyperset (infixl $\langle \sqcap \rangle$ 70)
where

$$A \sqcap B \equiv \text{case } A \text{ of } \text{None} \Rightarrow B \\ | [A] \Rightarrow (\text{case } B \text{ of } \text{None} \Rightarrow [A] | [B] \Rightarrow [A \cap B])$$

definition *hyperDiff1* :: 'a hyperset \Rightarrow 'a \Rightarrow 'a hyperset (infixl $\langle \ominus \rangle$ 65)
where

$$A \ominus a \equiv \text{case } A \text{ of } \text{None} \Rightarrow \text{None} | [A] \Rightarrow [A - \{a\}]$$

definition *hyper-isin* :: 'a \Rightarrow 'a hyperset \Rightarrow bool (infix $\langle \in \in \rangle$ 50)
where

$$a \in \in A \equiv \text{case } A \text{ of } \text{None} \Rightarrow \text{True} | [A] \Rightarrow a \in A$$

definition *hyper-subset* :: 'a hyperset \Rightarrow 'a hyperset \Rightarrow bool (infix $\langle \sqsubseteq \rangle$ 50)
where

$$A \sqsubseteq B \equiv \text{case } B \text{ of } \text{None} \Rightarrow \text{True} \\ | [B] \Rightarrow (\text{case } A \text{ of } \text{None} \Rightarrow \text{False} | [A] \Rightarrow A \subseteq B)$$

lemmas *hyperset-defs* =

hyperUn-def hyperInt-def hyperDiff1-def hyper-isin-def hyper-subset-def

lemma [*simp*]: $[\{\}] \sqcup A = A \wedge A \sqcup [\{\}] = A$

lemma [*simp*]: $[A] \sqcup [B] = [A \cup B] \wedge [A] \ominus a = [A - \{a\}]$

lemma [*simp*]: $\text{None} \sqcup A = \text{None} \wedge A \sqcup \text{None} = \text{None}$

lemma [*simp*]: $a \in \in \text{None} \wedge \text{None} \ominus a = \text{None}$

lemma *hyperUn-assoc*: $(A \sqcup B) \sqcup C = A \sqcup (B \sqcup C)$

lemma *hyper-insert-comm*: $A \sqcup [\{a\}] = [\{a\}] \sqcup A \wedge A \sqcup ([\{a\}] \sqcup B) = [\{a\}] \sqcup (A \sqcup B)$

lemma *sqSub-mem-lem* [*elim*]: $\llbracket A \sqsubseteq A'; a \in \in A \rrbracket \Longrightarrow a \in \in A'$

by(*auto simp add: hyperset-defs*)

lemma [*iff*]: $A \sqsubseteq \text{None}$

by(*auto simp add: hyperset-defs*)

lemma [*simp*]: $A \sqsubseteq A$

by(*auto simp add: hyperset-defs*)

lemma [*iff*]: $[A] \sqsubseteq [B] \longleftrightarrow A \subseteq B$

by(*auto simp add: hyperset-defs*)

lemma *sqUn-lem2*: $A \sqsubseteq A' \Longrightarrow B \sqcup A \sqsubseteq B \sqcup A'$

by(*simp add: hyperset-defs*) *blast*

lemma *sqSub-trans* [*trans, intro*]: $\llbracket A \sqsubseteq B; B \sqsubseteq C \rrbracket \Longrightarrow A \sqsubseteq C$

by(*auto simp add: hyperset-defs*)

lemma *hyperUn-comm*: $A \sqcup B = B \sqcup A$

by(*auto simp add: hyperset-defs*)

lemma *hyperUn-leftComm*: $A \sqcup (B \sqcup C) = B \sqcup (A \sqcup C)$

by(*auto simp add: hyperset-defs*)

lemmas *hyperUn-ac = hyperUn-comm hyperUn-leftComm hyperUn-assoc*

lemma [*simp*]: $[\{\}] \sqcup B = B$
by(*auto*)

lemma [*simp*]: $[\{\}] \sqsubseteq A$
by(*auto simp add: hyperset-defs*)

lemma *sqInt-lem*: $A \sqsubseteq A' \implies A \sqcap B \sqsubseteq A' \sqcap B$
by(*auto simp add: hyperset-defs*)

4.7.2 Definite assignment

primrec $\mathcal{A} :: ('a, 'b, 'addr) \text{exp} \Rightarrow 'a \text{hyperset}$
and $\mathcal{A}s :: ('a, 'b, 'addr) \text{exp list} \Rightarrow 'a \text{hyperset}$
where

$\mathcal{A} (\text{new } C) = [\{\}]$
 $\mathcal{A} (\text{newA } T[e]) = \mathcal{A} e$
 $\mathcal{A} (\text{Cast } C e) = \mathcal{A} e$
 $\mathcal{A} (e \text{ instanceof } T) = \mathcal{A} e$
 $\mathcal{A} (\text{Val } v) = [\{\}]$
 $\mathcal{A} (e_1 \ll\text{bop}\gg e_2) = \mathcal{A} e_1 \sqcup \mathcal{A} e_2$
 $\mathcal{A} (\text{Var } V) = [\{\}]$
 $\mathcal{A} (\text{LAss } V e) = [\{V\}] \sqcup \mathcal{A} e$
 $\mathcal{A} (a[i]) = \mathcal{A} a \sqcup \mathcal{A} i$
 $\mathcal{A} (a[i] := e) = \mathcal{A} a \sqcup \mathcal{A} i \sqcup \mathcal{A} e$
 $\mathcal{A} (a \cdot \text{length}) = \mathcal{A} a$
 $\mathcal{A} (e \cdot F\{D\}) = \mathcal{A} e$
 $\mathcal{A} (e_1 \cdot F\{D\} := e_2) = \mathcal{A} e_1 \sqcup \mathcal{A} e_2$
 $\mathcal{A} (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) = \mathcal{A} e1 \sqcup \mathcal{A} e2 \sqcup \mathcal{A} e3$
 $\mathcal{A} (e \cdot M(es)) = \mathcal{A} e \sqcup \mathcal{A}s es$
 $\mathcal{A} (\{V:T=vo; e\}) = \mathcal{A} e \ominus V$
 $\mathcal{A} (\text{sync}_V(o') e) = \mathcal{A} o' \sqcup \mathcal{A} e$
 $\mathcal{A} (\text{insync}_V(a) e) = \mathcal{A} e$
 $\mathcal{A} (e_1;; e_2) = \mathcal{A} e_1 \sqcup \mathcal{A} e_2$
 $\mathcal{A} (\text{if } (e) e_1 \text{ else } e_2) = \mathcal{A} e \sqcup (\mathcal{A} e_1 \sqcap \mathcal{A} e_2)$
 $\mathcal{A} (\text{while } (b) e) = \mathcal{A} b$
 $\mathcal{A} (\text{throw } e) = \text{None}$
 $\mathcal{A} (\text{try } e_1 \text{ catch}(C V) e_2) = \mathcal{A} e_1 \sqcap (\mathcal{A} e_2 \ominus V)$

$\mathcal{A}s ([\]) = [\{\}]$
 $\mathcal{A}s (e\#es) = \mathcal{A} e \sqcup \mathcal{A}s es$

primrec $\mathcal{D} :: ('a, 'b, 'addr) \text{exp} \Rightarrow 'a \text{hyperset} \Rightarrow \text{bool}$
and $\mathcal{D}s :: ('a, 'b, 'addr) \text{exp list} \Rightarrow 'a \text{hyperset} \Rightarrow \text{bool}$
where

$\mathcal{D} (\text{new } C) A = \text{True}$
 $\mathcal{D} (\text{newA } T[e]) A = \mathcal{D} e A$
 $\mathcal{D} (\text{Cast } C e) A = \mathcal{D} e A$
 $\mathcal{D} (e \text{ instanceof } T) = \mathcal{D} e$
 $\mathcal{D} (\text{Val } v) A = \text{True}$
 $\mathcal{D} (e_1 \ll\text{bop}\gg e_2) A = (\mathcal{D} e_1 A \wedge \mathcal{D} e_2 (A \sqcup \mathcal{A} e_1))$
 $\mathcal{D} (\text{Var } V) A = (V \in\in A)$
 $\mathcal{D} (\text{LAss } V e) A = \mathcal{D} e A$

```

|  $\mathcal{D} (a[i]) A = (\mathcal{D} a A \wedge \mathcal{D} i (A \sqcup \mathcal{A} a))$ 
|  $\mathcal{D} (a[i] := e) A = (\mathcal{D} a A \wedge \mathcal{D} i (A \sqcup \mathcal{A} a) \wedge \mathcal{D} e (A \sqcup \mathcal{A} a \sqcup \mathcal{A} i))$ 
|  $\mathcal{D} (a.length) A = \mathcal{D} a A$ 
|  $\mathcal{D} (e.F\{D\}) A = \mathcal{D} e A$ 
|  $\mathcal{D} (e_1.F\{D\}:=e_2) A = (\mathcal{D} e_1 A \wedge \mathcal{D} e_2 (A \sqcup \mathcal{A} e_1))$ 
|  $\mathcal{D} (e_1.compareAndSwap(D.F, e_2, e_3)) A = (\mathcal{D} e_1 A \wedge \mathcal{D} e_2 (A \sqcup \mathcal{A} e_1) \wedge \mathcal{D} e_3 (A \sqcup \mathcal{A} e_1 \sqcup \mathcal{A} e_2))$ 
|  $\mathcal{D} (e.M(es)) A = (\mathcal{D} e A \wedge \mathcal{D} s es (A \sqcup \mathcal{A} e))$ 
|  $\mathcal{D} (\{V:T=vo; e\}) A = (\text{if } vo = \text{None then } \mathcal{D} e (A \ominus V) \text{ else } \mathcal{D} e (A \sqcup [\{V\}]))$ 
|  $\mathcal{D} (sync_V (o') e) A = (\mathcal{D} o' A \wedge \mathcal{D} e (A \sqcup \mathcal{A} o'))$ 
|  $\mathcal{D} (insync_V (a) e) A = \mathcal{D} e A$ 
|  $\mathcal{D} (e_1;;e_2) A = (\mathcal{D} e_1 A \wedge \mathcal{D} e_2 (A \sqcup \mathcal{A} e_1))$ 
|  $\mathcal{D} (\text{if } (e) e_1 \text{ else } e_2) A = (\mathcal{D} e A \wedge \mathcal{D} e_1 (A \sqcup \mathcal{A} e) \wedge \mathcal{D} e_2 (A \sqcup \mathcal{A} e))$ 
|  $\mathcal{D} (\text{while } (e) c) A = (\mathcal{D} e A \wedge \mathcal{D} c (A \sqcup \mathcal{A} e))$ 
|  $\mathcal{D} (\text{throw } e) A = \mathcal{D} e A$ 
|  $\mathcal{D} (\text{try } e_1 \text{ catch}(C V) e_2) A = (\mathcal{D} e_1 A \wedge \mathcal{D} e_2 (A \sqcup [\{V\}]))$ 

|  $\mathcal{D} s (\square) A = \text{True}$ 
|  $\mathcal{D} s (e\#es) A = (\mathcal{D} e A \wedge \mathcal{D} s es (A \sqcup \mathcal{A} e))$ 

```

lemma *As-map-Val[simp]*: $\mathcal{A} s (\text{map Val } vs) = [\{\}]$

lemma *As-append [simp]*: $\mathcal{A} s (xs @ ys) = (\mathcal{A} s xs) \sqcup (\mathcal{A} s ys)$

by(*induct xs, auto simp add: hyperset-defs*)

lemma *Ds-map-Val[simp]*: $\mathcal{D} s (\text{map Val } vs) A$

lemma *D-append[iff]*: $\bigwedge A. \mathcal{D} s (es @ es') A = (\mathcal{D} s es A \wedge \mathcal{D} s es' (A \sqcup \mathcal{A} es))$

lemma fixes $e :: ('a, 'b, 'addr) \text{exp}$ **and** $es :: ('a, 'b, 'addr) \text{exp list}$

shows *A-fv*: $\bigwedge A. \mathcal{A} e = [A] \implies A \subseteq \text{fv } e$

and $\bigwedge A. \mathcal{A} s es = [A] \implies A \subseteq \text{fvs } es$

apply(*induct e and es rule: A.induct As.induct*)

apply (*simp-all add: hyperset-defs*)

apply *fast+*

done

lemma *sqUn-lem*: $A \sqsubseteq A' \implies A \sqcup B \sqsubseteq A' \sqcup B$

lemma *diff-lem*: $A \sqsubseteq A' \implies A \ominus b \sqsubseteq A' \ominus b$

lemma fixes $e :: ('a, 'b, 'addr) \text{exp}$ **and** $es :: ('a, 'b, 'addr) \text{exp list}$

shows *D-mono*: $\bigwedge A A'. A \sqsubseteq A' \implies \mathcal{D} e A \implies \mathcal{D} e A'$

and *Ds-mono*: $\bigwedge A A'. A \sqsubseteq A' \implies \mathcal{D} s es A \implies \mathcal{D} s es A'$

lemma *D-mono'*: $\mathcal{D} e A \implies A \sqsubseteq A' \implies \mathcal{D} e A'$

and *Ds-mono'*: $\mathcal{D} s es A \implies A \sqsubseteq A' \implies \mathcal{D} s es A'$

declare *hyperUn-comm [simp]*

declare *hyperUn-leftComm [simp]*

end

4.8 Well-formedness Constraints

theory *JWellForm*

imports

WWellForm

WellType

DefAss

begin

definition *wf-J-mdecl* :: 'addr J-prog ⇒ cname ⇒ 'addr J-mb mdecl ⇒ bool

where

wf-J-mdecl P C ≡ λ(*M, Ts, T, (pns, body)*).

length Ts = *length pns* ∧

distinct pns ∧

this ∉ *set pns* ∧

(∃ *T'*. *P, [this ↦ Class C, pns ↦ Ts]* ⊢ *body* :: *T' ∧ P* ⊢ *T' ≤ T*) ∧

D body [{ *this* } ∪ *set pns*]

lemma *wf-J-mdecl[simp]*:

wf-J-mdecl P C (M, Ts, T, pns, body) ≡

(*length Ts* = *length pns* ∧

distinct pns ∧

this ∉ *set pns* ∧

(∃ *T'*. *P, [this ↦ Class C, pns ↦ Ts]* ⊢ *body* :: *T' ∧ P* ⊢ *T' ≤ T*) ∧

D body [{ *this* } ∪ *set pns*])

abbreviation *wf-J-prog* :: 'addr J-prog ⇒ bool

where *wf-J-prog* == *wf-prog wf-J-mdecl*

lemma *wf-mdecl-wwf-mdecl*: *wf-J-mdecl P C Md* ⇒ *wwf-J-mdecl P C Md*

4.9 The source language as an instance of the framework

theory *Threaded*

imports

SmallStep

JWellForm

../Common/ConformThreaded

../Common/ExternalCallWF

../Framework/FWLiftingSem

../Framework/FWProgressAux

begin

context *heap-base* **begin** — Move to ?? - also used in BV

lemma *wset-Suspend-ok-start-state*:

fixes *final r convert-RA*

assumes *start-state f P C M vs* ∈ *I*

shows *start-state f P C M vs* ∈ *multithreaded-base.wset-Suspend-ok final r convert-RA I*

using *assms*

by (*rule multithreaded-base.wset-Suspend-okI*)(*simp add: start-state-def split-beta*)

end

abbreviation *final-expr* :: 'addr expr × 'addr locals ⇒ bool **where**

final-expr ≡ λ(*e, x*). *final e*

lemma *final-locks*: $\text{final } e \implies \text{expr-locks } e \text{ } l = 0$
by(*auto elim: finalE*)

context *J-heap-base* **begin**

abbreviation *mred*

$:: 'addr \text{ } J\text{-prog} \Rightarrow ('addr, 'thread\text{-id}, 'addr \text{ } expr \times 'addr \text{ } locals, 'heap, 'addr, ('addr, 'thread\text{-id})$
obs-event) *semantics*

where

$mred \text{ } P \text{ } t \equiv (\lambda((e, l), h) \text{ } ta \text{ } ((e', l'), h'). P, t \vdash \langle e, (h, l) \rangle -ta \rightarrow \langle e', (h', l') \rangle)$

lemma *red-new-thread-heap*:

$\llbracket \text{convert-extTA extNTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{NewThread } t'' \text{ } ex'' \text{ } h'' \in \text{set } \{ta\}_t \rrbracket \implies h'' =$
 $hp \text{ } s'$

and *reds-new-thread-heap*:

$\llbracket \text{convert-extTA extNTA}, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{NewThread } t'' \text{ } ex'' \text{ } h'' \in \text{set } \{ta\}_t \rrbracket \implies h'' =$
 $hp \text{ } s'$

apply(*induct rule: red-reds.inducts*)

apply(*fastforce dest: red-ext-new-thread-heap simp add: ta-upd-simps*)**+**

done

lemma *red-ta-Wakeup-no-Join-no-Lock-no-Interrupt*:

$\llbracket \text{convert-extTA extNTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{Notified} \in \text{set } \{ta\}_w \vee \text{WokenUp} \in \text{set } \{ta\}_w \rrbracket$
 $\implies \text{collect-locks } \{ta\}_l = \{\} \wedge \text{collect-cond-actions } \{ta\}_c = \{\} \wedge \text{collect-interrupts } \{ta\}_i = \{\}$

and *reds-ta-Wakeup-no-Join-no-Lock-no-Interrupt*:

$\llbracket \text{convert-extTA extNTA}, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{Notified} \in \text{set } \{ta\}_w \vee \text{WokenUp} \in \text{set } \{ta\}_w$
 \rrbracket
 $\implies \text{collect-locks } \{ta\}_l = \{\} \wedge \text{collect-cond-actions } \{ta\}_c = \{\} \wedge \text{collect-interrupts } \{ta\}_i = \{\}$

apply(*induct rule: red-reds.inducts*)

apply(*auto simp add: ta-upd-simps dest: red-external-Wakeup-no-Join-no-Lock-no-Interrupt del: conjI*)

done

lemma *final-no-red*:

$\text{final } e \implies \neg P, t \vdash \langle e, (h, l) \rangle -ta \rightarrow \langle e', (h', l') \rangle$

by(*auto elim: red.cases finalE*)

lemma *red-mthr*: *multithreaded final-expr* (*mred* *P*)

by(*unfold-locales*)(*auto dest: red-new-thread-heap*)

end

sublocale *J-heap-base* < *red-mthr*: *multithreaded*

final-expr

mred *P*

convert-RA

for *P*

by(*rule red-mthr*)

context *J-heap-base* **begin**

abbreviation

mredT $::$

$'addr \text{ } J\text{-prog} \Rightarrow ('addr, 'thread\text{-id}, 'addr \text{ } expr \times 'addr \text{ } locals, 'heap, 'addr) \text{ } state$

$\Rightarrow ('thread-id \times ('addr, 'thread-id, 'addr\ expr \times 'addr\ locals, 'heap)\ Jinja-thread-action)$
 $\Rightarrow ('addr, 'thread-id, 'addr\ expr \times 'addr\ locals, 'heap, 'addr)\ state \Rightarrow bool$

where

$mredT\ P \equiv red\ mthr.\ redT\ P$

abbreviation

$mredT\ syntax1 :: 'addr\ J\ prog \Rightarrow ('addr, 'thread-id, 'addr\ expr \times 'addr\ locals, 'heap, 'addr)\ state$
 $\Rightarrow 'thread-id \Rightarrow ('addr, 'thread-id, 'addr\ expr \times 'addr\ locals, 'heap)\ Jinja-thread-action$
 $\Rightarrow ('addr, 'thread-id, 'addr\ expr \times 'addr\ locals, 'heap, 'addr)\ state \Rightarrow bool$
 $(\langle \cdot \vdash - \dashv \rightarrow \rightarrow \rangle [50, 0, 0, 0, 50] 80)$

where

$mredT\ syntax1\ P\ s\ t\ ta\ s' \equiv mredT\ P\ s\ (t, ta)\ s'$

abbreviation

$mRedT\ syntax1 ::$
 $'addr\ J\ prog$
 $\Rightarrow ('addr, 'thread-id, 'addr\ expr \times 'addr\ locals, 'heap, 'addr)\ state$
 $\Rightarrow ('thread-id \times ('addr, 'thread-id, 'addr\ expr \times 'addr\ locals, 'heap)\ Jinja-thread-action)\ list$
 $\Rightarrow ('addr, 'thread-id, 'addr\ expr \times 'addr\ locals, 'heap, 'addr)\ state \Rightarrow bool$
 $(\langle \cdot \vdash - \dashv \rightarrow * \rightarrow \rangle [50, 0, 0, 50] 80)$

where

$P \vdash s \dashv tta \rightarrow * s' \equiv red\ mthr.\ RedT\ P\ s\ ttas\ s'$

end

context *J-heap* **begin**

lemma *redT-heat-incr*:

$P \vdash s \dashv ta \rightarrow s' \Longrightarrow shr\ s \trianglelefteq shr\ s'$

by(*erule red-mthr.redT.cases*)(*auto dest!*: *red-heat-incr intro: heat-trans*)

lemma *RedT-heat-incr*:

assumes $P \vdash s \dashv tta \rightarrow * s'$

shows $shr\ s \trianglelefteq shr\ s'$

using *assms unfolding red-mthr.RedT-def*

by(*induct*)(*auto dest: redT-heat-incr intro: heat-trans*)

end

4.9.1 Lifting *tconf* to multithreaded states

context *J-heap* **begin**

lemma *red-NewThread-Thread-Object*:

$\llbracket convert\ extTA\ extNTA, P, t \vdash \langle e, s \rangle \dashv ta \rightarrow \langle e', s' \rangle; NewThread\ t'\ x\ m \in set\ \{ta\}_t \rrbracket$
 $\Longrightarrow \exists C. typeof_addr\ (hp\ s')\ (thread-id2addr\ t') = \lfloor Class\ type\ C \rfloor \wedge P \vdash C \preceq^* Thread$

and *reds-NewThread-Thread-Objects*:

$\llbracket convert\ extTA\ extNTA, P, t \vdash \langle es, s \rangle \dashv ta \rightarrow \langle es', s' \rangle; NewThread\ t'\ x\ m \in set\ \{ta\}_t \rrbracket$
 $\Longrightarrow \exists C. typeof_addr\ (hp\ s')\ (thread-id2addr\ t') = \lfloor Class\ type\ C \rfloor \wedge P \vdash C \preceq^* Thread$

apply(*induct rule: red-reds.inducts*)

apply(*fastforce dest: red-external-new-thread-exists-thread-object simp add: ta-upd-simps*)+

done

lemma *lifting-wf-tconf*:

lifting-wf final-expr (mred P) ($\lambda t \text{ ex } h. P, h \vdash t \sqrt{t}$)
by(*unfold-locales*)(*fastforce dest: red-heap-incr red-NewThread-Thread-Object elim!: tconf-heap-mono*
intro: tconfI)+

end

sublocale $J\text{-heap} < \text{red-tconf}$: *lifting-wf final-expr mred P convert-RA* $\lambda t \text{ ex } h. P, h \vdash t \sqrt{t}$
by(*rule lifting-wf-tconf*)

4.9.2 Towards agreement between the framework semantics' lock state and the locks stored in the expressions

primrec *sync-ok* :: ('a,'b,'addr) *exp* \Rightarrow *bool*
and *sync-oks* :: ('a,'b,'addr) *exp list* \Rightarrow *bool*
where
sync-ok (*new C*) = *True*
| *sync-ok* (*newA T[i]*) = *sync-ok i*
| *sync-ok* (*Cast T e*) = *sync-ok e*
| *sync-ok* (*e instanceof T*) = *sync-ok e*
| *sync-ok* (*Val v*) = *True*
| *sync-ok* (*Var v*) = *True*
| *sync-ok* (*e «bop» e'*) = (*sync-ok e* \wedge *sync-ok e'* \wedge (*contains-insync e' \longrightarrow is-val e*))
| *sync-ok* (*V := e*) = *sync-ok e*
| *sync-ok* (*a[i]*) = (*sync-ok a* \wedge *sync-ok i* \wedge (*contains-insync i \longrightarrow is-val a*))
| *sync-ok* (*AAss a i e*) = (*sync-ok a* \wedge *sync-ok i* \wedge *sync-ok e* \wedge (*contains-insync i \longrightarrow is-val a*) \wedge
(*contains-insync e \longrightarrow is-val a* \wedge *is-val i*))
| *sync-ok* (*a.length*) = *sync-ok a*
| *sync-ok* (*e.F{D}*) = *sync-ok e*
| *sync-ok* (*FAss e F D e'*) = (*sync-ok e* \wedge *sync-ok e'* \wedge (*contains-insync e' \longrightarrow is-val e*))
| *sync-ok* (*e.compareAndSwap(D.F, e', e'')*) = (*sync-ok e* \wedge *sync-ok e'* \wedge *sync-ok e''* \wedge (*contains-insync*
e' \longrightarrow is-val e) \wedge (*contains-insync e'' \longrightarrow is-val e* \wedge *is-val e'*))
| *sync-ok* (*e.m(pns)*) = (*sync-ok e* \wedge *sync-oks pns* \wedge (*contains-insyncs pns \longrightarrow is-val e*))
| *sync-ok* (*{V : T=vo; e}*) = *sync-ok e*
| *sync-ok* (*sync_V (o') e*) = (*sync-ok o'* \wedge \neg *contains-insync e*)
| *sync-ok* (*insync_V (a) e*) = *sync-ok e*
| *sync-ok* (*e;;e'*) = (*sync-ok e* \wedge \neg *contains-insync e'*)
| *sync-ok* (*if (b) e else e'*) = (*sync-ok b* \wedge \neg *contains-insync e* \wedge \neg *contains-insync e'*)
| *sync-ok* (*while (b) e*) = (\neg *contains-insync b* \wedge \neg *contains-insync e*)
| *sync-ok* (*throw e*) = *sync-ok e*
| *sync-ok* (*try e catch (C v) e'*) = (*sync-ok e* \wedge \neg *contains-insync e'*)
| *sync-oks []* = *True*
| *sync-oks (x # xs)* = (*sync-ok x* \wedge *sync-oks xs* \wedge (*contains-insyncs xs \longrightarrow is-val x*))

lemma *sync-oks-append [simp]*:

sync-oks (xs @ ys) \longleftrightarrow *sync-oks xs* \wedge *sync-oks ys* \wedge (*contains-insyncs ys \longrightarrow (\exists vs. xs = map Val vs)*)

by(*induct xs*)(*auto simp add: Cons-eq-map-conv*)

lemma *fixes e* :: ('a,'b,'addr) *exp* **and** *es* :: ('a,'b,'addr) *exp list*

shows *not-contains-insync-sync-ok*: \neg *contains-insync e* \Longrightarrow *sync-ok e*

and *not-contains-insyncs-sync-oks*: \neg *contains-insyncs es* \Longrightarrow *sync-oks es*

by(*induct e and es rule: sync-ok.induct sync-oks.induct*)(*auto*)

lemma *expr-locks-sync-ok*: ($\bigwedge ad. \text{expr-locks } e \text{ ad} = 0$) \Longrightarrow *sync-ok e*

and *expr-locks-sync-oks*: $(\bigwedge ad. \text{expr-locks } es \text{ ad} = 0) \implies \text{sync-oks } es$
by(*auto intro!*: *not-contains-insync-sync-ok not-contains-insyncs-sync-oks*
simp add: contains-insync-conv contains-insyncs-conv)

lemma *sync-ok-extRet2J* [*simp, intro!*]: *sync-ok e* \implies *sync-ok (extRet2J e va)*
by(*cases va*) *auto*

abbreviation

sync-es-ok :: $(\text{'addr, 'thread-id, ('a, 'b, 'addr) exp} \times \text{'c}) \text{ thread-info} \Rightarrow \text{'heap} \Rightarrow \text{bool}$

where

sync-es-ok \equiv *ts-ok* $(\lambda t (e, x) m. \text{sync-ok } e)$

lemma *sync-es-ok-blocks* [*simp*]:

$\llbracket \text{length } pns = \text{length } Ts; \text{length } Ts = \text{length } vs \rrbracket \implies \text{sync-ok (blocks } pns \text{ } Ts \text{ } vs \text{ } e) = \text{sync-ok } e$
by(*induct pns Ts vs e rule: blocks.induct*) *auto*

context *J-heap-base* **begin**

lemma *assumes wf: wf-J-prog P*

shows *red-preserve-sync-ok*: $\llbracket \text{extTA}, P, t \vdash \langle e, s \rangle \text{ -ta} \rightarrow \langle e', s' \rangle; \text{sync-ok } e \rrbracket \implies \text{sync-ok } e'$

and *reds-preserve-sync-oks*: $\llbracket \text{extTA}, P, t \vdash \langle es, s \rangle \text{ [-ta} \rightarrow \langle es', s' \rangle; \text{sync-oks } es \rrbracket \implies \text{sync-oks } es'$

proof(*induct rule: red-reds.inducts*)

case (*RedCall s a U M Ts T pns body D vs*)

from *wf* $\langle P \vdash \text{class-type-of } U \text{ sees } M: Ts \rightarrow T = \llbracket (pns, \text{body}) \rrbracket \text{ in } D \rangle$

have *wf-mdecl wf-J-mdecl P D (M, Ts, T, \llbracket (pns, body) \rrbracket)*

by(*rule sees-wf-mdecl*)

then obtain *T* **where** $P, [\text{this} \mapsto \text{Class } D, pns \mapsto Ts] \vdash \text{body} :: T$

by(*auto simp add: wf-mdecl-def*)

hence *expr-locks body* = $(\lambda ad. 0)$ **by**(*rule WT-expr-locks*)

with $\langle \text{length } vs = \text{length } pns \rangle \langle \text{length } Ts = \text{length } pns \rangle$

have *expr-locks (blocks pns Ts vs body)* = $(\lambda ad. 0)$

by(*simp add: expr-locks-blocks*)

thus *?case* **by**(*auto intro: expr-locks-sync-ok*)

qed(*fastforce intro: not-contains-insync-sync-ok*)**+**

lemma *assumes wf: wf-J-prog P*

shows *expr-locks-new-thread*:

$\llbracket P, t \vdash \langle e, s \rangle \text{ -ta} \rightarrow \langle e', s' \rangle; \text{NewThread } t'' (e'', x'') h \in \text{set } \{\!|ta|\!\}_t \rrbracket \implies \text{expr-locks } e'' = (\lambda ad. 0)$

and *expr-locks-new-thread'*:

$\llbracket P, t \vdash \langle es, s \rangle \text{ [-ta} \rightarrow \langle es', s' \rangle; \text{NewThread } t'' (e'', x'') h \in \text{set } \{\!|ta|\!\}_t \rrbracket \implies \text{expr-locks } e'' = (\lambda ad. 0)$

proof(*induct rule: red-reds.inducts*)

case (*RedCallExternal s a U M Ts T D vs ta va h' ta' e' s'*)

then obtain *C fs a* **where** *subThread*: $P \vdash C \preceq^* \text{Thread}$ **and** *ext*: $\text{extNTA2J } P (C, \text{run}, a) = (e'', x'')$

by(*fastforce dest: red-external-new-thread-sub-thread*)

from *sub-Thread-sees-run[OF wf subThread]* **obtain** *D pns body*

where *sees*: $P \vdash C \text{ sees } \text{run}: \llbracket \rightarrow \text{Void} = \llbracket (pns, \text{body}) \rrbracket \text{ in } D$ **by** *auto*

from *sees-wf-mdecl[OF wf this]* **obtain** *T* **where** $P, [\text{this} \mapsto \text{Class } D] \vdash \text{body} :: T$

by(*auto simp add: wf-mdecl-def*)

hence *expr-locks body* = $(\lambda ad. 0)$ **by**(*rule WT-expr-locks*)

with *sees ext* **show** *?case* **by**(*auto simp add: extNTA2J-def*)

qed(*auto simp add: ta-upd-simps*)

lemma assumes *wf*: *wf-J-prog P*

shows *red-new-thread-sync-ok*: $\llbracket P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{NewThread } t'' (e'', x'') h'' \in \text{set } \{ta\}_t \rrbracket \Longrightarrow \text{sync-ok } e''$

and *reds-new-thread-sync-ok*: $\llbracket P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{NewThread } t'' (e'', x'') h'' \in \text{set } \{ta\}_t \rrbracket \Longrightarrow \text{sync-ok } e''$

by(*auto dest!*: *expr-locks-new-thread*[*OF wf*] *expr-locks-new-thread'*[*OF wf*] *intro*: *expr-locks-sync-ok expr-lockss-sync-oks*)

lemma *lifting-wf-sync-ok*: *wf-J-prog P* \Longrightarrow *lifting-wf final-expr* (*mred P*) ($\lambda t (e, x) m. \text{sync-ok } e$)

by(*unfold-locales*)(*auto intro*: *red-preserve-sync-ok red-new-thread-sync-ok*)

lemma *redT-preserve-sync-ok*:

assumes *red*: $P \vdash s -t \triangleright ta \rightarrow s'$

shows $\llbracket \text{wf-J-prog } P; \text{sync-es-ok } (thr\ s) (shr\ s) \rrbracket \Longrightarrow \text{sync-es-ok } (thr\ s') (shr\ s')$

by(*rule lifting-wf.redT-preserves*[*OF lifting-wf-sync-ok red*])

lemma *RedT-preserves-sync-ok*:

$\llbracket \text{wf-J-prog } P; P \vdash s -\triangleright ttas \rightarrow^* s'; \text{sync-es-ok } (thr\ s) (shr\ s) \rrbracket$

$\Longrightarrow \text{sync-es-ok } (thr\ s') (shr\ s')$

by(*rule lifting-wf.RedT-preserves*[*OF lifting-wf-sync-ok*])

lemma *sync-es-ok-J-start-state*:

$\llbracket \text{wf-J-prog } P; P \vdash C \text{ sees } M:Ts \rightarrow T = [(pns, body)] \text{ in } D; \text{length } Ts = \text{length } vs \rrbracket$

$\Longrightarrow \text{sync-es-ok } (thr\ (J\text{-start-state } P\ C\ M\ vs))\ m$

apply(*rule ts-okI*)

apply(*clarsimp simp add*: *start-state-def split-beta split*: *if-split-asm*)

apply(*drule* (1) *sees-wf-mdecl*)

apply(*clarsimp simp add*: *wf-mdecl-def*)

apply(*drule WT-expr-locks*)

apply(*rule expr-locks-sync-ok*)

apply *simp*

done

end

Framework lock state agrees with locks stored in the expression

definition *lock-ok* :: $(\text{'addr}, \text{'thread-id}) \text{ locks} \Rightarrow (\text{'addr}, \text{'thread-id}, (\text{'a}, \text{'b}, \text{'addr}) \text{ exp} \times \text{'x}) \text{ thread-info} \Rightarrow \text{bool}$ **where**

$\bigwedge ln. \text{lock-ok } ls\ ts \equiv \forall t. (\text{case } (ts\ t) \text{ of } \text{None} \Rightarrow (\forall l. \text{has-locks } (ls\ \$\ l)\ t = 0) \mid \llbracket ((e, x), ln) \rrbracket \Rightarrow (\forall l. \text{has-locks } (ls\ \$\ l)\ t + ln\ \$\ l = \text{expr-locks } e\ l))$

lemma *lock-okI*:

$\llbracket \bigwedge t\ l. ts\ t = \text{None} \Longrightarrow \text{has-locks } (ls\ \$\ l)\ t = 0; \bigwedge t\ e\ x\ ln\ l. ts\ t = \llbracket ((e, x), ln) \rrbracket \Longrightarrow \text{has-locks } (ls\ \$\ l)\ t + ln\ \$\ l = \text{expr-locks } e\ l \rrbracket \Longrightarrow \text{lock-ok } ls\ ts$

apply(*fastforce simp add*: *lock-ok-def*)

done

lemma *lock-okE*:

$\llbracket \text{lock-ok } ls\ ts;$

$\forall t. ts\ t = \text{None} \longrightarrow (\forall l. \text{has-locks } (ls\ \$\ l)\ t = 0) \Longrightarrow Q;$

$\forall t\ e\ x\ ln. ts\ t = \llbracket ((e, x), ln) \rrbracket \longrightarrow (\forall l. \text{has-locks } (ls\ \$\ l)\ t + ln\ \$\ l = \text{expr-locks } e\ l) \Longrightarrow Q \rrbracket$

$\Longrightarrow Q$

by(*fastforce simp add*: *lock-ok-def*)

lemma *lock-okD1*:

```

[[ lock-ok ls ts; ts t = None ]] ==> ∀ l. has-locks (ls $ l) t = 0
apply(simp add: lock-ok-def)
apply(erule-tac x=t in allE)
apply(auto)
done

```

lemma *lock-okD2*:

```

∧ ln. [[ lock-ok ls ts; ts t = [(e, x), ln] ]] ==> ∀ l. has-locks (ls $ l) t + ln $ l = expr-locks e l
apply(fastforce simp add: lock-ok-def)
done

```

lemma *lock-ok-lock-thread-ok*:

```

assumes lock: lock-ok ls ts
shows lock-thread-ok ls ts
proof(rule lock-thread-okI)
  fix l t
  assume lsl: has-lock (ls $ l) t
  show ∃ xw. ts t = [xw]
  proof(cases ts t)
    case None
      with lock have has-locks (ls $ l) t = 0
      by(auto dest: lock-okD1)
      with lsl show ?thesis by simp
    next
      case (Some a) thus ?thesis by blast
  qed
qed

```

lemma (in *J-heap-base*) *lock-ok-J-start-state*:

```

[[ wf-J-prog P; P ⊢ C sees M:Ts→T=[(pns, body)] in D; length Ts = length vs ]]
==> lock-ok (locks (J-start-state P C M vs)) (thr (J-start-state P C M vs))
apply(rule lock-okI)
apply(auto simp add: start-state-def split: if-split-asm)
apply(drule (1) sees-wf-mdecl)
apply(clarsimp simp add: wf-mdecl-def)
apply(drule WT-expr-locks)
apply(simp add: expr-locks-blocks)
done

```

4.9.3 Preservation of lock state agreement

fun *upd-expr-lock-action* :: int ⇒ lock-action ⇒ int

where

```

  upd-expr-lock-action i Lock = i + 1
| upd-expr-lock-action i Unlock = i - 1
| upd-expr-lock-action i UnlockFail = i
| upd-expr-lock-action i ReleaseAcquire = i

```

fun *upd-expr-lock-actions* :: int ⇒ lock-action list ⇒ int **where**

```

  upd-expr-lock-actions n [] = n
| upd-expr-lock-actions n (L # Ls) = upd-expr-lock-actions (upd-expr-lock-action n L) Ls

```

lemma *upd-expr-lock-actions-append* [simp]:

$upd\text{-}expr\text{-}lock\text{-}actions\ n\ (Ls\ @\ Ls') = upd\text{-}expr\text{-}lock\text{-}actions\ (upd\text{-}expr\text{-}lock\text{-}actions\ n\ Ls)\ Ls'$
by(*induct Ls arbitrary: n, auto*)

definition $upd\text{-}expr\text{-}locks :: ('l \Rightarrow int) \Rightarrow 'l\ lock\text{-}actions \Rightarrow 'l \Rightarrow int$
where $upd\text{-}expr\text{-}locks\ els\ las \equiv \lambda l. upd\text{-}expr\text{-}lock\text{-}actions\ (els\ l)\ (las\ \$\ l)$

lemma $upd\text{-}expr\text{-}locks\text{-}iff\ [simp]:$
 $upd\text{-}expr\text{-}locks\ els\ las\ l = upd\text{-}expr\text{-}lock\text{-}actions\ (els\ l)\ (las\ \$\ l)$
by(*simp add: upd-expr-locks-def*)

lemma $upd\text{-}expr\text{-}lock\text{-}action\text{-}add\ [simp]:$
 $upd\text{-}expr\text{-}lock\text{-}action\ (l + l')\ L = upd\text{-}expr\text{-}lock\text{-}action\ l\ L + l'$
by(*cases L, auto*)

lemma $upd\text{-}expr\text{-}lock\text{-}actions\text{-}add\ [simp]:$
 $upd\text{-}expr\text{-}lock\text{-}actions\ (l + l')\ Ls = upd\text{-}expr\text{-}lock\text{-}actions\ l\ Ls + l'$
by(*induct Ls arbitrary: l, auto*)

lemma $upd\text{-}expr\text{-}locks\text{-}add\ [simp]:$
 $upd\text{-}expr\text{-}locks\ (\lambda a. x\ a + y\ a)\ las = (\lambda a. upd\text{-}expr\text{-}locks\ x\ las\ a + y\ a)$
by(*auto intro: ext*)

lemma $expr\text{-}locks\text{-}extRet2J\ [simp, intro!]: expr\text{-}locks\ e = (\lambda ad. 0) \Longrightarrow expr\text{-}locks\ (extRet2J\ e\ va) =$
 $(\lambda ad. 0)$
by(*cases va*) *auto*

lemma (**in** $J\text{-heap-base}$)

assumes $wf: wf\text{-}J\text{-prog}\ P$

shows $red\text{-}update\text{-}expr\text{-}locks:$

$\llbracket convert\text{-}extTA\ extNTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; sync\text{-}ok\ e \rrbracket$
 $\Longrightarrow upd\text{-}expr\text{-}locks\ (int\ o\ expr\text{-}locks\ e)\ \{\!|ta|\!\}_l = int\ o\ expr\text{-}locks\ e'$

and $reds\text{-}update\text{-}expr\text{-}lockss:$

$\llbracket convert\text{-}extTA\ extNTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; sync\text{-}oks\ es \rrbracket$
 $\Longrightarrow upd\text{-}expr\text{-}locks\ (int\ o\ expr\text{-}lockss\ es)\ \{\!|ta|\!\}_l = int\ o\ expr\text{-}lockss\ es'$

proof –

have $\llbracket convert\text{-}extTA\ extNTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; sync\text{-}ok\ e \rrbracket$

$\Longrightarrow upd\text{-}expr\text{-}locks\ (\lambda ad. 0)\ \{\!|ta|\!\}_l = (\lambda ad. (int\ o\ expr\text{-}locks\ e')\ ad - (int\ o\ expr\text{-}locks\ e)\ ad)$

and $\llbracket convert\text{-}extTA\ extNTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; sync\text{-}oks\ es \rrbracket$

$\Longrightarrow upd\text{-}expr\text{-}locks\ (\lambda ad. 0)\ \{\!|ta|\!\}_l = (\lambda ad. (int\ o\ expr\text{-}lockss\ es')\ ad - (int\ o\ expr\text{-}lockss\ es)\ ad)$

proof(*induct rule: red-reds.inducts*)

case ($RedCall\ s\ a\ U\ M\ Ts\ T\ pns\ body\ D\ vs$)

from $wf\ \langle P \vdash class\text{-}type\text{-}of\ U\ sees\ M: Ts \rightarrow T = \llbracket (pns, body) \rrbracket\ in\ D \rangle$

have $wf\text{-}mdecl\ wf\text{-}J\text{-mdecl}\ P\ D\ (M, Ts, T, \llbracket (pns, body) \rrbracket)$

by(*rule sees-wf-mdecl*)

then obtain T **where** $P, \llbracket this \mapsto Class\ D, pns \mapsto \rrbracket Ts \vdash body :: T$

by(*auto simp add: wf-mdecl-def*)

hence $expr\text{-}locks\ body = (\lambda ad. 0)$ **by**(*rule WT-expr-locks*)

with $\langle length\ vs = length\ pns \rangle\ \langle length\ Ts = length\ pns \rangle$

have $expr\text{-}locks\ (blocks\ pns\ Ts\ vs\ body) = (\lambda ad. 0)$

by(*simp add: expr-locks-blocks*)

thus $?case$ **by**(*auto intro: expr-locks-sync-ok*)

next

case $RedCallExternal$ **thus** $?case$

by(*auto simp add: fun-eq-iff contains-insync-conv contains-insyncs-conv finfun-upd-apply ta-upd-simps*)

elim!: *red-external.cases*)

qed(*fastforce simp add: fun-eq-iff contains-insync-conv contains-insyncs-conv finfun-upd-apply ta-upd-simps*) +
hence $\llbracket \text{convert-extTA extNTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{sync-ok } e \rrbracket$
 $\implies \text{upd-expr-locks } (\lambda ad. 0 + (\text{int } \circ \text{expr-locks } e) \text{ ad}) \{\{ta\}\}_l = \text{int } \circ \text{expr-locks } e'$
and $\llbracket \text{convert-extTA extNTA}, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{sync-oks } es \rrbracket$
 $\implies \text{upd-expr-locks } (\lambda ad. 0 + (\text{int } \circ \text{expr-lockss } es) \text{ ad}) \{\{ta\}\}_l = \text{int } \circ \text{expr-lockss } es'$
by(*auto intro: ext simp only: upd-expr-locks-add*)
thus $\llbracket \text{convert-extTA extNTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{sync-ok } e \rrbracket$
 $\implies \text{upd-expr-locks } (\text{int } \circ \text{expr-locks } e) \{\{ta\}\}_l = \text{int } \circ \text{expr-locks } e'$
and $\llbracket \text{convert-extTA extNTA}, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{sync-oks } es \rrbracket$
 $\implies \text{upd-expr-locks } (\text{int } \circ \text{expr-lockss } es) \{\{ta\}\}_l = \text{int } \circ \text{expr-lockss } es'$
by(*auto simp add: o-def*)
qed

definition *lock-expr-locks-ok* :: 't FWState.lock \Rightarrow 't \Rightarrow nat \Rightarrow int \Rightarrow bool **where**
lock-expr-locks-ok l t n i \equiv (i = int (has-locks l t) + int n) \wedge i \geq 0

lemma *upd-lock-upd-expr-lock-action-preserve-lock-expr-locks-ok*:

assumes *lao*: *lock-action-ok* l t L
and *lelo*: *lock-expr-locks-ok* l t n i
shows *lock-expr-locks-ok* (upd-lock l t L) t (upd-threadR n l t L) (upd-expr-lock-action i L)

proof –

from *lelo* **have** i: i \geq 0
and *hl*: i = int (has-locks l t) + int n
by(*auto simp add: lock-expr-locks-ok-def*)
from *lelo*
show ?thesis
proof(*cases L*)
case *Lock*
with *lao* **have** *may-lock* l t **by**(*simp*)
with *hl* **have** *has-locks* (lock-lock l t) t = (Suc (has-locks l t)) **by**(*auto*)
with *Lock* i *hl* **show** ?thesis
by(*simp add: lock-expr-locks-ok-def*)
next
case *Unlock*
with *lao* **have** *has-lock* l t **by** *simp*
then **obtain** *n'*
where *hl'*: *has-locks* l t = Suc *n'*
by(*auto dest: has-lock-has-locks-Suc*)
hence *has-locks* (unlock-lock l) t = *n'* **by** *simp*
with *Unlock* i *hl* *hl'* **show** ?thesis
by(*simp add: lock-expr-locks-ok-def*)
qed(*auto simp add: lock-expr-locks-ok-def*)
qed

lemma *upd-locks-upd-expr-lock-preserve-lock-expr-locks-ok*:

$\llbracket \text{lock-actions-ok } l t Ls; \text{lock-expr-locks-ok } l t n i \rrbracket$
 $\implies \text{lock-expr-locks-ok } (\text{upd-locks } l t Ls) t (\text{upd-threadRs } n l t Ls) (\text{upd-expr-lock-actions } i Ls)$
by(*induct Ls arbitrary: l i n*)(*auto intro: upd-lock-upd-expr-lock-action-preserve-lock-expr-locks-ok*)

definition *ls-els-ok* :: ('addr, 'thread-id) locks \Rightarrow 'thread-id \Rightarrow ('addr \Rightarrow f nat) \Rightarrow ('addr \Rightarrow int) \Rightarrow bool
where

$\wedge \ln. \text{ls-els-ok } ls t \ln \text{ els} \equiv \forall l. \text{lock-expr-locks-ok } (ls \$ l) t (\ln \$ l) (\text{els } l)$

lemma *ls-els-okI*:

$\bigwedge ln. (\bigwedge l. \text{lock-expr-locks-ok } (ls \ \$ \ l) \ t \ (ln \ \$ \ l) \ (els \ l)) \implies \text{ls-els-ok } ls \ t \ ln \ els$
by(*auto simp add: ls-els-ok-def*)

lemma *ls-els-okE*:

$\bigwedge ln. [\text{ls-els-ok } ls \ t \ ln \ els; \forall l. \text{lock-expr-locks-ok } (ls \ \$ \ l) \ t \ (ln \ \$ \ l) \ (els \ l) \implies P] \implies P$
by(*auto simp add: ls-els-ok-def*)

lemma *ls-els-okD*:

$\bigwedge ln. \text{ls-els-ok } ls \ t \ ln \ els \implies \text{lock-expr-locks-ok } (ls \ \$ \ l) \ t \ (ln \ \$ \ l) \ (els \ l)$
by(*auto simp add: ls-els-ok-def*)

lemma *redT-updLs-upd-expr-locks-preserves-ls-els-ok*:

$\bigwedge ln. [\text{ls-els-ok } ls \ t \ ln \ els; \text{lock-ok-las } ls \ t \ las]$
 $\implies \text{ls-els-ok } (\text{redT-updLs } ls \ t \ las) \ t \ (\text{redT-updLns } ls \ t \ ln \ las) \ (\text{upd-expr-locks } els \ las)$
by(*auto intro!: ls-els-okI upd-locks-upd-expr-lock-preserve-lock-expr-locks-ok elim!: ls-els-okE simp add: redT-updLs-def lock-ok-las-def*)

lemma *sync-ok-redT-updT*:

assumes *sync-es-ok ts h*
and *nt*: $\bigwedge t \ e \ x \ h''. ta = \text{NewThread } t \ (e, x) \ h'' \implies \text{sync-ok } e$
shows *sync-es-ok (redT-updT ts ta) h'*
using *assms*
proof(*cases ta*)
case (*NewThread T x m*)
obtain *E X* **where** [*simp*]: $x = (E, X)$ **by** (*cases x, auto*)
with *NewThread* **have** *sync-ok E* **by**(*simp*)(*rule nt*)
with *NewThread* $\langle \text{sync-es-ok } ts \ h \rangle$ **show** *?thesis*
apply –
apply(*rule ts-okI*)
apply(*case-tac t=T*)
by(*auto dest: ts-okD*)
qed(*auto intro: ts-okI dest: ts-okD*)

lemma *sync-ok-redT-updTs*:

$[\text{sync-es-ok } ts \ h; \bigwedge t \ e \ x \ h. \text{NewThread } t \ (e, x) \ h \in \text{set } tas \implies \text{sync-ok } e]$
 $\implies \text{sync-es-ok } (\text{redT-updTs } ts \ tas) \ h'$
proof(*induct tas arbitrary: ts*)
case *Nil* **thus** *?case* **by**(*auto intro: ts-okI dest: ts-okD*)
next
case (*Cons TA TAS TS*)
note *IH* = $\langle \bigwedge ts. [\text{sync-es-ok } ts \ h; \bigwedge t \ e \ x \ h''. \text{NewThread } t \ (e, x) \ h'' \in \text{set } TAS \implies \text{sync-ok } e]$
 $\implies \text{sync-es-ok } (\text{redT-updTs } ts \ TAS) \ h' \rangle$
note *nt* = $\langle \bigwedge t \ e \ x \ h. \text{NewThread } t \ (e, x) \ h \in \text{set } (TA \ \# \ TAS) \implies \text{sync-ok } e \rangle$
from $\langle \text{sync-es-ok } TS \ h \rangle$ *nt*
have *sync-es-ok (redT-updT TS TA) h*
by(*auto elim!: sync-ok-redT-updT*)
hence *sync-es-ok (redT-updTs (redT-updT TS TA) TAS) h'*
by(*rule IH*)(*auto intro: nt*)
thus *?case* **by** *simp*
qed

lemma *lock-ok-thr-updI*:

$\wedge ln. \llbracket \text{lock-ok } ls \text{ } ts; \text{ } ts \text{ } t = \llbracket ((e, xs), ln) \rrbracket; \text{expr-locks } e = \text{expr-locks } e' \rrbracket$
 $\implies \text{lock-ok } ls \text{ } (ts(t \mapsto ((e', xs'), ln)))$

by(rule *lock-okI*)(auto *split: if-split-asm dest: lock-okD2 lock-okD1*)

context *J-heap-base begin*

lemma *redT-preserves-lock-ok*:

assumes *wf: wf-J-prog P*

and $P \vdash s -t>ta \rightarrow s'$

and *lock-ok (locks s) (thr s)*

and *sync-es-ok (thr s) (shr s)*

shows *lock-ok (locks s') (thr s')*

proof –

obtain *ls ts h ws is* **where** $s \text{ [simp]: } s = (ls, (ts, h), ws, is)$ **by**(cases *s*) *fastforce*

obtain *ls' ts' h' ws' is'* **where** $s' \text{ [simp]: } s' = (ls', (ts', h'), ws', is')$ **by**(cases s') *fastforce*

from *assms* **have** *redT: P ⊢ (ls, (ts, h), ws, is) -t>ta → (ls', (ts', h'), ws', is')*

and *loes: lock-ok ls ts*

and *aoes: sync-es-ok ts h* **by** *auto*

from *redT* **have** *lock-ok ls' ts'*

proof(cases rule: *red-mthr.redT-elim*s)

case (*normal a a' m'*)

moreover obtain $e \ x$ **where** $a = (e, x)$ **by** (*cases a, auto*)

moreover obtain $e' \ x'$ **where** $a' = (e', x')$ **by** (*cases a', auto*)

ultimately have $P, t \vdash \langle e, (h, x) \rangle -ta \rightarrow \langle e', (m', x') \rangle$

and *est: ts t = [((e, x), no-wait-locks)]*

and *lota: lock-ok-las ls t {ta}_l*

and *cctta: thread-oks ts {ta}_t*

and $ls': ls' = \text{redT-updLs } ls \text{ } t \text{ } \{ta\}_l$

and $s': ts' = (\text{redT-updT}_s \text{ } ts \text{ } \{ta\}_t)(t \mapsto ((e', x'), \text{redT-updLns } ls \text{ } t \text{ } \text{no-wait-locks } \{ta\}_l))$

by *auto*

let $?ts' = (\text{redT-updT}_s \text{ } ts \text{ } \{ta\}_t)(t \mapsto ((e', x'), \text{redT-updLns } ls \text{ } t \text{ } \text{no-wait-locks } \{ta\}_l))$

from *est aoes* **have** *aoe: sync-ok e* **by**(auto *dest: ts-okD*)

from *aoe P* **have** *aoe': sync-ok e'* **by**(auto *dest: red-preserve-sync-ok[OF wf]*)

from *aoes red-new-thread-sync-ok[OF wf P]*

have *sync-es-ok (redT-updT}_s \text{ } ts \text{ } \{ta\}_t) h'*

by(rule *sync-ok-redT-updT}_s*)

with *aoe'* **have** *aoes': sync-es-ok ?ts' m'*

by(auto *intro!: ts-okI dest: ts-okD split: if-split-asm*)

have *lock-ok ls' ?ts'*

proof(rule *lock-okI*)

fix $t'' \ l$

assume $?ts' \ t'' = \text{None}$

hence $ts \ t'' = \text{None}$

by(auto *split: if-split-asm intro: redT-updT}_s-\text{None}*)

with *loes* **have** *has-locks (ls \$ l) t'' = 0*

by(auto *dest: lock-okD1*)

moreover from $\langle ?ts' \ t'' = \text{None} \rangle$

have $t \neq t''$ **by**(simp *split: if-split-asm*)

ultimately show *has-locks (ls' \$ l) t'' = 0*

by(simp *add: red-mthr.redT-has-locks-inv[OF redT]*)

next

fix $t'' \ e'' \ x'' \ l \ ln''$

assume $ts't'': ?ts' \ t'' = \llbracket ((e'', x''), ln'') \rrbracket$

```

with aoes' have aoe'': sync-ok e'' by(auto dest: ts-okD)
show has-locks (ls' $ l) t'' + ln'' $ l = expr-locks e'' l
proof(cases t = t'')
  case True
  note tt'' = ⟨t = t''⟩
  with ts't'' have e'': e'' = e' and x'': x'' = x'
  and ln'': ln'' = redT-updLns ls t no-wait-locks {ta}_l by auto
  have ls-els-ok ls t no-wait-locks (int o expr-locks e)
  proof(rule ls-els-okI)
    fix l
    note lock-okD2[OF loes, OF est]
    thus lock-expr-locks-ok (ls $ l) t (no-wait-locks $ l) ((int o expr-locks e) l)
      by(simp add: lock-expr-locks-ok-def)
  qed
  hence ls-els-ok (redT-updLns ls t {ta}_l) t (redT-updLns ls t no-wait-locks {ta}_l) (upd-expr-locks
(int o expr-locks e) {ta}_l)
    by(rule redT-updLns-upd-expr-locks-preserves-ls-els-ok[OF - lota])
  hence ls-els-ok (redT-updLns ls t {ta}_l) t (redT-updLns ls t no-wait-locks {ta}_l) (int o expr-locks
e')
    by(simp only: red-update-expr-locks[OF wf P aoe])
  thus ?thesis using ls' e'' tt'' ln''
    by(auto dest: ls-els-okD[where l = l] simp: lock-expr-locks-ok-def)
next
case False
note tt'' = ⟨t ≠ t''⟩
from lota have lao: lock-actions-ok (ls $ l) t ({ta}_l $ l)
  by(simp add: lock-ok-las-def)
show ?thesis
proof(cases ts t'')
  case None
  with est ts't'' tt'' cctta
  obtain m where NewThread t'' (e'', x'') m ∈ set {ta}_t and ln'': ln'' = no-wait-locks
    by(auto dest: redT-updTs-new-thread)
  moreover with P have m' = m by(auto dest: red-new-thread-heap)
  ultimately have NewThread t'' (e'', x'') m' ∈ set {ta}_t by simp
  with wf P ln'' have expr-locks e'' = (λad. 0)
    by -(rule expr-locks-new-thread)
  hence elel: expr-locks e'' l = 0 by simp
  from loes None have has-locks (ls $ l) t'' = 0
    by(auto dest: lock-okD1)
  moreover note lock-actions-ok-has-locks-upd-locks-eq-has-locks[OF lao tt''[symmetric]]
  ultimately have has-locks (redT-updLns ls t {ta}_l $ l) t'' = 0
    by(auto simp add: fun-eq-iff)
  with elel ls' ln'' show ?thesis by(auto)
next
case (Some a)
then obtain E X LN where est'': ts t'' = [((E, X), LN)] by(cases a, auto)
with loes have IH: has-locks (ls $ l) t'' + LN $ l = expr-locks E l
  by(auto dest: lock-okD2)
from est est'' tt'' cctta have ?ts' t'' = [((E, X), LN)]
  by(simp)(rule redT-updTs-Some, simp-all)
with ts't'' have e'': E = e'' and x'': X = x''
  and ln'': ln'' = LN by(simp-all)
with lock-actions-ok-has-locks-upd-locks-eq-has-locks[OF lao tt''[symmetric]] IH ls'

```



```

    show ?thesis by (clarsimp simp add: redT-updLs-def fun-eq-iff)
  qed
  qed
  qed
  with s' show ?thesis by simp
next
case (acquire a ln n)
hence [simp]: ta = (K$ [], [], [], [], [], convert-RA ln) ws' = ws h' = h
  and ls': ls' = acquire-all ls t ln
  and ts': ts' = ts(t ↦ (a, no-wait-locks))
  and ts t = [(a, ln)]
  and may-acquire-all ls t ln
  by auto
obtain e x where [simp]: a = (e, x) by (cases a, auto)
from ts' have ts': ts' = ts(t ↦ ((e, x), no-wait-locks)) by simp
from ⟨ts t = [(a, ln)]⟩ have tst: ts t = [((e, x), ln)] by simp
show ?thesis
proof (rule lock-okI)
  fix t'' l
  assume rtutes: ts' t'' = None
  with ts' have tst'': ts t'' = None
  by (simp split: if-split-asm)
  with tst have tt'': t ≠ t'' by auto
  from tst'' loes have has-locks (ls $ l) t'' = 0
  by (auto dest: lock-okD1)
  thus has-locks (ls' $ l) t'' = 0
  by (simp add: red-mthr.redT-has-locks-inv[OF redT tt''])
next
fix t'' e'' x'' ln'' l
assume ts't'': ts' t'' = [((e'', x''), ln'')]
show has-locks (ls' $ l) t'' + ln'' $ l = expr-locks e'' l
proof (cases t = t'')
  case True
  note [simp] = this
  with ts't'' ts' tst
  have [simp]: ln'' = no-wait-locks e = e'' by auto
  from tst loes have has-locks (ls $ l) t + ln $ l = expr-locks e l
  by (auto dest: lock-okD2)
  show ?thesis
  proof (cases ln $ l > 0)
    case True
    with ⟨may-acquire-all ls t ln⟩ ls' have may-lock (ls $ l) t
    by (auto elim: may-acquire-allE)
    with ls'
    have has-locks (ls' $ l) t = has-locks (ls $ l) t + ln $ l
    by (simp add: has-locks-acquire-locks-conv)
    with ⟨has-locks (ls $ l) t + ln $ l = expr-locks e l⟩
    show ?thesis by (simp)
  next
  case False
  hence ln $ l = 0 by simp
  with ls' have has-locks (ls' $ l) t = has-locks (ls $ l) t
  by (simp)
  with ⟨has-locks (ls $ l) t + ln $ l = expr-locks e l⟩ ⟨ln $ l = 0⟩

```

```

    show ?thesis by(simp)
  qed
next
case False
with ts' ts't'' have tst'': ts t'' = [((e'', x''), ln'')] by(simp)
with loes have has-locks (ls $ l) t'' + ln'' $ l = expr-locks e'' l
  by(auto dest: lock-okD2)
show ?thesis
proof(cases ln $ l > 0)
  case False
  with <t ≠ t''> ls'
  have has-locks (ls' $ l) t'' = has-locks (ls $ l) t'' by(simp)
  with <has-locks (ls $ l) t'' + ln'' $ l = expr-locks e'' l>
  show ?thesis by(simp)
next
case True
with <may-acquire-all ls t ln> have may-lock (ls $ l) t
  by(auto elim: may-acquire-allE)
with ls' <t ≠ t''> have has-locks (ls' $ l) t'' = has-locks (ls $ l) t''
  by(simp add: has-locks-acquire-locks-conv')
with ls' <has-locks (ls $ l) t'' + ln'' $ l = expr-locks e'' l>
  show ?thesis by(simp)
qed
qed
qed
qed
thus ?thesis by simp
qed

```

lemma *invariant3p-sync-es-ok-lock-ok*:

```

  assumes wf: wf-J-prog P
  shows invariant3p (mredT P) {s. sync-es-ok (thr s) (shr s) ∧ lock-ok (locks s) (thr s)}
apply(rule invariant3pI)
apply clarify
apply(rule conjI)
  apply(rule lifting-wf.redT-preserves[OF lifting-wf-sync-ok[OF wf]], blast)
  apply(assumption)
apply(erule (2) redT-preserves-lock-ok[OF wf])
done

```

lemma *RedT-preserves-lock-ok*:

```

  assumes wf: wf-J-prog P
  and Red: P ⊢ s -▷ttas→* s'
  and ae: sync-es-ok (thr s) (shr s)
  and loes: lock-ok (locks s) (thr s)
  shows lock-ok (locks s') (thr s')
using invariant3p-rtrancl3p[OF invariant3p-sync-es-ok-lock-ok[OF wf] Red[unfolded red-mthr.RedT-def]]
ae loes
by simp

```

end

4.9.4 Determinism

context *J-heap-base* **begin**

lemma

fixes *final*

assumes *det: deterministic-heap-ops*

shows *red-deterministic*:

\llbracket *convert-extTA* $\text{extTA}, P, t \vdash \langle e, (\text{shr } s, xs) \rangle -ta \rightarrow \langle e', s' \rangle$;
convert-extTA $\text{extTA}, P, t \vdash \langle e, (\text{shr } s, xs) \rangle -ta' \rightarrow \langle e'', s'' \rangle$;
final-thread.actions-ok *final* $s \ t \ ta$; *final-thread.actions-ok* *final* $s \ t \ ta'$ \rrbracket
 $\implies ta = ta' \wedge e' = e'' \wedge s' = s''$

and *reds-deterministic*:

\llbracket *convert-extTA* $\text{extTA}, P, t \vdash \langle es, (\text{shr } s, xs) \rangle [-ta \rightarrow] \langle es', s' \rangle$;
convert-extTA $\text{extTA}, P, t \vdash \langle es, (\text{shr } s, xs) \rangle [-ta' \rightarrow] \langle es'', s'' \rangle$;
final-thread.actions-ok *final* $s \ t \ ta$; *final-thread.actions-ok* *final* $s \ t \ ta'$ \rrbracket
 $\implies ta = ta' \wedge es' = es'' \wedge s' = s''$

proof(*induct* $e \ (\text{shr } s, xs) \ ta \ e' \ s'$ **and** $es \ (\text{shr } s, xs) \ ta \ es' \ s'$ *arbitrary: e'' s'' xs* **and** $es'' \ s'' \ xs$ *rule: red-reds.inducts*)

case *RedNew*

thus *?case* **by**(*auto elim!*: *red-cases* *dest: deterministic-heap-ops-allocateD*[*OF det*])

next

case *RedNewArray*

thus *?case* **by**(*auto elim!*: *red-cases* *dest: deterministic-heap-ops-allocateD*[*OF det*])

next

case *RedCall* **thus** *?case*

by(*auto elim!*: *red-cases* *dest: sees-method-fun simp add: map-eq-append-conv*)

next

case *RedCallExternal* **thus** *?case*

by(*auto elim!*: *red-cases* *dest: red-external-deterministic*[*OF det*] *simp add: final-thread.actions-ok-iff map-eq-append-conv dest: sees-method-fun*)

next

case *RedCallNull* **thus** *?case* **by**(*auto elim!*: *red-cases* *dest: sees-method-fun simp add: map-eq-append-conv*)

next

case *CallThrowParams* **thus** *?case*

by(*auto elim!*: *red-cases* *dest: sees-method-fun simp add: map-eq-append-conv append-eq-map-conv append-eq-append-conv2 reds-map-Val-Throw Cons-eq-append-conv append-eq-Cons-conv*)

qed(*fastforce elim!*: *red-cases* *reds-cases* *dest: deterministic-heap-ops-readD*[*OF det*] *deterministic-heap-ops-writeD*[*OF det*] *iff: reds-map-Val-Throw*)**+**

lemma *red-mthr-deterministic*:

assumes *det: deterministic-heap-ops*

shows *red-mthr.deterministic* $P \ UNIV$

proof(*rule red-mthr.deterministicI*)

fix $s \ t \ x \ ta' \ x' \ m' \ ta'' \ x'' \ m''$

assume $\text{thr } s \ t = \llbracket (x, \text{no-wait-locks}) \rrbracket$

and *red*: $\text{mred } P \ t \ (x, \text{shr } s) \ ta' \ (x', m') \ \text{mred } P \ t \ (x, \text{shr } s) \ ta'' \ (x'', m'')$

and *aok*: $\text{red-mthr.actions-ok } s \ t \ ta' \ \text{red-mthr.actions-ok } s \ t \ ta''$

moreover obtain $e \ xs$ **where** [*simp*]: $x = (e, xs)$ **by**(*cases* x)

moreover obtain $e' \ xs'$ **where** [*simp*]: $x' = (e', xs')$ **by**(*cases* x')

moreover obtain $e'' \ xs''$ **where** [*simp*]: $x'' = (e'', xs'')$ **by**(*cases* x'')

ultimately have $\text{extTA}2J \ P, P, t \vdash \langle e, (\text{shr } s, xs) \rangle -ta' \rightarrow \langle e', (m', xs') \rangle$

and $\text{extTA}2J \ P, P, t \vdash \langle e, (\text{shr } s, xs) \rangle -ta'' \rightarrow \langle e'', (m'', xs'') \rangle$

by *simp-all*

```

from red-deterministic[OF det this aok]
show ta' = ta'' ∧ x' = x'' ∧ m' = m'' by simp
qed simp

end

end

```

4.10 Runtime Well-typedness

```
theory WellTypeRT
```

```
imports
```

```
  WellType
```

```
  JHeap
```

```
begin
```

```
context J-heap-base begin
```

```

inductive WTrt :: 'addr J-prog ⇒ 'heap ⇒ env ⇒ 'addr expr ⇒ ty ⇒ bool
and WTrts :: 'addr J-prog ⇒ 'heap ⇒ env ⇒ 'addr expr list ⇒ ty list ⇒ bool
for P :: 'addr J-prog and h :: 'heap
where

```

```
  WTrtNew:
```

```
    is-class P C ⇒ WTrt P h E (new C) (Class C)
```

```
| WTrtNewArray:
```

```
  [[ WTrt P h E e Integer; is-type P (T[]) ] ]
  ⇒ WTrt P h E (newA T[e]) (T[])
```

```
| WTrtCast:
```

```
  [[ WTrt P h E e T; is-type P U ] ] ⇒ WTrt P h E (Cast U e) U
```

```
| WTrtInstanceOf:
```

```
  [[ WTrt P h E e T; is-type P U ] ] ⇒ WTrt P h E (e instanceof U) Boolean
```

```
| WTrtVal:
```

```
  typeofh v = Some T ⇒ WTrt P h E (Val v) T
```

```
| WTrtVar:
```

```
  E V = Some T ⇒ WTrt P h E (Var V) T
```

```
| WTrtBinOp:
```

```
  [[ WTrt P h E e1 T1; WTrt P h E e2 T2; P ⊢ T1 «bop» T2 : T ] ]
  ⇒ WTrt P h E (e1 «bop» e2) T
```

```
| WTrtLAss:
```

```
  [[ E V = Some T; WTrt P h E e T'; P ⊢ T' ≤ T ] ]
  ⇒ WTrt P h E (V:=e) Void
```

```
| WTrtAAcc:
```

```
  [[ WTrt P h E a (T[]); WTrt P h E i Integer ] ]
  ⇒ WTrt P h E (a[i]) T
```

- | *WTrtAAccNT*:
 $\llbracket \text{WTrt } P \text{ h } E \text{ a } NT; \text{WTrt } P \text{ h } E \text{ i } Integer \rrbracket$
 $\implies \text{WTrt } P \text{ h } E \text{ (a[i]} \text{) } T$
- | *WTrtAAss*:
 $\llbracket \text{WTrt } P \text{ h } E \text{ a } (T[]); \text{WTrt } P \text{ h } E \text{ i } Integer; \text{WTrt } P \text{ h } E \text{ e } T' \rrbracket$
 $\implies \text{WTrt } P \text{ h } E \text{ (a[i]} := e \text{) } Void$
- | *WTrtAAssNT*:
 $\llbracket \text{WTrt } P \text{ h } E \text{ a } NT; \text{WTrt } P \text{ h } E \text{ i } Integer; \text{WTrt } P \text{ h } E \text{ e } T' \rrbracket$
 $\implies \text{WTrt } P \text{ h } E \text{ (a[i]} := e \text{) } Void$
- | *WTrtALength*:
 $\text{WTrt } P \text{ h } E \text{ a } (T[]) \implies \text{WTrt } P \text{ h } E \text{ (a.length) } Integer$
- | *WTrtALengthNT*:
 $\text{WTrt } P \text{ h } E \text{ a } NT \implies \text{WTrt } P \text{ h } E \text{ (a.length) } T$
- | *WTrtFAcc*:
 $\llbracket \text{WTrt } P \text{ h } E \text{ e } U; \text{class-type-of}' U = [C]; P \vdash C \text{ has } F:T \text{ (fm) in } D \rrbracket \implies$
 $\text{WTrt } P \text{ h } E \text{ (e.F}\{D\} \text{) } T$
- | *WTrtFAccNT*:
 $\text{WTrt } P \text{ h } E \text{ e } NT \implies \text{WTrt } P \text{ h } E \text{ (e.F}\{D\} \text{) } T$
- | *WTrtFAss*:
 $\llbracket \text{WTrt } P \text{ h } E \text{ e1 } U; \text{class-type-of}' U = [C]; P \vdash C \text{ has } F:T \text{ (fm) in } D; \text{WTrt } P \text{ h } E \text{ e2 } T2; P$
 $\vdash T2 \leq T \rrbracket$
 $\implies \text{WTrt } P \text{ h } E \text{ (e1.F}\{D\} := e2 \text{) } Void$
- | *WTrtFAssNT*:
 $\llbracket \text{WTrt } P \text{ h } E \text{ e1 } NT; \text{WTrt } P \text{ h } E \text{ e2 } T2 \rrbracket$
 $\implies \text{WTrt } P \text{ h } E \text{ (e1.F}\{D\} := e2 \text{) } Void$
- | *WTrtCAS*:
 $\llbracket \text{WTrt } P \text{ h } E \text{ e1 } U; \text{class-type-of}' U = [C]; P \vdash C \text{ has } F:T \text{ (fm) in } D; \text{volatile fm};$
 $\text{WTrt } P \text{ h } E \text{ e2 } T2; P \vdash T2 \leq T; \text{WTrt } P \text{ h } E \text{ e3 } T3; P \vdash T3 \leq T \rrbracket$
 $\implies \text{WTrt } P \text{ h } E \text{ (e1.compareAndSwap(D.F, e2, e3)) } Boolean$
- | *WTrtCASNT*:
 $\llbracket \text{WTrt } P \text{ h } E \text{ e1 } NT; \text{WTrt } P \text{ h } E \text{ e2 } T2; \text{WTrt } P \text{ h } E \text{ e3 } T3 \rrbracket$
 $\implies \text{WTrt } P \text{ h } E \text{ (e1.compareAndSwap(D.F, e2, e3)) } Boolean$
- | *WTrtCall*:
 $\llbracket \text{WTrt } P \text{ h } E \text{ e } U; \text{class-type-of}' U = [C]; P \vdash C \text{ sees } M:Ts \rightarrow T = \text{meth in } D;$
 $\text{WTrts } P \text{ h } E \text{ es } Ts'; P \vdash Ts' [\leq] Ts \rrbracket$
 $\implies \text{WTrt } P \text{ h } E \text{ (e.M(es)) } T$
- | *WTrtCallNT*:
 $\llbracket \text{WTrt } P \text{ h } E \text{ e } NT; \text{WTrts } P \text{ h } E \text{ es } Ts \rrbracket$
 $\implies \text{WTrt } P \text{ h } E \text{ (e.M(es)) } T$
- | *WTrtBlock*:

$$\begin{aligned} & \llbracket \text{WTrt } P \text{ h } E (E(V \mapsto T)) \text{ e } T'; \text{ case vo of None} \Rightarrow \text{True} \mid [v] \Rightarrow \exists T'. \text{typeof}_h v = [T'] \wedge P \vdash T' \\ & \leq T \rrbracket \\ & \Longrightarrow \text{WTrt } P \text{ h } E \{V:T=vo; e\} T' \end{aligned}$$

$$\begin{aligned} & | \text{WTrtSynchronized:} \\ & \llbracket \text{WTrt } P \text{ h } E \text{ o}' T; \text{is-refT } T; \text{WTrt } P \text{ h } E \text{ e } T' \rrbracket \\ & \Longrightarrow \text{WTrt } P \text{ h } E (\text{sync}(\text{o}') \text{ e}) T' \end{aligned}$$

$$\begin{aligned} & | \text{WTrtInSynchronized:} \\ & \llbracket \text{WTrt } P \text{ h } E (\text{addr } a) T; \text{WTrt } P \text{ h } E \text{ e } T' \rrbracket \\ & \Longrightarrow \text{WTrt } P \text{ h } E (\text{insync}(a) \text{ e}) T' \end{aligned}$$

$$\begin{aligned} & | \text{WTrtSeq:} \\ & \llbracket \text{WTrt } P \text{ h } E \text{ e1 } T1; \text{WTrt } P \text{ h } E \text{ e2 } T2 \rrbracket \\ & \Longrightarrow \text{WTrt } P \text{ h } E (e1;;e2) T2 \end{aligned}$$

$$\begin{aligned} & | \text{WTrtCond:} \\ & \llbracket \text{WTrt } P \text{ h } E \text{ e Boolean; WTrt } P \text{ h } E \text{ e1 } T1; \text{WTrt } P \text{ h } E \text{ e2 } T2; P \vdash \text{lub}(T1, T2) = T \rrbracket \\ & \Longrightarrow \text{WTrt } P \text{ h } E (\text{if } (e) \text{ e1 else } e2) T \end{aligned}$$

$$\begin{aligned} & | \text{WTrtWhile:} \\ & \llbracket \text{WTrt } P \text{ h } E \text{ e Boolean; WTrt } P \text{ h } E \text{ c } T \rrbracket \\ & \Longrightarrow \text{WTrt } P \text{ h } E (\text{while}(e) \text{ c}) \text{Void} \end{aligned}$$

$$\begin{aligned} & | \text{WTrtThrow:} \\ & \llbracket \text{WTrt } P \text{ h } E \text{ e } T; P \vdash T \leq \text{Class Throwable} \rrbracket \\ & \Longrightarrow \text{WTrt } P \text{ h } E (\text{throw } e) T' \end{aligned}$$

$$\begin{aligned} & | \text{WTrtTry:} \\ & \llbracket \text{WTrt } P \text{ h } E \text{ e1 } T1; \text{WTrt } P \text{ h } (E(V \mapsto \text{Class } C)) \text{ e2 } T2; P \vdash T1 \leq T2 \rrbracket \\ & \Longrightarrow \text{WTrt } P \text{ h } E (\text{try } e1 \text{ catch}(C \text{ } V) \text{ e2}) T2 \end{aligned}$$

$$| \text{WTrtNil: } \text{WTrts } P \text{ h } E \llbracket \rrbracket$$

$$| \text{WTrtCons: } \llbracket \text{WTrt } P \text{ h } E \text{ e } T; \text{WTrts } P \text{ h } E \text{ es } Ts \rrbracket \Longrightarrow \text{WTrts } P \text{ h } E (e \# \text{ es}) (T \# Ts)$$

abbreviation

$$\text{WTrt-syntax} :: 'addr \text{ J-prog} \Rightarrow \text{env} \Rightarrow 'heap \Rightarrow 'addr \text{ expr} \Rightarrow \text{ty} \Rightarrow \text{bool} (\langle -, -, \vdash - : - \rangle \rightarrow [51,51,51]50)$$

where

$$P, E, h \vdash e : T \equiv \text{WTrt } P \text{ h } E \text{ e } T$$

abbreviation

$$\text{WTrts-syntax} :: 'addr \text{ J-prog} \Rightarrow \text{env} \Rightarrow 'heap \Rightarrow 'addr \text{ expr list} \Rightarrow \text{ty list} \Rightarrow \text{bool} (\langle -, -, \vdash - [:] \rangle \rightarrow [51,51,51]50)$$

where

$$P, E, h \vdash \text{es } [:] Ts \equiv \text{WTrts } P \text{ h } E \text{ es } Ts$$

lemmas [intro] =

$$\begin{aligned} & \text{WTrtNew } \text{WTrtNewArray } \text{WTrtCast } \text{WTrtInstanceOf } \text{WTrtVal } \text{WTrtVar } \text{WTrtBinOp } \text{WTrtLAss} \\ & \text{WTrtBlock } \text{WTrtSynchronized } \text{WTrtInSynchronized } \text{WTrtSeq } \text{WTrtCond } \text{WTrtWhile} \\ & \text{WTrtThrow } \text{WTrtTry } \text{WTrtNil } \text{WTrtCons} \end{aligned}$$

lemmas [intro] =

$$\text{WTrtFAcc } \text{WTrtFAccNT } \text{WTrtFAss } \text{WTrtFAssNT } \text{WTrtCall } \text{WTrtCallNT}$$

WTrtAAcc WTrtAAccNT WTrtAAss WTrtAAssNT WTrtALength WTrtALengthNT

4.10.1 Easy consequences

inductive-simps *WTrts-iffs* [iff]:

$P, E, h \vdash [] \text{ [:] } Ts$
 $P, E, h \vdash e\#es \text{ [:] } T\#Ts$
 $P, E, h \vdash (e\#es) \text{ [:] } Ts$

lemma *WTrts-conv-list-all2*: $P, E, h \vdash es \text{ [:] } Ts = list\text{-all2 } (WTrt \ P \ h \ E) \ es \ Ts$

by(*induct es arbitrary: Ts*)(*auto simp add: list-all2-Cons1 elim: WTrts.cases*)

lemma [*simp*]: $(P, E, h \vdash es_1 \ @ \ es_2 \text{ [:] } Ts) =$

$(\exists Ts_1 \ Ts_2. Ts = Ts_1 \ @ \ Ts_2 \wedge P, E, h \vdash es_1 \text{ [:] } Ts_1 \ \& \ P, E, h \vdash es_2 \text{ [:] } Ts_2)$

by(*auto simp add: WTrts-conv-list-all2 list-all2-append1 dest: list-all2-lengthD[symmetric]*)

inductive-simps *WTrt-iffs* [iff]:

$P, E, h \vdash Val \ v : T$
 $P, E, h \vdash Var \ v : T$
 $P, E, h \vdash e_1;;e_2 : T_2$
 $P, E, h \vdash \{V:T=vo; e\} : T'$

inductive-cases *WTrt-elim-cases*[*elim!*]:

$P, E, h \vdash newA \ T \ [i] : U$
 $P, E, h \vdash v := e : T$
 $P, E, h \vdash if \ (e) \ e_1 \ else \ e_2 : T$
 $P, E, h \vdash while(e) \ c : T$
 $P, E, h \vdash throw \ e : T$
 $P, E, h \vdash try \ e_1 \ catch(C \ V) \ e_2 : T$
 $P, E, h \vdash Cast \ D \ e : T$
 $P, E, h \vdash e \ instanceof \ U : T$
 $P, E, h \vdash a \ [i] : T$
 $P, E, h \vdash a \ [i] := e : T$
 $P, E, h \vdash a \cdot length : T$
 $P, E, h \vdash e \cdot F \{D\} : T$
 $P, E, h \vdash e \cdot F \{D\} := v : T$
 $P, E, h \vdash e \cdot compareAndSwap(D \cdot F, \ e2, \ e3) : T$
 $P, E, h \vdash e_1 \ \ll bop \ \gg \ e_2 : T$
 $P, E, h \vdash new \ C : T$
 $P, E, h \vdash e \cdot M(es) : T$
 $P, E, h \vdash sync(o') \ e : T$
 $P, E, h \vdash insync(a) \ e : T$

4.10.2 Some interesting lemmas

lemma *WTrts-Val*[*simp*]:

$P, E, h \vdash map \ Val \ vs \text{ [:] } Ts \iff map \ (typeof_h) \ vs = map \ Some \ Ts$

by(*induct vs arbitrary: Ts*) *auto*

lemma *WTrt-env-mono*: $P, E, h \vdash e : T \implies (\bigwedge E'. E \subseteq_m E' \implies P, E', h \vdash e : T)$

and *WTrts-env-mono*: $P, E, h \vdash es \text{ [:] } Ts \implies (\bigwedge E'. E \subseteq_m E' \implies P, E', h \vdash es \text{ [:] } Ts)$

apply(*induct rule: WTrt-WTrts.inducts*)

apply(*simp add: WTrtNew*)

apply(*fastforce simp: WTrtNewArray*)

```

apply(fastforce simp: WTrtCast)
apply(fastforce simp: WTrtInstanceOf)
apply(fastforce simp: WTrtVal)
apply(simp add: WTrtVar map-le-def dom-def)
apply(fastforce simp add: WTrtBinOp)
apply(force simp: map-le-def)
apply(force simp: WTrtAcc)
apply(force simp: WTrtAccNT)
apply(rule WTrtAss, fastforce, blast, blast)
apply(fastforce)
apply(rule WTrtLength, blast)
apply(blast)
apply(fastforce simp: WTrtFAcc)
apply(simp add: WTrtFAccNT)
apply(fastforce simp: WTrtFAss)
apply(fastforce simp: WTrtFAssNT)
apply(fastforce simp: WTrtCAS)
apply(fastforce simp: WTrtCASNT)
apply(fastforce simp: WTrtCall)
apply(fastforce simp: WTrtCallNT)
apply(fastforce simp: map-le-def)
apply(fastforce)
apply(fastforce)
apply(fastforce)
apply(fastforce)
apply(fastforce simp: WTrtSeq)
apply(fastforce simp: WTrtCond)
apply(fastforce simp: WTrtWhile)
apply(fastforce simp: WTrtThrow)
apply(auto simp: WTrtTry map-le-def dom-def)
done

lemma WT-implies-WTrt: P, E ⊢ e :: T ⇒ P, E, h ⊢ e : T
  and WTs-implies-WTrts: P, E ⊢ es [::] Ts ⇒ P, E, h ⊢ es [:] Ts
apply(induct rule: WT-WTs.inducts)
apply fast
apply fast
apply fast
apply fast
apply(fastforce dest:typeof-lit-typeof)
apply(simp)
apply(fastforce intro: WT-binop-WTrt-binop)
apply(fastforce)
apply(erule WTrtAcc)
apply(assumption)
apply(erule WTrtAss)
apply(assumption)+
apply(erule WTrtLength)
apply(fastforce intro: has-visible-field)
apply(fastforce simp: WTrtFAss dest: has-visible-field)
apply(fastforce simp: WTrtCAS dest: has-visible-field)
apply(fastforce simp: WTrtCall)
apply(clarsimp simp del: fun-upd-apply, blast intro: typeof-lit-typeof)
apply(fastforce)+

```


done

lemma *wt-blocks*:

$\wedge E. \llbracket \text{length } Vs = \text{length } Ts; \text{length } vs = \text{length } Ts \rrbracket \implies$
 $(P, E, h \vdash \text{blocks } Vs \ Ts \ vs \ e : T) =$
 $(P, E(Vs[\mapsto] Ts), h \vdash e : T \wedge (\exists Ts'. \text{map } (\text{typeof}_h) \ vs = \text{map } \text{Some } Ts' \wedge P \vdash Ts' [\leq] Ts))$
apply(*induct* *Vs Ts vs e rule:blocks.induct*)
apply (*force*)
apply *simp-all*
done

end

context *J-heap* begin

lemma *WTrt-heap-mono*: $P, E, h \vdash e : T \implies h \sqsubseteq h' \implies P, E, h' \vdash e : T$
and *WTrts-heap-mono*: $P, E, h \vdash es [:] Ts \implies h \sqsubseteq h' \implies P, E, h' \vdash es [:] Ts$
apply(*induct rule: WTrt-WTrts.inducts*)
apply(*simp add: WTrtNew*)
apply(*fastforce simp: WTrtNewArray*)
apply(*fastforce simp: WTrtCast*)
apply(*fastforce simp: WTrtInstanceOf*)
apply(*fastforce simp: WTrtVal dest:heap-typeof-mono*)
apply(*simp add: WTrtVar*)
apply(*fastforce simp add: WTrtBinOp*)
apply(*fastforce simp add: WTrtLAss*)
apply *fastforce*
apply *fastforce*
apply *fastforce*
apply *fastforce*
apply *fastforce*
apply *fastforce*
apply *fastforce*
apply (*fast*)
apply(*simp add: WTrtFAccNT*)
apply(*fastforce simp: WTrtFAss del:WTrt-WTrts.intros WTrt-elim-cases*)
apply(*fastforce simp: WTrtFAssNT*)
apply(*fastforce simp: WTrtCAS*)
apply(*fastforce simp: WTrtCASNT*)
apply(*fastforce simp: WTrtCall*)
apply(*fastforce simp: WTrtCallNT*)
apply(*fastforce intro: heap-typeof-mono*)
apply *fastforce+*
done

end

end

4.11 Progress of Small Step Semantics

theory *Progress*

imports

WellTypeRT

```

    DefAss
    SmallStep
    ../Common/ExternalCallWF
    WWellForm
begin

context J-heap begin

lemma final-addrE [consumes 3, case-names addr Throw]:
  [[ P,E,h ⊢ e : T; class-type-of' T = [U]; final e;
    ∧ a. e = addr a ⇒ R;
    ∧ a. e = Throw a ⇒ R ]] ⇒ R
apply(auto elim!: final.cases)
apply(case-tac v)
apply auto
done

lemma finalRefE [consumes 3, case-names null Class Array Throw]:
  [[ P,E,h ⊢ e : T; is-refT T; final e;
    e = null ⇒ R;
    ∧ a C. [[ e = addr a; T = Class C ]] ⇒ R;
    ∧ a U. [[ e = addr a; T = U[] ]] ⇒ R;
    ∧ a. e = Throw a ⇒ R ]] ⇒ R
apply(auto simp:final-iff)
apply(case-tac v)
apply(auto elim!: is-refT.cases)
done

end

theorem (in J-progress) red-progress:
  assumes wf: wwf-J-prog P and hconf: hconf h
  shows progress: [[ P,E,h ⊢ e : T; D e [dom l]; ¬ final e ]] ⇒ ∃ e' s' ta. extTA,P,t ⊢ ⟨e,(h,l)⟩ -ta→
  ⟨e',s'⟩
  and progresss: [[ P,E,h ⊢ es [:] Ts; Ds es [dom l]; ¬ finals es ]] ⇒ ∃ es' s' ta. extTA,P,t ⊢ ⟨es,(h,l)⟩
  [-ta→] ⟨es',s'⟩
proof (induct arbitrary: l and l rule: WTrt-WTrts.inducts)
  case (WTrtNew C)
  thus ?case using WTrtNew
  by(cases allocate h (Class-type C) = {})(fastforce intro: RedNewFail RedNew)+
next
  case (WTrtNewArray E e T l)
  have IH: ∧ l. [[ D e [dom l]; ¬ final e ]] ⇒ ∃ e' s' tas. extTA,P,t ⊢ ⟨e,(h,l)⟩ -tas→ ⟨e', s'⟩
  and D: D (newA T[e]) [dom l]
  and ei: P,E,h ⊢ e : Integer by fact+
  from D have De: D e [dom l] by auto
  show ?case
  proof cases
    assume final e
    thus ?thesis
    proof (rule finalE)
      fix v
      assume e [simp]: e = Val v
      with ei have typeofh v = Some Integer by fastforce
    end
  end
end

```

hence $exei: \exists i. v = \text{Intg } i$ **by** *fastforce*
 then **obtain** i **where** $v: v = \text{Intg } i$ **by** *blast*
 thus *?thesis*
proof (*cases* $0 \leq s \ i$)
 case *True*
 thus *?thesis using True* $\langle v = \text{Intg } i \rangle$ *WTrtNewArray.prem*
 by(*cases* *allocate* h (*Array-type* T (*nat* (*sint* i))) = $\{\}$)(*auto simp del: split-paired-Ex intro: RedNewArrayFail RedNewArray*)
 next
 assume $\neg 0 \leq s \ i$
 hence $i < s \ 0$ **by** *simp*
 then **have** $extTA, P, t \vdash \langle \text{newA } T[\text{Val}(\text{Intg } i)], (h, l) \rangle -\varepsilon \rightarrow \langle \text{THROW NegativeArraySize}, (h, l) \rangle$
 by $-$ (*rule RedNewArrayNegative, auto*)
 with $e \ v$ **show** *?thesis* **by** *blast*
 qed
 next
 fix exa
 assume $e: e = \text{Throw } exa$
 then **have** $extTA, P, t \vdash \langle \text{newA } T[\text{Throw } exa], (h, l) \rangle -\varepsilon \rightarrow \langle \text{Throw } exa, (h, l) \rangle$
 by $-$ (*rule NewArrayThrow*)
 with e **show** *?thesis* **by** *blast*
 qed
 next
 assume $\neg \text{final } e$
 with *IH De* **have** $exes: \exists e' \ s' \ ta. extTA, P, t \vdash \langle e, (h, l) \rangle -ta \rightarrow \langle e', s' \rangle$ **by** *simp*
 then **obtain** $e' \ s' \ ta$ **where** $extTA, P, t \vdash \langle e, (h, l) \rangle -ta \rightarrow \langle e', s' \rangle$ **by** *blast*
 hence $extTA, P, t \vdash \langle \text{newA } T[e], (h, l) \rangle -ta \rightarrow \langle \text{newA } T[e'], s' \rangle$ **by** $-$ (*rule NewArrayRed*)
 thus *?thesis* **by** *blast*
 qed
 next
 case (*WTrtCast* $E \ e \ T \ U \ l$)
 have $wte: P, E, h \vdash e : T$
 and *IH: $\bigwedge l. \llbracket \mathcal{D} \ e \ \llbracket \text{dom } l \rrbracket; \neg \text{final } e \rrbracket$*
 $\implies \exists e' \ s' \ tas. extTA, P, t \vdash \langle e, (h, l) \rangle -tas \rightarrow \langle e', s' \rangle$
 and $D: \mathcal{D} (\text{Cast } U \ e) \ \llbracket \text{dom } l \rrbracket$ **by** *fact+*
 from D **have** $De: \mathcal{D} \ e \ \llbracket \text{dom } l \rrbracket$ **by** *auto*
 show *?case*
 proof (*cases* *final e*)
 assume *final e*
 thus *?thesis*
 proof (*rule* *finalE*)
 fix v
 assume $ev: e = \text{Val } v$
 with *WTrtCast* **obtain** V **where** $thvU: \text{typeof}_h \ v = \llbracket V \rrbracket$ **by** *fastforce*
 thus *?thesis*
 proof (*cases* $P \vdash V \leq U$)
 assume $P \vdash V \leq U$
 with $thvU$ **have** $extTA, P, t \vdash \langle \text{Cast } U (\text{Val } v), (h, l) \rangle -\varepsilon \rightarrow \langle \text{Val } v, (h, l) \rangle$
 by $-$ (*rule RedCast, auto*)
 with ev **show** *?thesis* **by** *blast*
 next
 assume $\neg P \vdash V \leq U$
 with $thvU$ **have** $extTA, P, t \vdash \langle \text{Cast } U (\text{Val } v), (h, l) \rangle -\varepsilon \rightarrow \langle \text{THROW ClassCast}, (h, l) \rangle$
 by $-$ (*rule RedCastFail, auto*)

```

    with ev show ?thesis by blast
  qed
next
  fix a
  assume e = Throw a
  thus ?thesis by (blast intro!: CastThrow)
  qed
next
  assume nf:  $\neg$  final e
  from IH[OF De nf] show ?thesis by (blast intro: CastRed)
  qed
next
  case (WTrtInstanceOf E e T U l)
  have wte:  $P, E, h \vdash e : T$ 
  and IH:  $\bigwedge l. \llbracket \mathcal{D} e \llbracket \text{dom } l \rrbracket; \neg \text{final } e \rrbracket$ 
     $\implies \exists e' s' \text{tas. } \text{extTA}, P, t \vdash \langle e, (h, l) \rangle \text{--tas} \rightarrow \langle e', s' \rangle$ 
  and D:  $\mathcal{D} (e \text{ instanceof } U) \llbracket \text{dom } l \rrbracket$  by fact+
  from D have De:  $\mathcal{D} e \llbracket \text{dom } l \rrbracket$  by auto
  show ?case
  proof (cases final e)
    assume final e
    thus ?thesis
    proof (rule finalE)
      fix v
      assume ev: e = Val v
      with WTrtInstanceOf obtain V where thvU: typeofh v =  $\llbracket V \rrbracket$  by fastforce
      hence extTA,  $P, t \vdash \langle (\text{Val } v) \text{ instanceof } U, (h, l) \rangle \text{--}\varepsilon \rightarrow \langle \text{Val } (\text{Bool } (v \neq \text{Null} \wedge P \vdash V \leq U)), (h, l) \rangle$ 
        by  $\text{--}(rule \text{RedInstanceOf}, auto)$ 
      with ev show ?thesis by blast
    next
      fix a
      assume e = Throw a
      thus ?thesis by (blast intro!: InstanceOfThrow)
    qed
  next
    assume nf:  $\neg$  final e
    from IH[OF De nf] show ?thesis by (blast intro: InstanceOfRed)
    qed
  next
    case WTrtVal thus ?case by (simp add:final-iff)
  next
    case WTrtVar thus ?case by (fastforce intro:RedVar simp:hyper-isin-def)
  next
    case (WTrtBinOp E e1 T1 e2 T2 bop T)
    show ?case
    proof cases
      assume final e1
      thus ?thesis
      proof (rule finalE)
        fix v1 assume [simp]: e1 = Val v1
        show ?thesis
        proof cases
          assume final e2
          thus ?thesis
        end
      end
    end
  end

```

```

proof (rule finalE)
  fix v2 assume [simp]: e2 = Val v2
  with WTrtBinOp have type: typeofh v1 = [T1] typeofh v2 = [T2] by auto
  from binop-progress[OF this ⟨P ⊢ T1 «bop» T2 : T⟩] obtain va
    where binop bop v1 v2 = [va] by blast
  thus ?thesis by(cases va)(fastforce intro: RedBinOp RedBinOpFail)+
next
  fix a assume e2 = Throw a
  thus ?thesis by(fastforce intro:BinOpThrow2)
qed
next
  assume ¬ final e2 with WTrtBinOp show ?thesis
    by simp (fast intro!:BinOpRed2)
qed
next
  fix a assume e1 = Throw a
  thus ?thesis by simp (fast intro:BinOpThrow1)
qed
next
  assume ¬ final e1 with WTrtBinOp show ?thesis
    by simp (fast intro:BinOpRed1)
qed
next
  case (WTrtLAss E V T e T')
  show ?case
  proof cases
    assume final e with WTrtLAss show ?thesis
      by(fastforce simp:final-iff intro!:RedLAss LAssThrow)
  next
    assume ¬ final e with WTrtLAss show ?thesis
      by simp (fast intro:LAssRed)
  qed
next
  case (WTrtAAcc E a T i l)
  have wte: P,E,h ⊢ a : T[]
  and wtei: P,E,h ⊢ i : Integer
  and IHa: ∧l. [[D a [dom l]; ¬ final a]]
    ⇒ ∃ e' s' tas. extTA,P,t ⊢ ⟨a,(h,l)⟩ -tas→ ⟨e',s'⟩
  and IHi: ∧l. [[D i [dom l]; ¬ final i]]
    ⇒ ∃ e' s' tas. extTA,P,t ⊢ ⟨i,(h,l)⟩ -tas→ ⟨e',s'⟩
  and D: D (a[i]) [dom l] by fact+
  have ref: is-refT (T[]) by simp
  from D have Da: D a [dom l] by simp
  show ?case
  proof (cases final a)
    assume final a
    with wte ref show ?case
    proof (cases rule: finalRefE)
      case null
      thus ?thesis
    proof (cases final i)
      assume final i
      thus ?thesis
    proof (rule finalE)

```

```

fix v
assume i: i = Val v
have extTA,P,t ⊢ ⟨null[Val v], (h, l)⟩ −ε→ ⟨THROW NullPointer, (h,l)⟩
  by(rule RedAAccNull)
with i null show ?thesis by blast
next
fix ex
assume i: i = Throw ex
have extTA,P,t ⊢ ⟨null[Throw ex], (h, l)⟩ −ε→ ⟨Throw ex, (h,l)⟩
  by(rule AAccThrow2)
with i null show ?thesis by blast
qed
next
assume ¬ final i
from WTrtAAcc null show ?thesis
  by simp
qed
next
case (Array ad U)
with wte obtain n where ty: typeof-addr h ad = [Array-type U n] by auto
thus ?thesis
proof (cases final i)
assume final i
thus ?thesis
proof(rule finalE)
  fix v
  assume [simp]: i = Val v
  with wtei have typeofh v = Some Integer by fastforce
  hence ∃ i. v = Intg i by fastforce
  then obtain i where [simp]: v = Intg i by blast
  thus ?thesis
  proof (cases i < s 0 ∨ sint i ≥ int n)
    case True
    with WTrtAAcc Array ty show ?thesis by (fastforce intro: RedAAccBounds)
  next
  case False
  then have nat (sint i) < n
    by (simp add: not-le word-sless-alt nat-less-iff)
  with ty have P,h ⊢ ad@ACell (nat (sint i)) : U by(auto intro!: addr-loc-type.intros)
  from heap-read-total[OF hconf this]
  obtain v where heap-read h ad (ACell (nat (sint i))) v by blast
  with False Array ty show ?thesis by(fastforce intro: RedAAcc)
  qed
next
fix ex
assume i = Throw ex
with WTrtAAcc Array show ?thesis by (fastforce intro: AAccThrow2)
qed
next
assume ¬ final i
with WTrtAAcc Array show ?thesis by (fastforce intro: AAccRed2)
qed
next
fix ex

```

```

    assume a = Throw ex
    with WTrtAAcc show ?thesis by (fastforce intro: AAccThrow1)
  qed simp
next
  assume ¬ final a
  with WTrtAAcc show ?thesis by (fastforce intro: AAccRed1)
  qed
next
case (WTrtAAccNT E a i T l)
have wte: P,E,h ⊢ a : NT
and wtei: P,E,h ⊢ i : Integer
and IHa: ∧l. [[D a [dom l]; ¬ final a]]
    ⇒ ∃ e' s' tas. extTA,P,t ⊢ ⟨a,(h,l)⟩ -tas→ ⟨e',s'⟩
and IHi: ∧l. [[D i [dom l]; ¬ final i]]
    ⇒ ∃ e' s' tas. extTA,P,t ⊢ ⟨i,(h,l)⟩ -tas→ ⟨e',s'⟩ by fact+
have ref: is-refT NT by simp
with WTrtAAccNT have Da: D a [dom l] by simp
thus ?case
proof (cases final a)
  case True
  with wte ref show ?thesis
  proof (cases rule: finalRefE)
    case null
    thus ?thesis
  proof (cases final i)
    assume final i
    thus ?thesis
  proof (rule finalE)
    fix v
    assume i: i = Val v
    have extTA,P,t ⊢ ⟨null[Val v], (h, l)⟩ -ε→ ⟨THROW NullPointer, (h,l)⟩
      by (rule RedAAccNull)
    with WTrtAAccNT ⟨final a⟩ null ⟨final i⟩ i show ?thesis by blast
  next
  fix ex
  assume i: i = Throw ex
  have extTA,P,t ⊢ ⟨null[Throw ex], (h, l)⟩ -ε→ ⟨Throw ex, (h,l)⟩
    by(rule AAccThrow2)
  with WTrtAAccNT ⟨final a⟩ null ⟨final i⟩ i show ?thesis by blast
  qed
  next
  assume ¬ final i
  with WTrtAAccNT null show ?thesis
  by(fastforce intro: AAccRed2)
  qed
  next
  case Throw thus ?thesis by (fastforce intro: AAccThrow1)
  qed simp-all
next
case False
with WTrtAAccNT Da show ?thesis by (fastforce intro: AAccRed1)
  qed
next
case (WTrtAAss E a T i e T' l)

```

```

have wta: P,E,h ⊢ a : T[]
and wti: P,E,h ⊢ i : Integer
and wte: P,E,h ⊢ e : T'
and D:  $\mathcal{D} (a[i] := e) [dom\ l]$ 
and IH1:  $\bigwedge l. [\mathcal{D} a [dom\ l]; \neg final\ a] \implies \exists e' s' tas. extTA,P,t \vdash \langle a,(h, l) \rangle -tas \rightarrow \langle e',s' \rangle$ 
and IH2:  $\bigwedge l. [\mathcal{D} i [dom\ l]; \neg final\ i] \implies \exists e' s' tas. extTA,P,t \vdash \langle i,(h, l) \rangle -tas \rightarrow \langle e',s' \rangle$ 
and IH3:  $\bigwedge l. [\mathcal{D} e [dom\ l]; \neg final\ e] \implies \exists e' s' tas. extTA,P,t \vdash \langle e,(h, l) \rangle -tas \rightarrow \langle e',s' \rangle$  by
fact+
have ref: is-refT (T[]) by simp
show ?case
proof (cases final a)
  assume fa: final a
  with wta ref show ?thesis
  proof(cases rule: finalRefE)
    case null
    show ?thesis
    proof(cases final i)
      assume final i
      thus ?thesis
      proof (rule finalE)
        fix v
        assume i: i = Val v
        with wti have typeofh v = Some Integer by fastforce
        then obtain idx where v = Intg idx by fastforce
        thus ?thesis
        proof (cases final e)
          assume final e
          thus ?thesis
          proof (rule finalE)
            fix w
            assume e = Val w
            with WTrtAAss null show ?thesis by (fastforce intro: RedAAssNull)
          next
            fix ex
            assume e = Throw ex
            with WTrtAAss null show ?thesis by (fastforce intro: AAssThrow3)
          qed
        next
          assume  $\neg final\ e$ 
          with WTrtAAss null show ?thesis by (fastforce intro: AAssRed3)
        qed
      next
        fix ex
        assume i = Throw ex
        with WTrtAAss null show ?thesis by (fastforce intro: AAssThrow2)
      qed
    next
      assume  $\neg final\ i$ 
      with WTrtAAss null show ?thesis by (fastforce intro: AAssRed2)
    qed
  next
    case (Array ad U)
    with wta obtain n where ty: typeof-addr h ad = [Array-type U n] by auto
    thus ?thesis

```



```

proof (cases final i)
  assume fi: final i
  thus ?thesis
proof (rule finalE)
  fix v
  assume ivalv: i = Val v
  with wti have typeofh v = Some Integer by fastforce
  then obtain idx where vidx: v = Intg idx by fastforce
  thus ?thesis
proof (cases final e)
  assume fe: final e
  thus ?thesis
proof(rule finalE)
  fix w
  assume evalw: e = Val w
  show ?thesis
proof(cases idx < s 0 ∨ sint idx ≥ int n)
  case True
  with ty evalw Array ivalv vidx show ?thesis by(fastforce intro: RedAAssBounds)
next
  case False
  then have nat (sint idx) < n
  by (simp add: not-le word-sless-alt nat-less-iff)
  with ty have adal: P,h ⊢ ad@ACell (nat (sint idx)) : U
  by(auto intro!: addr-loc-type.intros)
  show ?thesis
proof(cases P ⊢ T' ≤ U)
  case True
  with wte evalw have P,h ⊢ w :≤ U
  by(auto simp add: conf-def)
  from heap-write-total[OF hconf adal this]
  obtain h' where h': heap-write h ad (ACell (nat (sint idx))) w h' ..
  with ty False vidx ivalv evalw Array wte True
  show ?thesis by(fastforce intro: RedAAss)
next
  case False
  with ty vidx ivalv evalw Array wte <¬ (idx < s 0 ∨ sint idx ≥ int n)>
  show ?thesis by(fastforce intro: RedAAssStore)
  qed
qed
next
  fix ex
  assume e = Throw ex
  with Array ivalv show ?thesis by (fastforce intro: AAssThrow3)
  qed
next
  assume ¬ final e
  with WTrtAAss Array fi ivalv vidx show ?thesis by (fastforce intro: AAssRed3)
  qed
next
  fix ex
  assume i = Throw ex
  with WTrtAAss Array show ?thesis by (fastforce intro: AAssThrow2)
  qed

```

```

next
  assume  $\neg$  final i
  with WTrtAAss Array show ?thesis by (fastforce intro: AAssRed2)
qed
next
  fix ex
  assume a = Throw ex
  with WTrtAAss show ?thesis by (fastforce intro: AAssThrow1)
qed simp-all
next
  assume  $\neg$  final a
  with WTrtAAss show ?thesis by (fastforce intro: AAssRed1)
qed
next
  case (WTrtAAssNT E a i e T' l)
  have wta:  $P, E, h \vdash a : NT$ 
  and wti:  $P, E, h \vdash i : Integer$ 
  and wte:  $P, E, h \vdash e : T'$ 
  and D:  $\mathcal{D} (a[i] := e) [dom\ l]$ 
  and IH1:  $\bigwedge l. [\mathcal{D} a [dom\ l]; \neg final\ a] \implies \exists e' s' tas. extTA, P, t \vdash \langle a, (h, l) \rangle -tas \rightarrow \langle e', s' \rangle$ 
  and IH2:  $\bigwedge l. [\mathcal{D} i [dom\ l]; \neg final\ i] \implies \exists e' s' tas. extTA, P, t \vdash \langle i, (h, l) \rangle -tas \rightarrow \langle e', s' \rangle$ 
  and IH3:  $\bigwedge l. [\mathcal{D} e [dom\ l]; \neg final\ e] \implies \exists e' s' tas. extTA, P, t \vdash \langle e, (h, l) \rangle -tas \rightarrow \langle e', s' \rangle$  by
fact+
  have ref: is-refT NT by simp
  show ?case
  proof (cases final a)
    assume fa: final a
    show ?case
    proof (cases final i)
      assume fi: final i
      show ?case
      proof (cases final e)
        assume fe: final e
        with WTrtAAssNT fa fi show ?thesis
        by (fastforce simp: final-iff intro: RedAAssNull AAssThrow1 AAssThrow2 AAssThrow3)
      next
        assume  $\neg$  final e
        with WTrtAAssNT fa fi show ?thesis
        by (fastforce simp: final-iff intro!: AAssRed3 AAssThrow1 AAssThrow2)
      qed
    next
      assume  $\neg$  final i
      with WTrtAAssNT fa show ?thesis
      by (fastforce simp: final-iff intro!: AAssRed2 AAssThrow1)
    qed
  next
    assume  $\neg$  final a
    with WTrtAAssNT show ?thesis by (fastforce simp: final-iff intro!: AAssRed1)
  qed
next
  case (WTrtALength E a T l)
  show ?case
  proof (cases final a)
    case True

```

```

note  $wta = \langle P, E, h \vdash a : T[] \rangle$ 
thus ?thesis
proof(rule finalRefE[OF - - True])
  show is-refT (T[]) by simp
next
  assume  $a = null$ 
  thus ?thesis by(fastforce intro: RedALengthNull)
next
  fix ad U
  assume  $a = addr\ ad$  and  $T[] = U[]$ 
  with wta show ?thesis by(fastforce intro: RedALength)
next
  fix ad
  assume  $a = Throw\ ad$ 
  thus ?thesis by (fastforce intro: ALengthThrow)
qed simp
next
  case False
  from  $\langle \mathcal{D} (a \cdot length) [dom\ l] \rangle$  have  $\mathcal{D} a [dom\ l]$  by simp
  with False  $\langle \llbracket \mathcal{D} a [dom\ l]; \neg final\ a \rrbracket \implies \exists e' s' ta. extTA, P, t \vdash \langle a, (h, l) \rangle -ta \rightarrow \langle e', s' \rangle \rangle$ 
  obtain  $e' s' ta$  where  $extTA, P, t \vdash \langle a, (h, l) \rangle -ta \rightarrow \langle e', s' \rangle$  by blast
  thus ?thesis by(blast intro: ALengthRed)
qed
next
  case (WTrtALengthNT E a T l)
  show ?case
  proof(cases final a)
    case True
    note  $wta = \langle P, E, h \vdash a : NT \rangle$ 
    thus ?thesis
    proof(rule finalRefE[OF - - True])
      show is-refT NT by simp
    next
      assume  $a = null$ 
      thus ?thesis by(blast intro: RedALengthNull)
    next
      fix ad
      assume  $a = Throw\ ad$ 
      thus ?thesis by(blast intro: ALengthThrow)
    qed simp-all
  next
    case False
    from  $\langle \mathcal{D} (a \cdot length) [dom\ l] \rangle$  have  $\mathcal{D} a [dom\ l]$  by simp
    with False  $\langle \llbracket \mathcal{D} a [dom\ l]; \neg final\ a \rrbracket \implies \exists e' s' ta. extTA, P, t \vdash \langle a, (h, l) \rangle -ta \rightarrow \langle e', s' \rangle \rangle$ 
    obtain  $e' s' ta$  where  $extTA, P, t \vdash \langle a, (h, l) \rangle -ta \rightarrow \langle e', s' \rangle$  by blast
    thus ?thesis by(blast intro: ALengthRed)
  qed
next
  case (WTrtFAcc E e U C F T fm D l)
  have  $wte: P, E, h \vdash e : U$ 
  and  $icto: class\ type\ of' U = [C]$ 
  and  $field: P \vdash C$  has  $F:T (fm)$  in  $D$  by fact+
  show ?case
  proof cases

```

```

assume final e
with wte icto show ?thesis
proof (cases rule: final-addrE)
  case (addr a)
    with wte obtain hU where ty: typeof-addr h a = [hU] U = ty-of-htype hU by auto
    with icto field have P, h ⊢ a@CField D F : T by(auto intro: addr-loc-type.intros)
    from heap-read-total[OF hconf this]
    obtain v where heap-read h a (CField D F) v by blast
    with addr ty show ?thesis by(fastforce intro: RedFAcc)
  next
    fix a assume e = Throw a
    thus ?thesis by(fastforce intro: FAccThrow)
  qed
next
  assume  $\neg$  final e with WTrtFAcc show ?thesis
  by(fastforce intro!: FAccRed)
  qed
next
  case (WTrtFAccNT E e F D T l)
  show ?case
  proof cases
    assume final e — e is null or throw
    with WTrtFAccNT show ?thesis
    by(fastforce simp: final-iff intro: RedFAccNull FAccThrow)
  next
    assume  $\neg$  final e — e reduces by IH
    with WTrtFAccNT show ?thesis by simp (fast intro: FAccRed)
  qed
next
  case (WTrtFAss E e1 U C F T fm D e2 T2 l)
  have wte1: P, E, h ⊢ e1 : U
  and icto: class-type-of' U = [C]
  and field: P ⊢ C has F:T (fm) in D by fact+
  show ?case
  proof cases
    assume final e1
    with wte1 icto show ?thesis
    proof (rule final-addrE)
      fix a assume e1: e1 = addr a
      show ?thesis
    proof cases
      assume final e2
      thus ?thesis
    proof (rule finalE)
      fix v assume e2: e2 = Val v
      from wte1 field icto e1 have adal: P, h ⊢ a@CField D F : T
      by(auto intro: addr-loc-type.intros)
      from e2  $\langle P \vdash T2 \leq T \rangle$   $\langle P, E, h \vdash e2 : T2 \rangle$ 
      have P, h ⊢ v : ≤ T by(auto simp add: conf-def)
      from heap-write-total[OF hconf adal this] obtain h'
      where heap-write h a (CField D F) v h' ..
      with wte1 field e1 e2 show ?thesis
      by(fastforce intro: RedFAss)
    next

```

```

    fix a assume e2 = Throw a
    thus ?thesis using e1 by(fastforce intro:FAssThrow2)
  qed
next
  assume  $\neg$  final e2 with WTrtFAss <final e1> e1 show ?thesis
  by simp (fast intro!:FAssRed2)
  qed
next
  fix a assume e1 = Throw a
  thus ?thesis by(fastforce intro:FAssThrow1)
  qed
next
  assume  $\neg$  final e1 with WTrtFAss show ?thesis
  by(simp del: split-paired-Ex)(blast intro!:FAssRed1)
  qed
next
  case (WTrtFAssNT E e1 e2 T2 F D l)
  show ?case
  proof cases
    assume final e1 — e1 is null or throw
    show ?thesis
    proof cases
      assume final e2 — e2 is Val or throw
      with WTrtFAssNT <final e1> show ?thesis
      by(fastforce simp:final-iff intro: RedFAssNull FAssThrow1 FAssThrow2)
    next
      assume  $\neg$  final e2 — e2 reduces by IH
      with WTrtFAssNT <final e1> show ?thesis
      by (fastforce simp:final-iff intro!:FAssRed2 FAssThrow1)
    qed
  next
    assume  $\neg$  final e1 — e1 reduces by IH
    with WTrtFAssNT show ?thesis by (fastforce intro:FAssRed1)
  qed
next
  case (WTrtCAS E e1 U C F T fm D e2 T2 e3 T3)
  show ?case
  proof(cases final e1)
    case e1: True
    with WTrtCAS.hyps(1,3) show ?thesis
    proof(rule final-addrE)
      fix a
      assume e1: e1 = addr a
      with WTrtCAS.hyps(1) obtain hU
      where ty: typeof-addr h a = [hU] U = ty-of-htype hU by auto
      with WTrtCAS.hyps(3,4) have adal: P,h $\vdash$  a@CFIELD D F : T by(auto intro: addr-loc-type.intros)
      from heap-read-total[OF hconf this]
      obtain v where v: heap-read h a (CFIELD D F) v by blast
      show ?thesis
    proof(cases final e2)
      case e2: True
      show ?thesis
    proof(cases final e3)
      case e3: True

```

```

consider (Val2) v2 where e2 = Val v2 | (Throw2) a2 where e2 = Throw a2
  using e2 by(auto simp add: final-iff)
then show ?thesis
proof(cases)
  case Val2
    consider (Succeed) v3 where e3 = Val v3 v2 = v
      | (Fail) v3 where e3 = Val v3 v2 ≠ v
      | (Throw3) a3 where e3 = Throw a3
    using e3 by(auto simp add: final-iff)
    then show ?thesis
    proof cases
      case Succeed
        with WTrtCAS.hyps(9,11) adal have P,h ⊢ v3 :≤ T by(auto simp add: conf-def)
        from heap-write-total[OF hconf adal this] obtain h'
          where heap-write h a (CField D F) v3 h' ..
        with Val2 e1 v Succeed show ?thesis
          by(auto intro: RedCASSucceed simp del: split-paired-Ex)
      next
        case Fail
          with Val2 e1 v show ?thesis
            by(auto intro: RedCASFail simp del: split-paired-Ex)
      next
        case Throw3
          then show ?thesis using e1 Val2 by(auto intro: CASThrow3 simp del: split-paired-Ex)
    qed
  next
    case Throw2
      then show ?thesis using e1 by(auto intro: CASThrow2 simp del: split-paired-Ex)
    qed
  next
    case False
      with WTrtCAS e1 e2 show ?thesis
        by(fastforce simp del: split-paired-Ex simp add: final-iff intro: CASRed3 CASThrow2)
    qed
  next
    case False
      with WTrtCAS e1 show ?thesis
        by(fastforce intro: CASRed2 CASThrow2 simp del: split-paired-Ex)
    qed
  qed(fastforce intro: CASThrow)
next
  case False
    then show ?thesis using WTrtCAS by(fastforce intro: CASRed1)
  qed
next
  case (WTrtCASNT E e1 e2 T2 e3 T3 D F)
  note [simp del] = split-paired-Ex
  show ?case
  proof(cases final e1)
    case e1: True
      show ?thesis
    proof(cases final e2)
      case e2: True
        show ?thesis

```

```

proof(cases final e3)
  case True
  with e1 e2 WTrtCASNT show ?thesis
    by(fastforce simp add: final-iff intro: CASNull CASThrow CASThrow2 CASThrow3)
next
  case False
  with e1 e2 WTrtCASNT show ?thesis
    by(fastforce simp add: final-iff intro: CASRed3 CASThrow CASThrow2)
qed
next
  case False
  with e1 WTrtCASNT show ?thesis
    by(fastforce simp add: final-iff intro: CASRed2 CASThrow)
qed
next
  case False
  with WTrtCASNT show ?thesis
    by(fastforce simp add: final-iff intro: CASRed1)
qed
next
  case (WTrtCall E e U C M Ts T meth D es Ts' l)
  have wte: P,E,h ⊢ e : U
    and icto: class-type-of' U = [C] by fact+
  have IHe:  $\bigwedge l. [\mathcal{D} e \mid \text{dom } l]; \neg \text{final } e$ 
     $\implies \exists e' s' \text{tas. extTA,P,t} \vdash \langle e, (h, l) \rangle \text{-tas} \rightarrow \langle e', s' \rangle$  by fact
  have sees: P ⊢ C sees M: Ts → T = meth in D by fact
  have wtes: P,E,h ⊢ es [:] Ts' by fact
  have IHes:  $\bigwedge l. [\mathcal{D} s \text{ es} \mid \text{dom } l]; \neg \text{finals } \text{es}$   $\implies \exists es' s' \text{ta. extTA,P,t} \vdash \langle es, (h, l) \rangle \text{[-ta} \rightarrow] \langle es', s' \rangle$ 
by fact
  have subtype: P ⊢ Ts' [≤] Ts by fact
  have dae:  $\mathcal{D} (e.M(\text{es})) \mid \text{dom } l$  by fact
  show ?case
  proof(cases final e)
    assume fine: final e
    with wte icto show ?thesis
    proof (rule final-addrE)
      fix a assume e-addr: e = addr a
      show ?thesis
    proof(cases  $\exists vs. \text{es} = \text{map Val } vs$ )
      assume es:  $\exists vs. \text{es} = \text{map Val } vs$ 
      from wte e-addr obtain hU where ha: typeof-addr h a = [hU] U = ty-of-htype hU by(auto)
      have length es = length Ts' using wtes by(auto simp add: WTrts-conv-list-all2 dest: list-all2-lengthD)
      moreover from subtype have length Ts' = length Ts by(auto dest: list-all2-lengthD)
      ultimately have esTs: length es = length Ts by(auto)
      show ?thesis
    proof(cases meth)
      case (Some pnsbody)
        with esTs e-addr ha sees subtype es sees-wf-mdecl[OF wf sees] icto
        show ?thesis by(cases pnsbody) (fastforce intro!: RedCall simp:list-all2-iff wf-mdecl-def)
    next
      case None
      with sees wf have D.M(Ts) :: T by(auto intro: sees-wf-native)
      moreover from es obtain vs where vs: es = map Val vs ..
      with wtes have tyes: map typeofh vs = map Some Ts' by simp

```

```

with ha ⟨D·M(Ts) :: T⟩ icto sees None
have P,h ⊢ a·M(vs) : T using subtype by(auto simp add: external-WT'-iff)
from external-call-progress[OF wf this hconf, of t] obtain ta va h'
  where P,t ⊢ ⟨a·M(vs), h⟩ -ta→ext ⟨va, h'⟩ by blast
thus ?thesis using ha icto None sees vs e-addr
  by(fastforce intro: RedCallExternal simp del: split-paired-Ex)
qed
next
assume ¬(∃ vs. es = map Val vs)
hence not-all-Val: ¬(∀ e ∈ set es. ∃ v. e = Val v)
  by(simp add:ex-map-conv)
let ?ves = takeWhile (λe. ∃ v. e = Val v) es
let ?rest = dropWhile (λe. ∃ v. e = Val v) es
let ?ex = hd ?rest let ?rst = tl ?rest
from not-all-Val have nonempty: ?rest ≠ [] by auto
hence es: es = ?ves @ ?ex # ?rst by simp
have ∀ e ∈ set ?ves. ∃ v. e = Val v by(fastforce dest:set-takeWhileD)
then obtain vs where ves: ?ves = map Val vs
  using ex-map-conv by blast
show ?thesis
proof cases
  assume final ?ex
  moreover from nonempty have ¬(∃ v. ?ex = Val v)
    by(auto simp:neq-Nil-conv simp del:dropWhile-eq-Nil-conv)
    (simp add:dropWhile-eq-Cons-conv)
  ultimately obtain b where ex-Throw: ?ex = Throw b
    by(fast elim!:finalE)
  show ?thesis using e-addr es ex-Throw ves
    by(fastforce intro:CallThrowParams)
next
assume not-fin: ¬ final ?ex
have finals es = finals(?ves @ ?ex # ?rst) using es
  by(rule arg-cong)
also have ... = finals(?ex # ?rst) using ves by simp
finally have finals es = finals(?ex # ?rst) .
hence ¬ finals es using not-finals-ConsI[OF not-fin] by blast
thus ?thesis using e-addr dae IHes by(fastforce intro!:CallParams)
qed
qed
next
fix a assume e = Throw a
thus ?thesis by(fast intro!:CallThrowObj)
qed
next
assume ¬ final e
with WTrtCall show ?thesis by(simp del: split-paired-Ex)(blast intro!:CallObj)
qed
next
case (WTrtCallNT E e es Ts M T l)
have wte: P,E,h ⊢ e : NT by fact
have IHes: ∧l. [ [ D e [dom l]; ¬ final e ]
  ⇒ ∃ e' s' tas. extTA,P,t ⊢ ⟨e,(h, l)⟩ -tas→ ⟨e',s'⟩ by fact
have IHes: ∧l. [Ds es [dom l]; ¬ finals es] ⇒ ∃ es' s' ta. extTA,P,t ⊢ ⟨es,(h, l)⟩ [-ta→] ⟨es',s'⟩
by fact

```



```

have wtes:  $P, E, h \vdash es \text{ [:] } Ts$  by fact
have dae:  $\mathcal{D} (e \cdot M(es)) \text{ [dom } l]$  by fact
show ?case
proof(cases final e)
  assume final e
  moreover
  { fix  $v$  assume  $e = Val\ v$ 
    hence  $e = null$  using WTrtCallNT by simp
    have ?case
    proof cases
      assume finals es
      moreover
      { fix  $vs$  assume  $es = map\ Val\ vs$ 
        with WTrtCallNT  $\langle e = null \rangle \langle finals\ es \rangle$  have ?thesis by(fastforce intro: RedCallNull) }
      moreover
      { fix  $vs\ a\ es'$  assume  $es = map\ Val\ vs @ Throw\ a \# es'$ 
        with WTrtCallNT  $\langle e = null \rangle \langle finals\ es \rangle$  have ?thesis by(fastforce intro: CallThrowParams) }
    }
  }
  ultimately show ?thesis by(fastforce simp:finals-iff)
next
  assume  $\neg finals\ es$  —  $es$  reduces by IH
  with WTrtCallNT  $\langle e = null \rangle$  show ?thesis by(fastforce intro: CallParams)
qed
}
moreover
{ fix  $a$  assume  $e = Throw\ a$ 
  with WTrtCallNT have ?case by(fastforce intro: CallThrowObj) }
ultimately show ?thesis by(fastforce simp:final-iff)
next
  assume  $\neg final\ e$  —  $e$  reduces by IH
  with WTrtCallNT show ?thesis by (fastforce intro: CallObj)
qed
next
case (WTrtBlock E V T e T' vo l)
have  $IH: \bigwedge l. \llbracket \mathcal{D}\ e \text{ [dom } l]; \neg final\ e \rrbracket$ 
   $\implies \exists e'\ s'\ tas. extTA, P, t \vdash \langle e, (h, l) \rangle -tas \rightarrow \langle e', s' \rangle$ 
  and  $D: \mathcal{D} \{ V:T=vo; e \} \text{ [dom } l]$  by fact+
show ?case
proof cases
  assume final e
  thus ?thesis
  proof (rule finalE)
    fix  $v$  assume  $e = Val\ v$  thus ?thesis by(fast intro: RedBlock)
  next
    fix  $a$  assume  $e = Throw\ a$ 
    thus ?thesis by(fast intro: BlockThrow)
  qed
next
  assume not-fin:  $\neg final\ e$ 
  from  $D$  have  $De: \mathcal{D}\ e \text{ [dom}(l(V:=vo))]$  by(auto simp add: hyperset-defs)
  from  $IH[OF\ De\ not-fin]$ 
  obtain  $h'\ l'\ e'\ tas$  where  $red: extTA, P, t \vdash \langle e, (h, l(V:=vo)) \rangle -tas \rightarrow \langle e', (h', l') \rangle$ 
    by auto
  thus ?thesis by(blast intro: BlockRed)

```

```

qed
next
case (WTrtSynchronized E o' T e T' l)
note wto =  $\langle P, E, h \vdash o' : T \rangle$ 
note IHe =  $\langle \bigwedge l. [\mathcal{D} e \mid \text{dom } l]; \neg \text{final } e ] \implies \exists e' s' \text{tas. extTA}, P, t \vdash \langle e, (h, l) \rangle -\text{tas} \rightarrow \langle e', s' \rangle$ 
note wte =  $\langle P, E, h \vdash e : T' \rangle$ 
note IHo =  $\langle \bigwedge l. [\mathcal{D} o' \mid \text{dom } l]; \neg \text{final } o' ] \implies \exists e' s' \text{tas. extTA}, P, t \vdash \langle o', (h, l) \rangle -\text{tas} \rightarrow \langle e', s' \rangle$ 
note refT =  $\langle \text{is-refT } T \rangle$ 
note dae =  $\langle \mathcal{D} (\text{sync}(o') e) \mid \text{dom } l \rangle$ 
show ?case
proof(cases final o')
  assume fino: final o'
  thus ?thesis
  proof (rule finalE)
    fix v
    assume oval: o' = Val v
    with wto refT show ?thesis
    proof(cases v)
      assume vnull: v = Null
      with oval vnull show ?thesis
      by(fastforce intro: SynchronizedNull)
    next
    fix ad
    assume vaddr: v = Addr ad
    thus ?thesis using oval
    by(fastforce intro: LockSynchronized)
    qed(auto elim: refTE)
  next
  fix a
  assume othrow: o' = Throw a
  thus ?thesis by(fastforce intro: SynchronizedThrow1)
  qed
next
  assume nfino:  $\neg \text{final } o'$ 
  with dae IHo show ?case by(fastforce intro: SynchronizedRed1)
  qed
next
case (WTrtInSynchronized E a T e T' l)
show ?case
proof(cases final e)
  case True thus ?thesis
  by(fastforce elim!: finalE intro: UnlockSynchronized SynchronizedThrow2)
next
  case False
  moreover from  $\langle \mathcal{D} (\text{insync}(a) e) \mid \text{dom } l \rangle$  have  $\mathcal{D} e \mid \text{dom } l$  by simp
  moreover note IHe =  $\langle \bigwedge l. [\mathcal{D} e \mid \text{dom } l]; \neg \text{final } e ] \implies \exists e' s' \text{tas. extTA}, P, t \vdash \langle e, (h, l) \rangle -\text{tas} \rightarrow \langle e', s' \rangle$ 
  ultimately show ?thesis by(fastforce intro: SynchronizedRed2)
  qed
next
case (WTrtSeq E e1 T1 e2 T2 l)
show ?case
proof cases
  assume final e1

```

```

thus ?thesis
  by(fast elim:finalE intro:intro:RedSeq SeqThrow)
next
  assume  $\neg$  final e1 with WTrtSeq show ?thesis
    by(simp del: split-paired-Ex)(blast intro!:SeqRed)
qed
next
case (WTrtCond E e e1 T1 e2 T2 T l)
have wt: P,E,h  $\vdash$  e : Boolean by fact
show ?case
proof cases
  assume final e
  thus ?thesis
  proof (rule finalE)
    fix v assume val: e = Val v
    then obtain b where v: v = Bool b using wt by auto
    show ?thesis
    proof (cases b)
      case True with val v show ?thesis by(fastforce intro:RedCondT)
    next
      case False with val v show ?thesis by(fastforce intro:RedCondF)
    qed
  next
    fix a assume e = Throw a
    thus ?thesis by(fast intro:CondThrow)
  qed
next
  assume  $\neg$  final e with WTrtCond show ?thesis
    by simp (fast intro:CondRed)
  qed
next
case WTrtWhile show ?case by(fast intro:RedWhile)
next
case (WTrtThrow E e T T' l)
show ?case
proof cases
  assume final e — Then e must be throw or null
  thus ?thesis
  proof(induct rule: finalE)
    case (Val v)
    with  $\langle P \vdash T \leq \text{Class Throwable} \rangle \langle \neg \text{final } (\text{throw } e) \rangle \langle P, E, h \vdash e : T \rangle$ 
    have v = Null by(cases v)(auto simp add: final-iff widen-Class)
    thus ?thesis using Val by(fastforce intro: RedThrowNull)
  next
    case (Throw a)
    thus ?thesis by(fastforce intro: ThrowThrow)
  qed
next
  assume  $\neg$  final e — Then e must reduce
  with WTrtThrow show ?thesis by simp (blast intro:ThrowRed)
  qed
next
case (WTrtTry E e1 T1 V C e2 T2 l)
have wt1: P,E,h  $\vdash$  e1 : T1 by fact

```

```

show ?case
proof cases
  assume final e1
  thus ?thesis
  proof (rule finalE)
    fix v assume e1 = Val v
    thus ?thesis by(fast intro:RedTry)
  next
    fix a
    assume e1-Throw: e1 = Throw a
    with wt1 obtain D where ha: typeof-addr h a = [Class-type D]
    by(auto simp add: widen-Class)
    thus ?thesis using e1-Throw
    by(cases P ⊢ D ≤* C)(fastforce intro:RedTryCatch RedTryFail)+
  qed
next
  assume ¬ final e1
  with WTrtTry show ?thesis by simp (fast intro:TryRed)
qed
next
  case WTrtNil thus ?case by simp
next
  case (WTrtCons E e T es Ts)
  have IHe:  $\bigwedge l. \llbracket \mathcal{D} e \lfloor \text{dom } l \rfloor; \neg \text{final } e \rrbracket$ 
     $\implies \exists e' s' ta. \text{extTA},P,t \vdash \langle e,(h,l) \rangle -ta \rightarrow \langle e',s' \rangle$ 
  and IHes:  $\bigwedge l. \llbracket \mathcal{D} s \text{ es } \lfloor \text{dom } l \rfloor; \neg \text{finals } es \rrbracket$ 
     $\implies \exists es' s' ta. \text{extTA},P,t \vdash \langle es,(h,l) \rangle [-ta \rightarrow] \langle es',s' \rangle$ 
  and D:  $\mathcal{D} s (e \# es) \lfloor \text{dom } l \rfloor$  and not-fins:  $\neg \text{finals}(e \# es)$  by fact+
  have De:  $\mathcal{D} e \lfloor \text{dom } l \rfloor$  and Des:  $\mathcal{D} s \text{ es } (\lfloor \text{dom } l \rfloor \sqcup \mathcal{A} e)$ 
    using D by auto
  show ?case
  proof cases
    assume final e
    thus ?thesis
    proof (rule finalE)
      fix v assume e: e = Val v
      hence Des':  $\mathcal{D} s \text{ es } \lfloor \text{dom } l \rfloor$  using De Des by auto
      have not-fins-tl:  $\neg \text{finals } es$  using not-fins e by simp
      show ?thesis using e IHes[OF Des' not-fins-tl]
      by (blast intro!:ListRed2)
    next
      fix a assume e = Throw a
      hence False using not-fins by simp
      thus ?thesis ..
    qed
  next
    assume ¬ final e
    with IHe[OF De] show ?thesis by(fast intro!:ListRed1)
  qed
qed
end

```

4.12 Preservation of definite assignment

theory *DefAssPreservation*

imports

DefAss

JWellForm

SmallStep

begin

Preservation of definite assignment more complex and requires a few lemmas first.

lemma *D-extRetJ* [*intro!*]: $\mathcal{D} e A \implies \mathcal{D} (\text{extRet2J } e \text{ va}) A$

by (*cases va*) *simp-all*

lemma *blocks-defass* [*iff*]: $\bigwedge A. [\text{length } Vs = \text{length } Ts; \text{length } vs = \text{length } Ts] \implies$
 $\mathcal{D} (\text{blocks } Vs \ Ts \ vs \ e) A = \mathcal{D} e (A \sqcup [\text{set } Vs])$

context *J-heap-base* **begin**

lemma *red-lA-incr*: $\text{extTA}, P, t \vdash \langle e, s \rangle \text{ -ta-} \rightarrow \langle e', s' \rangle \implies [\text{dom } (lcl \ s)] \sqcup \mathcal{A} e \sqsubseteq [\text{dom } (lcl \ s')] \sqcup \mathcal{A} e'$

and *reds-lA-incr*: $\text{extTA}, P, t \vdash \langle es, s \rangle \text{ [-ta-} \rightarrow \langle es', s' \rangle \implies [\text{dom } (lcl \ s)] \sqcup \mathcal{A} s \ es \sqsubseteq [\text{dom } (lcl \ s')] \sqcup$
 $\mathcal{A} s \ es'$

apply (*induct rule:red-reds.inducts*)

apply (*simp-all del:fun-upd-apply add:hyperset-defs*)

apply *blast*

apply *blast*

apply *blast*

apply *blast*

apply *blast*

apply *blast*

apply *blast*

apply *blast*

apply *blast*

apply *blast*

apply *blast*

apply *blast*

apply *blast*

apply *blast*

apply *blast*

apply (*force split: if-split-asm*)

apply *blast*

apply *blast*

apply *blast*

apply *blast*

apply *blast*

apply (*blast dest: red-lcl-incr*)

apply (*blast dest: red-lcl-incr*)

by *blast+*

end

Now preservation of definite assignment.

declare *hyperUn-comm* [*simp del*]

declare *hyperUn-leftComm* [*simp del*]

context *J-heap-base* **begin**

```

lemma assumes wf: wf-J-prog P
  shows red-preserves-defass: extTA,P,t ⊢ ⟨e,s⟩ -ta→ ⟨e',s'⟩ ⇒ D e [dom (lcl s)] ⇒ D e' [dom
(lcl s')]
  and reds-preserves-defass: extTA,P,t ⊢ ⟨es,s⟩ [-ta→] ⟨es',s'⟩ ⇒ Ds es [dom (lcl s)] ⇒ Ds es'
[dom (lcl s')]
proof (induction rule:red-reds.inducts)
  case BinOpRed1 thus ?case by (auto elim!: D-mono[OF red-lA-incr])
next
  case AAccRed1 thus ?case by (auto elim!: D-mono[OF red-lA-incr])
next
  case AAssRed1 thus ?case by(auto intro: red-lA-incr sqUn-lem D-mono)
next
  case AAssRed2 thus ?case by (auto elim!: D-mono[OF red-lA-incr])
next
  case FAssRed1 thus ?case by (auto elim!: D-mono[OF red-lA-incr])
next
  case CASRed1 thus ?case by(auto intro: red-lA-incr sqUn-lem D-mono)
next
  case CASRed2 thus ?case by (auto elim!: D-mono[OF red-lA-incr])
next
  case CallObj thus ?case by (auto elim!: Ds-mono[OF red-lA-incr])
next
  case CallParams thus ?case by(auto elim!: Ds-mono[OF red-lA-incr])
next
  case RedCall thus ?case by(auto dest!:sees-wf-mdecl[OF wf] simp:wf-mdecl-def elim!:D-mono')
next
  case BlockRed thus ?case
  by(auto simp:hyperset-defs elim!:D-mono' simp del:fun-upd-apply split: if-split-asm)
next
  case SynchronizedRed1 thus ?case by(auto elim!: D-mono[OF red-lA-incr])
next
  case SeqRed thus ?case by (auto elim!: D-mono[OF red-lA-incr])
next
  case CondRed thus ?case by (auto elim!: D-mono[OF red-lA-incr])
next
  case TryRed thus ?case
  by (fastforce dest:red-lcl-incr intro:D-mono' simp:hyperset-defs)
next
  case RedWhile thus ?case by(auto simp:hyperset-defs elim!:D-mono')
next
  case ListRed1 thus ?case by (auto elim!: Ds-mono[OF red-lA-incr])
qed (auto simp:hyperset-defs)

end

end

```

4.13 Type Safety Proof

```

theory TypeSafe
imports
  Progress

```

DefAssPreservation
begin

4.13.1 Basic preservation lemmas

First two easy preservation lemmas.

theorem (in *J-conf-read*)

shows *red-preserves-hconf*:

$\llbracket \text{extTA}, P, t \vdash \langle e, s \rangle \text{ --ta--} \rightarrow \langle e', s' \rangle; P, E, hp\ s \vdash e : T; hconf\ (hp\ s) \rrbracket \Longrightarrow hconf\ (hp\ s')$

and *reds-preserves-hconf*:

$\llbracket \text{extTA}, P, t \vdash \langle es, s \rangle \text{ [-ta--]} \rightarrow \langle es', s' \rangle; P, E, hp\ s \vdash es\ [:]\ Ts; hconf\ (hp\ s) \rrbracket \Longrightarrow hconf\ (hp\ s')$

proof (*induct arbitrary: T E and Ts E rule: red-reds.inducts*)

case *RedNew* **thus** *?case*

by(*auto intro: hconf-heap-ops-mono*)

next

case *RedNewFail* **thus** *?case*

by(*auto intro: hconf-heap-ops-mono*)

next

case *RedNewArray* **thus** *?case*

by(*auto intro: hconf-heap-ops-mono*)

next

case *RedNewArrayFail* **thus** *?case*

by(*auto intro: hconf-heap-ops-mono*)

next

case (*RedAAss h a U n i v U' h' l*)

from $\langle \text{ sint } i < \text{ int } n \rangle \langle 0 \leq s\ i \rangle$

have $\text{ nat } (\text{ sint } i) < n$

by (*simp add: word-sle-eq nat-less-iff*)

thus *?case using RedAAss*

by(*fastforce elim: hconf-heap-write-mono intro: addr-loc-type.intros simp add: conf-def*)

next

case *RedFAss* **thus** *?case*

by(*fastforce elim: hconf-heap-write-mono intro: addr-loc-type.intros simp add: conf-def*)

next

case *RedCASSucceed* **thus** *?case*

by(*fastforce elim: hconf-heap-write-mono intro: addr-loc-type.intros simp add: conf-def*)

next

case (*RedCallExternal s a U M Ts T' D vs ta va h' ta' e' s'*)

hence $P, hp\ s \vdash a \cdot M(vs) : T$

by(*fastforce simp add: external-WT'-iff dest: sees-method-fun*)

with *RedCallExternal* **show** *?case by*(*auto dest: external-call-hconf*)

qed *auto*

theorem (in *J-heap*) *red-preserves-lconf*:

$\llbracket \text{extTA}, P, t \vdash \langle e, s \rangle \text{ --ta--} \rightarrow \langle e', s' \rangle; P, E, hp\ s \vdash e : T; P, hp\ s \vdash \text{ lcl } s\ (: \leq)\ E \rrbracket \Longrightarrow P, hp\ s' \vdash \text{ lcl } s'\ (: \leq)\ E$

and *reds-preserves-lconf*:

$\llbracket \text{extTA}, P, t \vdash \langle es, s \rangle \text{ [-ta--]} \rightarrow \langle es', s' \rangle; P, E, hp\ s \vdash es\ [:]\ Ts; P, hp\ s \vdash \text{ lcl } s\ (: \leq)\ E \rrbracket \Longrightarrow P, hp\ s' \vdash \text{ lcl } s'\ (: \leq)\ E$

proof(*induct arbitrary: T E and Ts E rule: red-reds.inducts*)

case *RedNew* **thus** *?case*

by(*fastforce intro: lconf-hext hext-heap-ops simp del: fun-upd-apply*)

next

case *RedNewFail* **thus** *?case*

by(*auto intro: lconf-hext hext-heap-ops simp del: fun-upd-apply*)

```

next
  case RedNewArray thus ?case
    by(fastforce intro:lconf-heap heap-heap-ops simp del: fun-upd-apply)
next
  case RedNewArrayFail thus ?case
    by(fastforce intro:lconf-heap heap-heap-ops simp del: fun-upd-apply)
next
  case RedLAss thus ?case
    by(fastforce elim: lconf-upd simp add: conf-def simp del: fun-upd-apply )
next
  case RedAAss thus ?case
    by(fastforce intro:lconf-heap heap-heap-ops simp del: fun-upd-apply)
next
  case RedFAss thus ?case
    by(fastforce intro:lconf-heap heap-heap-ops simp del: fun-upd-apply)
next
  case RedCASSucceed thus ?case
    by(fastforce intro:lconf-heap heap-heap-ops simp del: fun-upd-apply)
next
  case (BlockRed e h x V vo ta e' h' x' T T' E)
  note red = ⟨extTA,P,t ⊢ ⟨e,(h, x(V := vo))⟩ -ta→ ⟨e',(h', x')⟩⟩
  note IH = ⟨∧ T E. [P,E,hp (h, x(V := vo)) ⊢ e : T;
    P,hp (h, x(V := vo)) ⊢ lcl (h, x(V := vo)) (:≤) E]
    ⇒ P,hp (h', x') ⊢ lcl (h', x') (:≤) E⟩
  note wt = ⟨P,E,hp (h, x) ⊢ {V:T=vo; e} : T'⟩
  note lconf = ⟨P,hp (h, x) ⊢ lcl (h, x) (:≤) E⟩
  from lconf-heap[OF lconf[simplified] red-heap-incr[OF red, simplified]]
  have P,h' ⊢ x (:≤) E .
  moreover from wt have P,E(V↦T),h ⊢ e : T' by(cases vo, auto)
  moreover from lconf wt have P,h ⊢ x(V := vo) (:≤) E(V ↦ T)
    by(cases vo)(simp add: lconf-def,auto intro: lconf-upd2 simp add: conf-def)
  ultimately have P,h' ⊢ x' (:≤) E(V↦T)
    by(auto intro: IH[simplified])
  with ⟨P,h' ⊢ x (:≤) E⟩ show ?case
    by(auto simp add: lconf-def split: if-split-asm)
next
  case (RedCallExternal s a U M Ts T' D vs ta va h' ta' e' s')
  from ⟨P,t ⊢ ⟨a·M(vs),hp s⟩ -ta→ext ⟨va,h'⟩⟩ have hp s ⊑ h' by(rule red-external-heap)
  with ⟨s' = (h', lcl s)⟩ ⟨P,hp s ⊢ lcl s (:≤) E⟩ show ?case by(auto intro: lconf-heap)
qed auto

```

Combining conformance of heap and local variables:

definition (in *J-heap-conf-base*) *sconf* :: *env* ⇒ (*'addr*, *'heap*) *Jstate* ⇒ *bool* (⟨- ⊢ - √⟩ [51,51]50)
where $E \vdash s \sqrt{\quad} \equiv \text{let } (h,l) = s \text{ in } h\text{conf } h \wedge P,h \vdash l (: \leq) E \wedge \text{preallocated } h$

context *J-conf-read* **begin**

lemma *red-preserves-sconf*:

$\llbracket \text{extTA},P,t \vdash \langle e,s \rangle -tas \rightarrow \langle e',s' \rangle; P,E,hp s \vdash e : T; E \vdash s \sqrt{\quad} \rrbracket \implies E \vdash s' \sqrt{\quad}$
apply(auto dest: red-preserves-hconf red-preserves-lconf simp add:sconf-def)
apply(fastforce dest: red-heap-incr intro: preallocated-heap)
done

lemma *reds-preserves-sconf*:

$\llbracket \text{extTA}, P, t \vdash \langle e, s \rangle [-ta \rightarrow] \langle e', s' \rangle; P, E, hp \ s \vdash es \ [:] \ Ts; E \vdash s \ \checkmark \rrbracket \Longrightarrow E \vdash s' \ \checkmark$
apply(*auto dest: reds-preserves-hconf reds-preserves-lconf simp add: sconf-def*)
apply(*fastforce dest: reds-heap-incr intro: preallocated-heap*)
done

end

lemma (in *J-heap-base*) *wt-external-call*:

$\llbracket \text{conf-extRet } P \ h \ va \ T; P, E, h \vdash e : T \rrbracket \Longrightarrow \exists T'. P, E, h \vdash \text{extRet2J } e \ va : T' \wedge P \vdash T' \leq T$
by(*cases va*)(*auto simp add: conf-def*)

4.13.2 Subject reduction

theorem (in *J-conf-read*) **assumes** *wf: wf-J-prog P*

shows *subject-reduction*:

$\llbracket \text{extTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; E \vdash s \ \checkmark; P, E, hp \ s \vdash e : T; P, hp \ s \vdash t \ \checkmark \rrbracket$
 $\Longrightarrow \exists T'. P, E, hp \ s' \vdash e' : T' \wedge P \vdash T' \leq T$

and *subjects-reduction*:

$\llbracket \text{extTA}, P, t \vdash \langle e, s \rangle [-ta \rightarrow] \langle e', s' \rangle; E \vdash s \ \checkmark; P, E, hp \ s \vdash es \ [:] \ Ts; P, hp \ s \vdash t \ \checkmark \rrbracket$
 $\Longrightarrow \exists Ts'. P, E, hp \ s' \vdash es' \ [:] \ Ts' \wedge P \vdash Ts' \ [\leq] \ Ts$

proof (*induct arbitrary: T E and Ts E rule:red-reds.inducts*)

case *RedNew*

thus *?case by*(*auto dest: allocate-SomeD*)

next

case *RedNewFail* **thus** *?case unfolding sconf-def*

by(*fastforce intro:typeof-OutOfMemory preallocated-heap-ops simp add: xcpt-subcls-Throwable[OF - wf]*)

next

case *NewArrayRed*

thus *?case by fastforce*

next

case *RedNewArray*

thus *?case by*(*auto dest: allocate-SomeD*)

next

case *RedNewArrayNegative* **thus** *?case unfolding sconf-def*

by(*fastforce intro: preallocated-heap-ops simp add: xcpt-subcls-Throwable[OF - wf]*)

next

case *RedNewArrayFail* **thus** *?case unfolding sconf-def*

by(*fastforce intro:typeof-OutOfMemory preallocated-heap-ops simp add: xcpt-subcls-Throwable[OF - wf]*)

next

case (*CastRed e s ta e' s' C T E*)

have *esse: extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle*

and *IH: \bigwedge T E. \llbracket E \vdash s \ \checkmark; P, E, hp \ s \vdash e : T; P, hp \ s \vdash t \ \checkmark \rrbracket \Longrightarrow \exists T'. P, E, hp \ s' \vdash e' : T' \wedge P \vdash T' \leq T*

and *hconf: E \vdash s \ \checkmark*

and *wtc: P, E, hp \ s \vdash \text{Cast } C \ e : T* **by** *fact+*

thus *?case*

proof(*clarsimp*)

fix *T'*

assume *wte: P, E, hp \ s \vdash e : T'* *is-type P C*

from *wte* **and** *hconf* **and** *IH* **and** $\langle P, hp \ s \vdash t \ \checkmark \rangle$ **have** $\exists U. P, E, hp \ s' \vdash e' : U \wedge P \vdash U \leq T'$

by *simp*

then obtain *U* **where** *wtee: P, E, hp \ s' \vdash e' : U* **and** *UsTT: P \vdash U \leq T'* **by** *blast*

from *wtee* $\langle is\text{-type } P \ C \rangle$ **have** $P, E, hp \ s' \vdash \text{Cast } C \ e' : C$ **by** (*rule WTrtCast*)
thus $\exists T'. P, E, hp \ s' \vdash \text{Cast } C \ e' : T' \wedge P \vdash T' \leq C$ **by** *blast*
qed
next
case *RedCast* **thus** *?case*
by (*clarsimp simp add: is-refT-def*)
next
case *RedCastFail* **thus** *?case* **unfolding** *sconf-def*
by (*fastforce simp add: xcpt-subcls-Throwable[OF - wf]*)
next
case (*InstanceOfRed e s ta e' s' U T E*)
have $IH: \bigwedge T E. \llbracket E \vdash s \ \checkmark; P, E, hp \ s \vdash e : T; P, hp \ s \vdash t \ \checkmark t \rrbracket \implies \exists T'. P, E, hp \ s' \vdash e' : T' \wedge P \vdash T' \leq T$
and *hconf*: $E \vdash s \ \checkmark$
and *wtc*: $P, E, hp \ s \vdash e \text{ instanceof } U : T$
and *tconf*: $P, hp \ s \vdash t \ \checkmark t$ **by** *fact+*
from *wtc* **obtain** T' **where** $P, E, hp \ s \vdash e : T'$ **by** *auto*
from $IH[OF \text{ hconf this tconf}]$ **obtain** T'' **where** $P, E, hp \ s' \vdash e' : T''$ **by** *auto*
with *wtc* **show** *?case* **by** *auto*
next
case *RedInstanceOf* **thus** *?case*
by (*clarsimp*)
next
case (*BinOpRed1 e1 s ta e1' s' bop e2 T E*)
have *red*: $extTA, P, t \vdash \langle e_1, s \rangle -ta \rightarrow \langle e_1', s' \rangle$
and $IH: \bigwedge T E. \llbracket E \vdash s \ \checkmark; P, E, hp \ s \vdash e_1 : T; P, hp \ s \vdash t \ \checkmark t \rrbracket \implies \exists U. P, E, hp \ s' \vdash e_1' : U \wedge P \vdash U \leq T$
and *conf*: $E \vdash s \ \checkmark$ **and** *wt*: $P, E, hp \ s \vdash e_1 \llbracket bop \rrbracket e_2 : T$
and *tconf*: $P, hp \ s \vdash t \ \checkmark t$ **by** *fact+*
from *wt* **obtain** $T1 \ T2$ **where** $wt1: P, E, hp \ s \vdash e_1 : T1$
and $wt2: P, E, hp \ s \vdash e_2 : T2$ **and** *wtbop*: $P \vdash T1 \llbracket bop \rrbracket T2 : T$ **by** *auto*
from $IH[OF \text{ conf wt1 tconf}]$ **obtain** $T1'$ **where** $wt1': P, E, hp \ s' \vdash e_1' : T1'$
and *sub*: $P \vdash T1' \leq T1$ **by** *blast*
from *WTrt-binop-widen-mono[OF wtbop sub widen-refl]*
obtain T' **where** *wtbop'*: $P \vdash T1' \llbracket bop \rrbracket T2 : T'$ **and** *sub'*: $P \vdash T' \leq T$ **by** *blast*
from $wt1' \ WTrt\text{-hext-mono}[OF \text{ wt2 red-hext-incr}[OF \text{ red}]] \ wtbop'$
have $P, E, hp \ s' \vdash e_1' \llbracket bop \rrbracket e_2 : T'$ **by** (*rule WTrtBinOp*)
with *sub'* **show** *?case* **by** *blast*
next
case (*BinOpRed2 e2 s ta e2' s' v1 bop T E*)
have *red*: $extTA, P, t \vdash \langle e_2, s \rangle -ta \rightarrow \langle e_2', s' \rangle$ **by** *fact*
have $IH: \bigwedge E T. \llbracket E \vdash s \ \checkmark; P, E, hp \ s \vdash e_2 : T; P, hp \ s \vdash t \ \checkmark t \rrbracket \implies \exists U. P, E, hp \ s' \vdash e_2' : U \wedge P \vdash U \leq T$
and *tconf*: $P, hp \ s \vdash t \ \checkmark t$ **by** *fact+*
have *conf*: $E \vdash s \ \checkmark$ **and** *wt*: $P, E, hp \ s \vdash (Val \ v_1) \llbracket bop \rrbracket e_2 : T$ **by** *fact+*
from *wt* **obtain** $T1 \ T2$ **where** $wt1: P, E, hp \ s \vdash Val \ v_1 : T1$
and $wt2: P, E, hp \ s \vdash e_2 : T2$ **and** *wtbop*: $P \vdash T1 \llbracket bop \rrbracket T2 : T$ **by** *auto*
from $IH[OF \text{ conf wt2 tconf}]$ **obtain** $T2'$ **where** $wt2': P, E, hp \ s' \vdash e_2' : T2'$
and *sub*: $P \vdash T2' \leq T2$ **by** *blast*
from *WTrt-binop-widen-mono[OF wtbop widen-refl sub]*
obtain T' **where** *wtbop'*: $P \vdash T1 \llbracket bop \rrbracket T2' : T'$ **and** *sub'*: $P \vdash T' \leq T$ **by** *blast*
from $WTrt\text{-hext-mono}[OF \text{ wt1 red-hext-incr}[OF \text{ red}]] \ wt2' \ wtbop'$
have $P, E, hp \ s' \vdash Val \ v_1 \llbracket bop \rrbracket e_2' : T'$ **by** (*rule WTrtBinOp*)
with *sub'* **show** *?case* **by** *blast*

next
case (*RedBinOp* *bop v1 v2 v s*)
from $\langle E \vdash s \checkmark \rangle$ **have** *preh: preallocated (hp s)* **by**(*cases s*)(*simp add: sconf-def*)
from $\langle P, E, hp \ s \vdash \text{Val } v1 \ \langle\langle bop \rangle\rangle \ \text{Val } v2 : T \rangle$ **obtain** *T1 T2*
where *typeof_{hp} s v1 = [T1] typeof_{hp} s v2 = [T2] P ⊢ T1 «bop» T2 : T* **by** *auto*
with *wf preh* **have** $P, hp \ s \vdash v : \leq T$ **using** $\langle binop \ bop \ v1 \ v2 = [Inl \ v] \rangle$
by(*rule binop-type*)
thus *?case* **by**(*auto simp add: conf-def*)

next
case (*RedBinOpFail* *bop v1 v2 a s*)
from $\langle E \vdash s \checkmark \rangle$ **have** *preh: preallocated (hp s)* **by**(*cases s*)(*simp add: sconf-def*)
from $\langle P, E, hp \ s \vdash \text{Val } v1 \ \langle\langle bop \rangle\rangle \ \text{Val } v2 : T \rangle$ **obtain** *T1 T2*
where *typeof_{hp} s v1 = [T1] typeof_{hp} s v2 = [T2] P ⊢ T1 «bop» T2 : T* **by** *auto*
with *wf preh* **have** $P, hp \ s \vdash \text{Addr } a : \leq \text{Class Throwable}$ **using** $\langle binop \ bop \ v1 \ v2 = [Inr \ a] \rangle$
by(*rule binop-type*)
thus *?case* **by**(*auto simp add: conf-def*)

next
case *RedVar* **thus** *?case* **by** (*fastforce simp:sconf-def lconf-def conf-def*)

next
case *LAssRed* **thus** *?case* **by**(*blast intro:widen-trans*)

next
case *RedLAss* **thus** *?case* **by** *fastforce*

next
case (*AAccRed1* *a s ta a' s' i T E*)
have $IH: \bigwedge E \ T. \llbracket E \vdash s \checkmark; P, E, hp \ s \vdash a : T; P, hp \ s \vdash t \sqrt{t} \rrbracket \implies \exists T'. P, E, hp \ s' \vdash a' : T' \wedge P \vdash T' \leq T$
and *assa: extTA, P, t ⊢ ⟨a, s⟩ -ta→ ⟨a', s'⟩*
and *wt: P, E, hp \ s ⊢ a[i] : T*
and *hconf: E ⊢ s √*
and *tconf: P, hp \ s ⊢ t √t* **by** *fact+*
from *wt* **have** *wti: P, E, hp \ s ⊢ i : Integer* **by** *auto*
from *wti red-heat-incr[OF assa]* **have** *wti': P, E, hp \ s' ⊢ i : Integer* **by** $-$ (*rule WTrt-heat-mono*)
{ assume *wta: P, E, hp \ s ⊢ a : T[]*
from *IH[OF hconf wta tconf]*
obtain *U* **where** *wta': P, E, hp \ s' ⊢ a' : U* **and** *UsubT: P ⊢ U ≤ T[]* **by** *fastforce*
with *wta' wti'* **have** *?case* **by**(*cases U, auto simp add: widen-Array*) **}**

moreover
{ assume *wta: P, E, hp \ s ⊢ a : NT*
from *IH[OF hconf wta tconf]* **have** $P, E, hp \ s' \vdash a' : NT$ **by** *fastforce*
from *this wti'* **have** *?case*
by(*fastforce intro:WTrtAAccNT*) **}**

ultimately show *?case* **using** *wt* **by** *auto*

next
case (*AAccRed2* *i s ta i' s' a T E*)
have $IH: \bigwedge E \ T. \llbracket E \vdash s \checkmark; P, E, hp \ s \vdash i : T; P, hp \ s \vdash t \sqrt{t} \rrbracket \implies \exists T'. P, E, hp \ s' \vdash i' : T' \wedge P \vdash T' \leq T$
and *issi: extTA, P, t ⊢ ⟨i, s⟩ -ta→ ⟨i', s'⟩*
and *wt: P, E, hp \ s ⊢ Val a[i] : T*
and *sconf: E ⊢ s √*
and *tconf: P, hp \ s ⊢ t √t* **by** *fact+*
from *wt* **have** *wti: P, E, hp \ s ⊢ i : Integer* **by** *auto*
from *wti IH sconf tconf* **have** *wti': P, E, hp \ s' ⊢ i' : Integer* **by** *blast*
from *wt* **show** *?case*
proof (*rule WTrt-elim-cases*)

```

    assume wta: P,E,hp s ⊢ Val a : T[]
    from wta red-heap-incr[OF issi] have wta': P,E,hp s' ⊢ Val a : T[] by (rule WTrt-heap-mono)
    from wta' wti' show ?case by(fastforce)
  next
    assume wta: P,E,hp s ⊢ Val a : NT
    from wta red-heap-incr[OF issi] have wta': P,E,hp s' ⊢ Val a : NT by (rule WTrt-heap-mono)
    from wta' wti' show ?case
      by(fastforce elim: WTrtAccNT)
  qed
next
  case RedAAccNull thus ?case unfolding sconf-def
    by(fastforce simp add: xcpt-subcls-Throwable[OF - wf])
next
  case RedAAccBounds thus ?case unfolding sconf-def
    by(fastforce simp add: xcpt-subcls-Throwable[OF - wf])
next
  case (RedAAcc h a T n i v l T' E)
  from ⟨E ⊢ (h, l) √⟩ have hconf h by(clarsimp simp add: sconf-def)
  from ⟨0 ≤ s i⟩ ⟨sint i < int n⟩
  have nat (sint i) < n
    by (simp add: word-sle-eq nat-less-iff)
  with ⟨typeof-addr h a = [Array-type T n]⟩ have P,h ⊢ a@ACell (nat (sint i)) : T
    by(auto intro: addr-loc-type.intros)
  from heap-read-conf[OF ⟨heap-read h a (ACell (nat (sint i))) v⟩ this] ⟨hconf h⟩
  have P,h ⊢ v :≤ T by simp
  thus ?case using RedAAcc by(auto simp add: sconf-def)
next
  case (AAssRed1 a s ta a' s' i e T E)
  have IH: ∧E T. [E ⊢ s √; P,E,hp s ⊢ a : T; P,hp s ⊢ t √t] ⇒ ∃ T'. P,E,hp s' ⊢ a' : T' ∧ P ⊢
  T' ≤ T
    and assa: extTA,P,t ⊢ ⟨a,s⟩ -ta→ ⟨a',s'⟩
    and wt: P,E,hp s ⊢ a[i] := e : T
    and sconf: E ⊢ s √
    and tconf: P,hp s ⊢ t √t by fact+
  from wt have void: T = Void by blast
  from wt have wti: P,E,hp s ⊢ i : Integer by auto
  from wti red-heap-incr[OF assa] have wti': P,E,hp s' ⊢ i : Integer by - (rule WTrt-heap-mono)
  { assume wta: P,E,hp s ⊢ a : NT
    from IH[OF sconf wta tconf] have wta': P,E,hp s' ⊢ a' : NT by fastforce
    from wt wta obtain V where wte: P,E,hp s ⊢ e : V by(auto)
    from wte red-heap-incr[OF assa] have wte': P,E,hp s' ⊢ e : V by - (rule WTrt-heap-mono)
    from wta' wti' wte' void have ?case
      by(fastforce elim: WTrtAAssNT) }
  moreover
  { fix U
    assume wta: P,E,hp s ⊢ a : U[]
    from IH[OF sconf wta tconf]
    obtain U' where wta': P,E,hp s' ⊢ a' : U' and UsubT: P ⊢ U' ≤ U[] by fastforce
    with wta' have ?case
    proof(cases U')
      case NT
      assume UNT: U' = NT
      from UNT wt wta obtain V where wte: P,E,hp s ⊢ e : V by(auto)
      from wte red-heap-incr[OF assa] have wte': P,E,hp s' ⊢ e : V by - (rule WTrt-heap-mono)

```

```

from  $wta' UNT wti' wte' void$  show  $?thesis$ 
  by( $fastforce\ elim: WTrtAAssNT$ )
next
  case ( $Array\ A$ )
  have  $UA: U' = A[]$  by  $fact$ 
  with  $UA\ UsubT\ wt\ wta$  obtain  $V$  where  $wte: P, E, hp\ s \vdash e : V$  by  $auto$ 
  from  $wte\ red\ hext\ incr[OF\ assa]$  have  $wte': P, E, hp\ s' \vdash e : V$  by  $-(rule\ WTrt\ hext\ mono)$ 
  with  $wta' wte' UA\ wti' void$  show  $?thesis$  by ( $fast\ elim: WTrtAAss$ )
  qed( $simp\ all\ add: widen\ Array$ ) }
ultimately show  $?case$  using  $wt$  by  $blast$ 
next
  case ( $AAssRed2\ i\ s\ ta\ i'\ s'\ a\ e\ T\ E$ )
  have  $IH: \bigwedge E\ T. \llbracket E \vdash s \sqrt{\quad}; P, E, hp\ s \vdash i : T; P, hp\ s \vdash t \sqrt{t} \rrbracket \implies \exists T'. P, E, hp\ s' \vdash i' : T' \wedge P \vdash$ 
 $T' \leq T$ 
  and  $issi: extTA, P, t \vdash \langle i, s \rangle -ta \rightarrow \langle i', s' \rangle$ 
  and  $wt: P, E, hp\ s \vdash Val\ a[i] := e : T$ 
  and  $sconf: E \vdash s \sqrt{\quad}$  and  $tconf: P, hp\ s \vdash t \sqrt{t}$  by  $fact+$ 
from  $wt$  have  $void: T = Void$  by  $blast$ 
from  $wt$  have  $wti: P, E, hp\ s \vdash i : Integer$  by  $auto$ 
from  $IH[OF\ sconf\ wti\ tconf]$  have  $wti': P, E, hp\ s' \vdash i' : Integer$  by  $fastforce$ 
from  $wt$  show  $?case$ 
proof( $rule\ WTrt\ elim\ cases$ )
  fix  $U\ T'$ 
  assume  $wta: P, E, hp\ s \vdash Val\ a : U[]$ 
  and  $wte: P, E, hp\ s \vdash e : T'$ 
  from  $wte\ red\ hext\ incr[OF\ issi]$  have  $wte': P, E, hp\ s' \vdash e : T'$  by  $-(rule\ WTrt\ hext\ mono)$ 
  from  $wta\ red\ hext\ incr[OF\ issi]$  have  $wta': P, E, hp\ s' \vdash Val\ a : U[]$  by  $-(rule\ WTrt\ hext\ mono)$ 
  from  $wta' wti' wte' void$  show  $?case$  by ( $fastforce\ elim: WTrtAAss$ )
next
  fix  $T'$ 
  assume  $wta: P, E, hp\ s \vdash Val\ a : NT$ 
  and  $wte: P, E, hp\ s \vdash e : T'$ 
  from  $wte\ red\ hext\ incr[OF\ issi]$  have  $wte': P, E, hp\ s' \vdash e : T'$  by  $-(rule\ WTrt\ hext\ mono)$ 
  from  $wta\ red\ hext\ incr[OF\ issi]$  have  $wta': P, E, hp\ s' \vdash Val\ a : NT$  by  $-(rule\ WTrt\ hext\ mono)$ 
  from  $wta' wti' wte' void$  show  $?case$  by ( $fastforce\ elim: WTrtAAss$ )
qed
next
  case ( $AAssRed3\ e\ s\ ta\ e'\ s'\ a\ i\ T\ E$ )
  have  $IH: \bigwedge E\ T. \llbracket E \vdash s \sqrt{\quad}; P, E, hp\ s \vdash e : T; P, hp\ s \vdash t \sqrt{t} \rrbracket \implies \exists T'. P, E, hp\ s' \vdash e' : T' \wedge P \vdash$ 
 $T' \leq T$ 
  and  $issi: extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle$ 
  and  $wt: P, E, hp\ s \vdash Val\ a[Val\ i] := e : T$ 
  and  $sconf: E \vdash s \sqrt{\quad}$  and  $tconf: P, hp\ s \vdash t \sqrt{t}$  by  $fact+$ 
from  $wt$  have  $void: T = Void$  by  $blast$ 
from  $wt$  have  $wti: P, E, hp\ s \vdash Val\ i : Integer$  by  $auto$ 
from  $wti\ red\ hext\ incr[OF\ issi]$  have  $wti': P, E, hp\ s' \vdash Val\ i : Integer$  by  $-(rule\ WTrt\ hext\ mono)$ 
from  $wt$  show  $?case$ 
proof( $rule\ WTrt\ elim\ cases$ )
  fix  $U\ T'$ 
  assume  $wta: P, E, hp\ s \vdash Val\ a : U[]$ 
  and  $wte: P, E, hp\ s \vdash e : T'$ 
  from  $wta\ red\ hext\ incr[OF\ issi]$  have  $wta': P, E, hp\ s' \vdash Val\ a : U[]$  by  $-(rule\ WTrt\ hext\ mono)$ 
  from  $IH[OF\ sconf\ wte\ tconf]$ 
  obtain  $V$  where  $wte': P, E, hp\ s' \vdash e' : V$  by  $fastforce$ 

```

```

    from wta' wti' wte' void show ?case by (fastforce elim:WTrtAAss)
  next
    fix T'
    assume wta: P,E,hp s ⊢ Val a : NT
      and wte: P,E,hp s ⊢ e : T'
    from wta red-heat-incr[OF issi] have wta': P,E,hp s' ⊢ Val a : NT by - (rule WTrt-heat-mono)
    from IH[OF sconf wte tconf]
    obtain V where wte': P,E,hp s' ⊢ e' : V by fastforce
    from wta' wti' wte' void show ?case by (fastforce elim:WTrtAAss)
  qed
next
  case RedAAssNull thus ?case unfolding sconf-def
    by(fastforce simp add: xcpt-subcls-Throwable[OF - wf])
next
  case RedAAssBounds thus ?case unfolding sconf-def
    by(fastforce simp add: xcpt-subcls-Throwable[OF - wf])
next
  case RedAAssStore thus ?case unfolding sconf-def
    by(fastforce simp add: xcpt-subcls-Throwable[OF - wf])
next
  case RedAAss thus ?case
    by(auto simp del:fun-upd-apply)
next
  case (ALengthRed a s ta a' s' T E)
  note IH = ⟨∧ T'. [E ⊢ s √; P,E,hp s ⊢ a : T'; P,hp s ⊢ t √t]
    ⇒ ∃ T''. P,E,hp s' ⊢ a' : T'' ∧ P ⊢ T'' ≤ T'⟩
  from ⟨P,E,hp s ⊢ a.length : T⟩
  show ?case
  proof(rule WTrt-elim-cases)
    fix T'
    assume [simp]: T = Integer
      and wta: P,E,hp s ⊢ a : T'[]
    from wta ⟨E ⊢ s √⟩ IH ⟨P,hp s ⊢ t √t⟩
    obtain T'' where wta': P,E,hp s' ⊢ a' : T''
      and sub: P ⊢ T'' ≤ T'[] by blast
    from sub have P,E,hp s' ⊢ a'.length : Integer
      unfolding widen-Array
    proof(rule disjE)
      assume T'' = NT
        with wta' show ?thesis by(auto)
    next
      assume ∃ V. T'' = V[] ∧ P ⊢ V ≤ T'
        then obtain V where T'' = V[] P ⊢ V ≤ T' by blast
        with wta' show ?thesis by -(rule WTrtALength, simp)
    qed
  thus ?thesis by(simp)
next
  assume P,E,hp s ⊢ a : NT
  with ⟨E ⊢ s √⟩ IH ⟨P,hp s ⊢ t √t⟩
  obtain T'' where wta': P,E,hp s' ⊢ a' : T''
    and sub: P ⊢ T'' ≤ NT by blast
  from sub have T'' = NT by auto
  with wta' show ?thesis by(auto)
qed

```

```

next
  case (RedALength h a T n l T' E)
  from ⟨P,E,hp (h, l) ⊢ addr a·length : T'⟩ ⟨typeof-addr h a = [Array-type T n]⟩
  have [simp]: T' = Integer by(auto)
  thus ?case by(auto)
next
  case RedALengthNull thus ?case unfolding sconf-def
    by(fastforce simp add: xcpt-subcls-Throwable[OF - wf])
next
  case (FAccRed e s ta e' s' F D T E)
  have IH:  $\bigwedge E T. \llbracket E \vdash s \sqrt{\phantom{x}}; P, E, hp \ s \vdash e : T; P, hp \ s \vdash t \sqrt{t} \rrbracket$ 
     $\implies \exists U. P, E, hp \ s' \vdash e' : U \wedge P \vdash U \leq T$ 
  and conf:  $E \vdash s \sqrt{\phantom{x}}$  and wt:  $P, E, hp \ s \vdash e \cdot F\{D\} : T$ 
  and tconf:  $P, hp \ s \vdash t \sqrt{t}$  by fact+
  — Now distinguish the two cases how wt can have arisen.
  { fix T' C fm
    assume wte:  $P, E, hp \ s \vdash e : T'$ 
      and icto:  $class\text{-}type\text{-}of' \ T' = [C]$ 
      and has:  $P \vdash C \text{ has } F:T (fm) \text{ in } D$ 
    from IH[OF conf wte tconf]
    obtain U where wte':  $P, E, hp \ s' \vdash e' : U$  and UsubC:  $P \vdash U \leq T'$  by auto
    — Now distinguish what U can be.
    with UsubC have ?case
    proof(cases U = NT)
      case True
      thus ?thesis using wte' by(blast intro: WTrtFAccNT widen-refl)
    next
      case False
      with icto UsubC obtain C' where icto':  $class\text{-}type\text{-}of' \ U = [C']$ 
      and C'subC:  $P \vdash C' \preceq^* C$ 
      by(rule widen-is-class-type-of)
      from has-field-mono[OF has C'subC] wte' icto'
      show ?thesis by(auto intro!: WTrtFAcc)
    qed }
  moreover
  { assume  $P, E, hp \ s \vdash e : NT$ 
    hence  $P, E, hp \ s' \vdash e' : NT$  using IH[OF conf - tconf] by fastforce
    hence ?case by(fastforce intro: WTrtFAccNT widen-refl) }
  ultimately show ?case using wt by blast
next
  case RedFAcc thus ?case unfolding sconf-def
    by(fastforce dest: heap-read-conf intro: addr-loc-type.intros simp add: conf-def)
next
  case RedFAccNull thus ?case unfolding sconf-def
    by(fastforce simp add: xcpt-subcls-Throwable[OF - wf])
next
  case (FAssRed1 e s ta e' s' F D e2)
  have red:  $extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle$ 
  and IH:  $\bigwedge E T. \llbracket E \vdash s \sqrt{\phantom{x}}; P, E, hp \ s \vdash e : T; P, hp \ s \vdash t \sqrt{t} \rrbracket$ 
     $\implies \exists U. P, E, hp \ s' \vdash e' : U \wedge P \vdash U \leq T$ 
  and conf:  $E \vdash s \sqrt{\phantom{x}}$  and wt:  $P, E, hp \ s \vdash e \cdot F\{D\} = e_2 : T$ 
  and tconf:  $P, hp \ s \vdash t \sqrt{t}$  by fact+
  from wt have void:  $T = Void$  by blast
  — We distinguish if e has type NT or a Class type

```

```

{ assume  $P, E, hp\ s \vdash e : NT$ 
  hence  $P, E, hp\ s' \vdash e' : NT$  using  $IH[OF\ conf - tconf]$  by fastforce
  moreover obtain  $T_2$  where  $P, E, hp\ s \vdash e_2 : T_2$  using wt by auto
  from this red-heat-incr[OF red] have  $P, E, hp\ s' \vdash e_2 : T_2$ 
    by(rule WTrt-heat-mono)
  ultimately have ?case using void by(blast intro!: WTrtFAssNT)
}
moreover
{ fix  $T' C TF T_2\ fm$ 
  assume  $wt_1: P, E, hp\ s \vdash e : T'$  and icto: class-type-of'  $T' = \lfloor C \rfloor$  and  $wt_2: P, E, hp\ s \vdash e_2 : T_2$ 
    and has:  $P \vdash C$  has  $F:TF (fm)$  in  $D$  and sub:  $P \vdash T_2 \leq TF$ 
  obtain  $U$  where  $wt_1': P, E, hp\ s' \vdash e' : U$  and UsubC:  $P \vdash U \leq T'$ 
    using  $IH[OF\ conf\ wt_1\ tconf]$  by blast
  have  $wt_2': P, E, hp\ s' \vdash e_2 : T_2$ 
    by(rule WTrt-heat-mono[OF wt_2 red-heat-incr[OF red]])
  — Is  $U$  the null type or a class type?
  have ?case
  proof(cases  $U = NT$ )
    case True
      with  $wt_1'\ wt_2'$  void show ?thesis by(blast intro!: WTrtFAssNT)
    next
      case False
        with icto UsubC obtain  $C'$  where icto': class-type-of'  $U = \lfloor C' \rfloor$ 
          and subclass:  $P \vdash C' \preceq^* C$  by(rule widen-is-class-type-of)
          have  $P \vdash C'$  has  $F:TF (fm)$  in  $D$  by(rule has-field-mono[OF has subclass])
          with  $wt_1'$  show ?thesis using  $wt_2'$  sub void icto' by(blast intro: WTrtFAss)
        qed }
  ultimately show ?case using wt by blast
}
next
case (FAssRed2  $e_2\ s\ ta\ e_2'\ s'\ v\ F\ D\ T\ E$ )
have red: extTA,  $P, t \vdash \langle e_2, s \rangle -ta \rightarrow \langle e_2', s' \rangle$ 
and  $IH: \bigwedge E\ T. \llbracket E \vdash s \sqrt{\quad}; P, E, hp\ s \vdash e_2 : T; P, hp\ s \vdash t \sqrt{t} \rrbracket$ 
   $\implies \exists U. P, E, hp\ s' \vdash e_2' : U \wedge P \vdash U \leq T$ 
and conf:  $E \vdash s \sqrt{\quad}$  and wt:  $P, E, hp\ s \vdash Val\ v \cdot F\{D\} := e_2 : T$ 
and tconf:  $P, hp\ s \vdash t \sqrt{t}$  by fact+
from wt have [simp]:  $T = Void$  by auto
from wt show ?case
proof (rule WTrt-elim-cases)
  fix  $U C TF T_2\ fm$ 
  assume  $wt_1: P, E, hp\ s \vdash Val\ v : U$ 
    and icto: class-type-of'  $U = \lfloor C \rfloor$ 
    and has:  $P \vdash C$  has  $F:TF (fm)$  in  $D$ 
    and  $wt_2: P, E, hp\ s \vdash e_2 : T_2$  and TsubTF:  $P \vdash T_2 \leq TF$ 
  have  $wt_1': P, E, hp\ s' \vdash Val\ v : U$ 
    by(rule WTrt-heat-mono[OF wt_1 red-heat-incr[OF red]])
  obtain  $T_2'$  where  $wt_2': P, E, hp\ s' \vdash e_2' : T_2'$  and T'subT:  $P \vdash T_2' \leq T_2$ 
    using  $IH[OF\ conf\ wt_2\ tconf]$  by blast
  have  $P, E, hp\ s' \vdash Val\ v \cdot F\{D\} := e_2' : Void$ 
    by(rule WTrtFAss[OF wt_1' icto has wt_2' widen-trans[OF T'subT TsubTF]])
  thus ?case by auto
}
next
fix  $T_2$  assume null:  $P, E, hp\ s \vdash Val\ v : NT$  and  $wt_2: P, E, hp\ s \vdash e_2 : T_2$ 
from null have  $v = Null$  by simp
moreover

```



```

obtain  $T_2'$  where  $P, E, hp\ s' \vdash e_2' : T_2' \wedge P \vdash T_2' \leq T_2$ 
  using  $IH[OF\ conf\ wt_2\ tconf]$  by blast
  ultimately show ?thesis by(fastforce intro: WTrtFAssNT)
qed
next
  case RedFAss thus ?case by(auto simp del: fun-upd-apply)
next
  case RedFAssNull thus ?case unfolding sconf-def
  by(fastforce simp add: xcpt-subcls-Throwable[OF - wf])
next
  case (CASRed1 e s ta e' s' D F e2 e3)
  from CASRed1.prems(2) consider (NT)  $T_2\ T_3$  where
     $P, E, hp\ s \vdash e : NT\ T = Boolean\ P, E, hp\ s \vdash e_2 : T_2\ P, E, hp\ s \vdash e_3 : T_3$ 
    | (RefT)  $U\ T'\ C\ fm\ T_2\ T_3$  where
       $P, E, hp\ s \vdash e : U\ T = Boolean\ class\ type\ of'\ U = \lfloor C \rfloor\ P \vdash C\ has\ F: T'\ (fm)\ in\ D$ 
       $P, E, hp\ s \vdash e_2 : T_2\ P \vdash T_2 \leq T'\ P, E, hp\ s \vdash e_3 : T_3\ P \vdash T_3 \leq T'\ volatile\ fm$  by fastforce
  thus ?case
  proof cases
    case NT
    have  $P, E, hp\ s' \vdash e' : NT$  using CASRed1.hyps(2)[OF CASRed1.prems(1) NT(1) CASRed1.prems(3)]
  by auto
    moreover from NT CASRed1.hyps(1)[THEN red-heat-incr]
    have  $P, E, hp\ s' \vdash e_2 : T_2\ P, E, hp\ s' \vdash e_3 : T_3$  by(auto intro: WTrt-heat-mono)
    ultimately show ?thesis using NT by(auto intro: WTrtCASNT)
  next
    case RefT
    from CASRed1.hyps(2)[OF CASRed1.prems(1) RefT(1) CASRed1.prems(3)]
    obtain  $U'$  where  $wt1: P, E, hp\ s' \vdash e' : U'\ P \vdash U' \leq U$  by blast
    from RefT CASRed1.hyps(1)[THEN red-heat-incr]
    have  $wt2: P, E, hp\ s' \vdash e_2 : T_2$  and  $wt3: P, E, hp\ s' \vdash e_3 : T_3$  by(auto intro: WTrt-heat-mono)
    show ?thesis
    proof(cases U' = NT)
      case True
      with RefT wt1 wt2 wt3 show ?thesis by(auto intro: WTrtCASNT)
    next
      case False
      with RefT(3)  $wt1$  obtain  $C'$  where  $icto': class\ type\ of'\ U' = \lfloor C' \rfloor$ 
        and  $subclass: P \vdash C' \preceq^* C$  by(blast intro: widen-is-class-type-of)
      have  $P \vdash C' has\ F: T'\ (fm)\ in\ D$  by(rule has-field-mono[OF RefT(4) subclass])
      with RefT wt1 wt2 wt3 icto' show ?thesis by(auto intro!: WTrtCAS)
    qed
  qed
next
  case (CASRed2 e s ta e' s' v D F e3)
  consider (Null)  $v = Null$  | (Val)  $U\ C\ T'\ fm\ T_2\ T_3$  where
     $class\ type\ of'\ U = \lfloor C \rfloor\ P \vdash C\ has\ F: T'\ (fm)\ in\ D\ volatile\ fm$ 
     $P, E, hp\ s \vdash e : T_2\ P \vdash T_2 \leq T'\ P, E, hp\ s \vdash e_3 : T_3\ P \vdash T_3 \leq T'\ T = Boolean$ 
     $typeof_{hp\ s}\ v = \lfloor U \rfloor$  using CASRed2.prems(2) by auto
  then show ?case
  proof cases
    case Null
    then show ?thesis using CASRed2
    by(force dest: red-heat-incr intro: WTrt-heat-mono WTrtCASNT)
  next

```

```

case Val
from CASRed2.hyps(1) have hext: hp s ≤ hp s' by(auto dest: red-hext-incr)
with Val(9) have typeof_hp_s' v = [U] by(rule type-of-hext-type-of)
moreover from CASRed2.hyps(2)[OF CASRed2.prem(1) Val(4) CASRed2.prem(3)] Val(5)
obtain T2' where P,E,hp s' ⊢ e' : T2' P ⊢ T2' ≤ T' by(auto intro: widen-trans)
moreover from Val(6) hext have P,E,hp s' ⊢ e3 : T3 by(rule WTrt-hext-mono)
ultimately show ?thesis using Val by(auto intro: WTrtCAS)
qed
next
case (CASRed3 e s ta e' s' v D F v')
consider (Null) v = Null | (Val) U C T' fm T2 T3 where
  T = Boolean class-type-of' U = [C] P ⊢ C has F:T' (fm) in D volatile fm
  P ⊢ T2 ≤ T' P,E,hp s ⊢ e : T3 P ⊢ T3 ≤ T'
  typeof_hp_s v = [U] typeof_hp_s v' = [T2]
using CASRed3.prem(2) by auto
then show ?case
proof cases
case Null
then show ?thesis using CASRed3
  by(force dest: red-hext-incr intro: type-of-hext-type-of WTrtCASNT)
next
case Val
from CASRed3.hyps(1) have hext: hp s ≤ hp s' by(auto dest: red-hext-incr)
with Val(8,9) have typeof_hp_s' v = [U] typeof_hp_s' v' = [T2]
  by(blast intro: type-of-hext-type-of)+
moreover from CASRed3.hyps(2)[OF CASRed3.prem(1) Val(6) CASRed3.prem(3)] Val(7)
obtain T3' where P,E,hp s' ⊢ e' : T3' P ⊢ T3' ≤ T' by(auto intro: widen-trans)
ultimately show ?thesis using Val by(auto intro: WTrtCAS)
qed
next
case CASNull thus ?case unfolding sconf-def
  by(fastforce simp add: xcpt-subcls-Throwable[OF - wf])
next
case (CallObj e s ta e' s' M es T E)
have red: extTA,P,t ⊢ ⟨e,s⟩ -ta→ ⟨e',s'⟩
and IH: ∧E T. [E ⊢ s √; P,E,hp s ⊢ e : T; P,hp s ⊢ t √t]
  ⇒ ∃ U. P,E,hp s' ⊢ e' : U ∧ P ⊢ U ≤ T
and conf: E ⊢ s √ and wt: P,E,hp s ⊢ e.M(es) : T
and tconf: P,hp s ⊢ t √t by fact+
— We distinguish if e has type NT or a Class type
from wt show ?case
proof(rule WTrt-elim-cases)
fix T' C Ts meth D Us
assume wte: P,E,hp s ⊢ e : T' and icto: class-type-of' T' = [C]
and method: P ⊢ C sees M:Ts→T = meth in D
and wtes: P,E,hp s ⊢ es [:] Us and subs: P ⊢ Us [≤] Ts
obtain U where wte': P,E,hp s' ⊢ e' : U and UsubC: P ⊢ U ≤ T'
using IH[OF conf wte tconf] by blast
show ?thesis
proof(cases U = NT)
case True
moreover have P,E,hp s' ⊢ es [:] Us
  by(rule WTrts-hext-mono[OF wtes red-hext-incr[OF red]])
ultimately show ?thesis using wte' by(blast intro!: WTrtCallNT)

```

next
case *False*
with *icto* *UsubC* **obtain** *C'*
where *icto'*: *class-type-of' U = [C']* **and** *subclass*: $P \vdash C' \preceq^* C$
by(*rule widen-is-class-type-of*)

obtain *Ts' T' meth' D'*
where *method'*: $P \vdash C'$ *sees* $M:Ts' \rightarrow T' = meth'$ *in* *D'*
and *subs'*: $P \vdash Ts \leq Ts'$ **and** *sub'*: $P \vdash T' \leq T$
using *Call-lemma*[*OF method subclass wf*] **by** *fast*
have *wtes'*: $P, E, hp \ s' \vdash es \ [:] \ Us$
by(*rule WTrts-heat-mono*[*OF wtes red-heat-incr*[*OF red*]])
show *?thesis* **using** *wtes' wte' icto' subs method' sub' sub'* **by**(*blast intro:widens-trans*)
qed
next
fix *Ts*
assume $P, E, hp \ s \vdash e:NT$
hence $P, E, hp \ s' \vdash e' : NT$ **using** *IH*[*OF conf - tconf*] **by** *fastforce*
moreover
fix *Ts* **assume** *wtes*: $P, E, hp \ s \vdash es \ [:] \ Ts$
have $P, E, hp \ s' \vdash es \ [:] \ Ts$
by(*rule WTrts-heat-mono*[*OF wtes red-heat-incr*[*OF red*]])
ultimately show *?thesis* **by**(*blast intro:WTrtCallNT*)
qed
next
case (*CallParams es s ta es' s' v M T E*)
have *reds*: $extTA, P, t \vdash \langle es, s \rangle \ [-ta \rightarrow] \ \langle es', s' \rangle$
and *IH*: $\bigwedge Ts \ E. \llbracket E \vdash s \ \checkmark; P, E, hp \ s \vdash es \ [:] \ Ts; P, hp \ s \vdash t \ \checkmark t \rrbracket$
 $\implies \exists Ts'. P, E, hp \ s' \vdash es' \ [:] \ Ts' \wedge P \vdash Ts' \leq Ts$
and *conf*: $E \vdash s \ \checkmark$ **and** *wt*: $P, E, hp \ s \vdash Val \ v \cdot M(es) : T$
and *tconf*: $P, hp \ s \vdash t \ \checkmark t$ **by** *fact+*
from *wt* **show** *?case*
proof (*rule WTrt-elim-cases*)
fix *U C Ts meth D Us*
assume *wte*: $P, E, hp \ s \vdash Val \ v : U$ **and** *icto*: *class-type-of' U = [C]*
and $P \vdash C$ *sees* $M:Ts \rightarrow T = meth$ *in* *D*
and *wtes*: $P, E, hp \ s \vdash es \ [:] \ Us$ **and** $P \vdash Us \leq Ts$
moreover **have** $P, E, hp \ s' \vdash Val \ v : U$
by(*rule WTrt-heat-mono*[*OF wte reds-heat-incr*[*OF reds*]])
moreover **obtain** *Us'* **where** $P, E, hp \ s' \vdash es' \ [:] \ Us'$ $P \vdash Us' \leq Us$
using *IH*[*OF conf wtes tconf*] **by** *blast*
ultimately show *?thesis* **by**(*fastforce intro:WTrtCall widens-trans*)
next
fix *Us*
assume *null*: $P, E, hp \ s \vdash Val \ v : NT$ **and** *wtes*: $P, E, hp \ s \vdash es \ [:] \ Us$
from *null* **have** $v = Null$ **by** *simp*
moreover
obtain *Us'* **where** $P, E, hp \ s' \vdash es' \ [:] \ Us' \wedge P \vdash Us' \leq Us$
using *IH*[*OF conf wtes tconf*] **by** *blast*
ultimately show *?thesis* **by**(*fastforce intro:WTrtCallNT*)
qed
next
case (*RedCall s a U M Ts T pns body D vs T' E*)
have *hp*: *typeof-addr* (*hp s*) $a = [U]$

and method: $P \vdash \text{class-type-of } U \text{ sees } M: Ts \rightarrow T = [(pns, \text{body})]$ in D
and wt: $P, E, hp \ s \vdash \text{addr } a \cdot M(\text{map Val } vs) : T'$ **by** *fact+*
obtain Ts' **where** $wtes: P, E, hp \ s \vdash \text{map Val } vs [:] Ts'$
and subs: $P \vdash Ts' [\leq] Ts$ **and** $T'isT: T' = T$
using *wt method hp wf by(auto 4 3 dest: sees-method-fun)*
from $wtes \ \text{subs}$ **have** $\text{length-vs: length } vs = \text{length } Ts$
by *(auto simp add: WTrts-conv-list-all2 dest!: list-all2-lengthD)*
have $UsubD: P \vdash \text{ty-of-htype } U \leq \text{Class } (\text{class-type-of } U)$
by *(cases U)(simp-all add: widen-array-object)*
from $\text{sees-wf-mdecl}[OF \ \text{wf method}]$ **obtain** T''
where $wtbody: P, [this\#pns \mapsto] \text{Class } D\#Ts \vdash \text{body} :: T''$
and $T''subT: P \vdash T'' \leq T$ **and** $\text{length-pns: length } pns = \text{length } Ts$
by *(fastforce simp: wf-mdecl-def simp del: map-upds-twist)*
from $wtbody$ **have** $P, \text{Map.empty}(this\#pns \mapsto) \text{Class } D\#Ts, hp \ s \vdash \text{body} : T''$
by *(rule WT-implies-WTrt)*
hence $P, E(this\#pns \mapsto) \text{Class } D\#Ts, hp \ s \vdash \text{body} : T''$
by *(rule WTrt-env-mono) simp*
hence $P, E, hp \ s \vdash \text{blocks } (this\#pns) (\text{Class } D\#Ts) (\text{Addr } a\#vs) \text{body} : T''$
using $wtes \ \text{subs} \ \text{hp} \ \text{sees-method-decl-above}[OF \ \text{method}] \ \text{length-vs} \ \text{length-pns} \ UsubD$
by *(auto simp add: wt-blocks rel-list-all2-Cons2 intro: widen-trans)*
with $T''subT \ T'isT$ **show** $?case$ **by** *blast*
next
case $(\text{RedCallExternal } s \ a \ U \ M \ Ts \ T' \ D \ vs \ ta \ va \ h' \ ta' \ e' \ s')$
from $\langle P, t \vdash \langle a \cdot M(vs), hp \ s \rangle -ta \rightarrow \text{ext } \langle va, h' \rangle \rangle$ **have** $hp \ s \leq h'$ **by** *(rule red-external-hext)*
with $\langle P, E, hp \ s \vdash \text{addr } a \cdot M(\text{map Val } vs) : T \rangle$
have $P, E, h' \vdash \text{addr } a \cdot M(\text{map Val } vs) : T$ **by** *(rule WTrt-hext-mono)*
moreover from $\langle \text{typeof-addr } (hp \ s) \ a = [U] \rangle \langle P \vdash \text{class-type-of } U \text{ sees } M: Ts \rightarrow T' = \text{Native in } D \rangle$
 $\langle P, E, hp \ s \vdash \text{addr } a \cdot M(\text{map Val } vs) : T \rangle$
have $P, hp \ s \vdash a \cdot M(vs) : T'$
by *(fastforce simp add: external-WT'-iff dest: sees-method-fun)*
ultimately show $?case$ **using** *RedCallExternal*
by *(auto 4 3 intro: red-external-conf-extRet[OF wf] intro!: wt-external-call simp add: sconf-def dest: sees-method-fun[where C=class-type-of U])*
next
case RedCallNull **thus** $?case$ **unfolding** *sconf-def*
by *(fastforce simp add: xcpt-subcls-Throwable[OF - wf])*
next
case $(\text{BlockRed } e \ h \ x \ V \ vo \ ta \ e' \ h' \ x' \ T \ T' \ E)$
note $IH = \langle \wedge T \ E. [E \vdash (h, x(V := vo)) \checkmark]; P, E, hp \ (h, x(V := vo)) \vdash e : T; P, hp \ (h, x(V := vo)) \vdash t \checkmark \rangle$
 $\implies \exists T'. P, E, hp \ (h', x') \vdash e' : T' \wedge P \vdash T' \leq T$ *[simplified]*
from $\langle P, E, hp \ (h, x) \vdash \{V:T=vo; e\} : T' \rangle$ **have** $P, E(V \mapsto T), h \vdash e : T'$ **by** *(cases vo, auto)*
moreover from $\langle E \vdash (h, x) \checkmark \rangle \langle P, E, hp \ (h, x) \vdash \{V:T=vo; e\} : T' \rangle$
have $(E(V \mapsto T)) \vdash (h, x(V := vo)) \checkmark$
by *(cases vo)(simp add: lconf-def sconf-def, auto simp add: sconf-def conf-def intro: lconf-upd2)*
ultimately obtain T'' **where** $wt': P, E(V \mapsto T), h' \vdash e' : T'' \ P \vdash T'' \leq T'$ **using** $\langle P, hp \ (h, x) \vdash t \checkmark \rangle$
by *(auto dest: IH)*
{ fix v
assume $vo: x' \ V = [v]$
from $\langle (E(V \mapsto T)) \vdash (h, x(V := vo)) \checkmark \rangle \langle \text{extTA}, P, t \vdash \langle e, (h, x(V := vo)) \rangle -ta \rightarrow \langle e', (h', x') \rangle \rangle$
 $\langle P, E(V \mapsto T), h \vdash e : T' \rangle$
have $P, h' \vdash x' (: \leq) (E(V \mapsto T))$ **by** *(auto simp add: sconf-def dest: red-preserves-lconf)*
with vo **have** $\exists T'. \text{typeof}_{h'} \ v = [T'] \wedge P \vdash T' \leq T$ **by** *(fastforce simp add: sconf-def lconf-def*

conf-def)
then obtain T' **where** $\text{typeof}_{h'} v = \lfloor T' \rfloor$ $P \vdash T' \leq T$ **by** *blast*
hence $?case$ **using** $wt' vo$ **by**(*auto*) }
moreover
{ **assume** $x' V = None$ **with** wt' **have** $?case$ **by**(*auto*) }
ultimately show $?case$ **by** *blast*
next
case *RedBlock* **thus** $?case$ **by** *auto*
next
case (*SynchronizedRed1* $o' s ta o'' s' e T E$)
have $red: extTA, P, t \vdash \langle o', s \rangle -ta \rightarrow \langle o'', s' \rangle$ **by** *fact*
have $IH: \bigwedge T E. \llbracket E \vdash s \checkmark; P, E, hp s \vdash o' : T; P, hp s \vdash t \checkmark t \rrbracket \implies \exists T'. P, E, hp s' \vdash o'' : T' \wedge P \vdash T' \leq T$ **by** *fact*
have $conf: E \vdash s \checkmark$ **by** *fact*
have $wt: P, E, hp s \vdash sync(o') e : T$ **by** *fact+*
thus $?case$
proof(*rule WTrt-elim-cases*)
fix To
assume $wto: P, E, hp s \vdash o' : To$
and $refT: is-refT To$
and $wte: P, E, hp s \vdash e : T$
from $IH[OF conf wto \langle P, hp s \vdash t \checkmark t \rangle]$ **obtain** To' **where** $P, E, hp s' \vdash o'' : To'$ **and** $sub: P \vdash To' \leq To$ **by** *auto*
moreover **have** $P, E, hp s' \vdash e : T$
by(*rule WTrt-heat-mono[OF wte red-heat-incr[OF red]]*)
moreover **have** $is-refT To'$ **using** $refT sub$ **by**(*auto intro: widen-refT*)
ultimately show $?thesis$ **by**(*auto*)
qed
next
case *SynchronizedNull* **thus** $?case$ **unfolding** *sconf-def*
by(*fastforce simp add: xcpt-subcls-Throwable[OF - wf]*)
next
case *LockSynchronized* **thus** $?case$ **by**(*auto*)
next
case (*SynchronizedRed2* $e s ta e' s' a T E$)
have $red: extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle$ **by** *fact*
have $IH: \bigwedge T E. \llbracket E \vdash s \checkmark; P, E, hp s \vdash e : T; P, hp s \vdash t \checkmark t \rrbracket \implies \exists T'. P, E, hp s' \vdash e' : T' \wedge P \vdash T' \leq T$ **by** *fact*
have $conf: E \vdash s \checkmark$ **by** *fact*
have $wt: P, E, hp s \vdash insync(a) e : T$ **by** *fact*
thus $?case$
proof(*rule WTrt-elim-cases*)
fix Ta
assume $P, E, hp s \vdash e : T$
and $hpa: \text{typeof-addr} (hp s) a = \lfloor Ta \rfloor$
from $\langle P, E, hp s \vdash e : T \rangle conf \langle P, hp s \vdash t \checkmark t \rangle$ **obtain** T'
where $P, E, hp s' \vdash e' : T' P \vdash T' \leq T$ **by**(*blast dest: IH*)
moreover **from** red **have** $hext: hp s \leq hp s'$ **by**(*auto dest: red-heat-incr*)
with hpa **have** $P, E, hp s' \vdash \text{addr } a : \text{ty-of-htype } Ta$
by(*auto intro: typeof-addr-heat-mono*)
ultimately show $?thesis$ **by** *auto*
qed
next
case *UnlockSynchronized* **thus** $?case$ **by**(*auto*)

```

next
  case SeqRed thus ?case
    apply(auto)
    apply(druse WTrt-heat-mono[OF - red-heat-incr], assumption)
    by auto
next
  case (CondRed b s ta b' s' e1 e2 T E)
  have red: extTA,P,t ⊢ ⟨b,s⟩ -ta→ ⟨b',s'⟩ by fact
  have IH: ∧T E. [[E ⊢ s √; P,E,hp s ⊢ b : T; P,hp s ⊢ t √t]] ⇒ ∃ T'. P,E,hp s' ⊢ b' : T' ∧ P ⊢
  T' ≤ T by fact
  have conf: E ⊢ s √ by fact
  have wt: P,E,hp s ⊢ if (b) e1 else e2 : T by fact
  thus ?case
  proof(rule WTrt-elim-cases)
    fix T1 T2
    assume wtb: P,E,hp s ⊢ b : Boolean
    and wte1: P,E,hp s ⊢ e1 : T1
    and wte2: P,E,hp s ⊢ e2 : T2
    and lub: P ⊢ lub(T1, T2) = T
    from IH[OF conf wtb ⟨P,hp s ⊢ t √t⟩] have P,E,hp s' ⊢ b' : Boolean by(auto)
    moreover have P,E,hp s' ⊢ e1 : T1
      by(rule WTrt-heat-mono[OF wte1 red-heat-incr[OF red]])
    moreover have P,E,hp s' ⊢ e2 : T2
      by(rule WTrt-heat-mono[OF wte2 red-heat-incr[OF red]])
    ultimately show ?thesis using lub by auto
  qed
next
  case (ThrowRed e s ta e' s' T E)
  have IH: ∧T E. [[E ⊢ s √; P,E,hp s ⊢ e : T; P,hp s ⊢ t √t]] ⇒ ∃ T'. P,E,hp s' ⊢ e' : T' ∧ P ⊢
  T' ≤ T by fact
  have conf: E ⊢ s √ by fact
  have wt: P,E,hp s ⊢ throw e : T by fact
  then obtain T'
    where wte: P,E,hp s ⊢ e : T'
    and nobject: P ⊢ T' ≤ Class Throwable by auto
  from IH[OF conf wte ⟨P,hp s ⊢ t √t⟩] obtain T''
    where wte': P,E,hp s' ⊢ e' : T''
    and PT'T'': P ⊢ T'' ≤ T' by blast
  from nobject PT'T'' have P ⊢ T'' ≤ Class Throwable
    by(auto simp add: widen-Class)(erule notE, rule rtranclp-trans)
  hence P,E,hp s' ⊢ throw e' : T using wte' PT'T''
    by -(erule WTrtThrow)
  thus ?case by(auto)
next
  case RedThrowNull thus ?case unfolding sconf-def
    by(fastforce simp add: xcpt-subcls-Throwable[OF - wf])
next
  case (TryRed e s ta e' s' C V e2 T E)
  have red: extTA,P,t ⊢ ⟨e,s⟩ -ta→ ⟨e',s'⟩ by fact
  have IH: ∧T E. [[E ⊢ s √; P,E,hp s ⊢ e : T; P,hp s ⊢ t √t]] ⇒ ∃ T'. P,E,hp s' ⊢ e' : T' ∧ P ⊢
  T' ≤ T by fact
  have conf: E ⊢ s √ by fact
  have wt: P,E,hp s ⊢ try e catch(C V) e2 : T by fact
  thus ?case

```

```

proof(rule WTrt-elim-cases)
  fix T1
  assume wte: P,E,hp s ⊢ e : T1
    and wte2: P,E(V ↦ Class C),hp s ⊢ e2 : T
    and sub: P ⊢ T1 ≤ T
  from IH[OF conf wte ⟨P,hp s ⊢ t √t⟩] obtain T1' where P,E,hp s' ⊢ e' : T1' and P ⊢ T1' ≤
T1 by(auto)
  moreover have P,E(V ↦ Class C),hp s' ⊢ e2 : T
    by(rule WTrt-heat-mono[OF wte2 red-heat-incr[OF red]])
  ultimately show ?thesis using sub by(auto elim: widen-trans)
qed
next
  case RedTryFail thus ?case unfolding sconf-def
    by(fastforce simp add: xcpt-subcls-Throwable[OF - wf])
next
  case RedSeq thus ?case by auto
next
  case RedCondT thus ?case by(auto dest: is-lub-upper)
next
  case RedCondF thus ?case by(auto dest: is-lub-upper)
next
  case RedWhile thus ?case by(fastforce)
next
  case RedTry thus ?case by auto
next
  case RedTryCatch thus ?case by(fastforce)
next
  case (ListRed1 e s ta e' s' es Ts E)
  note IH = ⟨ $\bigwedge T E. \llbracket E \vdash s \sqrt{}; P, E, hp s \vdash e : T; P, hp s \vdash t \sqrt{} \rrbracket \implies \exists T'. P, E, hp s' \vdash e' : T' \wedge P \vdash T' \leq T$ ⟩
  from ⟨P,E,hp s ⊢ e # es [:] Ts⟩ obtain T Ts' where Ts = T # Ts' P,E,hp s ⊢ e : T P,E,hp s ⊢
es [:] Ts' by auto
  with IH[of E T] ⟨E ⊢ s √⟩ WTrts-heat-mono[OF ⟨P,E,hp s ⊢ es [:] Ts'⟩ red-heat-incr[OF ⟨extTA,P,t
⊢ ⟨e,s⟩ -ta→ ⟨e',s'⟩⟩]]
  show ?case using ⟨P,hp s ⊢ t √t⟩ by(auto simp add: list-all2-Cons2 intro: widens-refl)
next
  case ListRed2 thus ?case
    by(fastforce dest: heat-typeof-mono[OF reds-heat-incr])
qed(fastforce)+

end

```

4.14 Progress and type safety theorem for the multithreaded system

```

theory ProgressThreaded
imports
  Threaded
  TypeSafe
  ../Framework/FWProgress
begin

```

```

lemma lock-ok-ls-Some-ex-ts-not-final:

```

assumes *lock*: *lock-ok* *ls* *ts*
and *hl*: *has-lock* (*ls* \$ *l*) *t*
shows $\exists e\ x\ ln. ts\ t = \llbracket ((e, x), ln) \rrbracket \wedge \neg\ final\ e$
proof –
from *lock* **have** *lock-thread-ok* *ls* *ts*
by(*rule* *lock-ok-lock-thread-ok*)
with *hl* **obtain** *e* *x* *ln*
where *tst*: *ts* *t* = $\llbracket ((e, x), ln) \rrbracket$
by(*auto* *dest*!: *lock-thread-okD*)
{ **assume** *final* *e*
hence *expr-locks* *e* *l* = 0 **by**(*rule* *final-locks*)
with *lock* *tst* **have** *has-locks* (*ls* \$ *l*) *t* = 0
by(*auto* *dest*: *lock-okD2*[*rule-format*, **where** *l*=*l*])
with *hl* **have** *False* **by** *simp* }
with *tst* **show** *?thesis* **by** *auto*
qed

4.14.1 Preservation lemmata

4.14.2 Definite assignment

abbreviation

def-ass-ts-ok :: (*'addr*, *'thread-id*, *'addr* *expr* × *'addr* *locals*) *thread-info* ⇒ *'heap* ⇒ *bool*

where

def-ass-ts-ok ≡ *ts-ok* ($\lambda t\ (e, x)\ h.\ \mathcal{D}\ e\ \llbracket dom\ x \rrbracket$)

context *J-heap-base* **begin**

lemma **assumes** *wf*: *wf-J-prog* *P*

shows *red-def-ass-new-thread*:

$\llbracket P, t \vdash \langle e, s \rangle \rightarrow \langle e', s' \rangle; NewThread\ t''\ (e'', x'')\ c'' \in set\ \{\{ta\}_t\} \rrbracket \implies \mathcal{D}\ e''\ \llbracket dom\ x'' \rrbracket$

and *reds-def-ass-new-thread*:

$\llbracket P, t \vdash \langle es, s \rangle \rightarrow \langle es', s' \rangle; NewThread\ t''\ (e'', x'')\ c'' \in set\ \{\{ta\}_t\} \rrbracket \implies \mathcal{D}\ e''\ \llbracket dom\ x'' \rrbracket$

proof(*induct* *rule*: *red-reds.inducts*)

case (*RedCallExternal* *s* *a* *T* *M* *vs* *ta* *va* *h'* *ta'* *e'* *s'*)

then **obtain** *C* *fs* *a* **where** *subThread*: $P \vdash C \preceq^* Thread$ **and** *ext*: *extNTA2J* *P* (*C*, *run*, *a*) = (*e''*, *x''*)

by(*fastforce* *dest*: *red-external-new-thread-sub-thread*)

from *sub-Thread-sees-run*[*OF* *wf* *subThread*] **obtain** *D* *pns* *body*

where *sees*: $P \vdash C\ sees\ run: \llbracket \rightarrow Void = \llbracket (pns, body) \rrbracket$ **in** *D* **by** *auto*

from *sees-wf-mdecl*[*OF* *wf* *this*] **have** $\mathcal{D}\ body\ \llbracket \{this\} \rrbracket$

by(*auto* *simp* *add*: *wf-mdecl-def*)

with *sees* *ext* **show** *?case* **by**(*clarsimp* *simp* *del*: *fun-upd-apply*)

qed(*auto* *simp* *add*: *ta-upd-simps*)

lemma *lifting-wf-def-ass*: *wf-J-prog* *P* ⇒ *lifting-wf* *final-expr* (*mred* *P*) ($\lambda t\ (e, x)\ m.\ \mathcal{D}\ e\ \llbracket dom\ x \rrbracket$)

apply(*unfold-locales*)

apply(*auto* *dest*: *red-preserves-defass* *red-def-ass-new-thread*)

done

lemma *def-ass-ts-ok-J-start-state*:

$\llbracket wf\text{-}J\text{-prog}\ P; P \vdash C\ sees\ M:Ts \rightarrow T = \llbracket (pns, body) \rrbracket\ in\ D; length\ vs = length\ Ts \rrbracket \implies$

def-ass-ts-ok (*thr* (*J-start-state* *P* *C* *M* *vs*)) *h*


```

apply(rule ts-okI)
apply(drule (1) sees-wf-mdecl)
apply(clarsimp simp add: wf-mdecl-def start-state-def split: if-split-asm)
done

```

end

4.14.3 typeability

context *J-heap-base* **begin**

definition *type-ok* :: 'addr *J-prog* \Rightarrow *env* \times *ty* \Rightarrow 'addr *expr* \Rightarrow 'heap \Rightarrow *bool*
where *type-ok* *P* \equiv ($\lambda(E, T) e$ c. ($\exists T'. (P, E, c \vdash e : T' \wedge P \vdash T' \leq T)$))

definition *J-sconf-type-ET-start* :: 'm *prog* \Rightarrow *cname* \Rightarrow *mname* \Rightarrow ('thread-id \rightarrow (*env* \times *ty*))

where

J-sconf-type-ET-start *P C M* \equiv
 let (\cdot , \cdot , *T*, \cdot) = *method* *P C M*
 in ([*start-tid* \mapsto (*Map.empty*, *T*)])

lemma *fixes* *E* :: *env*

assumes *wf*: *wf-J-prog* *P*

shows *red-type-newthread*:

$\llbracket P, t \vdash \langle e, s \rangle \text{--}ta \rightarrow \langle e', s' \rangle; P, E, hp \ s \vdash e : T; NewThread \ t'' (e'', x'') (hp \ s') \in set \ \{ta\}_t \rrbracket$
 $\implies \exists E \ T. P, E, hp \ s' \vdash e'' : T \wedge P, hp \ s' \vdash x'' (\leq) E$

and *reds-type-newthread*:

$\llbracket P, t \vdash \langle es, s \rangle \text{--}ta \rightarrow \langle es', s' \rangle; NewThread \ t'' (e'', x'') (hp \ s') \in set \ \{ta\}_t; P, E, hp \ s \vdash es \ [:] \ Ts \rrbracket$
 $\implies \exists E \ T. P, E, hp \ s' \vdash e'' : T \wedge P, hp \ s' \vdash x'' (\leq) E$

proof(*induct arbitrary: E T and E Ts rule: red-reds.inducts*)

case (*RedCallExternal* *s a U M Ts T' D vs ta va h' ta' e' s'*)

from $\langle NewThread \ t'' (e'', x'') (hp \ s') \in set \ \{ta'\}_t \rangle \langle ta' = extTA2J \ P \ ta \rangle$

obtain *C' M' a'* **where** *nt*: $NewThread \ t'' (C', M', a') (hp \ s') \in set \ \{ta\}_t$

and *extNTA2J* *P (C', M', a') = (e'', x'')* **by** *fastforce*

from *red-external-new-thread-sees*[*OF wf* $\langle P, t \vdash \langle a \cdot M(vs), hp \ s \rangle \text{--}ta \rightarrow ext \ \langle va, h' \rangle \ nt \rangle \langle typeof-addr$
 (*hp s*) *a* = [*U*] \rangle

obtain *T pns body D* **where** *h'a'*: *typeof-addr* *h' a' = [Class-type C']*

and *sees*: $P \vdash C' \ sees \ M'$: $\llbracket \rightarrow T = \llbracket (pns, body) \rrbracket$ *in* *D* **by** *auto*

from *sees-wf-mdecl*[*OF wf sees*] **obtain** *T* **where** $P, [this \mapsto Class \ D] \vdash body :: T$

by(*auto simp add: wf-mdecl-def*)

hence *WTrt* *P (hp s')* [*this* \mapsto *Class D*] *body T* **by**(*rule WT-implies-WTrt*)

moreover from *sees* **have** $P \vdash C' \preceq^* D$ **by**(*rule sees-method-decl-above*)

with *h'a'* **have** $P, h' \vdash [this \mapsto Addr \ a'] (\leq) [this \mapsto Class \ D]$ **by**(*auto simp add: lconf-def conf-def*)

ultimately show *?case* **using** *h'a' sees* $\langle s' = (h', lcl \ s) \rangle$

$\langle extNTA2J \ P (C', M', a') = (e'', x'') \rangle$ **by**(*fastforce intro: sees-method-decl-above*)

qed(*fastforce simp add: ta-upd-simps*)**+**

end

context *J-heap-conf-base* **begin**

definition *sconf-type-ok* :: (*env* \times *ty*) \Rightarrow 'thread-id \Rightarrow 'addr *expr* \times 'addr *locals* \Rightarrow 'heap \Rightarrow *bool*

where

sconf-type-ok *ET t ex h* \equiv *fst* *ET* $\vdash (h, snd \ ex) \surd \wedge$ *type-ok* *P ET (fst \ ex) h* \wedge $P, h \vdash t \surd t$

abbreviation *sconf-type-ts-ok* ::

$(\text{'thread-id} \rightarrow (\text{env} \times \text{ty})) \Rightarrow (\text{'addr}, \text{'thread-id}, \text{'addr expr} \times \text{'addr locals}) \text{ thread-info} \Rightarrow \text{'heap} \Rightarrow \text{bool}$

where

$\text{sconf-type-ts-ok} \equiv \text{ts-inv sconf-type-ok}$

lemma *ts-inv-ok-J-sconf-type-ET-start*:

$\text{ts-inv-ok} (\text{thr} (\text{J-start-state } P \ C \ M \ \text{vs})) (\text{J-sconf-type-ET-start } P \ C \ M)$

by(rule *ts-inv-okI*)(simp add: *start-state-def J-sconf-type-ET-start-def split-beta*)

end

lemma (in *J-heap*) *red-preserve-welltype*:

$\llbracket \text{extTA}, P, t \vdash \langle e, (h, x) \rangle -\text{ta} \rightarrow \langle e', (h', x') \rangle; P, E, h \vdash e'' : T \rrbracket \Longrightarrow P, E, h' \vdash e'' : T$

by(auto elim: *WTrt-heap-mono dest!: red-heap-incr*)

context *J-heap-conf* **begin**

lemma *sconf-type-ts-ok-J-start-state*:

$\llbracket \text{wf-J-prog } P; \text{wf-start-state } P \ C \ M \ \text{vs} \rrbracket$

$\Longrightarrow \text{sconf-type-ts-ok} (\text{J-sconf-type-ET-start } P \ C \ M) (\text{thr} (\text{J-start-state } P \ C \ M \ \text{vs})) (\text{shr} (\text{J-start-state } P \ C \ M \ \text{vs}))$

apply(erule *wf-start-state.cases*)

apply(rule *ts-invI*)

apply(simp add: *start-state-def split: if-split-asm*)

apply(frule (1) *sees-wf-mdecl*)

apply(auto simp add: *wf-mdecl-def J-sconf-type-ET-start-def sconf-type-ok-def sconf-def type-ok-def*)

apply(erule *hconf-start-heap*)

apply(erule *preallocated-start-heap*)

apply(erule *wf-prog-wf-syscls*)

apply(frule *list-all2-lengthD*)

apply(auto simp add: *wt-blocks confs-conv-map intro: WT-implies-WTrt*)[1]

apply(erule *tconf-start-heap-start-tid*)

apply(erule *wf-prog-wf-syscls*)

done

lemma *J-start-state-sconf-type-ok*:

assumes *wf: wf-J-prog P*

and *ok: wf-start-state P C M vs*

shows *ts-ok* $(\lambda t \ x \ h. \exists ET. \text{sconf-type-ok } ET \ t \ x \ h) (\text{thr} (\text{J-start-state } P \ C \ M \ \text{vs})) \text{ start-heap}$

using *sconf-type-ts-ok-J-start-state*[*OF assms*]

unfolding *shr-start-state* **by**(rule *ts-inv-into-ts-ok-Ex*)

end

context *J-conf-read* **begin**

lemma *red-preserves-type-ok*:

$\llbracket \text{extTA}, P, t \vdash \langle e, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle; \text{wf-J-prog } P; E \vdash s \ \checkmark; \text{type-ok } P \ (E, T) \ e \ (hp \ s); P, hp \ s \vdash t \ \checkmark/t \rrbracket$

$\Longrightarrow \text{type-ok } P \ (E, T) \ e' \ (hp \ s')$

apply(*clarsimp simp add: type-ok-def*)

apply(*subgoal-tac* $\exists T''. P, E, hp \ s' \vdash e' : T'' \wedge P \vdash T'' \leq T'$)

apply(*fast elim: widen-trans*)

by(rule *subject-reduction*)

lemma *lifting-inv-sconf-subject-ok*:

assumes *wf*: *wf-J-prog P*
 shows *lifting-inv final-expr (mred P) sconf-type-ok*
 proof(*unfold-locales*)
 fix *t x m ta x' m' i*
 assume *mred*: *mred P t (x, m) ta (x', m')*
 and *sconf-type-ok i t x m*
 moreover obtain *e l* where *x [simp]: x = (e, l) by(cases x, auto)*
 moreover obtain *e' l'* where *x' [simp]: x' = (e', l') by(cases x', auto)*
 moreover obtain *E T* where *i [simp]: i = (E, T) by(cases i, auto)*
 ultimately have *sconf-type: sconf-type-ok (E, T) t (e, l) m*
 and *red: P, t ⊢ ⟨e, (m, l)⟩ -ta→ ⟨e', (m', l')⟩ by auto*
 from *sconf-type* have *sconf: E ⊢ (m, l) √ and type-ok P (E, T) e m and tconf: P, m ⊢ t √/t*
 by(*auto simp add: sconf-type-ok-def*)
 then obtain *T'* where *P, E, m ⊢ e : T' P ⊢ T' ≤ T by(auto simp add: type-ok-def)*
 from *⟨E ⊢ (m, l) √⟩ ⟨P, E, m ⊢ e : T'⟩ red tconf*
 have *E ⊢ (m', l') √ by(auto elim: red-preserves-sconf)*
 moreover
 from *red ⟨P, E, m ⊢ e : T'⟩ wf ⟨E ⊢ (m, l) √⟩ tconf*
 obtain *T''* where *P, E, m' ⊢ e' : T'' P ⊢ T'' ≤ T'*
 by(*auto dest: subject-reduction*)
 note *⟨P, E, m' ⊢ e' : T''⟩*
 moreover
 from *⟨P ⊢ T'' ≤ T'⟩ ⟨P ⊢ T' ≤ T⟩*
 have *P ⊢ T'' ≤ T by(rule widen-trans)*
 moreover from *mred tconf* have *P, m' ⊢ t √/t by(rule red-tconf.preserves-red)*
 ultimately have *sconf-type-ok (E, T) t (e', l') m'*
 by(*auto simp add: sconf-type-ok-def type-ok-def*)
 thus *sconf-type-ok i t x' m' by simp*
 next
 fix *t x m ta x' m' i t'' x''*
 assume *mred*: *mred P t (x, m) ta (x', m')*
 and *sconf-type-ok i t x m*
 and *NewThread t'' x'' m' ∈ set {ta}_t*
 moreover obtain *e l* where *x [simp]: x = (e, l) by(cases x, auto)*
 moreover obtain *e' l'* where *x' [simp]: x' = (e', l') by(cases x', auto)*
 moreover obtain *E T* where *i [simp]: i = (E, T) by(cases i, auto)*
 moreover obtain *e'' l''* where *x'' [simp]: x'' = (e'', l'') by(cases x'', auto)*
 ultimately have *sconf-type: sconf-type-ok (E, T) t (e, l) m*
 and *red: P, t ⊢ ⟨e, (m, l)⟩ -ta→ ⟨e', (m', l')⟩*
 and *nt: NewThread t'' (e'', l'') m' ∈ set {ta}_t by auto*
 from *sconf-type* have *sconf: E ⊢ (m, l) √ and type-ok P (E, T) e m and tconf: P, m ⊢ t √/t*
 by(*auto simp add: sconf-type-ok-def*)
 then obtain *T'* where *P, E, m ⊢ e : T' P ⊢ T' ≤ T by(auto simp add: type-ok-def)*
 from *nt ⟨P, E, m ⊢ e : T'⟩ red* have *∃ E T. P, E, m' ⊢ e'' : T ∧ P, m' ⊢ l'' (:≤) E*
 by(*fastforce dest: red-type-newthread[OF wf]*)
 then obtain *E'' T''* where *P, E'', m' ⊢ e'' : T'' P, m' ⊢ l'' (:≤) E'' by blast*
 moreover
 from *sconf red ⟨P, E, m ⊢ e : T'⟩ tconf* have *E ⊢ (m', l') √*
 by(*auto intro: red-preserves-sconf*)
 moreover from *mred tconf ⟨NewThread t'' x'' m' ∈ set {ta}_t⟩* have *P, m' ⊢ t'' √/t*
 by(*rule red-tconf.preserves-NewThread*)
 ultimately show *∃ i''. sconf-type-ok i'' t'' x'' m'*
 by(*auto simp add: sconf-type-ok-def type-ok-def sconf-def*)

next

```

fix  $t x m ta x' m' i i'' t'' x''$ 
assume  $mred: mred P t (x, m) ta (x', m')$ 
  and  $sconf\text{-}type\text{-}ok i t x m$ 
  and  $sconf\text{-}type\text{-}ok i'' t'' x'' m$ 
moreover obtain  $e l$  where  $x [simp]: x = (e, l)$  by(cases  $x$ , auto)
moreover obtain  $e' l'$  where  $x' [simp]: x' = (e', l')$  by(cases  $x'$ , auto)
moreover obtain  $E T$  where  $i [simp]: i = (E, T)$  by(cases  $i$ , auto)
moreover obtain  $e'' l''$  where  $x'' [simp]: x'' = (e'', l'')$  by(cases  $x''$ , auto)
moreover obtain  $E'' T''$  where  $i'' [simp]: i'' = (E'', T'')$  by(cases  $i''$ , auto)
ultimately have  $sconf\text{-}type: sconf\text{-}type\text{-}ok (E, T) t (e, l) m$ 
  and  $red: P, t \vdash \langle e, (m, l) \rangle -ta \rightarrow \langle e', (m', l') \rangle$ 
  and  $sc: sconf\text{-}type\text{-}ok (E'', T'') t'' (e'', l'') m$  by auto
from  $sconf\text{-}type$  obtain  $T'$  where  $P, E, m \vdash e : T'$  and  $P, m \vdash t \sqrt{t}$ 
  by(auto simp add: sconf-type-ok-def type-ok-def)
from  $sc$  have  $sconf: E'' \vdash (m, l'') \sqrt{t}$  and  $type\text{-}ok P (E'', T'') e'' m$  and  $P, m \vdash t'' \sqrt{t}$ 
  by(auto simp add: sconf-type-ok-def)
then obtain  $T'''$  where  $P, E'', m \vdash e'' : T'''$   $P \vdash T''' \leq T''$  by(auto simp add: type-ok-def)
moreover from  $red \langle P, E'', m \vdash e'' : T''' \rangle$  have  $P, E'', m' \vdash e'' : T'''$ 
  by(rule red-preserve-welltype)
moreover from  $sconf red \langle P, E, m \vdash e : T' \rangle$  have  $hconf m'$ 
  unfolding  $sconf\text{-}def$  by(auto dest: red-preserves-hconf)
moreover {
  from  $red$  have  $heat m m'$  by(auto dest: red-heat-incr)
  moreover from  $sconf$  have  $P, m \vdash l'' (\leq) E''$  preallocated  $m$ 
    by(simp-all add: sconf-def)
  ultimately have  $P, m' \vdash l'' (\leq) E''$  preallocated  $m'$ 
    by(blast intro: lconf-heat preallocated-heat)+ }
moreover from  $mred \langle P, m \vdash t \sqrt{t} \rangle \langle P, m \vdash t'' \sqrt{t} \rangle$ 
have  $P, m' \vdash t'' \sqrt{t}$  by(rule red-tconf.preserves-other)
ultimately have  $sconf\text{-}type\text{-}ok (E'', T'') t'' (e'', l'') m'$ 
  by(auto simp add: sconf-type-ok-def sconf-def type-ok-def)
thus  $sconf\text{-}type\text{-}ok i'' t'' x'' m'$  by simp

```

qed

end

4.14.4 wf-red

context $J\text{-progress}$ **begin**

context **begin**

```

declare  $red\text{-}mthr.actions\text{-}ok\text{-}iff [simp del]$ 
declare  $red\text{-}mthr.actions\text{-}ok.cases [rule del]$ 
declare  $red\text{-}mthr.actions\text{-}ok.intros [rule del]$ 

```

lemma **assumes** $wf: wf\text{-}prog wf\text{-}md P$

shows $red\text{-}wf\text{-}red\text{-}aux:$

```

[[  $P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \neg red\text{-}mthr.actions\text{-}ok' (ls, (ts, m), ws, is) t ta;$ 
   $sync\text{-}ok e; hconf (hp s); P, hp s \vdash t \sqrt{t};$ 
   $\forall l. has\text{-}locks (ls \$ l) t \geq expr\text{-}locks e l;$ 
   $ws t = None \vee$ 
   $(\exists a vs w T Ts Tr D. call e = [(a, wait, vs)] \wedge typeof\text{-}addr (hp s) a = [T] \wedge P \vdash class\text{-}type\text{-}of T$ 

```

sees wait: $Ts \rightarrow Tr = \text{Native in } D \wedge ws \ t = \lfloor \text{PostWS } w \rfloor \rfloor$
 $\implies \exists e'' \ s'' \ ta'. P, t \vdash \langle e, s \rangle -ta' \rightarrow \langle e'', s'' \rangle \wedge$
 $(\text{red-mthr.actions-ok } (ls, (ts, m), ws, is) \ t \ ta' \vee$
 $\text{red-mthr.actions-ok}' (ls, (ts, m), ws, is) \ t \ ta' \wedge \text{red-mthr.actions-subset } ta' \ ta)$
(is $\llbracket -; -; -; -; -; ?\text{wakeup } e \ s \rrbracket \implies ?\text{concl } e \ s \ ta)$
and *reds-wf-red-aux*:
 $\llbracket P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \neg \text{red-mthr.actions-ok}' (ls, (ts, m), ws, is) \ t \ ta;$
 $\text{sync-oks } es; \text{hconf } (hp \ s); P, hp \ s \vdash t \ \sqrt{t};$
 $\forall l. \text{has-locks } (ls \ \$ \ l) \ t \geq \text{expr-lockss } es \ l;$
 $ws \ t = \text{None} \vee$
 $(\exists a \ vs \ w \ T \ Ts \ T \ Tr \ D. \text{calls } es = \lfloor (a, \text{wait}, vs) \rfloor \wedge \text{typeof-addr } (hp \ s) \ a = \lfloor T \rfloor \wedge P \vdash \text{class-type-of}$
T sees wait: $Ts \rightarrow Tr = \text{Native in } D \wedge ws \ t = \lfloor \text{PostWS } w \rfloor \rfloor$
 $\implies \exists es'' \ s'' \ ta'. P, t \vdash \langle es, s \rangle [-ta' \rightarrow] \langle es'', s'' \rangle \wedge$
 $(\text{red-mthr.actions-ok } (ls, (ts, m), ws, is) \ t \ ta' \vee$
 $\text{red-mthr.actions-ok}' (ls, (ts, m), ws, is) \ t \ ta' \wedge \text{red-mthr.actions-subset } ta' \ ta)$
proof(*induct rule: red-reds.inducts*)
case (*SynchronizedRed2* $e \ s \ ta \ e' \ s' \ a$)
note $IH = \llbracket \neg \text{red-mthr.actions-ok}' (ls, (ts, m), ws, is) \ t \ ta; \text{sync-ok } e; \text{hconf } (hp \ s); P, hp \ s \vdash t \ \sqrt{t};$
 $\forall l. \text{expr-locks } e \ l \leq \text{has-locks } (ls \ \$ \ l) \ t; ?\text{wakeup } e \ s \rrbracket$
 $\implies ?\text{concl } e \ s \ ta$
note $\neg \text{red-mthr.actions-ok}' (ls, (ts, m), ws, is) \ t \ ta$
moreover from $\langle \text{sync-ok } (\text{insync}(a) \ e) \rangle$ **have** $\text{sync-ok } e$ **by** *simp*
moreover note $\langle \text{hconf } (hp \ s) \rangle \langle P, hp \ s \vdash t \ \sqrt{t} \rangle$
moreover from $\forall l. \text{expr-locks } (\text{insync}(a) \ e) \ l \leq \text{has-locks } (ls \ \$ \ l) \ t$
have $\forall l. \text{expr-locks } e \ l \leq \text{has-locks } (ls \ \$ \ l) \ t$ **by**(*force split: if-split-asm*)
moreover from $\langle ?\text{wakeup } (\text{insync}(a) \ e) \ s \rangle$ **have** $?\text{wakeup } e \ s$ **by** *auto*
ultimately have $?\text{concl } e \ s \ ta$ **by**(*rule IH*)
thus $?\text{case}$ **by**(*fastforce intro: red-reds.SynchronizedRed2*)
next
case *RedCall* **thus** $?\text{case}$
by(*auto simp add: is-val-iff contains-insync-conv contains-insyncs-conv red-mthr.actions-ok'-empty*
red-mthr.actions-ok'-ta-upd-obs dest: sees-method-fun)
next
case (*RedCallExternal* $s \ a \ U \ M \ Ts \ T \ D \ vs \ ta \ va \ h' \ ta' \ e' \ s'$)
from $\langle ?\text{wakeup } (\text{addr } a \cdot M(\text{map } \text{Val } vs)) \ s \rangle$
have $\text{wset } (ls, (ts, m), ws, is) \ t = \text{None} \vee (M = \text{wait} \wedge (\exists w. \text{wset } (ls, (ts, m), ws, is) \ t = \lfloor \text{PostWS}$
 $w \rfloor))$ **by** *auto*
with $wf \ \langle P, t \vdash \langle a \cdot M(vs), hp \ s \rangle -ta \rightarrow \text{ext } \langle va, h' \rangle \ \langle P, hp \ s \vdash t \ \sqrt{t} \rangle \ \langle \text{hconf } (hp \ s) \rangle$
obtain $ta'' \ va' \ h''$ **where** $\text{red}' : P, t \vdash \langle a \cdot M(vs), hp \ s \rangle -ta'' \rightarrow \text{ext } \langle va', h'' \rangle$
and $\text{aok} : \text{red-mthr.actions-ok } (ls, (ts, m), ws, is) \ t \ ta'' \vee$
 $\text{red-mthr.actions-ok}' (ls, (ts, m), ws, is) \ t \ ta'' \wedge \text{final-thread.actions-subset } ta'' \ ta$
by(*rule red-external-wf-red*)
from $\text{aok } \langle ta' = \text{extTA2J } P \ ta \rangle$
have $\text{red-mthr.actions-ok } (ls, (ts, m), ws, is) \ t \ (\text{extTA2J } P \ ta'') \vee$
 $\text{red-mthr.actions-ok}' (ls, (ts, m), ws, is) \ t \ (\text{extTA2J } P \ ta'') \wedge \text{red-mthr.actions-subset } (\text{extTA2J}$
 $P \ ta'') \ ta'$
by(*auto simp add: red-mthr.actions-ok'-convert-extTA red-mthr.actions-ok-iff elim: final-thread.actions-subset.cases*
del: subsetI)
moreover from $\text{red}' \ \langle \text{typeof-addr } (hp \ s) \ a = \lfloor U \rfloor \rangle \ \langle P \vdash \text{class-type-of } U \text{ sees } M : Ts \rightarrow T = \text{Native in}$
 $D \rangle$
obtain $s'' \ e''$ **where** $P, t \vdash \langle \text{addr } a \cdot M(\text{map } \text{Val } vs), s \rangle -\text{extTA2J } P \ ta'' \rightarrow \langle e'', s'' \rangle$
by(*fastforce intro: red-reds.RedCallExternal*)
ultimately show $?\text{case}$ **by** *blast*
next

```

case LockSynchronized
hence False by(auto simp add: lock-ok-las'-def finfun-upd-apply ta-upd-simps)
thus ?case ..
next
case (UnlockSynchronized a v s)
from  $\langle \forall l. \text{expr-locks } (\text{insync}(a) \text{ Val } v) \ l \leq \text{has-locks } (ls \ \$ \ l) \ t \rangle$ 
have has-lock (ls $ a) t by(force split: if-split-asm)
with UnlockSynchronized have False by(auto simp add: lock-ok-las'-def finfun-upd-apply ta-upd-simps)
thus ?case ..
next
case (SynchronizedThrow2 a ad s)
from  $\langle \forall l. \text{expr-locks } (\text{insync}(a) \text{ Throw } ad) \ l \leq \text{has-locks } (ls \ \$ \ l) \ t \rangle$ 
have has-lock (ls $ a) t by(force split: if-split-asm)
with SynchronizedThrow2 have False
  by(auto simp add: lock-ok-las'-def finfun-upd-apply ta-upd-simps)
thus ?case ..
next
case BlockRed thus ?case by(simp)(blast intro: red-reds.intros)
qed
(simp-all add: is-val-iff contains-insync-conv contains-insyncs-conv red-mthr.actions-ok'-empty
  red-mthr.actions-ok'-ta-upd-obs thread-action'-to-thread-action.simps red-mthr.actions-ok-iff
  split: if-split-asm del: split-paired-Ex,
  (blast intro: red-reds.intros elim: add-leE)+)

end

end

context J-heap-base begin

lemma shows red-ta-satisfiable:
   $P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies \exists s. \text{red-mthr.actions-ok } s \ t \ ta$ 
  and reds-ta-satisfiable:
   $P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies \exists s. \text{red-mthr.actions-ok } s \ t \ ta$ 
apply(induct rule: red-reds.inducts)
apply(fastforce simp add: lock-ok-las-def finfun-upd-apply intro: exI[where x=K$ None] exI[where
  x=K$ [(t, 0)]] may-lock.intros dest: red-external-ta-satisfiable[where final=final-expr :: ('addr expr
   $\times$  'addr locals) \Rightarrow bool])+)
done

end

context J-typesafe begin

lemma wf-progress:
  assumes wf: wf-J-prog P
  shows progress final-expr (mred P)
  ( $\text{red-mthr.wset-Suspend-ok } P \ (\{s. \text{sync-es-ok } (\text{thr } s) \ (\text{shr } s) \wedge \text{lock-ok } (\text{locks } s) \ (\text{thr } s)\} \cap \{s.$ 
   $\exists Es. \text{sconf-type-ts-ok } Es \ (\text{thr } s) \ (\text{shr } s)\} \cap \{s. \text{def-ass-ts-ok } (\text{thr } s) \ (\text{shr } s)\})$ )
  (is progress - - ?wf-state)
proof
  {
    fix s t x ta x' m' w
    assume mred P t (x, shr s) ta (x', m')
  }

```

```

    and Suspend: Suspend  $w \in \text{set } \{ta\}_w$ 
  moreover obtain  $e \ xs$  where  $x: x = (e, xs)$  by(cases  $x$ )
  moreover obtain  $e' \ xs'$  where  $x': x' = (e', xs')$  by(cases  $x'$ )
  ultimately have  $\text{red}: P, t \vdash \langle e, (\text{shr } s, xs) \rangle -ta\rightarrow \langle e', (m', xs') \rangle$  by simp
  from red-Suspend-is-call[OF red Suspend]
  show  $\neg \text{final-expr } x'$  by(auto simp add:  $x'$ )
}
note Suspend-final = this
{
  fix  $s$ 
  assume  $s: s \in ?wf\text{-state}$ 
  hence lock-thread-ok (locks  $s$ ) (thr  $s$ )
    by(auto dest: red-mthr.wset-Suspend-okD1 intro: lock-ok-lock-thread-ok)
  moreover
  have red-mthr.wset-final-ok (wset  $s$ ) (thr  $s$ )
  proof(rule red-mthr.wset-final-okI)
    fix  $t \ w$ 
    assume wset  $s \ t = \lfloor w \rfloor$ 
    from red-mthr.wset-Suspend-okD2[OF  $s$  this]
    obtain  $x0 \ ta \ x \ m1 \ w' \ ln''$  and  $s0 :: ('addr, 'thread-id, 'heap) \ J\text{-state}$ 
      where  $mred: mred \ P \ t \ (x0, \text{shr } s0) \ ta \ (x, m1)$ 
      and Suspend: Suspend  $w' \in \text{set } \{ta\}_w$ 
      and  $tst: \text{thr } s \ t = \lfloor (x, ln'') \rfloor$  by blast
    from Suspend-final[OF  $mred$  Suspend]  $tst$ 
    show  $\exists x \ ln. \text{thr } s \ t = \lfloor (x, ln) \rfloor \wedge \neg \text{final-expr } x$  by blast
  qed
  ultimately show lock-thread-ok (locks  $s$ ) (thr  $s$ )  $\wedge$  red-mthr.wset-final-ok (wset  $s$ ) (thr  $s$ ) ..
}
next
fix  $s \ t \ ex \ ta \ e'x' \ m'$ 
assume  $wfs: s \in ?wf\text{-state}$ 
  and  $\text{thr } s \ t = \lfloor (ex, \text{no-wait-locks}) \rfloor$ 
  and  $mred \ P \ t \ (ex, \text{shr } s) \ ta \ (e'x', m')$ 
  and  $\text{wait}: \neg \text{waiting} \ (wset \ s \ t)$ 
moreover obtain  $ls \ ts \ m \ ws \ is$  where  $s: s = (ls, (ts, m), ws, is)$  by(cases  $s$ ) fastforce
moreover obtain  $e \ x$  where  $ex: ex = (e, x)$  by(cases  $ex$ )
moreover obtain  $e' \ x'$  where  $e'x': e'x' = (e', x')$  by(cases  $e'x'$ )
ultimately have  $tst: ts \ t = \lfloor (ex, \text{no-wait-locks}) \rfloor$ 
  and  $\text{red}: P, t \vdash \langle e, (m, x) \rangle -ta\rightarrow \langle e', (m', x') \rangle$  by auto
from  $wf$  have  $wwf: wwf\text{-J-prog } P$  by(rule  $wf\text{-prog-wwf-prog}$ )
from  $wfs \ s$  obtain  $Es$  where  $aeos: \text{sync-es-ok } ts \ m$ 
  and  $\text{lockok}: \text{lock-ok } ls \ ts$ 
  and  $\text{sconf-type-ts-ok } Es \ ts \ m$ 
  by(auto dest: red-mthr.wset-Suspend-okD1)
with  $tst \ ex$  obtain  $E \ T$  where  $\text{sconf}: \text{sconf-type-ok } (E, T) \ t \ (e, x) \ m$ 
  and  $\text{aoe}: \text{sync-ok } e$  by(fastforce dest:  $ts\text{-okD } ts\text{-invD}$ )
then obtain  $T'$  where  $\text{hconf } m \ P, E, m \vdash e : T' \text{ preallocated } m$ 
  by(auto simp add:  $\text{sconf-type-ok-def } \text{sconf-def } \text{type-ok-def}$ )
from  $\langle \text{sconf-type-ts-ok } Es \ ts \ m \rangle \ s$  have  $\text{thread-conf } P \ (thr \ s) \ (\text{shr } s)$ 
  by(auto dest:  $ts\text{-invD}$  intro!:  $ts\text{-okI}$  simp add:  $\text{sconf-type-ok-def}$ )
with  $\langle \text{thr } s \ t = \lfloor (ex, \text{no-wait-locks}) \rfloor \rangle$  have  $P, \text{shr } s \vdash t \ \sqrt{t}$  by(auto dest:  $ts\text{-okD}$ )

show  $\exists ta' \ x' \ m'. mred \ P \ t \ (ex, \text{shr } s) \ ta' \ (x', m') \wedge$ 
  ( $\text{red-mthr.actions-ok } s \ t \ ta' \vee \text{red-mthr.actions-ok}' \ s \ t \ ta' \wedge \text{red-mthr.actions-subset } ta' \ ta$ )

```

```

proof(cases red-mthr.actions-ok' s t ta)
  case True
    have red-mthr.actions-subset ta ta ..
    with True ⟨mred P t (ex, shr s) ta (e'x', m')⟩ show ?thesis by blast
  next
    case False
    from lock-okD2[OF lockok, OF tst[unfolded ex]]
    have locks:  $\forall l. \text{has-locks } (ls \ \$ \ l) \ t \geq \text{expr-locks } e \ l$  by simp
    have ws t = None  $\vee (\exists a \ vs \ w \ T \ Ts \ Tr \ D. \text{call } e = \lfloor(a, \text{wait}, \text{vs})\rfloor \wedge \text{typeof-addr } (hp \ (m, \ x)) \ a = \lfloor T \rfloor \wedge P \vdash \text{class-type-of } T \text{ sees wait: } Ts \rightarrow Tr = \text{Native in } D \wedge ws \ t = \lfloor \text{PostWS } w \rfloor)$ 
    proof(cases ws t)
      case None thus ?thesis ..
    next
      case (Some w)
      with red-mthr.wset-Suspend-okD2[OF wfs, of t w] tst ex s
      obtain e0 x0 m0 ta0 w' s1 tta1
        where red0:  $P, t \vdash \langle e0, (m0, x0) \rangle - ta0 \rightarrow \langle e, (shr \ s1, \ x) \rangle$ 
        and Suspend:  $\text{Suspend } w' \in \text{set } \{\lfloor ta0 \rfloor\}_w$ 
        and s1:  $P \vdash s1 \rightarrow tta1 \rightarrow * \ s$  by auto
      from red-Suspend-is-call[OF red0 Suspend] obtain a vs T Ts Tr D
        where call:  $\text{call } e = \lfloor(a, \text{wait}, \text{vs})\rfloor$ 
        and type:  $\text{typeof-addr } m0 \ a = \lfloor T \rfloor$ 
        and iec:  $P \vdash \text{class-type-of } T \text{ sees wait: } Ts \rightarrow Tr = \text{Native in } D$  by fastforce
      from red0 have m0  $\trianglelefteq shr \ s1$  by(auto dest: red-hext-incr)
      also from s1 have shr s1  $\trianglelefteq shr \ s$  by(rule RedT-hext-incr)
      finally have typeof-addr (shr s) a =  $\lfloor T \rfloor$  using type
        by(rule typeof-addr-hext-mono)
      moreover from Some wait s obtain w' where ws t =  $\lfloor \text{PostWS } w' \rfloor$ 
        by(auto simp add: not-waiting-iff)
      ultimately show ?thesis using call iec s by auto
    qed
    from red-wf-red-aux[OF wf red False[unfolded s] aoe - - locks, OF - - this] ⟨hconf m⟩ ⟨P, shr s  $\vdash t \sqrt{t}$ ⟩ ex s
    show ?thesis by fastforce
    qed
  next
    fix s t x
    assume wfs:  $s \in ?wf\text{-state}$ 
    and tst:  $\text{thr } s \ t = \lfloor(x, \text{no-wait-locks})\rfloor$ 
    and nfin:  $\neg \text{final-expr } x$ 
    obtain e xs where x:  $x = (e, \ xs)$  by(cases x)
    from wfs have def-ass-ts-ok (thr s) (shr s) by(auto dest: red-mthr.wset-Suspend-okD1)
    with tst x have DA:  $\mathcal{D} \ e \ \lfloor \text{dom } xs \rfloor$  by(auto dest: ts-okD)
    from wfs obtain Es where sconfg-type-ts-ok Es (thr s) (shr s)
      by(auto dest: red-mthr.wset-Suspend-okD1)
    with tst x obtain E T where sconfg-type-ok (E, T) t (e, xs) (shr s) by(auto dest: ts-invD)
    then obtain T' where hconf (shr s) P, E, shr s  $\vdash e : T'$ 
      by(auto simp add: sconfg-type-ok-def sconfg-def type-ok-def)
    from red-progress(1)[OF wf-prog-wwf-prog[OF wf] this DA, where extTA=extTA2J P and t=t] nfin
    show  $\exists ta \ x' \ m'. \text{mred } P \ t \ (x, \ shr \ s) \ ta \ (x', \ m')$  by fastforce
  next
    fix s t x xm ta xm'
    assume s  $\in ?wf\text{-state}$ 

```



```

and thr s t = [(x, no-wait-locks)]
and mred P t xm ta xm'
and Notified ∈ set {ta}_w ∨ WokenUp ∈ set {ta}_w
thus collect-waits ta = {}
by(auto dest: red-ta-Wakeup-no-Join-no-Lock-no-Interrupt simp: split-beta)
next
fix s t x ta x' m'
assume s ∈ ?wf-state
and thr s t = [(x, no-wait-locks)]
and mred P t (x, shr s) ta (x', m')
thus ∃ s'. red-mthr.actions-ok s' t ta
by(fastforce simp add: split-beta dest!: red-ta-satisfiable)
qed

```

lemma redT-progress-deadlock:

```

assumes wf: wf-J-prog P
and wf-start: wf-start-state P C M vs
and Red: P ⊢ J-start-state P C M vs  $\rightarrow$ ttas $\rightarrow$ * s
and ndead:  $\neg$  red-mthr.deadlock P s
shows ∃ t' ta' s'. P ⊢ s  $\rightarrow$ t'>ta' $\rightarrow$  s'

```

proof –

```

let ?wf-state = red-mthr.wset-Suspend-ok P ({s. sync-es-ok (thr s) (shr s) ∧ lock-ok (locks s) (thr s)} ∩ {s. ∃ Es. sconf-type-ts-ok Es (thr s) (shr s)} ∩ {s. def-ass-ts-ok (thr s) (shr s)})

```

interpret red-mthr: progress

final-expr mred P convert-RA ?wf-state

using wf **by**(rule wf-progress)

from wf-start **obtain** Ts T pns body D

where start: start-heap-ok P ⊢ C sees M:Ts \rightarrow T = [(pns, body)] in D P, start-heap ⊢ vs [≤] Ts

by(cases) auto

from start **have** len: length Ts = length vs **by**(auto dest: list-all2-lengthD)

have invariant3p (mredT P) ?wf-state

```

by(rule red-mthr.invariant3p-wset-Suspend-ok) (intro invariant3p-IntI invariant3p-sync-es-ok-lock-ok[OF wf]
lifting-inv.invariant3p-ts-inv[OF lifting-inv-sconf-subject-ok[OF wf]] lifting-wf.invariant3p-ts-ok[OF
lifting-wf-def-ass[OF wf]])

```

moreover note Red **moreover**

have start': J-start-state P C M vs ∈ ?wf-state

apply(rule red-mthr.wset-Suspend-okI)

```

apply(blast intro: sconf-type-ts-ok-J-start-state sync-es-ok-J-start-state lock-ok-J-start-state def-ass-ts-ok-J-start-state
start wf len len[symmetric] wf-start)

```

apply(simp add: start-state-def split-beta)

done

ultimately have s ∈ ?wf-state **unfolding** red-mthr.RedT-def

by(rule invariant3p-rtrancl3p)

thus ?thesis **using** ndead **by**(rule red-mthr.redT-progress)

qed

lemma redT-progress-deadlocked:

```

assumes wf: wf-J-prog P

```

```

and wf-start: wf-start-state P C M vs

```

```

and Red: P ⊢ J-start-state P C M vs  $\rightarrow$ ttas $\rightarrow$ * s

```

```

and ndead: red-mthr.not-final-thread s t  $\neg$  t ∈ red-mthr.deadlocked P s

```

```

shows ∃ t' ta' s'. P ⊢ s  $\rightarrow$ t'>ta' $\rightarrow$  s'

```

using wf wf-start Red

```

proof(rule redT-progress-deadlock)
  from ndead show  $\neg$  red-mthr.deadlock P s
    unfolding red-mthr.deadlock-eq-deadlocked'
    by(auto simp add: red-mthr.deadlocked'-def)
qed

```

4.14.5 Type safety proof

```

theorem TypeSafetyT:
  fixes C and M and ttas and Es
  defines Es == J-sconf-type-ET-start P C M
  and Es' == upd-invs Es sconf-type-ok (concat (map (thr-a  $\circ$  snd) ttas))
  assumes wf: wf-J-prog P
  and start-wf: wf-start-state P C M vs
  and RedT: P  $\vdash$  J-start-state P C M vs  $\rightarrow$  ttas  $\rightarrow$  s'
  and nored:  $\neg$  ( $\exists$  t ta s''. P  $\vdash$  s'  $\rightarrow$  ta  $\rightarrow$  s'')
  shows thread-conf P (thr s') (shr s')
  and thr s' t =  $\llbracket$ ((e', x'), ln') $\rrbracket \implies$ 
    ( $\exists$  v. e' = Val v  $\wedge$  ( $\exists$  E T. Es' t =  $\llbracket$ (E, T) $\rrbracket \wedge$  P,shr s'  $\vdash$  v : $\leq$  T)  $\wedge$  ln' = no-wait-locks)
     $\vee$  ( $\exists$  a C. e' = Throw a  $\wedge$  typeof-addr (shr s') a =  $\llbracket$ Class-type C $\rrbracket \wedge$  P  $\vdash$  C  $\preceq^*$  Throwable  $\wedge$  ln'
  = no-wait-locks)
     $\vee$  (t  $\in$  red-mthr.deadlocked P s'  $\wedge$  ( $\exists$  E T. Es' t =  $\llbracket$ (E, T) $\rrbracket \wedge$  ( $\exists$  T'. P,E,shr s'  $\vdash$  e' : T'  $\wedge$  P
   $\vdash$  T'  $\leq$  T)))
    (is -  $\implies$  ?thesis2)
  and Es  $\subseteq_m$  Es'
proof -
  from start-wf obtain Ts T pns body D
    where start-heap: start-heap-ok
    and sees: P  $\vdash$  C sees M:Ts $\rightarrow$ T =  $\llbracket$ (pns, body) $\rrbracket$  in D
    and conf: P,start-heap  $\vdash$  vs  $\llbracket$ : $\leq$  $\rrbracket$  Ts
    by cases auto

  from RedT show thread-conf P (thr s') (shr s')
    by(rule red-tconf.RedT-preserves)(rule thread-conf-start-state[OF start-heap wf-prog-wf-syscls[OF wf]])

  show Es  $\subseteq_m$  Es' using RedT ts-inv-ok-J-sconf-type-ET-start
    unfolding Es'-def Es-def by(rule red-mthr.RedT-upd-inv-ext)

  assume thr s' t =  $\llbracket$ ((e', x'), ln') $\rrbracket$ 
  moreover obtain ls' ts' m' ws' is' where s' [simp]: s' = (ls', (ts', m'), ws', is') by(cases s') fastforce
  ultimately have es't: ts' t =  $\llbracket$ ((e', x'), ln') $\rrbracket$  by simp
  from wf have wwf: wwf-J-prog P by(rule wf-prog-wwf-prog)
  from conf have len: length vs = length Ts by(rule list-all2-lengthD)
  from RedT def-ass-ts-ok-J-start-state[OF wf sees len] have defass': def-ass-ts-ok ts' m'
    by(fastforce dest: lifting-wf.RedT-preserves[OF lifting-wf-def-ass, OF wf])
  from RedT sync-es-ok-J-start-state[OF wf sees len[symmetric]] lock-ok-J-start-state[OF wf sees len[symmetric]]
  have lock': lock-ok ls' ts' by (fastforce dest: RedT-preserves-lock-ok[OF wf])
  from RedT sync-es-ok-J-start-state[OF wf sees len[symmetric]] have addr': sync-es-ok ts' m'
    by(fastforce dest: RedT-preserves-sync-ok[OF wf])
  from RedT sconf-type-ts-ok-J-start-state[OF wf start-wf]
  have sconf-subject': sconf-type-ts-ok Es' ts' m' unfolding Es'-def Es-def
    by(fastforce dest: lifting-wf.RedT-invariant[OF lifting-wf-def-ass, OF wf] intro: thread-conf-start-state
  - wf-prog-wf-syscls[OF wf])

```

```

with  $es't$  obtain  $E T$  where  $ET: Es' t = \lfloor (E, T) \rfloor$ 
  and  $sconf\text{-}type\text{-}ok (E, T) t (e', x') m'$  by( $auto\ dest!: ts\text{-}invD$ )
{ assume  $final\ e'$ 
  have  $ln' = no\text{-}wait\text{-}locks$ 
  proof( $rule\ ccontr$ )
    assume  $ln' \neq no\text{-}wait\text{-}locks$ 
    then obtain  $l$  where  $ln' \$ l > 0$ 
      by( $auto\ simp\ add: neq\text{-}no\text{-}wait\text{-}locks\text{-}conv$ )
    from  $lock' es't$  have  $has\text{-}locks (ls' \$ l) t + ln' \$ l = expr\text{-}locks\ e' l$ 
      by( $auto\ dest: lock\text{-}okD2$ )
    with  $\langle ln' \$ l > 0 \rangle$  have  $expr\text{-}locks\ e' l > 0$  by  $simp$ 
    moreover from  $\langle final\ e' \rangle$  have  $expr\text{-}locks\ e' l = 0$  by( $rule\ final\text{-}locks$ )
    ultimately show  $False$  by  $simp$ 
  qed }
note  $ln' = this$ 
{ assume  $\exists v. e' = Val\ v$ 
  then obtain  $v$  where  $v: e' = Val\ v$  by  $blast$ 
  with  $sconf\text{-}subject' ET es't$  have  $P, m' \vdash v : \leq T$ 
    by( $auto\ dest: ts\text{-}invD\ simp\ add: type\text{-}ok\text{-}def\ sconf\text{-}type\text{-}ok\text{-}def\ conf\text{-}def$ )
  moreover from  $v\ ln'$  have  $ln' = no\text{-}wait\text{-}locks$  by( $auto$ )
  ultimately have  $\exists v. e' = Val\ v \wedge (\exists E T. Es' t = \lfloor (E, T) \rfloor) \wedge P, m' \vdash v : \leq T \wedge ln' = no\text{-}wait\text{-}locks$ 
    using  $ET\ v$  by  $blast$  }
moreover
{ assume  $\exists a. e' = Throw\ a$ 
  then obtain  $a$  where  $a: e' = Throw\ a$  by  $blast$ 
  with  $sconf\text{-}subject' ET es't$  have  $\exists T'. P, E, m' \vdash e' : T' \wedge P \vdash T' \leq T$ 
    apply –
    apply( $drule\ ts\text{-}invD, assumption$ )
    by( $clarsimp\ simp\ add: type\text{-}ok\text{-}def\ sconf\text{-}type\text{-}ok\text{-}def$ )
  then obtain  $T'$  where  $P, E, m' \vdash e' : T'$  and  $P \vdash T' \leq T$  by  $blast$ 
  with  $a$  have  $\exists C. typeof\text{-}addr\ m' a = \lfloor Class\text{-}type\ C \rfloor \wedge P \vdash C \preceq^* Throwable$ 
    by( $auto\ simp\ add: widen\text{-}Class$ )
  moreover from  $a\ ln'$  have  $ln' = no\text{-}wait\text{-}locks$  by( $auto$ )
  ultimately have  $\exists a C. e' = Throw\ a \wedge typeof\text{-}addr\ m' a = \lfloor Class\text{-}type\ C \rfloor \wedge P \vdash C \preceq^* Throwable$ 
     $\wedge ln' = no\text{-}wait\text{-}locks$ 
    using  $a$  by  $blast$  }
moreover
{ assume  $nfine': \neg final\ e'$ 
  with  $es't$  have  $red\text{-}mthr.\text{not}\text{-}final\text{-}thread\ s' t$ 
    by( $auto\ intro: red\text{-}mthr.\text{not}\text{-}final\text{-}thread.\text{intros}$ )
  with  $nored$  have  $t \in red\text{-}mthr.\text{deadlocked}\ P\ s'$ 
    by  $-(erule\ contrapos\text{-}np, rule\ redT\text{-}progress\text{-}deadlocked[OF\ wf\ start\text{-}wf\ RedT])$ 
  moreover
  from  $\langle sconf\text{-}type\text{-}ok (E, T) t (e', x') m' \rangle$ 
  obtain  $T''$  where  $P, E, m' \vdash e' : T''\ P \vdash T'' \leq T$ 
    by( $auto\ simp\ add: sconf\text{-}type\text{-}ok\text{-}def\ type\text{-}ok\text{-}def$ )
  with  $ET$  have  $\exists E T. Es' t = \lfloor (E, T) \rfloor \wedge (\exists T'. P, E, m' \vdash e' : T' \wedge P \vdash T' \leq T)$ 
    by  $blast$ 
  ultimately have  $t \in red\text{-}mthr.\text{deadlocked}\ P\ s' \wedge (\exists E T. Es' t = \lfloor (E, T) \rfloor) \wedge (\exists T'. P, E, m' \vdash e' : T' \wedge P \vdash T' \leq T)$  .. }
  ultimately show  $?thesis2$  by  $simp(blast)$ 
qed
end

```

end

4.15 Preservation of Deadlock

theory *Deadlocked*

imports

ProgressThreaded

begin

context *J-progress* begin

lemma *red-wt-hconf-hext*:

assumes *wf*: *wf-J-prog P*

and *hconf*: *hconf H*

and *tconf*: $P, H \vdash t \surd t$

shows $\llbracket \text{convert-extTA } \text{extNTA}, P, t \vdash \langle e, s \rangle \text{ } \text{-ta} \rightarrow \langle e', s' \rangle; P, E, H \vdash e : T; \text{hext } H (hp\ s) \rrbracket$

$\implies \exists ta' e' s'. \text{convert-extTA } \text{extNTA}, P, t \vdash \langle e, (H, lcl\ s) \rangle \text{ } \text{-ta}' \rightarrow \langle e', s' \rangle \wedge$

$\text{collect-locks } \llbracket ta \rrbracket_l = \text{collect-locks } \llbracket ta' \rrbracket_l \wedge \text{collect-cond-actions } \llbracket ta \rrbracket_c = \text{collect-cond-actions } \llbracket ta' \rrbracket_c \wedge$

$\text{collect-interrupts } \llbracket ta \rrbracket_i = \text{collect-interrupts } \llbracket ta' \rrbracket_i$

and $\llbracket \text{convert-extTA } \text{extNTA}, P, t \vdash \langle es, s \rangle \text{ } [-\text{ta} \rightarrow] \langle es', s' \rangle; P, E, H \vdash es \text{ } [:] Ts; \text{hext } H (hp\ s) \rrbracket$

$\implies \exists ta' es' s'. \text{convert-extTA } \text{extNTA}, P, t \vdash \langle es, (H, lcl\ s) \rangle \text{ } [-\text{ta}' \rightarrow] \langle es', s' \rangle \wedge$

$\text{collect-locks } \llbracket ta \rrbracket_l = \text{collect-locks } \llbracket ta' \rrbracket_l \wedge \text{collect-cond-actions } \llbracket ta \rrbracket_c = \text{collect-cond-actions } \llbracket ta' \rrbracket_c \wedge$

$\text{collect-interrupts } \llbracket ta \rrbracket_i = \text{collect-interrupts } \llbracket ta' \rrbracket_i$

proof(*induct arbitrary: E T and E Ts rule: red-reds.inducts*)

case (*RedNew h' a h C l*)

thus ?*case*

by(*cases allocate H (Class-type C) = {}*)(*fastforce simp add: ta-upd-simps intro: RedNewFail red-reds.RedNew*)+

next

case (*RedNewFail h C l*)

thus ?*case*

by(*cases allocate H (Class-type C) = {}*)(*fastforce simp add: ta-upd-simps intro: red-reds.RedNewFail RedNew*)+

next

case *NewArrayRed* thus ?*case* by(*fastforce intro: red-reds.intros*)

next

case (*RedNewArray i h' a h T l E T'*)

thus ?*case*

by(*cases allocate H (Array-type T (nat (sint i))) = {}*)(*fastforce simp add: ta-upd-simps intro: red-reds.RedNewArray RedNewArrayFail*)+

next

case *RedNewArrayNegative* thus ?*case* by(*fastforce intro: red-reds.intros*)

next

case (*RedNewArrayFail i h T l E T'*)

thus ?*case*

by(*cases allocate H (Array-type T (nat (sint i))) = {}*)(*fastforce simp add: ta-upd-simps intro: RedNewArray red-reds.RedNewArrayFail*)+

next

case *CastRed* thus ?*case* by(*fastforce intro: red-reds.intros*)

next

```

case (RedCast s v U T E T')
from ⟨P,E,H ⊢ Cast T (Val v) : T'⟩ show ?case
proof(rule WTrt-elim-cases)
  fix T''
  assume wt: P,E,H ⊢ Val v : T'' T' = T
  thus ?thesis
    by(cases P ⊢ T'' ≤ T)(fastforce intro: red-reds.RedCast red-reds.RedCastFail)+
qed
next
case (RedCastFail s v U T E T')
from ⟨P,E,H ⊢ Cast T (Val v) : T'⟩
obtain T'' where P,E,H ⊢ Val v : T'' T = T' by auto
thus ?case
  by(cases P ⊢ T'' ≤ T)(fastforce intro: red-reds.RedCast red-reds.RedCastFail)+
next
case InstanceOfRed thus ?case by(fastforce intro: red-reds.intros)
next
case RedInstanceOf thus ?case
  using [[hypsubst-thin = true]]
  by auto((rule exI conjI red-reds.RedInstanceOf)+, auto)
next
case BinOpRed1 thus ?case by(fastforce intro: red-reds.intros)
next
case BinOpRed2 thus ?case by(fastforce intro: red-reds.intros)
next
case RedBinOp thus ?case by(fastforce intro: red-reds.intros)
next
case RedBinOpFail thus ?case by(fastforce intro: red-reds.intros)
next
case RedVar thus ?case by(fastforce intro: red-reds.intros)
next
case LAssRed thus ?case by(fastforce intro: red-reds.intros)
next
case RedLAss thus ?case by(fastforce intro: red-reds.intros)
next
case AAccRed1 thus ?case by(fastforce intro: red-reds.intros)
next
case AAccRed2 thus ?case by(fastforce intro: red-reds.intros)
next
case RedAAccNull thus ?case by(fastforce intro: red-reds.intros)
next
case RedAAccBounds thus ?case
  by(fastforce intro: red-reds.RedAAccBounds dest: hext-arrD)
next
case (RedAAcc h a T n i v l E T')
from ⟨P,E,H ⊢ addr a [Val (Intg i)] : T'⟩
have wt: P,E,H ⊢ addr a : T'[ ] by(auto)
with ⟨H ⊆ hp (h, l)⟩ ⟨typeof-addr h a = [Array-type T n]⟩
have Ha: typeof-addr H a = [Array-type T n] by(auto dest: hext-arrD)
with ⟨0 ≤ s i⟩ ⟨sint i < int n⟩
have nat (sint i) < n
  by (simp add: word-sle-eq nat-less-iff)
with Ha have P,H ⊢ a@ACell (nat (sint i)) : T
  by(auto intro: addr-loc-type.intros)

```

```

from heap-read-total[OF hconf this]
obtain v where heap-read H a (ACell (nat (sint i))) v by blast
with Ha ⟨0 ≤ s i⟩ ⟨sint i < int n⟩ show ?case
  by(fastforce intro: red-reds.RedAAcc simp add: ta-upd-simps)
next
  case AAssRed1 thus ?case by(fastforce intro: red-reds.intros)
next
  case AAssRed2 thus ?case by(fastforce intro: red-reds.intros)
next
  case AAssRed3 thus ?case by(fastforce intro: red-reds.intros)
next
  case RedAAssNull thus ?case by(fastforce intro: red-reds.intros)
next
  case RedAAssBounds thus ?case by(fastforce intro: red-reds.RedAAssBounds dest: hevt-arrD)
next
  case (RedAAssStore s a T n i w U E T')
    from ⟨P,E,H ⊢ addr a [Val (Intg i)] := Val w : T'⟩
    obtain T'' T''' where wt: P,E,H ⊢ addr a : T''[]
      and wtw: P,E,H ⊢ Val w : T''' by auto
    with ⟨H ≤ hp s⟩ ⟨typeof-addr (hp s) a = [Array-type T n]⟩
    have Ha: typeof-addr H a = [Array-type T n] by(auto dest: hevt-arrD)
    from ⟨typeofhp s w = [U]⟩ wtw ⟨H ≤ hp s⟩ have typeofH w = [U]
      by(auto dest: type-of-hevt-type-of)
    with Ha ⟨0 ≤ s i⟩ ⟨sint i < int n⟩ ⟨¬ P ⊢ U ≤ T⟩ show ?case
      by(fastforce intro: red-reds.RedAAssStore)
next
  case (RedAAss h a T n i w U h' l E T')
    from ⟨P,E,H ⊢ addr a [Val (Intg i)] := Val w : T'⟩
    obtain T'' T''' where wt: P,E,H ⊢ addr a : T''[]
      and wtw: P,E,H ⊢ Val w : T''' by auto
    with ⟨H ≤ hp (h, l)⟩ ⟨typeof-addr h a = [Array-type T n]⟩
    have Ha: typeof-addr H a = [Array-type T n] by(auto dest: hevt-arrD)
    from ⟨typeofh w = [U]⟩ wtw ⟨H ≤ hp (h, l)⟩ have typeofH w = [U]
      by(auto dest: type-of-hevt-type-of)
    moreover
    with ⟨P ⊢ U ≤ T⟩ have conf: P,H ⊢ w := T
      by(auto simp add: conf-def)
    from ⟨0 ≤ s i⟩ ⟨sint i < int n⟩
    have nat (sint i) < n
      by (simp add: word-sle-eq nat-less-iff)
    with Ha have P,H ⊢ a@ACell (nat (sint i)) : T
      by(auto intro: addr-loc-type.intros)
    from heap-write-total[OF hconf this conf]
    obtain H' where heap-write H a (ACell (nat (sint i))) w H' ..
    ultimately show ?case using ⟨0 ≤ s i⟩ ⟨sint i < int n⟩ Ha ⟨P ⊢ U ≤ T⟩
      by(fastforce simp del: split-paired-Ex intro: red-reds.RedAAss)
next
  case ALengthRed thus ?case by(fastforce intro: red-reds.intros)
next
  case (RedALength h a T n l E T')
    from ⟨P,E,H ⊢ addr a.length : T'⟩
    obtain T'' where [simp]: T' = Integer
      and wta: P,E,H ⊢ addr a : T''[] by(auto)
    then obtain n'' where typeof-addr H a = [Array-type T'' n''] by(auto)

```

```

thus ?case by(fastforce intro: red-reds.RedALength)
next
  case RedALengthNull show ?case by(fastforce intro: red-reds.RedALengthNull)
next
  case FAccRed thus ?case by(fastforce intro: red-reds.intros)
next
  case (RedFAcc h a D F v l E T)
  from  $\langle P, E, H \vdash \text{addr } a \cdot F\{D\} : T \rangle$  obtain U C' fm
    where wt:  $P, E, H \vdash \text{addr } a : U$ 
    and icto: class-type-of' U =  $\lfloor C' \rfloor$ 
    and has:  $P \vdash C' \text{ has } F:T (fm) \text{ in } D$ 
    by(auto)
  then obtain hU where Ha: typeof-addr H a =  $\lfloor hU \rfloor$  U = ty-of-htype hU by(auto)
  with icto  $\langle P \vdash C' \text{ has } F:T (fm) \text{ in } D \rangle$  have  $P, H \vdash a @ CField D F : T$ 
    by(auto intro: addr-loc-type.intros)
  from heap-read-total[OF hconf this]
  obtain v where heap-read H a (CField D F) v by blast
  thus ?case by(fastforce intro: red-reds.RedFAcc simp add: ta-upd-simps)
next
  case RedFAccNull thus ?case by(fastforce intro: red-reds.intros)
next
  case FAssRed1 thus ?case by(fastforce intro: red-reds.intros)
next
  case FAssRed2 thus ?case by(fastforce intro: red-reds.intros)
next
  case RedFAssNull thus ?case by(fastforce intro: red-reds.intros)
next
  case (RedFAss h a D F v h' l E T)
  from  $\langle P, E, H \vdash \text{addr } a \cdot F\{D\} := \text{Val } v : T \rangle$  obtain U C' T' T2 fm
    where wt:  $P, E, H \vdash \text{addr } a : U$ 
    and icto: class-type-of' U =  $\lfloor C' \rfloor$ 
    and has:  $P \vdash C' \text{ has } F:T' (fm) \text{ in } D$ 
    and wtv:  $P, E, H \vdash \text{Val } v : T2$ 
    and T2T:  $P \vdash T2 \leq T' \text{ by}(auto)$ 
  moreover from wt obtain hU where Ha: typeof-addr H a =  $\lfloor hU \rfloor$  U = ty-of-htype hU by(auto)
  with icto has have adal:  $P, H \vdash a @ CField D F : T' \text{ by}(auto \text{ intro: addr-loc-type.intros})$ 
  from wtv T2T have  $P, H \vdash v : \leq T' \text{ by}(auto \text{ simp add: conf-def})$ 
  from heap-write-total[OF hconf adal this]
  obtain h' where heap-write H a (CField D F) v h' ..
  thus ?case by(fastforce intro: red-reds.RedFAss)
next
  case CASRed1 thus ?case by(fastforce intro: red-reds.intros)
next
  case CASRed2 thus ?case by(fastforce intro: red-reds.intros)
next
  case CASRed3 thus ?case by(fastforce intro: red-reds.intros)
next
  case CASNull thus ?case by(fastforce intro: red-reds.intros)
next
  case (RedCASSucceed h a D F v v' h' l)
  note split-paired-Ex[simp del]
  from RedCASSucceed.prem1 obtain T' fm T2 T3 U C where *:
    T = Boolean class-type-of' U =  $\lfloor C \rfloor$   $P \vdash C \text{ has } F:T' (fm) \text{ in } D$ 
    volatile fm  $P \vdash T2 \leq T' P \vdash T3 \leq T'$ 

```

```

  P,E,H ⊢ Val v : T2 P,E,H ⊢ Val v' : T3 P,E,H ⊢ addr a : U by auto
then have adal: P,H ⊢ a@CField D F : T' by(auto intro: addr-loc-type.intros)
from heap-read-total[OF hconf this] obtain v'' where v': heap-read H a (CField D F) v'' by blast
show ?case
proof(cases v'' = v)
  case True
    from * have P,H ⊢ v' :≤ T' by(auto simp add: conf-def)
    from heap-write-total[OF hconf adal this] True * v'
    show ?thesis by(fastforce intro: red-reds.RedCASSucceed)
  next
    case False
    then show ?thesis using * v' by(fastforce intro: RedCASFail)
qed
next
case (RedCASFail h a D F v'' v v' l)
note split-paired-Ex[simp del]
from RedCASFail.prem1 obtain T' fm T2 T3 U C where *:
  T = Boolean class-type-of' U = [C] P ⊢ C has F:T' (fm) in D
  volatile fm P ⊢ T2 ≤ T' P ⊢ T3 ≤ T'
  P,E,H ⊢ Val v : T2 P,E,H ⊢ Val v' : T3 P,E,H ⊢ addr a : U by auto
then have adal: P,H ⊢ a@CField D F : T' by(auto intro: addr-loc-type.intros)
from heap-read-total[OF hconf this] obtain v''' where v'': heap-read H a (CField D F) v''' by blast
show ?case
proof(cases v''' = v)
  case True
    from * have P,H ⊢ v' :≤ T' by(auto simp add: conf-def)
    from heap-write-total[OF hconf adal this] True * v''
    show ?thesis by(fastforce intro: red-reds.RedCASSucceed)
  next
    case False
    then show ?thesis using * v'' by(fastforce intro: red-reds.RedCASFail)
qed
next
case CallObj thus ?case by(fastforce intro: red-reds.intros)
next
case CallParams thus ?case by(fastforce intro: red-reds.intros)
next
case (RedCall s a U M Ts T pns body D vs E T')
from ⟨P,E,H ⊢ addr a·M(map Val vs) : T'⟩
obtain U' C' Ts' meth D' Ts''
  where wta: P,E,H ⊢ addr a : U'
  and icto: class-type-of' U' = [C']
  and sees: P ⊢ C' sees M: Ts'→T' = meth in D'
  and wtes: P,E,H ⊢ map Val vs [:] Ts''
  and widens: P ⊢ Ts'' [≤] Ts' by auto
from wta obtain hU' where Ha: typeof-addr H a = [hU'] U' = ty-of-htype hU' by(auto)
moreover from ⟨typeof-addr (hp s) a = [U]⟩ ⟨H ⊆ hp s⟩ Ha
have [simp]: U = hU' by(auto dest: typeof-addr-hext-mono)
from wtes have length vs = length Ts''
  by(auto intro: map-eq-imp-length-eq)
moreover from widens have length Ts'' = length Ts'
  by(auto dest: widens-lengthD)
moreover from sees icto sees ⟨P ⊢ class-type-of U sees M: Ts'→T = [(pns, body)] in D⟩ Ha
have [simp]: meth = [(pns, body)] by(auto dest: sees-method-fun)

```



```

with sees wf have wf-mdecl wf-J-mdecl P D' (M, Ts', T', [(pns, body)])
  by(auto intro: sees-wf-mdecl)
hence length pns = length Ts' by(simp add: wf-mdecl-def)
ultimately show ?case using sees icto
  by(fastforce intro: red-reds.RedCall)
next
case (RedCallExternal s a U M Ts T' D vs ta va h' ta' e' s')
from ⟨P,E,H ⊢ addr a·M(map Val vs) : T⟩
obtain U' C' Ts' meth D' Ts''
  where wta: P,E,H ⊢ addr a : U' and icto: class-type-of' U' = [C']
  and sees: P ⊢ C' sees M: Ts'→T = meth in D'
  and wtvs: P,E,H ⊢ map Val vs [:] Ts''
  and sub: P ⊢ Ts'' [≤] Ts' by auto
from wta ⟨typeof-addr (hp s) a = [U]⟩ ⟨hext H (hp s)⟩ have [simp]: U' = ty-of-htype U
  by(auto dest: typeof-addr-hext-mono)
with icto have [simp]: C' = class-type-of U by(auto)
from sees ⟨P ⊢ class-type-of U sees M: Ts→T' = Native in D⟩
have [simp]: meth = Native by(auto dest: sees-method-fun)
with wta sees icto wtvs sub have P,H ⊢ a·M(vs) : T
  by(cases U)(auto 4 4 simp add: external-WT'-iff)
from red-external-wt-hconf-hext[OF wf ⟨P,t ⊢ ⟨a·M(vs),hp s⟩ -ta→ext ⟨va,h'⟩ ⟨H ≤ hp s⟩ this
tconf hconf]
  wta icto sees ⟨ta' = convert-extTA extNTA ta⟩ ⟨e' = extRet2J (addr a·M(map Val vs)) va⟩ ⟨s' =
(h', lcl s)⟩
  show ?case by(cases U)(auto 4 5 intro: red-reds.RedCallExternal simp del: split-paired-Ex)
next
case RedCallNull thus ?case by(fastforce intro: red-reds.intros)
next
case (BlockRed e h l V vo ta e' h' l' T E T')
note IH = BlockRed.hyps(2)
from IH[of E(V ↦ T) T'] ⟨P,E,H ⊢ {V:T=vo; e} : T'⟩ ⟨hext H (hp (h, l))⟩
show ?case by(fastforce dest: red-reds.BlockRed)
next
case RedBlock thus ?case by(fastforce intro: red-reds.intros)
next
case SynchronizedRed1 thus ?case by(fastforce intro: red-reds.intros)
next
case SynchronizedNull thus ?case by(fastforce intro: red-reds.intros)
next
case LockSynchronized thus ?case by(fastforce intro: red-reds.intros)
next
case SynchronizedRed2 thus ?case by(fastforce intro: red-reds.intros)
next
case UnlockSynchronized thus ?case by(fastforce intro: red-reds.intros)
next
case SeqRed thus ?case by(fastforce intro: red-reds.intros)
next
case RedSeq thus ?case by(fastforce intro: red-reds.intros)
next
case CondRed thus ?case by(fastforce intro: red-reds.intros)
next
case RedCondT thus ?case by(fastforce intro: red-reds.intros)
next
case RedCondF thus ?case by(fastforce intro: red-reds.intros)

```

```

next
  case RedWhile thus ?case by(fastforce intro: red-reds.intros)
next
  case ThrowRed thus ?case by(fastforce intro: red-reds.intros)
next
  case RedThrowNull thus ?case by(fastforce intro: red-reds.intros)
next
  case TryRed thus ?case by(fastforce intro: red-reds.intros)
next
  case RedTry thus ?case by(fastforce intro: red-reds.intros)
next
  case (RedTryCatch s a D C V e2 E T)
  from ⟨P,E,H ⊢ try Throw a catch(C V) e2 : T⟩
  obtain T' where P,E,H ⊢ addr a : T' by auto
  with ⟨typeof-addr (hp s) a = [Class-type D]⟩ ⟨hext H (hp s)⟩
  have Ha: typeof-addr H a = [Class-type D]
  by(auto dest: typeof-addr-hext-mono)
  with ⟨P ⊢ D ≲* C⟩ show ?case
  by(fastforce intro: red-reds.RedTryCatch)
next
  case (RedTryFail s a D C V e2 E T)
  from ⟨P,E,H ⊢ try Throw a catch(C V) e2 : T⟩
  obtain T' where P,E,H ⊢ addr a : T' by auto
  with ⟨typeof-addr (hp s) a = [Class-type D]⟩ ⟨hext H (hp s)⟩
  have Ha: typeof-addr H a = [Class-type D]
  by(auto dest: typeof-addr-hext-mono)
  with ⟨¬ P ⊢ D ≲* C⟩ show ?case
  by(fastforce intro: red-reds.RedTryFail)
next
  case ListRed1 thus ?case by(fastforce intro: red-reds.intros)
next
  case ListRed2 thus ?case by(fastforce intro: red-reds.intros)
next
  case NewArrayThrow thus ?case by(fastforce intro: red-reds.intros)
next
  case CastThrow thus ?case by(fastforce intro: red-reds.intros)
next
  case InstanceOfThrow thus ?case by(fastforce intro: red-reds.intros)
next
  case BinOpThrow1 thus ?case by(fastforce intro: red-reds.intros)
next
  case BinOpThrow2 thus ?case by(fastforce intro: red-reds.intros)
next
  case LAssThrow thus ?case by(fastforce intro: red-reds.intros)
next
  case AAccThrow1 thus ?case by(fastforce intro: red-reds.intros)
next
  case AAccThrow2 thus ?case by(fastforce intro: red-reds.intros)
next
  case AAssThrow1 thus ?case by(fastforce intro: red-reds.intros)
next
  case AAssThrow2 thus ?case by(fastforce intro: red-reds.intros)
next
  case AAssThrow3 thus ?case by(fastforce intro: red-reds.intros)

```

```

next
  case ALengthThrow thus ?case by(fastforce intro: red-reds.intros)
next
  case FAccThrow thus ?case by(fastforce intro: red-reds.intros)
next
  case FAssThrow1 thus ?case by(fastforce intro: red-reds.intros)
next
  case FAssThrow2 thus ?case by(fastforce intro: red-reds.intros)
next
  case CASThrow thus ?case by(fastforce intro: red-reds.intros)
next
  case CASThrow2 thus ?case by(fastforce intro: red-reds.intros)
next
  case CASThrow3 thus ?case by(fastforce intro: red-reds.intros)
next
  case CallThrowObj thus ?case by(fastforce intro: red-reds.intros)
next
  case CallThrowParams thus ?case by(fastforce intro: red-reds.intros)
next
  case BlockThrow thus ?case by(fastforce intro: red-reds.intros)
next
  case SynchronizedThrow1 thus ?case by(fastforce intro: red-reds.intros)
next
  case SynchronizedThrow2 thus ?case by(fastforce intro: red-reds.intros)
next
  case SeqThrow thus ?case by(fastforce intro: red-reds.intros)
next
  case CondThrow thus ?case by(fastforce intro: red-reds.intros)
next
  case ThrowThrow thus ?case by(fastforce intro: red-reds.intros)
qed

```

lemma *can-lock-devreserp*:

$$\llbracket wf\text{-}J\text{-}prog\ P; red\text{-}mthr.can\text{-}sync\ P\ t\ (e, l)\ h'\ L; P, E, h \vdash e : T; P, h \vdash t \sqrt{t}; hconf\ h; h \sqsubseteq h' \rrbracket \\ \implies red\text{-}mthr.can\text{-}sync\ P\ t\ (e, l)\ h\ L$$

```

apply(erule red-mthr.can-syncE)
apply(clarsimp)
apply(drule red-wt-hconf-hext, assumption+)
apply(simp)
apply(fastforce intro!: red-mthr.can-syncI)
done

```

end

context *J-typesafe* **begin**

lemma *preserve-deadlocked*:

```

assumes wf: wf-J-prog P
shows preserve-deadlocked final-expr (mred P) convert-RA ({s. sync-es-ok (thr s) (shr s)  $\wedge$  lock-ok
(lock s) (thr s)}  $\cap$  {s.  $\exists$  Es. sconf-type-ts-ok Es (thr s) (shr s)}  $\cap$  {s. def-ass-ts-ok (thr s) (shr s)})
  (is preserve-deadlocked - - - ?wf-state)
proof(unfold-locales)
show inv: invariant3p (mredT P) ?wf-state
by(intro invariant3p-IntI invariant3p-sync-es-ok-lock-ok[OF wf] lifting-inv.invariant3p-ts-inv[OF

```

lifting-inv-sconf-subject-ok[*OF wf*] *lifting-wf.invariant3p-ts-ok*[*OF lifting-wf-def-ass*[*OF wf*]]

```

fix s t' ta' s' t x ln
assume wfs: s ∈ ?wf-state
  and redT: P ⊢ s -t'▷ta'→ s'
  and tst: thr s t = [(x, ln)]
from redT have heaxt: shr s ≤ shr s' by(rule redT-heaxt-incr)

from inv redT wfs have wfs': s' ∈ ?wf-state by(rule invariant3pD)
from redT tst obtain x' ln' where ts't: thr s' t = [(x', ln')]
  by(cases thr s' t)(cases s, cases s', auto dest: red-mthr.redT-thread-not-disappear)

from wfs tst obtain E T where wt: P,E,shr s ⊢ fst x : T
  and hconf: hconf (shr s)
  and da: D (fst x) [dom (snd x)]
  and tconf: P,shr s ⊢ t √t
  by(force dest: ts-invD ts-okD simp add: type-ok-def sconf-def sconf-type-ok-def)
from wt heaxt have wt': P,E,shr s' ⊢ fst x : T by(rule WTrt-heaxt-mono)
from wfs' ts't have hconf': hconf (shr s')
  by(auto dest: ts-invD simp add: type-ok-def sconf-def sconf-type-ok-def)

{
assume cs: red-mthr.must-sync P t x (shr s)
from cs have ¬ final (fst x) by(auto elim!: red-mthr.must-syncE simp add: split-beta)

from progress[OF wf-prog-wwf-prog[OF wf] hconf' wt' da this, of extTA2J P t]
obtain e' h x' ta where P,t ⊢ ⟨fst x,(shr s', snd x)⟩ -ta→ ⟨e', (h, x')⟩ by auto
with red-ta-satisfiable[OF this]
show red-mthr.must-sync P t x (shr s')
  by-(rule red-mthr.must-syncI, fastforce simp add: split-beta)
next
fix LT
assume red-mthr.can-sync P t x (shr s') LT
with can-lock-devreserp[OF wf - wt tconf hconf heaxt, of snd x LT]
show ∃ LT'⊆LT. red-mthr.can-sync P t x (shr s) LT' by auto
}
qed

end

end

```

4.16 Program annotation

theory *Annotate*

imports

WellType

begin

abbreviation (output)

unanFAcc :: 'addr expr ⇒ vname ⇒ 'addr expr (⟨⟨--⟩⟩ [10,10] 90)

where

unanFAcc e F ≡ FAcc e F (*STR* '''')

abbreviation (output)

$unanFAss :: 'addr\ expr \Rightarrow vname \Rightarrow 'addr\ expr \Rightarrow 'addr\ expr \langle (\dots := -) \rangle [10,0,90] 90$

where

$unanFAss\ e\ F\ e' \equiv FAss\ e\ F\ (STR\ ''''')\ e'$

definition *array-length-field-name* :: *vname*

where *array-length-field-name* = *STR* "length"

notation (output) *array-length-field-name* ($\langle length \rangle$)**definition** *super* :: *vname*

where *super* = *STR* "super"

lemma *super-neq-this* [*simp*]: *super* \neq *this* *this* \neq *super*

by(*simp-all* *add: this-def super-def*)

inductive *Anno* :: (*ty* \Rightarrow *ty* \Rightarrow *ty* \Rightarrow *bool*) \Rightarrow 'addr *J-prog* \Rightarrow *env* \Rightarrow 'addr *expr* \Rightarrow 'addr *expr* \Rightarrow *bool*

($\langle -, -, \vdash - \rightsquigarrow - \rangle [51,51,0,0,51] 50$)

and *Annos* :: (*ty* \Rightarrow *ty* \Rightarrow *ty* \Rightarrow *bool*) \Rightarrow 'addr *J-prog* \Rightarrow *env* \Rightarrow 'addr *expr list* \Rightarrow 'addr *expr list* \Rightarrow *bool*

($\langle -, -, \vdash - [\rightsquigarrow] - \rangle [51,51,0,0,51] 50$)

for *is-lub* :: *ty* \Rightarrow *ty* \Rightarrow *ty* \Rightarrow *bool* **and** *P* :: 'addr *J-prog*

where

AnnoNew: *is-lub, P, E* \vdash *new C* \rightsquigarrow *new C*

| *AnnoNewArray*: *is-lub, P, E* \vdash *i* \rightsquigarrow *i'* \Longrightarrow *is-lub, P, E* \vdash *newA T[i]* \rightsquigarrow *newA T[i']*

| *AnnoCast*: *is-lub, P, E* \vdash *e* \rightsquigarrow *e'* \Longrightarrow *is-lub, P, E* \vdash *Cast C e* \rightsquigarrow *Cast C e'*

| *AnnoInstanceOf*: *is-lub, P, E* \vdash *e* \rightsquigarrow *e'* \Longrightarrow *is-lub, P, E* \vdash *e instanceof T* \rightsquigarrow *e' instanceof T*

| *AnnoVal*: *is-lub, P, E* \vdash *Val v* \rightsquigarrow *Val v*

| *AnnoVarVar*: $\llbracket E\ V = [T];\ V \neq super \rrbracket \Longrightarrow is-lub, P, E \vdash Var\ V \rightsquigarrow Var\ V$

| *AnnoVarField*:

— There is no need to handle access of array fields explicitly, because arrays do not implement methods, i.e. *this* is always of a *Class* type.

$\llbracket E\ V = None; V \neq super; E\ this = [Class\ C]; P \vdash C\ sees\ V:T\ (fm)\ in\ D \rrbracket$

$\Longrightarrow is-lub, P, E \vdash Var\ V \rightsquigarrow Var\ this \cdot V\{D\}$

| *AnnoBinOp*:

$\llbracket is-lub, P, E \vdash e1 \rightsquigarrow e1'; is-lub, P, E \vdash e2 \rightsquigarrow e2' \rrbracket$

$\Longrightarrow is-lub, P, E \vdash e1 \llcorner bop \llcorner e2 \rightsquigarrow e1' \llcorner bop \llcorner e2'$

| *AnnoLAssVar*:

$\llbracket E\ V = [T]; V \neq super; is-lub, P, E \vdash e \rightsquigarrow e' \rrbracket \Longrightarrow is-lub, P, E \vdash V := e \rightsquigarrow V := e'$

| *AnnoLAssField*:

$\llbracket E\ V = None; V \neq super; E\ this = [Class\ C]; P \vdash C\ sees\ V:T\ (fm)\ in\ D; is-lub, P, E \vdash e \rightsquigarrow e' \rrbracket$

$\Longrightarrow is-lub, P, E \vdash V := e \rightsquigarrow Var\ this \cdot V\{D\} := e'$

| *AnnoAAcc*:

$\llbracket is-lub, P, E \vdash a \rightsquigarrow a'; is-lub, P, E \vdash i \rightsquigarrow i' \rrbracket \Longrightarrow is-lub, P, E \vdash a[i] \rightsquigarrow a'[i']$

| *AnnoAAss*:

$\llbracket is-lub, P, E \vdash a \rightsquigarrow a'; is-lub, P, E \vdash i \rightsquigarrow i'; is-lub, P, E \vdash e \rightsquigarrow e' \rrbracket \Longrightarrow is-lub, P, E \vdash a[i] := e \rightsquigarrow a'[i'] := e'$

| *AnnoALength*:

$is-lub, P, E \vdash a \rightsquigarrow a' \Longrightarrow is-lub, P, E \vdash a \cdot length \rightsquigarrow a' \cdot length$

— All arrays implicitly declare a final field called *length* to store the array length, which hides a potential field of the same name in *Object* (cf. JLS 6.4.5). The last premise implements the hiding because field lookup does not model the implicit declaration.

AnnoFAcc:

$$\begin{aligned} & \llbracket \text{is-lub}, P, E \vdash e \rightsquigarrow e'; \text{is-lub}, P, E \vdash e' :: U; \text{class-type-of}' U = [C]; P \vdash C \text{ sees } F:T \text{ (fm) in } D; \\ & \quad \text{is-Array } U \longrightarrow F \neq \text{array-length-field-name} \rrbracket \\ & \implies \text{is-lub}, P, E \vdash e \cdot F\{\text{STR } ''''\} \rightsquigarrow e' \cdot F\{D\} \end{aligned}$$

| *AnnoFAccALength*:

$$\begin{aligned} & \llbracket \text{is-lub}, P, E \vdash e \rightsquigarrow e'; \text{is-lub}, P, E \vdash e' :: T[] \rrbracket \\ & \implies \text{is-lub}, P, E \vdash e \cdot \text{array-length-field-name}\{\text{STR } ''''\} \rightsquigarrow e' \cdot \text{length} \end{aligned}$$

| *AnnoFAccSuper*:
 — In class C with super class D, "super" is syntactic sugar for "(D this)" (cf. JLS, 15.11.2)

$$\begin{aligned} & \llbracket E \text{ this} = [\text{Class } C]; C \neq \text{Object}; \text{class } P \ C = [(D, fs, ms)]; \\ & \quad P \vdash D \text{ sees } F:T \text{ (fm) in } D' \rrbracket \\ & \implies \text{is-lub}, P, E \vdash \text{Var super} \cdot F\{\text{STR } ''''\} \rightsquigarrow (\text{Cast } (\text{Class } D) (\text{Var this})) \cdot F\{D'\} \end{aligned}$$

| *AnnoFAss*:

$$\begin{aligned} & \llbracket \text{is-lub}, P, E \vdash e1 \rightsquigarrow e1'; \text{is-lub}, P, E \vdash e2 \rightsquigarrow e2'; \\ & \quad \text{is-lub}, P, E \vdash e1' :: U; \text{class-type-of}' U = [C]; P \vdash C \text{ sees } F:T \text{ (fm) in } D; \\ & \quad \text{is-Array } U \longrightarrow F \neq \text{array-length-field-name} \rrbracket \\ & \implies \text{is-lub}, P, E \vdash e1 \cdot F\{\text{STR } ''''\} := e2 \rightsquigarrow e1' \cdot F\{D\} := e2' \end{aligned}$$

| *AnnoFAssSuper*:

$$\begin{aligned} & \llbracket E \text{ this} = [\text{Class } C]; C \neq \text{Object}; \text{class } P \ C = [(D, fs, ms)]; \\ & \quad P \vdash D \text{ sees } F:T \text{ (fm) in } D'; \text{is-lub}, P, E \vdash e \rightsquigarrow e' \rrbracket \\ & \implies \text{is-lub}, P, E \vdash \text{Var super} \cdot F\{\text{STR } ''''\} := e \rightsquigarrow (\text{Cast } (\text{Class } D) (\text{Var this})) \cdot F\{D'\} := e' \end{aligned}$$

| *AnnoCAS*:

$$\begin{aligned} & \llbracket \text{is-lub}, P, E \vdash e1 \rightsquigarrow e1'; \text{is-lub}, P, E \vdash e2 \rightsquigarrow e2'; \text{is-lub}, P, E \vdash e3 \rightsquigarrow e3' \rrbracket \\ & \implies \text{is-lub}, P, E \vdash e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3) \rightsquigarrow e1' \cdot \text{compareAndSwap}(D \cdot F, e2', e3') \end{aligned}$$

| *AnnoCall*:

$$\begin{aligned} & \llbracket \text{is-lub}, P, E \vdash e \rightsquigarrow e'; \text{is-lub}, P, E \vdash es [\rightsquigarrow] es' \rrbracket \\ & \implies \text{is-lub}, P, E \vdash \text{Call } e \ M \ es \rightsquigarrow \text{Call } e' \ M \ es' \end{aligned}$$

| *AnnoBlock*:

$$\text{is-lub}, P, E (V \mapsto T) \vdash e \rightsquigarrow e' \implies \text{is-lub}, P, E \vdash \{V:T=vo; e\} \rightsquigarrow \{V:T=vo; e'\}$$

| *AnnoSync*:

$$\begin{aligned} & \llbracket \text{is-lub}, P, E \vdash e1 \rightsquigarrow e1'; \text{is-lub}, P, E \vdash e2 \rightsquigarrow e2' \rrbracket \\ & \implies \text{is-lub}, P, E \vdash \text{sync}(e1) \ e2 \rightsquigarrow \text{sync}(e1') \ e2' \end{aligned}$$

| *AnnoComp*:

$$\begin{aligned} & \llbracket \text{is-lub}, P, E \vdash e1 \rightsquigarrow e1'; \text{is-lub}, P, E \vdash e2 \rightsquigarrow e2' \rrbracket \\ & \implies \text{is-lub}, P, E \vdash e1;;e2 \rightsquigarrow e1';;e2' \end{aligned}$$

| *AnnoCond*:

$$\begin{aligned} & \llbracket \text{is-lub}, P, E \vdash e \rightsquigarrow e'; \text{is-lub}, P, E \vdash e1 \rightsquigarrow e1'; \text{is-lub}, P, E \vdash e2 \rightsquigarrow e2' \rrbracket \\ & \implies \text{is-lub}, P, E \vdash \text{if } (e) \ e1 \ \text{else } e2 \rightsquigarrow \text{if } (e') \ e1' \ \text{else } e2' \end{aligned}$$

| *AnnoLoop*:

$$\begin{aligned} & \llbracket \text{is-lub}, P, E \vdash e \rightsquigarrow e'; \text{is-lub}, P, E \vdash c \rightsquigarrow c' \rrbracket \\ & \implies \text{is-lub}, P, E \vdash \text{while } (e) \ c \rightsquigarrow \text{while } (e') \ c' \end{aligned}$$

| *AnnoThrow*:

$$\text{is-lub}, P, E \vdash e \rightsquigarrow e' \implies \text{is-lub}, P, E \vdash \text{throw } e \rightsquigarrow \text{throw } e'$$

| *AnnoTry*:

$$\begin{aligned} & \llbracket \text{is-lub}, P, E \vdash e1 \rightsquigarrow e1'; \text{is-lub}, P, E (V \mapsto \text{Class } C) \vdash e2 \rightsquigarrow e2' \rrbracket \\ & \implies \text{is-lub}, P, E \vdash \text{try } e1 \ \text{catch}(C \ V) \ e2 \rightsquigarrow \text{try } e1' \ \text{catch}(C \ V) \ e2' \end{aligned}$$

| *AnnoNil*:

$$\text{is-lub}, P, E \vdash [] [\rightsquigarrow] []$$

| *AnnoCons*:

$$\llbracket \text{is-lub}, P, E \vdash e \rightsquigarrow e'; \text{is-lub}, P, E \vdash es [\rightsquigarrow] es' \rrbracket \implies \text{is-lub}, P, E \vdash e\#es [\rightsquigarrow] e'\#es'$$

inductive-cases *Anno-cases* [elim!]:

$$\text{is-lub}', P, E \vdash \text{new } C \rightsquigarrow e$$

$is-lub', P, E \vdash newA\ T[e] \rightsquigarrow e'$
 $is-lub', P, E \vdash Cast\ T\ e \rightsquigarrow e'$
 $is-lub', P, E \vdash e\ instanceof\ T \rightsquigarrow e'$
 $is-lub', P, E \vdash Val\ v \rightsquigarrow e'$
 $is-lub', P, E \vdash Var\ V \rightsquigarrow e'$
 $is-lub', P, E \vdash e1\ \langle\langle bop \rangle\rangle\ e2 \rightsquigarrow e'$
 $is-lub', P, E \vdash V := e \rightsquigarrow e'$
 $is-lub', P, E \vdash e1[e2] \rightsquigarrow e'$
 $is-lub', P, E \vdash e1[e2] := e3 \rightsquigarrow e'$
 $is-lub', P, E \vdash e.length \rightsquigarrow e'$
 $is-lub', P, E \vdash e.F\{D\} \rightsquigarrow e'$
 $is-lub', P, E \vdash e1.F\{D\} := e2 \rightsquigarrow e'$
 $is-lub', P, E \vdash e1.compareAndSwap(D.F, e2, e3) \rightsquigarrow e'$
 $is-lub', P, E \vdash e.M(es) \rightsquigarrow e'$
 $is-lub', P, E \vdash \{V:T=vo; e\} \rightsquigarrow e'$
 $is-lub', P, E \vdash sync(e1)\ e2 \rightsquigarrow e'$
 $is-lub', P, E \vdash insync(a)\ e2 \rightsquigarrow e'$
 $is-lub', P, E \vdash e1;;\ e2 \rightsquigarrow e'$
 $is-lub', P, E \vdash if\ (e)\ e1\ else\ e2 \rightsquigarrow e'$
 $is-lub', P, E \vdash while(e1)\ e2 \rightsquigarrow e'$
 $is-lub', P, E \vdash throw\ e \rightsquigarrow e'$
 $is-lub', P, E \vdash try\ e1\ catch(C\ V)\ e2 \rightsquigarrow e'$

inductive-cases *Annos-cases* [*elim!*]:

$is-lub', P, E \vdash [] [\rightsquigarrow] es'$
 $is-lub', P, E \vdash e \# es [\rightsquigarrow] es'$

abbreviation *Anno'* :: 'addr J-prog \Rightarrow env \Rightarrow 'addr expr \Rightarrow 'addr expr \Rightarrow bool ($\langle -, - \vdash - \rightsquigarrow - \rangle$ [51,0,0,51]50)

where *Anno'* P \equiv Anno (TypeRel.is-lub P) P

abbreviation *Annos'* :: 'addr J-prog \Rightarrow env \Rightarrow 'addr expr list \Rightarrow 'addr expr list \Rightarrow bool ($\langle -, - \vdash - [\rightsquigarrow] - \rangle$ [51,0,0,51]50)

where *Annos'* P \equiv Annos (TypeRel.is-lub P) P

definition *annotate* :: 'addr J-prog \Rightarrow env \Rightarrow 'addr expr \Rightarrow 'addr expr

where *annotate* P E e = THE-default e ($\lambda e'. P, E \vdash e \rightsquigarrow e'$)

lemma fixes *is-lub* :: ty \Rightarrow ty \Rightarrow ty \Rightarrow bool ($\langle \vdash lub'((- / -)^ \rangle = \rightarrow$ [51,51,51] 50)

assumes *is-lub-unique*: $\bigwedge T1\ T2\ T3\ T4. \llbracket \vdash lub(T1, T2) = T3; \vdash lub(T1, T2) = T4 \rrbracket \Longrightarrow T3 = T4$

shows *Anno-fun*: $\llbracket is-lub, P, E \vdash e \rightsquigarrow e'; is-lub, P, E \vdash e \rightsquigarrow e'' \rrbracket \Longrightarrow e' = e''$

and *Annos-fun*: $\llbracket is-lub, P, E \vdash es [\rightsquigarrow] es'; is-lub, P, E \vdash es [\rightsquigarrow] es'' \rrbracket \Longrightarrow es' = es''$

proof(*induct arbitrary: e'' and es'' rule: Anno-Annos.inducts*)

case (*AnnoFAcc* E e e' U C F T fm D)

from $\langle is-lub, P, E \vdash e.F\{STR\ \''''\} \rightsquigarrow e'' \rangle$ **show** ?case

proof(*rule Anno-cases*)

fix e''' U' C' T' fm' D'

assume $is-lub, P, E \vdash e \rightsquigarrow e''' is-lub, P, E \vdash e''' :: U'$

and *class-type-of'* U' = [C']

and P \vdash C' sees F:T' (fm') in D' e'' = e''' . F{D'}

from $\langle is-lub, P, E \vdash e \rightsquigarrow e''' \rangle$ **have** e' = e''' **by**(*rule AnnoFAcc*)

with $\langle is-lub, P, E \vdash e' :: U \rangle \langle is-lub, P, E \vdash e''' :: U' \rangle$

have U = U' **by**(*auto intro: WT-unique is-lub-unique*)

```

with ⟨class-type-of' U = [C]⟩ ⟨class-type-of' U' = [C']⟩
have C = C' by(auto)
with ⟨P ⊢ C' sees F:T' (fm') in D'⟩ ⟨P ⊢ C sees F:T (fm) in D⟩
have D' = D by(auto dest: sees-field-fun)
with ⟨e'' = e'''·F{D'}⟩ ⟨e' = e'''⟩ show ?thesis by simp
next
fix e''' T
assume e'' = e'''·length
  and is-lub,P,E ⊢ e''' :: T[]
  and is-lub,P,E ⊢ e ~ e'''
  and F = array-length-field-name
from ⟨is-lub,P,E ⊢ e ~ e'''⟩ have e' = e''' by(rule AnnoFAcc)
with ⟨is-lub,P,E ⊢ e' :: U⟩ ⟨is-lub,P,E ⊢ e''' :: T[]⟩ have U = T[] by(auto intro: WT-unique
is-lub-unique)
with ⟨class-type-of' U = [C]⟩ ⟨is-Array U ⟶ F ≠ array-length-field-name⟩
show ?thesis using ⟨F = array-length-field-name⟩ by simp
next
fix C' D' fs ms T D''
assume E this = [Class C']
  and class P C' = [(D', fs, ms)]
  and e = Var super
  and e'' = Cast (Class D') (Var this)·F{D''}
with ⟨is-lub,P,E ⊢ e ~ e'⟩ have False by(auto)
thus ?thesis ..
qed
next
case AnnoFAccALength thus ?case by(fastforce intro: WT-unique[OF is-lub-unique])
next
case (AnnoFAss E e1 e1' e2 e2' U C F T fm D)
from ⟨is-lub,P,E ⊢ e1·F{STR ''''} := e2 ~ e'⟩
show ?case
proof(rule Anno-cases)
fix e1'' e2'' U' C' T' fm' D'
assume is-lub,P,E ⊢ e1 ~ e1'' is-lub,P,E ⊢ e2 ~ e2''
  and is-lub,P,E ⊢ e1'' :: U' and class-type-of' U' = [C']
  and P ⊢ C' sees F:T' (fm') in D'
  and e'' = e1''·F{D'} := e2''
from ⟨is-lub,P,E ⊢ e1 ~ e1''⟩ have e1' = e1'' by(rule AnnoFAss)
moreover with ⟨is-lub,P,E ⊢ e1' :: U⟩ ⟨is-lub,P,E ⊢ e1'' :: U'⟩
have U = U' by(auto intro: WT-unique is-lub-unique)
with ⟨class-type-of' U = [C]⟩ ⟨class-type-of' U' = [C']⟩
have C = C' by(auto)
with ⟨P ⊢ C' sees F:T' (fm') in D'⟩ ⟨P ⊢ C sees F:T (fm) in D⟩
have D' = D by(auto dest: sees-field-fun)
moreover from ⟨is-lub,P,E ⊢ e2 ~ e2''⟩ have e2' = e2'' by(rule AnnoFAss)
ultimately show ?thesis using ⟨e'' = e1''·F{D'} := e2''⟩ by simp
next
fix C' D' fs ms T' fm' D'' e'''
assume e'' = Cast (Class D') (Var this)·F{D''} := e'''
  and E this = [Class C']
  and class P C' = [(D', fs, ms)]
  and P ⊢ D' sees F:T' (fm') in D''
  and is-lub,P,E ⊢ e2 ~ e'''
  and e1 = Var super

```



```

  with  $\langle is-lub, P, E \vdash e_1 \rightsquigarrow e_1' \rangle$  have False by (auto elim: Anno-cases)
  thus ?thesis ..
qed
qed(fastforce dest: sees-field-fun)+

```

4.16.1 Code generation

definition *Anno-code* :: 'addr J-prog \Rightarrow env \Rightarrow 'addr expr \Rightarrow 'addr expr \Rightarrow bool ($\langle -, - \vdash - \rightsquigarrow' \rangle$ \rightarrow [51,0,0,51]50)
where *Anno-code* P = Anno (*is-lub-sup* P) P

definition *Annos-code* :: 'addr J-prog \Rightarrow env \Rightarrow 'addr expr list \Rightarrow 'addr expr list \Rightarrow bool ($\langle -, - \vdash - [\rightsquigarrow'] \rangle$ \rightarrow [51,0,0,51]50)
where *Annos-code* P = Annos (*is-lub-sup* P) P

primrec *block-types* :: ('a, 'b, 'addr) exp \Rightarrow ty list
and *blocks-types* :: ('a, 'b, 'addr) exp list \Rightarrow ty list
where

```

  block-types (new C) = []
| block-types (newA T[e]) = block-types e
| block-types (Cast U e) = block-types e
| block-types (e instanceof U) = block-types e
| block-types (e  $\langle bop \rangle$  e2) = block-types e1 @ block-types e2
| block-types (Val v) = []
| block-types (Var V) = []
| block-types (V := e) = block-types e
| block-types (a[i]) = block-types a @ block-types i
| block-types (a[i] := e) = block-types a @ block-types i @ block-types e
| block-types (a.length) = block-types a
| block-types (e.F{D}) = block-types e
| block-types (e.F{D} := e') = block-types e @ block-types e'
| block-types (e.compareAndSwap(D.F, e', e'')) = block-types e @ block-types e' @ block-types e''
| block-types (e.M(es)) = block-types e @ blocks-types es
| block-types {V:T=vo; e} = T # block-types e
| block-types (syncV(e) e') = block-types e @ block-types e'
| block-types (insyncV(a) e) = block-types e
| block-types (e;;e') = block-types e @ block-types e'
| block-types (if (e) e1 else e2) = block-types e @ block-types e1 @ block-types e2
| block-types (while (b) c) = block-types b @ block-types c
| block-types (throw e) = block-types e
| block-types (try e catch(C V) e') = block-types e @ Class C # block-types e'

| blocks-types [] = []
| blocks-types (e#es) = block-types e @ blocks-types es

```

lemma *fixes is-lub1* :: ty \Rightarrow ty \Rightarrow ty \Rightarrow bool ($\langle \vdash 1 \text{ lub}'((- / -)' \rangle = \rightarrow$ [51,51,51] 50)

and *is-lub2* :: ty \Rightarrow ty \Rightarrow ty \Rightarrow bool ($\langle \vdash 2 \text{ lub}'((- / -)' \rangle = \rightarrow$ [51,51,51] 50)

assumes *wf: wf-prog wf-md* P

and *is-lub1-into-is-lub2*: $\bigwedge T1 T2 T3. \llbracket \vdash 1 \text{ lub}(T1, T2) = T3; \text{is-type } P T1; \text{is-type } P T2 \rrbracket \Longrightarrow \vdash 2$
lub(T1, T2) = T3

and *is-lub2-is-type*: $\bigwedge T1 T2 T3. \llbracket \vdash 2 \text{ lub}(T1, T2) = T3; \text{is-type } P T1; \text{is-type } P T2 \rrbracket \Longrightarrow \text{is-type}$
P T3

shows *Anno-change-is-lub*:

$\llbracket \text{is-lub1}, P, E \vdash e \rightsquigarrow e'; \text{ran } E \cup \text{set}(\text{block-types } e) \subseteq \text{types } P \rrbracket \Longrightarrow \text{is-lub2}, P, E \vdash e \rightsquigarrow e'$

and *Annos-change-is-lub*:
 $\llbracket is-lub1, P, E \vdash es [\rightsquigarrow] es'; ran E \cup set (blocks-types es) \subseteq types P \rrbracket \implies is-lub2, P, E \vdash es [\rightsquigarrow] es'$

proof (*induct rule: Anno-Annos.inducts*)
case (*AnnoBlock E V T e e' vo*)
from $\langle ran E \cup set (block-types \{V:T=vo; e\}) \subseteq types P \rangle$
have $ran (E(V \mapsto T)) \cup set (block-types e) \subseteq types P$
by (*auto simp add: ran-def*)
thus *?case using AnnoBlock by(blast intro: Anno-Annos.intros)*

next
case (*AnnoTry E e1 e1' V C e2 e2'*)
from $\langle ran E \cup set (block-types (try e1 catch(C V) e2)) \subseteq types P \rangle$
have $ran (E(V \mapsto Class C)) \cup set (block-types e2) \subseteq types P$
by (*auto simp add: ran-def*)
thus *?case using AnnoTry by(simp del: fun-upd-apply)(blast intro: Anno-Annos.intros)*

qed (*simp-all del: is-Array.simps is-Array-conv, (blast intro: Anno-Annos.intros WT-change-is-lub[OF wf, where ?is-lub1.0=is-lub1 and ?is-lub2.0=is-lub2] is-lub1-into-is-lub2 is-lub2-is-type)+*)

lemma assumes *wf: wf-prog wf-md P*
shows *Anno-into-Anno-code*: $\llbracket P, E \vdash e \rightsquigarrow e'; ran E \cup set (block-types e) \subseteq types P \rrbracket \implies P, E \vdash e \rightsquigarrow' e'$
and *Annos-into-Annos-code*: $\llbracket P, E \vdash es [\rightsquigarrow] es'; ran E \cup set (blocks-types es) \subseteq types P \rrbracket \implies P, E \vdash es [\rightsquigarrow'] es'$

proof –
assume *anno*: $P, E \vdash e \rightsquigarrow e'$
and *ran*: $ran E \cup set (block-types e) \subseteq types P$
show $P, E \vdash e \rightsquigarrow' e'$ **unfolding** *Anno-code-def*
by (*rule Anno-change-is-lub[OF wf - - anno ran]*)(*blast intro!: is-lub-sup.intros intro: is-lub-subD[OF wf] sup-is-type[OF wf] elim!: is-lub-sup.cases*)+

next
assume *annos*: $P, E \vdash es [\rightsquigarrow] es'$
and *ran*: $ran E \cup set (blocks-types es) \subseteq types P$
show $P, E \vdash es [\rightsquigarrow'] es'$ **unfolding** *Annos-code-def*
by (*rule Annos-change-is-lub[OF wf - - annos ran]*)(*blast intro!: is-lub-sup.intros intro: is-lub-subD[OF wf] sup-is-type[OF wf] elim!: is-lub-sup.cases*)+

qed

lemma assumes *wf: wf-prog wf-md P*
shows *Anno-code-into-Anno*: $\llbracket P, E \vdash e \rightsquigarrow' e'; ran E \cup set (block-types e) \subseteq types P \rrbracket \implies P, E \vdash e \rightsquigarrow e'$
and *Annos-code-into-Annos*: $\llbracket P, E \vdash es [\rightsquigarrow'] es'; ran E \cup set (blocks-types es) \subseteq types P \rrbracket \implies P, E \vdash es [\rightsquigarrow] es'$

proof –
assume *anno*: $P, E \vdash e \rightsquigarrow' e'$
and *ran*: $ran E \cup set (block-types e) \subseteq types P$
show $P, E \vdash e \rightsquigarrow e'$
by (*rule Anno-change-is-lub[OF wf - - anno[unfolded Anno-code-def] ran]*)(*blast elim!: is-lub-sup.cases intro: sup-is-lubI[OF wf] is-lub-is-type[OF wf]*)+

next
assume *annos*: $P, E \vdash es [\rightsquigarrow'] es'$
and *ran*: $ran E \cup set (blocks-types es) \subseteq types P$
show $P, E \vdash es [\rightsquigarrow] es'$
by (*rule Annos-change-is-lub[OF wf - - annos[unfolded Annos-code-def] ran]*)(*blast elim!: is-lub-sup.cases intro: sup-is-lubI[OF wf] is-lub-is-type[OF wf]*)+

qed

lemma fixes *is-lub*

assumes *wf*: *wf-prog wf-md P*

shows *WT-block-types-is-type*: $is-lub, P, E \vdash e :: T \implies set (block-types\ e) \subseteq types\ P$

and *WTs-blocks-types-is-type*: $is-lub, P, E \vdash es [::] Ts \implies set (blocks-types\ es) \subseteq types\ P$

apply(*induct rule*: *WT-WTs.inducts*)

apply(*auto intro*: *is-class-sub-Throwable[OF wf]*)

done

lemma fixes *is-lub*

shows *Anno-block-types*: $is-lub, P, E \vdash e \rightsquigarrow e' \implies block-types\ e = block-types\ e'$

and *Annos-blocks-types*: $is-lub, P, E \vdash es \rightsquigarrow es' \implies blocks-types\ es = blocks-types\ es'$

by(*induct rule*: *Anno-Annos.inducts*) *auto*

code-pred

(*modes*: $(i \Rightarrow i \Rightarrow o \Rightarrow bool) \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow bool$)

[*detect-switches, skip-proof*]

Anno

.

definition *annotate-code* :: $'addr\ J-prog \Rightarrow env \Rightarrow 'addr\ expr \Rightarrow 'addr\ expr$

where *annotate-code* *P E e* = *THE-default e* ($\lambda e'. P, E \vdash e \rightsquigarrow' e'$)

code-pred

(*modes*: $i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow bool$)

[*inductify*]

Anno-code

.

lemma *eval-Anno-i-i-i-o-conv*:

Predicate.eval (*Anno-code-i-i-i-o P E e*) = ($\lambda e'. P, E \vdash e \rightsquigarrow' e'$)

by(*auto intro!*: *ext intro*: *Anno-code-i-i-i-oI elim*: *Anno-code-i-i-i-oE*)

lemma *annotate-code* [*code*]:

annotate-code P E e = *Predicate.singleton* ($\lambda-. Code.abort (STR\ "annotate") (\lambda-. e)$) (*Anno-code-i-i-i-o P E e*)

by(*simp add*: *THE-default-def Predicate.singleton-def annotate-code-def eval-Anno-i-i-i-o-conv*)

end

theory *J-Main*

imports

State

Deadlocked

Annotate

begin

end

Chapter 5

Jinja Virtual Machine

5.1 State of the JVM

```
theory JVMState
imports
  ../Common/Observable-Events
begin
```

5.1.1 Frame Stack

```
type-synonym
```

```
  pc = nat
```

```
type-synonym
```

```
  'addr frame = 'addr val list × 'addr val list × cname × mname × pc
  — operand stack
  — registers (including this pointer, method parameters, and local variables)
  — name of class where current method is defined
  — parameter types
  — program counter within frame
```

```
print-translation <
```

```
  let
    fun tr'
      [Const (@{type-syntax list}, -) $ (Const (@{type-syntax val}, -) $ a1),
       Const (@{type-syntax prod}, -) $
         (Const (@{type-syntax list}, -) $ (Const (@{type-syntax val}, -) $ a2)) $
         (Const (@{type-syntax prod}, -) $
           Const (@{type-syntax String.literal}, -) $
             (Const (@{type-syntax prod}, -) $
               Const (@{type-syntax String.literal}, -) $
                 Const (@{type-syntax nat}, -)))] =
        if a1 = a2 then Syntax.const @{type-syntax frame} $ a1
        else raise Match;
    in [(@{type-syntax prod}, K tr')]
  end
>
typ 'addr frame
```

5.1.2 Runtime State

type-synonym

$(\text{'addr}, \text{'heap}) \text{jvm-state} = \text{'addr option} \times \text{'heap} \times \text{'addr frame list}$
 — exception flag, heap, frames

type-synonym

$\text{'addr jvm-thread-state} = \text{'addr option} \times \text{'addr frame list}$
 — exception flag, frames, thread lock state

type-synonym

$(\text{'addr}, \text{'thread-id}, \text{'heap}) \text{jvm-thread-action} = (\text{'addr}, \text{'thread-id}, \text{'addr jvm-thread-state}, \text{'heap}) \text{Jinja-thread-action}$

type-synonym

$(\text{'addr}, \text{'thread-id}, \text{'heap}) \text{jvm-ta-state} = (\text{'addr}, \text{'thread-id}, \text{'heap}) \text{jvm-thread-action} \times (\text{'addr}, \text{'heap}) \text{jvm-state}$

print-translation <

```

let
  fun tr'
    [a1, t
    , Const (@{type-syntax prod}, -) $
      (Const (@{type-syntax option}, -) $ a2) $
      (Const (@{type-syntax list}, -) $
        (Const (@{type-syntax prod}, -) $
          (Const (@{type-syntax list}, -) $ (Const (@{type-syntax val}, -) $ a3)) $
          (* Next bit: same syntax translation as for frame *)
          (Const (@{type-syntax prod}, -) $
            (Const (@{type-syntax list}, -) $ (Const (@{type-syntax val}, -) $ a4)) $
            (Const (@{type-syntax prod}, -) $
              Const (@{type-syntax String.literal}, -) $
              (Const (@{type-syntax prod}, -) $
                Const (@{type-syntax String.literal}, -) $
                Const (@{type-syntax nat}, -))))))
        , h] =
      if a1 = a2 andalso a2 = a3 andalso a3 = a4 then Syntax.const @{type-syntax jvm-thread-action}
      $ a1 $ t $ h
      else raise Match;
    in [(@{type-syntax Jinja-thread-action}, K tr')
    end
  >
typ ('addr, 'thread-id, 'heap) jvm-thread-action

```

end

5.2 Instructions of the JVM

theory *JVMInstructions*

imports

JVMState

../Common/BinOp

begin

datatype *'addr instr*

<i>= Load nat</i>	— load from local variable
<i>Store nat</i>	— store into local variable
<i>Push 'addr val</i>	— push a value (constant)
<i>New cname</i>	— create object
<i>NewArray ty</i>	— create array for elements of given type
<i>ALoad</i>	— Load array element from heap to stack
<i>AStore</i>	— Set element in array
<i>ALength</i>	— Return the length of the array
<i>Getfield vname cname</i>	— Fetch field from object
<i>Putfield vname cname</i>	— Set field in object
<i>CAS vname cname</i>	— Compare-and-swap instruction
<i>Checkcast ty</i>	— Check whether object is of given type
<i>Instanceof ty</i>	— instanceof test
<i>Invoke mname nat</i>	— inv. instance meth of an object
<i>Return</i>	— return from method
<i>Pop</i>	— pop top element from opstack
<i>Dup</i>	— duplicate top stack element
<i>Swap</i>	— swap top stack elements
<i>BinOpInstr bop</i>	— binary operator instruction
<i>Goto int</i>	— goto relative address
<i>IfFalse int</i>	— branch if top of stack false
<i>ThrowExc</i>	— throw top of stack as exception
<i>MEnter</i>	— enter the monitor of object on top of the stack
<i>MExit</i>	— exit the monitor of object on top of the stack

abbreviation *CmpEq :: 'addr instr*
where *CmpEq* \equiv *BinOpInstr Eq*

abbreviation *CmpLeq :: 'addr instr*
where *CmpLeq* \equiv *BinOpInstr LessOrEqual*

abbreviation *CmpGeq :: 'addr instr*
where *CmpGeq* \equiv *BinOpInstr GreaterOrEqual*

abbreviation *CmpLt :: 'addr instr*
where *CmpLt* \equiv *BinOpInstr LessThan*

abbreviation *CmpGt :: 'addr instr*
where *CmpGt* \equiv *BinOpInstr GreaterThan*

abbreviation *IAdd :: 'addr instr*
where *IAdd* \equiv *BinOpInstr Add*

abbreviation *ISub :: 'addr instr*
where *ISub* \equiv *BinOpInstr Subtract*

abbreviation *IMult :: 'addr instr*
where *IMult* \equiv *BinOpInstr Mult*

abbreviation *IDiv :: 'addr instr*
where *IDiv* \equiv *BinOpInstr Div*

abbreviation *IMod :: 'addr instr*

where $IMod \equiv BinOpInstr Mod$

abbreviation $IShl :: 'addr instr$
 where $IShl \equiv BinOpInstr ShiftLeft$

abbreviation $IShr :: 'addr instr$
 where $IShr \equiv BinOpInstr ShiftRightSigned$

abbreviation $IUShr :: 'addr instr$
 where $IUShr \equiv BinOpInstr ShiftRightZeros$

abbreviation $IAnd :: 'addr instr$
 where $IAnd \equiv BinOpInstr BinAnd$

abbreviation $IOr :: 'addr instr$
 where $IOr \equiv BinOpInstr BinOr$

abbreviation $IXor :: 'addr instr$
 where $IXor \equiv BinOpInstr BinXor$

type-synonym
 $'addr bytecode = 'addr instr list$

type-synonym
 $ex-entry = pc \times pc \times cname option \times pc \times nat$
 — start-pc, end-pc, exception type (None = Any), handler-pc, remaining stack depth

type-synonym
 $ex-table = ex-entry list$

type-synonym
 $'addr jvm-method = nat \times nat \times 'addr bytecode \times ex-table$
 — max stacksize
 — number of local variables. Add 1 + no. of parameters to get no. of registers
 — instruction sequence
 — exception handler table

type-synonym
 $'addr jvm-prog = 'addr jvm-method prog$

end

5.3 Abstract heap locales for byte code programs

theory $JVMHeap$
imports
 $../Common/Conform$
 $JVMInstructions$
begin

locale $JVM-heap-base =$
 $heap-base +$
constrains $addr2thread-id :: ('addr :: addr) \Rightarrow 'thread-id$


```

and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool

```

```

locale JVM-heap =
  JVM-heap-base +
  heap +
  constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and P :: 'addr jvm-prog

```

```

locale JVM-heap-conf-base =
  heap-conf-base +
  constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool
and P :: 'addr jvm-prog

```

```

sublocale JVM-heap-conf-base < JVM-heap-base .

```

```

locale JVM-heap-conf-base' =
  JVM-heap-conf-base +
  heap +
  constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool
and P :: 'addr jvm-prog

```

```

sublocale JVM-heap-conf-base' < JVM-heap by(unfold-locales)

```

```

locale JVM-heap-conf =
  JVM-heap-conf-base' +

```

```

heap-conf +
constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool
and P :: 'addr jvm-prog

```

```

locale JVM-progress =
  heap-progress +
  JVM-heap-conf-base' +
constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool
and P :: 'addr jvm-prog

```

```

locale JVM-conf-read =
  heap-conf-read +
  JVM-heap-conf +
constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool
and P :: 'addr jvm-prog

```

```

locale JVM-typesafe =
  heap-typesafe +
  JVM-conf-read +
  JVM-progress +
constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool
and P :: 'addr jvm-prog

```

end

5.4 JVM Instruction Semantics

theory *JVMExecInstr*

imports

JVMInstructions

JVMHeap

../Common/ExternalCall

begin

primrec *extRet2JVM* ::

nat \Rightarrow *'heap* \Rightarrow *'addr val list* \Rightarrow *'addr val list* \Rightarrow *cname* \Rightarrow *mname* \Rightarrow *pc* \Rightarrow *'addr frame list*
 \Rightarrow *'addr extCallRet* \Rightarrow (*'addr*, *'heap*) *jvm-state*

where

extRet2JVM *n h stk loc C M pc frs* (*RetVal* *v*) = (*None*, *h*, (*v* # *drop* (*Suc* *n*) *stk*, *loc*, *C*, *M*, *pc* + 1) # *frs*)

| *extRet2JVM* *n h stk loc C M pc frs* (*RetExc* *a*) = ($\lfloor a \rfloor$, *h*, (*stk*, *loc*, *C*, *M*, *pc*) # *frs*)

| *extRet2JVM* *n h stk loc C M pc frs* *RetStaySame* = (*None*, *h*, (*stk*, *loc*, *C*, *M*, *pc*) # *frs*)

lemma *eq-extRet2JVM-conv* [*simp*]:

(*xcp*, *h'*, *frs'*) = *extRet2JVM* *n h stk loc C M pc frs va* \longleftrightarrow

h' = *h* \wedge (*case* *va* of *RetVal* *v* \Rightarrow *xcp* = *None* \wedge *frs'* = (*v* # *drop* (*Suc* *n*) *stk*, *loc*, *C*, *M*, *pc* + 1) # *frs*

| *RetExc* *a* \Rightarrow *xcp* = $\lfloor a \rfloor$ \wedge *frs'* = (*stk*, *loc*, *C*, *M*, *pc*) # *frs*

| *RetStaySame* \Rightarrow *xcp* = *None* \wedge *frs'* = (*stk*, *loc*, *C*, *M*, *pc*) # *frs*)

by(*cases* *va*) *auto*

definition *extNTA2JVM* :: *'addr jvm-prog* \Rightarrow (*cname* \times *mname* \times *'addr*) \Rightarrow *'addr jvm-thread-state*

where *extNTA2JVM* *P* \equiv (λ (*C*, *M*, *a*). *let* (*D*, *M'*, *Ts*, *meth*) = *method* *P C M*; (*mxs*, *mxl0*, *ins*, *xt*) = *the meth*

in (*None*, ($\lfloor \rfloor$, *Addr* *a* # *replicate* *mxl0* *undefined-value*, *D*, *M*, 0)))

abbreviation *extTA2JVM* ::

'addr jvm-prog \Rightarrow (*'addr*, *'thread-id*, *'heap*) *external-thread-action* \Rightarrow (*'addr*, *'thread-id*, *'heap*) *jvm-thread-action*

where *extTA2JVM* *P* \equiv *convert-extTA* (*extNTA2JVM* *P*)

context *JVM-heap-base* **begin**

primrec *exec-instr* ::

'addr instr \Rightarrow *'addr jvm-prog* \Rightarrow *'thread-id* \Rightarrow *'heap* \Rightarrow *'addr val list* \Rightarrow *'addr val list*

\Rightarrow *cname* \Rightarrow *mname* \Rightarrow *pc* \Rightarrow *'addr frame list* \Rightarrow

((*'addr*, *'thread-id*, *'heap*) *jvm-thread-action* \times (*'addr*, *'heap*) *jvm-state*) *set*

where

exec-instr-Load:

exec-instr (*Load* *n*) *P t h stk loc C₀ M₀ pc frs* =

{(ε , (*None*, *h*, ((*loc* ! *n*) # *stk*, *loc*, *C₀*, *M₀*, *pc*+1)#*frs*))}

| *exec-instr* (*Store* *n*) *P t h stk loc C₀ M₀ pc frs* =

{(ε , (*None*, *h*, (*tl* *stk*, *loc*[*n*:=*hd* *stk*], *C₀*, *M₀*, *pc*+1)#*frs*))}

| *exec-instr-Push*:

```

exec-instr (Push v) P t h stk loc C0 M0 pc frs =
  {(ε, (None, h, (v # stk, loc, C0, M0, pc+1)#frs))}

| exec-instr-New:
exec-instr (New C) P t h stk loc C0 M0 pc frs =
  (let HA = allocate h (Class-type C)
   in if HA = {} then {(ε, [addr-of-sys-xcpt OutOfMemory], h, (stk, loc, C0, M0, pc) # frs)}
   else (λ(h', a). ({NewHeapElem a (Class-type C)}}, None, h', (Addr a # stk, loc, C0, M0, pc + 1)#frs)) ' HA)

| exec-instr-NewArray:
exec-instr (NewArray T) P t h stk loc C0 M0 pc frs =
  (let si = the-Intg (hd stk);
   i = nat (sint si)
   in (if si < s 0
    then {(ε, [addr-of-sys-xcpt NegativeArraySize], h, (stk, loc, C0, M0, pc) # frs)}
    else let HA = allocate h (Array-type T i)
        in if HA = {} then {(ε, [addr-of-sys-xcpt OutOfMemory], h, (stk, loc, C0, M0, pc) # frs)}
        else (λ(h', a). ({NewHeapElem a (Array-type T i)}}, None, h', (Addr a # tl stk, loc, C0, M0, pc + 1) # frs)) ' HA))

| exec-instr-ALoad:
exec-instr ALoad P t h stk loc C0 M0 pc frs =
  (let i = the-Intg (hd stk);
   va = hd (tl stk);
   a = the-Addr va;
   len = alen-of-htype (the (typeof-addr h a))
   in (if va = Null then {(ε, [addr-of-sys-xcpt NullPointer], h, (stk, loc, C0, M0, pc) # frs)}
    else if i < s 0 ∨ int len ≤ sint i then
      {(ε, [addr-of-sys-xcpt ArrayIndexOutOfBounds], h, (stk, loc, C0, M0, pc) # frs)}
    else {(λ(a, v). ({ReadMem a (ACell (nat (sint i))) v}}}, None, h, (v # tl (tl stk), loc, C0, M0, pc + 1) # frs) | v.
      heap-read h a (ACell (nat (sint i))) v })))

| exec-instr-ASore:
exec-instr AStore P t h stk loc C0 M0 pc frs =
  (let ve = hd stk;
   vi = hd (tl stk);
   va = hd (tl (tl stk))
   in (if va = Null then {(ε, [addr-of-sys-xcpt NullPointer], h, (stk, loc, C0, M0, pc) # frs)}
    else (let i = the-Intg vi;
        idx = nat (sint i);
        a = the-Addr va;
        hT = the (typeof-addr h a);
        T = ty-of-htype hT;
        len = alen-of-htype hT;
        U = the (typeofh ve)
        in (if i < s 0 ∨ int len ≤ sint i then
          {(ε, [addr-of-sys-xcpt ArrayIndexOutOfBounds], h, (stk, loc, C0, M0, pc) # frs)}
          else if P ⊢ U ≤ the-Array T then
            {(λ(a, v). ({WriteMem a (ACell idx) ve}}}, None, h', (tl (tl (tl stk)), loc, C0, M0, pc+1) # frs)
              | h'. heap-write h a (ACell idx) ve h')}
            else {(ε, ([addr-of-sys-xcpt ArrayStore], h, (stk, loc, C0, M0, pc) # frs))}))))))

```

| *exec-instr-ALength*:
exec-instr ALength P t h stk loc C0 M0 pc frs =
 $\{(\varepsilon, (\text{let } va = \text{hd } stk$
 $\text{ in if } va = \text{Null}$
 $\text{ then } (\lfloor \text{addr-of-sys-xcpt NullPointer} \rfloor, h, (stk, loc, C0, M0, pc) \# frs)$
 $\text{ else } (None, h, (\text{Intg } (\text{word-of-int } (\text{int } (\text{alen-of-htype } (\text{the } (\text{typeof-addr } h (\text{the-Addr } va)))))) \#$
 $\text{tl } stk, loc, C0, M0, pc+1) \# frs))\}$

| *exec-instr (Getfield F C) P t h stk loc C0 M0 pc frs* =
 $(\text{let } v = \text{hd } stk$
 $\text{ in if } v = \text{Null then } \{(\varepsilon, \lfloor \text{addr-of-sys-xcpt NullPointer} \rfloor, h, (stk, loc, C0, M0, pc) \# frs)\}$
 $\text{ else let } a = \text{the-Addr } v$
 $\text{ in } \{(\lfloor \text{ReadMem } a (\text{CField } C F) v \rfloor, None, h, (v' \# (\text{tl } stk), loc, C0, M0, pc + 1) \# frs) \mid$
 $v'. \text{ heap-read } h a (\text{CField } C F) v'\}$

| *exec-instr (Putfield F C) P t h stk loc C0 M0 pc frs* =
 $(\text{let } v = \text{hd } stk;$
 $r = \text{hd } (\text{tl } stk)$
 $\text{ in if } r = \text{Null then } \{(\varepsilon, \lfloor \text{addr-of-sys-xcpt NullPointer} \rfloor, h, (stk, loc, C0, M0, pc) \# frs)\}$
 $\text{ else let } a = \text{the-Addr } r$
 $\text{ in } \{(\lfloor \text{WriteMem } a (\text{CField } C F) v \rfloor, None, h', (\text{tl } (\text{tl } stk), loc, C0, M0, pc + 1) \# frs) \mid h'. \text{ heap-write } h a (\text{CField } C F) v h'\}$

| *exec-instr (CAS F C) P t h stk loc C0 M0 pc frs* =
 $(\text{let } v'' = \text{hd } stk; v' = \text{hd } (\text{tl } stk); v = \text{hd } (\text{tl } (\text{tl } stk))$
 $\text{ in if } v = \text{Null then } \{(\varepsilon, \lfloor \text{addr-of-sys-xcpt NullPointer} \rfloor, h, (stk, loc, C0, M0, pc) \# frs)\}$
 $\text{ else let } a = \text{the-Addr } v$
 $\text{ in } \{(\lfloor \text{ReadMem } a (\text{CField } C F) v', \text{WriteMem } a (\text{CField } C F) v'' \rfloor, None, h', (\text{Bool True } \#$
 $\text{tl } (\text{tl } (\text{tl } stk)), loc, C0, M0, pc + 1) \# frs) \mid h'. \text{ heap-read } h a (\text{CField } C F) v' \wedge \text{ heap-write } h a (\text{CField } C F) v'' h'\} \cup$
 $\{(\lfloor \text{ReadMem } a (\text{CField } C F) v'' \rfloor, None, h, (\text{Bool False } \# \text{tl } (\text{tl } (\text{tl } stk)), loc, C0, M0, pc$
 $+ 1) \# frs) \mid v''. \text{ heap-read } h a (\text{CField } C F) v'' \wedge v'' \neq v'\}$

| *exec-instr (Checkcast T) P t h stk loc C0 M0 pc frs* =
 $\{(\varepsilon, \text{let } U = \text{the } (\text{typeof}_h (\text{hd } stk))$
 $\text{ in if } P \vdash U \leq T \text{ then } (None, h, (stk, loc, C0, M0, pc + 1) \# frs)$
 $\text{ else } (\lfloor \text{addr-of-sys-xcpt ClassCast} \rfloor, h, (stk, loc, C0, M0, pc) \# frs)\}$

| *exec-instr (Instanceof T) P t h stk loc C0 M0 pc frs* =
 $\{(\varepsilon, None, h, (\text{Bool } (\text{hd } stk \neq \text{Null} \wedge P \vdash \text{the } (\text{typeof}_h (\text{hd } stk)) \leq T) \# \text{tl } stk, loc, C0, M0, pc +$
 $1) \# frs)\}$

| *exec-instr-Invoke*:
exec-instr (Invoke M n) P t h stk loc C0 M0 pc frs =
 $(\text{let } ps = \text{rev } (\text{take } n \text{ stk});$
 $r = \text{stk } ! n;$
 $a = \text{the-Addr } r;$
 $T = \text{the } (\text{typeof-addr } h a)$
 $\text{ in } (\text{if } r = \text{Null then } \{(\varepsilon, \lfloor \text{addr-of-sys-xcpt NullPointer} \rfloor, h, (stk, loc, C0, M0, pc) \# frs)\}$
 else
 $\text{ let } C = \text{class-type-of } T;$
 $(D, M', Ts, \text{meth}) = \text{method } P C M$

in case meth of
Native \Rightarrow
 $\{(extTA2JVM P ta, extRet2JVM n h' stk loc C_0 M_0 pc frs va) \mid ta va h'. \\ (ta, va, h') \in red\text{-external-aggr } P t a M ps h\}$
 $\mid \lfloor (m\acute{x}s, m\acute{x}l_0, ins, xt) \rfloor \Rightarrow$
 $let f' = (\lfloor, [r] @ ps @ (replicate m\acute{x}l_0 undefined\text{-value}), D, M, 0)$
 $in \{(\varepsilon, None, h, f' \# (stk, loc, C_0, M_0, pc) \# frs)\}$

$\mid exec\text{-instr Return } P t h stk_0 loc_0 C_0 M_0 pc frs =$
 $\{(\varepsilon, (if frs = \lfloor then (None, h, \lfloor) else$
 $let v = hd stk_0;$
 $(stk, loc, C, m, pc) = hd frs;$
 $n = length (fst (snd (method P C_0 M_0)))$
 $in (None, h, (v \# (drop (n+1) stk), loc, C, m, pc+1) \# tl frs)) \}$

$\mid exec\text{-instr Pop } P t h stk loc C_0 M_0 pc frs =$
 $\{(\varepsilon, (None, h, (tl stk, loc, C_0, M_0, pc+1) \# frs)) \}$

$\mid exec\text{-instr Dup } P t h stk loc C_0 M_0 pc frs =$
 $\{(\varepsilon, (None, h, (hd stk \# stk, loc, C_0, M_0, pc+1) \# frs)) \}$

$\mid exec\text{-instr Swap } P t h stk loc C_0 M_0 pc frs =$
 $\{(\varepsilon, (None, h, (hd (tl stk) \# hd stk \# tl (tl stk), loc, C_0, M_0, pc+1) \# frs)) \}$

$\mid exec\text{-instr (BinOpInstr bop) } P t h stk loc C_0 M_0 pc frs =$
 $\{(\varepsilon,$
 $case the (binop bop (hd (tl stk)) (hd stk)) of$
 $Inl v \Rightarrow (None, h, (v \# tl (tl stk), loc, C_0, M_0, pc+1) \# frs)$
 $\mid Inr a \Rightarrow (Some a, h, (stk, loc, C_0, M_0, pc) \# frs)\}$

$\mid exec\text{-instr (IfFalse i) } P t h stk loc C_0 M_0 pc frs =$
 $\{(\varepsilon, (let pc' = if hd stk = Bool False then nat(int pc+i) else pc+1$
 $in (None, h, (tl stk, loc, C_0, M_0, pc') \# frs)) \}$

$\mid exec\text{-instr-Goto:}$
 $exec\text{-instr (Goto i) } P t h stk loc C_0 M_0 pc frs =$
 $\{(\varepsilon, (None, h, (stk, loc, C_0, M_0, nat(int pc+i)) \# frs)) \}$

$\mid exec\text{-instr ThrowExc } P t h stk loc C_0 M_0 pc frs =$
 $\{(\varepsilon, (let xp' = if hd stk = Null then \lfloor addr\text{-of-sys-xcpt NullPointer} \rfloor else \lfloor the\text{-Addr}(hd stk) \rfloor$
 $in (xp', h, (stk, loc, C_0, M_0, pc) \# frs)) \}$

$\mid exec\text{-instr-MEnter:}$
 $exec\text{-instr MEnter } P t h stk loc C_0 M_0 pc frs =$
 $\{let v = hd stk$
 $in if v = Null$
 $then (\varepsilon, \lfloor addr\text{-of-sys-xcpt NullPointer} \rfloor, h, (stk, loc, C_0, M_0, pc) \# frs)$
 $else (\{\!| Lock \rightarrow the\text{-Addr } v, SyncLock (the\text{-Addr } v) \!\}, None, h, (tl stk, loc, C_0, M_0, pc + 1) \# frs)\}$

$\mid exec\text{-instr-MExit:}$
 $exec\text{-instr MExit } P t h stk loc C_0 M_0 pc frs =$
 $(let v = hd stk$
 $in if v = Null$
 $then \{(\varepsilon, \lfloor addr\text{-of-sys-xcpt NullPointer} \rfloor, h, (stk, loc, C_0, M_0, pc) \# frs)\}$

```

    else {({Unlock→the-Addr v, SyncUnlock (the-Addr v)}, None, h, (tl stk, loc, C0, M0, pc + 1) #
    frs),
    ({UnlockFail→the-Addr v}, [addr-of-sys-xcpt IllegalMonitorState], h, (stk, loc, C0, M0, pc)
    # frs)}})

```

end

end

5.5 Exception handling in the JVM

theory *JVMExceptions*

imports

JVMInstructions

begin

abbreviation *Any* :: *cname option*

where *Any* ≡ *None*

definition *matches-ex-entry* :: '*m prog* ⇒ *cname* ⇒ *pc* ⇒ *ex-entry* ⇒ *bool*

where

```

matches-ex-entry P C pc xcp ≡
  let (s, e, C', h, d) = xcp in
  s ≤ pc ∧ pc < e ∧ (case C' of None ⇒ True | [C''] ⇒ P ⊢ C ≤* C')

```

primrec

match-ex-table :: '*m prog* ⇒ *cname* ⇒ *pc* ⇒ *ex-table* ⇒ (*pc* × *nat*) *option*

where

```

match-ex-table P C pc [] = None
| match-ex-table P C pc (e#es) = (if matches-ex-entry P C pc e
  then Some (snd(snd(snd e)))
  else match-ex-table P C pc es)

```

abbreviation *ex-table-of* :: '*addr jvm-prog* ⇒ *cname* ⇒ *mname* ⇒ *ex-table*

where *ex-table-of* P C M == snd (snd (snd (the (snd (snd (snd (method P C M)))))))

lemma *match-ex-table-SomeD*:

```

match-ex-table P C pc xt = Some (pc',d') ⇒
  ∃ (f,t,D,h,d) ∈ set xt. matches-ex-entry P C pc (f,t,D,h,d) ∧ h = pc' ∧ d=d'
by (induct xt) (auto split: if-split-asm)

```

end

5.6 Program Execution in the JVM

theory *JVMExec*

imports

JVMExecInstr

JVMExceptions

../Common/StartConfig

begin

abbreviation $instrs\text{-}of :: 'addr\ jvm\text{-}prog \Rightarrow cname \Rightarrow mname \Rightarrow 'addr\ instr\ list$
where $instrs\text{-}of\ P\ C\ M == fst(snd(snd(the(snd(snd(snd(method\ P\ C\ M)))))))$

5.6.1 single step execution

context $JVM\text{-}heap\text{-}base\ begin$

fun $exception\text{-}step :: 'addr\ jvm\text{-}prog \Rightarrow 'addr \Rightarrow 'heap \Rightarrow 'addr\ frame \Rightarrow 'addr\ frame\ list \Rightarrow ('addr,$
 $'heap)\ jvm\text{-}state$

where

$exception\text{-}step\ P\ a\ h\ (stk,\ loc,\ C,\ M,\ pc)\ frs =$
 $(case\ match\text{-}ex\text{-}table\ P\ (cname\text{-}of\ h\ a)\ pc\ (ex\text{-}table\text{-}of\ P\ C\ M)\ of$
 $\quad None \Rightarrow ([a], h, frs)$
 $\quad | Some\ (pc',\ d) \Rightarrow (None,\ h,\ (Addr\ a\ \# \ drop\ (size\ stk - d)\ stk,\ loc,\ C,\ M,\ pc')\ \# \ frs))$

lemma $exception\text{-}step\text{-}def\text{-}raw:$

$exception\text{-}step =$
 $(\lambda P\ a\ h\ (stk,\ loc,\ C,\ M,\ pc)\ frs.$
 $\quad case\ match\text{-}ex\text{-}table\ P\ (cname\text{-}of\ h\ a)\ pc\ (ex\text{-}table\text{-}of\ P\ C\ M)\ of$
 $\quad \quad None \Rightarrow ([a], h, frs)$
 $\quad \quad | Some\ (pc',\ d) \Rightarrow (None,\ h,\ (Addr\ a\ \# \ drop\ (size\ stk - d)\ stk,\ loc,\ C,\ M,\ pc')\ \# \ frs))$

by($intro\ ext$) $auto$

fun $exec :: 'addr\ jvm\text{-}prog \Rightarrow 'thread\text{-}id \Rightarrow ('addr,\ 'heap)\ jvm\text{-}state \Rightarrow ('addr,\ 'thread\text{-}id,\ 'heap)$
 $jvm\text{-}ta\text{-}state\ set\ \mathbf{where}$

$exec\ P\ t\ (xcp,\ h,\ []) = \{\}$
 $| exec\ P\ t\ (None,\ h,\ (stk,\ loc,\ C,\ M,\ pc)\ \# \ frs) = exec\text{-}instr\ (instrs\text{-}of\ P\ C\ M\ !\ pc)\ P\ t\ h\ stk\ loc\ C\ M$
 $pc\ frs$
 $| exec\ P\ t\ ([a],\ h,\ fr\ \# \ frs) = \{(\varepsilon,\ exception\text{-}step\ P\ a\ h\ fr\ frs)\}$

5.6.2 relational view

inductive $exec\text{-}1 ::$

$'addr\ jvm\text{-}prog \Rightarrow 'thread\text{-}id \Rightarrow ('addr,\ 'heap)\ jvm\text{-}state$
 $\Rightarrow ('addr,\ 'thread\text{-}id,\ 'heap)\ jvm\text{-}thread\text{-}action \Rightarrow ('addr,\ 'heap)\ jvm\text{-}state \Rightarrow bool$
 $(\langle -, \vdash / - \text{---}jvm\text{-}\rangle / \rightarrow [61,0,61,0,61]\ 60)$
for $P :: 'addr\ jvm\text{-}prog$ **and** $t :: 'thread\text{-}id$

where

$exec\text{-}1I:$
 $(ta,\ \sigma') \in exec\ P\ t\ \sigma \Longrightarrow P,\ t \vdash \sigma \text{---}ta\text{-}jvm\text{-}\rightarrow \sigma'$

lemma $exec\text{-}1\text{-}iff:$

$P,\ t \vdash \sigma \text{---}ta\text{-}jvm\text{-}\rightarrow \sigma' \longleftrightarrow (ta,\ \sigma') \in exec\ P\ t\ \sigma$

by($auto\ intro: exec\text{-}1I\ elim: exec\text{-}1.cases$)

end

The start configuration of the JVM: in the start heap, we call a method m of class C in program P with parameters vs . The *this* pointer of the frame is set to *Null* to simulate a static method invocation.

abbreviation $JVM\text{-}local\text{-}start ::$

$cname \Rightarrow mname \Rightarrow ty\ list \Rightarrow ty \Rightarrow 'addr\ jvm\text{-}method \Rightarrow 'addr\ val\ list$
 $\Rightarrow 'addr\ jvm\text{-}thread\text{-}state$

where

JVM-local-start \equiv
 $\lambda C M Ts T (m\acute{x}s, m\acute{x}l0, b) \text{ vs.}$
 $(None, [([], Null \# \text{ vs } @ \text{ replicate } m\acute{x}l0 \text{ undefined-value}, C, M, 0)])$

context *JVM-heap-base* **begin**

abbreviation *JVM-start-state* ::

$'addr \text{ jvm-prog} \Rightarrow \text{cname} \Rightarrow \text{mname} \Rightarrow 'addr \text{ val list} \Rightarrow ('addr, 'thread-id, 'addr \text{ jvm-thread-state}, 'heap, 'addr)$
state

where

$JVM\text{-start-state} \equiv \text{start-state } JVM\text{-local-start}$

definition *JVM-start-state'* :: $'addr \text{ jvm-prog} \Rightarrow \text{cname} \Rightarrow \text{mname} \Rightarrow 'addr \text{ val list} \Rightarrow ('addr, 'heap)$
jvm-state

where

$JVM\text{-start-state}' P C M \text{ vs} \equiv$

$\text{let } (D, Ts, T, \text{meth}) = \text{method } P C M;$

$(m\acute{x}s, m\acute{x}l0, \text{ins}, \text{xt}) = \text{the meth}$

$\text{in } (None, \text{start-heap}, [([], Null \# \text{ vs } @ \text{ replicate } m\acute{x}l0 \text{ undefined-value}, D, M, 0)])$

end

end

5.7 A Defensive JVM

theory *JVMDefensive*

imports *JVMExec ../Common/ExternalCallWF*

begin

Extend the state space by one element indicating a type error (or other abnormal termination)

datatype $'a \text{ type-error} = \text{TypeError} \mid \text{Normal } 'a$

context *JVM-heap-base* **begin**

definition *is-Array-ref* :: $'addr \text{ val} \Rightarrow 'heap \Rightarrow \text{bool}$ **where**

$\text{is-Array-ref } v h \equiv$

$\text{is-Ref } v \wedge$

$(v \neq \text{Null} \longrightarrow \text{typeof-addr } h (\text{the-Addr } v) \neq \text{None} \wedge \text{is-Array } (\text{ty-of-htype } (\text{the } (\text{typeof-addr } h (\text{the-Addr } v))))))$

declare *is-Array-ref-def*[*simp*]

primrec *check-instr* :: $'addr \text{ instr}, 'addr \text{ jvm-prog}, 'heap, 'addr \text{ val list}, 'addr \text{ val list},$
 $\text{cname}, \text{mname}, \text{pc}, 'addr \text{ frame list} \Rightarrow \text{bool}$

where

check-instr-Load:

$\text{check-instr } (\text{Load } n) P h \text{ stk } \text{loc } C M_0 \text{ pc } \text{frs} =$

$(n < \text{length } \text{loc})$

| *check-instr-Store:*

$\text{check-instr } (\text{Store } n) P h \text{ stk } \text{loc } C_0 M_0 \text{ pc } \text{frs} =$

$(0 < \text{length } stk \wedge n < \text{length } loc)$

| *check-instr-Push*:

check-instr (*Push* v) P h stk loc C_0 M_0 pc frs =
 $(\neg \text{is-Addr } v)$

| *check-instr-New*:

check-instr (*New* C) P h stk loc C_0 M_0 pc frs =
 $\text{is-class } P$ C

| *check-instr-NewArray*:

check-instr (*NewArray* T) P h stk loc C_0 M_0 pc frs =
 $(\text{is-type } P$ (T []) $\wedge 0 < \text{length } stk \wedge \text{is-Intg}(\text{hd } stk))$

| *check-instr-ALoad*:

check-instr *ALoad* P h stk loc C_0 M_0 pc frs =
 $(1 < \text{length } stk \wedge \text{is-Intg}(\text{hd } stk) \wedge \text{is-Array-ref}(\text{hd}(\text{tl } stk))$ $h)$

| *check-instr-ASStore*:

check-instr *ASStore* P h stk loc C_0 M_0 pc frs =
 $(2 < \text{length } stk \wedge \text{is-Intg}(\text{hd}(\text{tl } stk)) \wedge \text{is-Array-ref}(\text{hd}(\text{tl}(\text{tl } stk)))$ $h \wedge \text{typeof}_h(\text{hd } stk) \neq \text{None})$

| *check-instr-ALength*:

check-instr *ALength* P h stk loc C_0 M_0 pc frs =
 $(0 < \text{length } stk \wedge \text{is-Array-ref}(\text{hd } stk)$ $h)$

| *check-instr-Getfield*:

check-instr (*Getfield* F C) P h stk loc C_0 M_0 pc frs =
 $(0 < \text{length } stk \wedge (\exists C' T \text{ fm}. P \vdash C \text{ sees } F:T(\text{fm}) \text{ in } C') \wedge$
 $(\text{let } (C', T, \text{fm}) = \text{field } P C F; \text{ref} = \text{hd } stk \text{ in}$
 $C' = C \wedge \text{is-Ref } \text{ref} \wedge (\text{ref} \neq \text{Null} \rightarrow$
 $(\exists T. \text{typeof-addr } h(\text{the-Addr } \text{ref}) = \lfloor T \rfloor \wedge P \vdash \text{class-type-of } T \preceq^* C))))$

| *check-instr-Putfield*:

check-instr (*Putfield* F C) P h stk loc C_0 M_0 pc frs =
 $(1 < \text{length } stk \wedge (\exists C' T \text{ fm}. P \vdash C \text{ sees } F:T(\text{fm}) \text{ in } C') \wedge$
 $(\text{let } (C', T, \text{fm}) = \text{field } P C F; v = \text{hd } stk; \text{ref} = \text{hd}(\text{tl } stk) \text{ in}$
 $C' = C \wedge \text{is-Ref } \text{ref} \wedge (\text{ref} \neq \text{Null} \rightarrow$
 $(\exists T'. \text{typeof-addr } h(\text{the-Addr } \text{ref}) = \lfloor T' \rfloor \wedge P \vdash \text{class-type-of } T' \preceq^* C \wedge P, h \vdash v : \leq T))))$

| *check-instr-CAS*:

check-instr (*CAS* F C) P h stk loc C_0 M_0 pc frs =
 $(2 < \text{length } stk \wedge (\exists C' T \text{ fm}. P \vdash C \text{ sees } F:T(\text{fm}) \text{ in } C') \wedge$
 $(\text{let } (C', T, \text{fm}) = \text{field } P C F; v'' = \text{hd } stk; v' = \text{hd}(\text{tl } stk); v = \text{hd}(\text{tl}(\text{tl } stk)) \text{ in}$
 $C' = C \wedge \text{is-Ref } v \wedge \text{volatile } \text{fm} \wedge (v \neq \text{Null} \rightarrow$
 $(\exists T'. \text{typeof-addr } h(\text{the-Addr } v) = \lfloor T' \rfloor \wedge P \vdash \text{class-type-of } T' \preceq^* C \wedge P, h \vdash v' : \leq T \wedge P, h \vdash$
 $v'' : \leq T))))$

| *check-instr-Checkcast*:

check-instr (*Checkcast* T) P h stk loc C_0 M_0 pc frs =
 $(0 < \text{length } stk \wedge \text{is-type } P T)$

| *check-instr-Instanceof*:

check-instr (*Instanceof* T) P h stk loc C_0 M_0 pc frs =

$(0 < \text{length } stk \wedge \text{is-type } P \ T \wedge \text{is-Ref } (hd \ stk))$

| *check-instr-Invoke:*

check-instr (Invoke $M \ n$) $P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$
 $(n < \text{length } stk \wedge \text{is-Ref } (stk!n) \wedge$
 $(stk!n \neq \text{Null} \longrightarrow$
 $(\text{let } a = \text{the-Addr } (stk!n);$
 $\quad T = \text{the } (\text{typeof-addr } h \ a);$
 $\quad C = \text{class-type-of } T;$
 $\quad (D, \ Ts, \ Tr, \ meth) = \text{method } P \ C \ M$
 $\text{in } \text{typeof-addr } h \ a \neq \text{None} \wedge P \vdash C \ \text{has } M \wedge$
 $P, h \vdash \text{rev } (\text{take } n \ stk) \ [:\leq] \ Ts \wedge$
 $(meth = \text{None} \longrightarrow D \cdot M(Ts) :: Tr))))$

| *check-instr-Return:*

check-instr Return $P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$
 $(0 < \text{length } stk \wedge ((0 < \text{length } frs) \longrightarrow$
 $(P \vdash C_0 \ \text{has } M_0) \wedge$
 $(\text{let } v = hd \ stk;$
 $\quad T = \text{fst } (\text{snd } (\text{snd } (\text{method } P \ C_0 \ M_0))))$
 $\text{in } P, h \vdash v : \leq T))$

| *check-instr-Pop:*

check-instr Pop $P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$
 $(0 < \text{length } stk)$

| *check-instr-Dup:*

check-instr Dup $P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$
 $(0 < \text{length } stk)$

| *check-instr-Swap:*

check-instr Swap $P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$
 $(1 < \text{length } stk)$

| *check-instr-BinOpInstr:*

check-instr (BinOpInstr bop) $P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$
 $(1 < \text{length } stk \wedge (\exists T1 \ T2 \ T. \text{typeof}_h (hd \ stk) = \lfloor T2 \rfloor \wedge \text{typeof}_h (hd \ (tl \ stk)) = \lfloor T1 \rfloor \wedge P \vdash$
 $T1 \ll bop \gg T2 : T))$

| *check-instr-IfFalse:*

check-instr (IfFalse b) $P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$
 $(0 < \text{length } stk \wedge \text{is-Bool } (hd \ stk) \wedge 0 \leq \text{int } pc+b)$

| *check-instr-Goto:*

check-instr (Goto b) $P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$
 $(0 \leq \text{int } pc+b)$

| *check-instr-Throw:*

check-instr ThrowExc $P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$
 $(0 < \text{length } stk \wedge \text{is-Ref } (hd \ stk) \wedge P \vdash \text{the } (\text{typeof}_h (hd \ stk)) \leq \text{Class } \text{Throwable})$

| *check-instr-MEnter:*

check-instr MEnter $P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$
 $(0 < \text{length } stk \wedge \text{is-Ref } (hd \ stk))$

| *check-instr-MExit*:
check-instr MExit P h stk loc C₀ M₀ pc frs =
(0 < length stk ∧ is-Ref (hd stk))

definition *check-xcpt* :: 'addr jvm-prog ⇒ 'heap ⇒ nat ⇒ pc ⇒ ex-table ⇒ 'addr ⇒ bool
where
check-xcpt P h n pc xt a ↔
(∃ C. typeof-addr h a = [Class-type C] ∧
(case match-ex-table P C pc xt of None ⇒ True | Some (pc', d') ⇒ d' ≤ n))

definition *check* :: 'addr jvm-prog ⇒ ('addr, 'heap) jvm-state ⇒ bool
where
check P σ ≡ *let (xcpt, h, frs) = σ in*
(case frs of [] ⇒ True | (stk, loc, C, M, pc) # frs' ⇒
P ⊢ C has M ∧
(let (C', Ts, T, meth) = method P C M; (mxs, mxl₀, ins, xt) = the meth; i = ins!pc in
meth ≠ None ∧ pc < size ins ∧ size stk ≤ mxs ∧
(case xcpt of None ⇒ check-instr i P h stk loc C M pc frs'
| Some a ⇒ check-xcpt P h (length stk) pc xt a)))

definition *exec-d* ::
'addr jvm-prog ⇒ 'thread-id ⇒ ('addr, 'heap) jvm-state ⇒ ('addr, 'thread-id, 'heap) jvm-ta-state set
type-error
where
exec-d P t σ ≡ *if check P σ then Normal (exec P t σ) else TypeError*

inductive
exec-1-d ::
'addr jvm-prog ⇒ 'thread-id ⇒ ('addr, 'heap) jvm-state type-error
⇒ ('addr, 'thread-id, 'heap) jvm-thread-action ⇒ ('addr, 'heap) jvm-state type-error ⇒ bool
(⟨-, - ⊢ - - - - jvmd → -⟩ [61, 0, 61, 0, 61] 60)
for *P* :: 'addr jvm-prog **and** *t* :: 'thread-id
where
exec-1-d-ErrorI: *exec-d P t σ = TypeError ⇒ P, t ⊢ Normal σ -ε-jvmd → TypeError*
| *exec-1-d-NormalI*: *[exec-d P t σ = Normal Σ; (tas, σ') ∈ Σ] ⇒ P, t ⊢ Normal σ -tas-jvmd → Normal σ'*

lemma *jvmd-NormalD*:
P, t ⊢ Normal σ -ta-jvmd → Normal σ' ⇒ check P σ ∧ (ta, σ') ∈ exec P t σ ∧ (∃ xcp h f frs. σ = (xcp, h, f # frs))
apply(erule *exec-1-d.cases*, auto simp add: *exec-d-def split: if-split-asm*)
apply(case-tac *b*, auto)
done

lemma *jvmd-NormalE*:
assumes *P, t ⊢ Normal σ -ta-jvmd → Normal σ'*
obtains *xcp h f frs* **where** *check P σ (ta, σ') ∈ exec P t σ σ = (xcp, h, f # frs)*
using *assms*
by(auto dest: *jvmd-NormalD*)

lemma *exec-d-eq-TypeError*: *exec-d P t σ = TypeError ↔ ¬ check P σ*
by(simp add: *exec-d-def*)

lemma *exec-d-eq-Normal*: $exec-d\ P\ t\ \sigma = Normal\ (exec\ P\ t\ \sigma) \longleftrightarrow check\ P\ \sigma$
by(*auto simp add: exec-d-def*)

end

declare *split-paired-All* [*simp del*]

declare *split-paired-Ex* [*simp del*]

lemma *if-neq* [*dest!*]:

$(if\ P\ then\ A\ else\ B) \neq B \implies P$

by (*cases P, auto*)

context *JVM-heap-base* **begin**

lemma *exec-d-no-errorI* [*intro*]:

$check\ P\ \sigma \implies exec-d\ P\ t\ \sigma \neq TypeError$

by (*unfold exec-d-def simp*)

theorem *no-type-error-commutes*:

$exec-d\ P\ t\ \sigma \neq TypeError \implies exec-d\ P\ t\ \sigma = Normal\ (exec\ P\ t\ \sigma)$

by (*unfold exec-d-def, auto*)

lemma *defensive-imp-aggressive-1*:

$P, t \vdash (Normal\ \sigma) -tas-jvmd \rightarrow (Normal\ \sigma') \implies P, t \vdash \sigma -tas-jvm \rightarrow \sigma'$

by(*auto elim!: exec-1-d.cases intro!: exec-1.intros simp add: exec-d-def split: if-split-asm*)

end

context *JVM-heap* **begin**

lemma *check-exec-heap*:

assumes *exec*: $(ta, xcp', h', frs') \in exec\ P\ t\ (xcp, h, frs)$

and *check*: $check\ P\ (xcp, h, frs)$

shows $h \sqsubseteq h'$

proof –

from *exec* **have** $frs \neq []$ **by**(*auto*)

then obtain $f\ Frs$ **where** frs [*simp*]: $frs = f \# Frs$

by(*fastforce simp add: neq-Nil-conv*)

obtain $stk\ loc\ C0\ M0\ pc$ **where** f [*simp*]: $f = (stk, loc, C0, M0, pc)$

by(*cases f, blast*)

show *?thesis*

proof(*cases xcp*)

case *None*

with *check* **obtain** $C'\ Ts\ T\ mxs\ mxl0\ ins\ xt$

where *mthd*: $P \vdash C0\ sees\ M0 : Ts \rightarrow T = [(mxs, mxl0, ins, xt)]$ *in* C'

method $P\ C0\ M0 = (C', Ts, T, [(mxs, mxl0, ins, xt)])$

and *check-ins*: $check-instr\ (ins\ !\ pc)\ P\ h\ stk\ loc\ C0\ M0\ pc\ Frs$

and $pc < length\ ins$

and $length\ stk \leq mxs$

by(*auto simp add: check-def has-method-def*)

from *None* *exec* *mthd*

have *xexec*: $(ta, xcp', h', frs') \in exec-instr\ (ins\ !\ pc)\ P\ t\ h\ stk\ loc\ C0\ M0\ pc\ Frs$ **by**(*clarsimp*)

thus *?thesis*

```

proof(cases ins ! pc)
  case (New C)
    with xexec show ?thesis
    by(auto intro: hext-allocate split: if-split-asm)
  next
    case (NewArray T)
      with xexec show ?thesis
      by(auto intro: hext-allocate split: if-split-asm)
    next
      case AStore
        with xexec check-ins show ?thesis
        by(auto simp add: split-beta split: if-split-asm intro: hext-heap-write)
      next
        case Putfield
          with xexec check-ins show ?thesis
          by(auto intro: hext-heap-write simp add: split-beta split: if-split-asm)
        next
          case CAS
            with xexec check-ins show ?thesis
            by(auto intro: hext-heap-write simp add: split-beta split: if-split-asm)
          next
            case (Invoke M n)
              with xexec check-ins show ?thesis
              apply(auto simp add: min-def split-beta is-Ref-def extRet2JVM-def has-method-def
                split: if-split-asm intro: red-external-aggr-hext)
              apply(case-tac va)
              apply(auto 4 3 intro: red-external-aggr-hext is-native.intros)
              done
            next
              case (BinOpInstr bop)
                with xexec check-ins show ?thesis by(auto split: sum.split-asm)
              qed(auto simp add: split-beta split: if-split-asm)
            next
              case (Some a)
                with exec have h' = h by auto
                thus ?thesis by auto
              qed
            qed
          qed
        qed
      qed
    qed
  lemma exec-1-d-hext:
    
$$\llbracket P, t \vdash \text{Normal}(xcp, h, frs) \text{ --ta--} \text{jvmd} \rightarrow \text{Normal}(xcp', h', frs') \rrbracket \implies h \trianglelefteq h'$$

  by(auto elim!: exec-1-d.cases simp add: exec-d-def split: if-split-asm intro: check-exec-hext)
end
end

```

5.8 Instantiating the framework semantics with the JVM

```

theory JVMThreaded
imports
  JVMDefensive
  ../Common/ConformThreaded

```

```

../Framework/FWLiftingSem
../Framework/FWProgressAux
begin

primrec JVM-final :: 'addr jvm-thread-state  $\Rightarrow$  bool
where
  JVM-final (xcp, frs) = (frs = [])

  The aggressive JVM

context JVM-heap-base begin

abbreviation mexec ::
  'addr jvm-prog  $\Rightarrow$  'thread-id  $\Rightarrow$  ('addr jvm-thread-state  $\times$  'heap)
 $\Rightarrow$  ('addr, 'thread-id, 'heap) jvm-thread-action  $\Rightarrow$  ('addr jvm-thread-state  $\times$  'heap)  $\Rightarrow$  bool
where
  mexec P t  $\equiv$  ( $\lambda((xcp, frstls), h)$  ta ((xcp', frstls'), h'). P, t  $\vdash$  (xcp, h, frstls) -ta-jvm $\rightarrow$  (xcp', h', frstls'))

lemma NewThread-memory-exec-instr:
   $\llbracket (ta, s) \in \text{exec-instr } I P t h \text{ stk loc } C M pc \text{ frs}; \text{NewThread } t' x m \in \text{set } \{ta\}_t \rrbracket \Longrightarrow m = \text{fst } (\text{snd } s)$ 
apply(cases I)
apply(auto split: if-split-asm simp add: split-beta ta-upd-simps)
apply(auto dest!: red-ext-aggr-new-thread-heap simp add: extRet2JVM-def split: extCallRet.split)
done

lemma NewThread-memory-exec:
   $\llbracket P, t \vdash \sigma -ta-jvm\rightarrow \sigma'; \text{NewThread } t' x m \in \text{set } \{ta\}_t \rrbracket \Longrightarrow m = (\text{fst } (\text{snd } \sigma'))$ 
apply(erule exec-1.cases)
apply(clarsimp)
apply(case-tac bb, simp)
apply(case-tac ag, auto simp add: exception-step-def-raw split: list.split-asm)
apply(drule NewThread-memory-exec-instr, simp+)
done

lemma exec-instr-Wakeup-no-Lock-no-Join-no-Interrupt:
   $\llbracket (ta, s) \in \text{exec-instr } I P t h \text{ stk loc } C M pc \text{ frs}; \text{Notified} \in \text{set } \{ta\}_w \vee \text{WokenUp} \in \text{set } \{ta\}_w \rrbracket$ 
 $\Longrightarrow \text{collect-locks } \{ta\}_l = \{\} \wedge \text{collect-cond-actions } \{ta\}_c = \{\} \wedge \text{collect-interrupts } \{ta\}_i = \{\}$ 
apply(cases I)
apply(auto split: if-split-asm simp add: split-beta ta-upd-simps dest: red-external-aggr-Wakeup-no-Join)
done

lemma mexec-instr-Wakeup-no-Join:
   $\llbracket P, t \vdash \sigma -ta-jvm\rightarrow \sigma'; \text{Notified} \in \text{set } \{ta\}_w \vee \text{WokenUp} \in \text{set } \{ta\}_w \rrbracket$ 
 $\Longrightarrow \text{collect-locks } \{ta\}_l = \{\} \wedge \text{collect-cond-actions } \{ta\}_c = \{\} \wedge \text{collect-interrupts } \{ta\}_i = \{\}$ 
apply(erule exec-1.cases)
apply(clarsimp)
apply(case-tac bb, simp)
apply(case-tac ag, clarsimp simp add: exception-step-def-raw split: list.split-asm del: disjE)
apply(drule exec-instr-Wakeup-no-Lock-no-Join-no-Interrupt)
apply auto
done

lemma mexec-final:
   $\llbracket \text{mexec } P t (x, m) ta (x', m'); \text{JVM-final } x \rrbracket \Longrightarrow \text{False}$ 

```

by(cases x)(auto simp add: exec-1-iff)

lemma *exec-mthr: multithreaded JVM-final (mexec P)*
apply(unfold-locales)
apply(clararsimp, drule NewThread-memory-exec, fastforce, simp)
apply(erule (1) mexec-final)
done

end

sublocale *JVM-heap-base < exec-mthr:*

multithreaded
 JVM-final
 mexec P
 convert-RA
 for P

by(rule *exec-mthr*)

context *JVM-heap-base begin*

abbreviation *mexecT* ::

 ' $addr$ *jvm-prog*
 \Rightarrow (' $addr$, ' $thread-id$, ' $addr$ *jvm-thread-state*, ' $heap$, ' $addr$) *state*
 \Rightarrow ' $thread-id$ \times (' $addr$, ' $thread-id$, ' $heap$) *jvm-thread-action*
 \Rightarrow (' $addr$, ' $thread-id$, ' $addr$ *jvm-thread-state*, ' $heap$, ' $addr$) *state* \Rightarrow *bool*

where

mexecT P \equiv *exec-mthr.redT P*

abbreviation *mexecT-syntax1* ::

 ' $addr$ *jvm-prog* \Rightarrow (' $addr$, ' $thread-id$, ' $addr$ *jvm-thread-state*, ' $heap$, ' $addr$) *state*
 \Rightarrow ' $thread-id$ \Rightarrow (' $addr$, ' $thread-id$, ' $heap$) *jvm-thread-action*
 \Rightarrow (' $addr$, ' $thread-id$, ' $addr$ *jvm-thread-state*, ' $heap$, ' $addr$) *state* \Rightarrow *bool*
 ($\langle - \vdash - \dashv \rightarrow_{jvm} - \rangle$ [50,0,0,50] 80)

where

mexecT-syntax1 P s t ta s' \equiv *mexecT P s (t, ta) s'*

abbreviation *mExecT-syntax1* ::

 ' $addr$ *jvm-prog* \Rightarrow (' $addr$, ' $thread-id$, ' $addr$ *jvm-thread-state*, ' $heap$, ' $addr$) *state*
 \Rightarrow (' $thread-id$ \times (' $addr$, ' $thread-id$, ' $heap$) *jvm-thread-action*) *list*
 \Rightarrow (' $addr$, ' $thread-id$, ' $addr$ *jvm-thread-state*, ' $heap$, ' $addr$) *state* \Rightarrow *bool*
 ($\langle - \vdash - \dashv \rightarrow_{jvm^*} - \rangle$ [50,0,0,50] 80)

where

$P \vdash s \dashv \rightarrow_{tas} s' \equiv$ *exec-mthr.RedT P s ttas s'*

The defensive JVM

abbreviation *mexecd* ::

 ' $addr$ *jvm-prog* \Rightarrow ' $thread-id$ \Rightarrow ' $addr$ *jvm-thread-state* \times ' $heap$
 \Rightarrow (' $addr$, ' $thread-id$, ' $heap$) *jvm-thread-action* \Rightarrow ' $addr$ *jvm-thread-state* \times ' $heap$ \Rightarrow *bool*

where

mexecd P t \equiv ($\lambda((xcp, frstls), h)$ *ta* ((xcp' , $frstls'$), h'). $P, t \vdash$ *Normal* ($xcp, h, frstls$) \dashv *ta* \dashv *jvmd* \rightarrow *Normal* ($xcp', h', frstls'$))

lemma *execd-mthr: multithreaded JVM-final (mexecd P)*


```

apply(unfold-locales)
  apply(fastforce dest: defensive-imp-aggressive-1 NewThread-memory-exec)
apply(auto elim: jvmd-NormalE)
done

```

end

sublocale *JVM-heap-base* < *execd-mthr*:

```

  multithreaded
  JVM-final
  mexecd P
  convert-RA
  for P
by(rule execd-mthr)

```

context *JVM-heap-base* **begin**

abbreviation *mexecdT* ::

```

  'addr jvm-prog ⇒ ('addr, 'thread-id, 'addr jvm-thread-state, 'heap, 'addr) state
  ⇒ 'thread-id × ('addr, 'thread-id, 'heap) jvm-thread-action
  ⇒ ('addr, 'thread-id, 'addr jvm-thread-state, 'heap, 'addr) state ⇒ bool

```

where

```

  mexecdT P ≡ execd-mthr.redT P

```

abbreviation *mexecdT-syntax1* ::

```

  'addr jvm-prog ⇒ ('addr, 'thread-id, 'addr jvm-thread-state, 'heap, 'addr) state
  ⇒ 'thread-id ⇒ ('addr, 'thread-id, 'heap) jvm-thread-action
  ⇒ ('addr, 'thread-id, 'addr jvm-thread-state, 'heap, 'addr) state ⇒ bool
  (⟦-⟧ - - ▷ ->→ jvmd -> [50,0,0,0,50] 80)

```

where

```

  mexecdT-syntax1 P s t ta s' ≡ mexecdT P s (t, ta) s'

```

abbreviation *mExecdT-syntax1* ::

```

  'addr jvm-prog ⇒ ('addr, 'thread-id, 'addr jvm-thread-state, 'heap, 'addr) state
  ⇒ ('thread-id × ('addr, 'thread-id, 'heap) jvm-thread-action) list
  ⇒ ('addr, 'thread-id, 'addr jvm-thread-state, 'heap, 'addr) state ⇒ bool
  (⟦-⟧ - - ▷ ->→ jvmd* -> [50,0,0,50] 80)

```

where

```

  P ⊢ s -> ttas -> jvmd* s' ≡ execd-mthr.RedT P s ttas s'

```

lemma *mexecd-Suspend-Invoke*:

```

  [ [ mexecd P t (x, m) ta (x', m'); Suspend w ∈ set {ta}w ]
  ⇒ ∃ stk loc C M pc frs' n a T Ts Tr D. x' = (None, (stk, loc, C, M, pc) # frs') ∧ instrs-of P
  C M ! pc = Invoke wait n ∧ stk ! n = Addr a ∧ typeof-addr m a = [ T ] ∧ P ⊢ class-type-of T sees
  wait: Ts → Tr = Native in D ∧ D.wait(Ts) :: Tr

```

apply(*cases x'*)

apply(*cases x*)

apply(*cases fst x*)

apply(*auto elim!: jvmd-NormalE simp add: split-beta*)

apply(*rename-tac [!] stk loc C M pc frs*)

apply(*case-tac [!] instrs-of P C M ! pc*)

apply(*auto split: if-split-asm simp add: split-beta check-def is-Ref-def has-method-def*)

apply(*frule red-external-aggr-Suspend-StaySame, simp, drule red-external-aggr-Suspend-waitD, simp,*

fastforce)+
done

end

context *JVM-heap* **begin**

lemma *exec-instr-New-Thread-exists-thread-object*:

$\llbracket (ta, xcp', h', frs') \in \text{exec-instr ins } P \ t \ h \ \text{stk loc } C \ M \ \text{pc frs};$
 $\text{check-instr ins } P \ h \ \text{stk loc } C \ M \ \text{pc frs};$
 $\text{NewThread } t' \ x \ h'' \in \text{set } \{\{ta\}_t\} \rrbracket$
 $\implies \exists C. \text{typeof-addr } h' \ (\text{thread-id2addr } t') = \lfloor \text{Class-type } C \rfloor \wedge P \vdash C \preceq^* \text{Thread}$

apply(*cases ins*)

apply(*fastforce simp add: split-beta ta-upd-simps split: if-split-asm intro: red-external-aggr-new-thread-exists-thread*)
done

lemma *exec-New-Thread-exists-thread-object*:

$\llbracket P, t \vdash \text{Normal } (xcp, h, frs) -ta-jvmd \rightarrow \text{Normal } (xcp', h', frs'); \text{NewThread } t' \ x \ h'' \in \text{set } \{\{ta\}_t\} \rrbracket$
 $\implies \exists C. \text{typeof-addr } h' \ (\text{thread-id2addr } t') = \lfloor \text{Class-type } C \rfloor \wedge P \vdash C \preceq^* \text{Thread}$

apply(*cases xcp*)

apply(*case-tac [!] frs*)

apply(*auto simp add: check-def elim!: jvmd-NormalE dest!: exec-instr-New-Thread-exists-thread-object*)
done

lemma *exec-instr-preserve-tconf*:

$\llbracket (ta, xcp', h', frs') \in \text{exec-instr ins } P \ t \ h \ \text{stk loc } C \ M \ \text{pc frs};$
 $\text{check-instr ins } P \ h \ \text{stk loc } C \ M \ \text{pc frs};$
 $P, h \vdash t' \sqrt{t} \rrbracket$
 $\implies P, h' \vdash t' \sqrt{t}$

apply(*cases ins*)

apply(*auto intro: tconf-hext-mono hext-allocate hext-heap-write red-external-aggr-preserves-tconf split: if-split-asm sum.split-asm simp add: split-beta has-method-def intro!: is-native.intros cong del: image-cong-simp*)

done

lemma *exec-preserve-tconf*:

$\llbracket P, t \vdash \text{Normal } (xcp, h, frs) -ta-jvmd \rightarrow \text{Normal } (xcp', h', frs'); P, h \vdash t' \sqrt{t} \rrbracket \implies P, h' \vdash t' \sqrt{t}$

apply(*cases xcp*)

apply(*case-tac [!] frs*)

apply(*auto simp add: check-def elim!: jvmd-NormalE elim!: exec-instr-preserve-tconf*)

done

lemma *lifting-wf-thread-conf: lifting-wf JVM-final (mexecd P) ($\lambda t \ x \ m. P, m \vdash t \sqrt{t}$)*

by(*unfold-locales*)(*auto intro: exec-preserve-tconf dest: exec-New-Thread-exists-thread-object intro: tconfI*)

end

sublocale *JVM-heap* < *execd-tconf: lifting-wf JVM-final mexecd P convert-RA $\lambda t \ x \ m. P, m \vdash t \sqrt{t}$*
by(*rule lifting-wf-thread-conf*)

context *JVM-heap* **begin**

lemma *execd-hext*:

$P \vdash s -t>ta \rightarrow jvmd \ s' \implies \text{shr } s \leq \text{shr } s'$

by(*auto elim!*: *execd-mthr.RedT.cases dest!*: *exec-1-d-hext intro: hext-trans*)

lemma *Execd-hext*:

assumes $P \vdash s \rightarrow_{\text{tta} \rightarrow \text{jvmd}^*} s'$

shows $\text{shr } s \sqsubseteq \text{shr } s'$

using *assms unfolding execd-mthr.RedT-def*

by(*induct*)(*auto dest!*: *execd-hext intro: hext-trans simp add: execd-mthr.RedT-def*)

end

end

theory *JVM-Main*

imports

JVMState

JVMThreaded

begin

end

Chapter 6

Bytecode verifier

6.1 The JVM Type System as Semilattice

```
theory JVM-SemiType
imports
  ../Common/SemiType
begin

type-synonym tyl = ty err list
type-synonym tys = ty list
type-synonym tyi = tys × tyl
type-synonym tyi' = tyi option
type-synonym tym = tyi' list
type-synonym tyP = mname ⇒ cname ⇒ tym

definition stk-esl :: 'c prog ⇒ nat ⇒ tys esl
where
  stk-esl P mxs ≡ upto-esl mxs (SemiType.esl P)

definition loc-sl :: 'c prog ⇒ nat ⇒ tyl sl
where
  loc-sl P mxl ≡ Listn.sl mxl (Err.sl (SemiType.esl P))

definition sl :: 'c prog ⇒ nat ⇒ nat ⇒ tyi' err sl
where
  sl P mxs mxl ≡
    Err.sl(Opt.esl(Product.esl (stk-esl P mxs) (Err.esl(loc-sl P mxl))))

definition states :: 'c prog ⇒ nat ⇒ nat ⇒ tyi' err set
where
  states P mxs mxl ≡ fst(sl P mxs mxl)

definition le :: 'c prog ⇒ nat ⇒ nat ⇒ tyi' err ord
where
  le P mxs mxl ≡ fst(snd(sl P mxs mxl))

definition sup :: 'c prog ⇒ nat ⇒ nat ⇒ tyi' err binop
where
  sup P mxs mxl ≡ snd(snd(sl P mxs mxl))
```

definition $sup\text{-}ty\text{-}opt :: [c\ prog, ty\ err, ty\ err] \Rightarrow bool$
 $(\langle - \vdash - \leq_T \rightarrow [71, 71, 71] \ 70)$

where

$sup\text{-}ty\text{-}opt\ P \equiv Err.le\ (widen\ P)$

definition $sup\text{-}state :: [c\ prog, ty_i, ty_i] \Rightarrow bool$
 $(\langle - \vdash - \leq_i \rightarrow [71, 71, 71] \ 70)$

where

$sup\text{-}state\ P \equiv Product.le\ (Listn.le\ (widen\ P))\ (Listn.le\ (sup\text{-}ty\text{-}opt\ P))$

definition $sup\text{-}state\text{-}opt :: [c\ prog, ty_i', ty_i'] \Rightarrow bool$
 $(\langle - \vdash - \leq'' \rightarrow [71, 71, 71] \ 70)$

where

$sup\text{-}state\text{-}opt\ P \equiv Opt.le\ (sup\text{-}state\ P)$

abbreviation $sup\text{-}loc :: [c\ prog, ty_l, ty_l] \Rightarrow bool\ (\langle - \vdash - [\leq_T] \rightarrow [71, 71, 71] \ 70)$

where $P \vdash LT\ [\leq_T]\ LT' \equiv list\text{-}all2\ (sup\text{-}ty\text{-}opt\ P)\ LT\ LT'$

notation (*ASCII*)

$sup\text{-}ty\text{-}opt\ (\langle - \mid - \leq = T \rightarrow [71, 71, 71] \ 70)$ **and**

$sup\text{-}state\ (\langle - \mid - \leq = i \rightarrow [71, 71, 71] \ 70)$ **and**

$sup\text{-}state\text{-}opt\ (\langle - \mid - \leq = ' \rightarrow [71, 71, 71] \ 70)$ **and**

$sup\text{-}loc\ (\langle - \mid - [\leq = T] \rightarrow [71, 71, 71] \ 70)$

6.1.1 Unfolding

lemma *JVM-states-unfold*:

$states\ P\ mxs\ mxl \equiv err(opt((Union\ \{list\ n\ (types\ P)\ \mid n.\ n \leq mxs\ \}) \times$
 $list\ mxl\ (err(types\ P))))$

apply ($unfold\ states\text{-}def\ sl\text{-}def\ Opt.esl\text{-}def\ Err.sl\text{-}def$

$stk\text{-}esl\text{-}def\ loc\text{-}sl\text{-}def\ Product.esl\text{-}def$

$Listn.sl\text{-}def\ upto\text{-}esl\text{-}def\ SemiType.esl\text{-}def\ Err.esl\text{-}def$)

apply *simp*

done

lemma *JVM-le-unfold*:

$le\ P\ m\ n \equiv$

$Err.le(Opt.le(Product.le(Listn.le(widen\ P))(Listn.le(Err.le(widen\ P))))))$

apply ($unfold\ le\text{-}def\ sl\text{-}def\ Opt.esl\text{-}def\ Err.sl\text{-}def$

$stk\text{-}esl\text{-}def\ loc\text{-}sl\text{-}def\ Product.esl\text{-}def$

$Listn.sl\text{-}def\ upto\text{-}esl\text{-}def\ SemiType.esl\text{-}def\ Err.esl\text{-}def$)

apply *simp*

done

lemma *sl-def2*:

$JVM\text{-}SemiType.sl\ P\ mxs\ mxl \equiv$

$(states\ P\ mxs\ mxl,\ JVM\text{-}SemiType.le\ P\ mxs\ mxl,\ JVM\text{-}SemiType.sup\ P\ mxs\ mxl)$

by ($unfold\ JVM\text{-}SemiType.sup\text{-}def\ states\text{-}def\ JVM\text{-}SemiType.le\text{-}def$) *simp*

lemma *JVM-le-conv*:

$le\ P\ m\ n\ (OK\ t1)\ (OK\ t2) = P \vdash t1 \leq' t2$

by ($simp\ add:\ JVM\text{-}le\text{-}unfold\ Err.le\text{-}def\ lesub\text{-}def\ sup\text{-}state\text{-}opt\text{-}def$

$sup\text{-}state\text{-}def\ sup\text{-}ty\text{-}opt\text{-}def$)

lemma *JVM-le-Err-conv*:
 $le\ P\ m\ n = Err.le\ (sup\ state\ opt\ P)$
by (*unfold sup-state-opt-def sup-state-def*
sup-ty-opt-def JVM-le-unfold) *simp*

lemma *err-le-unfold* [*iff*]:
 $Err.le\ r\ (OK\ a)\ (OK\ b) = r\ a\ b$
by (*simp add: Err.le-def lesub-def*)

6.1.2 Semilattice

lemma *order-sup-state-opt* [*intro, simp*]:
 $wf\ prog\ wf\ mb\ P \implies order\ (sup\ state\ opt\ P)$
by (*unfold sup-state-opt-def sup-state-def sup-ty-opt-def*) *blast*

lemma *semilat-JVM* [*intro?*]:
 $wf\ prog\ wf\ mb\ P \implies semilat\ (JVM\ SemiType.sl\ P\ mxs\ mxl)$
apply (*unfold JVM-SemiType.sl-def stk-esl-def loc-sl-def*)
apply (*blast intro: err-semilat-Product-esl err-semilat-upto-esl*
Listn-sl err-semilat-JType-esl)
done

lemma *acc-JVM* [*intro*]:
 $wf\ prog\ wf\ mb\ P \implies acc\ (JVM\ SemiType.states\ P\ mxs\ mxl)\ (JVM\ SemiType.le\ P\ mxs\ mxl)$
by(*unfold JVM-le-unfold JVM-states-unfold*) *blast*

6.1.3 Widening with \top

lemma *widen-refl*[*iff*]: $widen\ P\ t\ t$ **by** (*simp add: fun-of-def*)

lemma *sup-ty-opt-refl* [*iff*]: $P \vdash T \leq_{\top} T$
apply (*unfold sup-ty-opt-def*)
apply (*fold lesub-def*)
apply (*rule le-err-refl*)
apply (*simp add: lesub-def*)
done

lemma *Err-any-conv* [*iff*]: $P \vdash Err \leq_{\top} T = (T = Err)$
by (*unfold sup-ty-opt-def*) (*rule Err-le-conv [simplified lesub-def]*)

lemma *any-Err* [*iff*]: $P \vdash T \leq_{\top} Err$
by (*unfold sup-ty-opt-def*) (*rule le-Err [simplified lesub-def]*)

lemma *OK-OK-conv* [*iff*]:
 $P \vdash OK\ T \leq_{\top} OK\ T' = P \vdash T \leq T'$
by (*simp add: sup-ty-opt-def fun-of-def*)

lemma *any-OK-conv* [*iff*]:
 $P \vdash X \leq_{\top} OK\ T' = (\exists T. X = OK\ T \wedge P \vdash T \leq T')$
apply (*unfold sup-ty-opt-def*)
apply (*rule le-OK-conv [simplified lesub-def]*)
done

lemma *OK-any-conv*:

$P \vdash OK T \leq_{\top} X = (X = Err \vee (\exists T'. X = OK T' \wedge P \vdash T \leq T'))$
apply (*unfold sup-ty-opt-def*)
apply (*rule OK-le-conv [simplified lesub-def]*)
done

lemma *sup-ty-opt-trans* [*intro?*, *trans*]:

$\llbracket P \vdash a \leq_{\top} b; P \vdash b \leq_{\top} c \rrbracket \implies P \vdash a \leq_{\top} c$

by (*auto intro: widen-trans*

simp add: sup-ty-opt-def Err.le-def lesub-def fun-of-def
split: err.splits)

6.1.4 Stack and Registers

lemma *stk-convert*:

$P \vdash ST \llbracket \leq \rrbracket ST' = Listn.le (widen P) ST ST'$

by (*simp add: Listn.le-def lesub-def*)

lemma *sup-loc-refl* [*iff*]: $P \vdash LT \llbracket \leq_{\top} \rrbracket LT$

by (*rule list-all2-refl simp*)

lemmas *sup-loc-Cons1* [*iff*] = *list-all2-Cons1* [*of sup-ty-opt P*] **for** P

lemma *sup-loc-def*:

$P \vdash LT \llbracket \leq_{\top} \rrbracket LT' \equiv Listn.le (sup-ty-opt P) LT LT'$

by (*simp add: Listn.le-def lesub-def*)

lemma *sup-loc-widens-conv* [*iff*]:

$P \vdash map OK Ts \llbracket \leq_{\top} \rrbracket map OK Ts' = P \vdash Ts \llbracket \leq \rrbracket Ts'$

by (*simp add: list-all2-map1 list-all2-map2*)

lemma *sup-loc-trans* [*intro?*, *trans*]:

$\llbracket P \vdash a \llbracket \leq_{\top} \rrbracket b; P \vdash b \llbracket \leq_{\top} \rrbracket c \rrbracket \implies P \vdash a \llbracket \leq_{\top} \rrbracket c$

by (*rule list-all2-trans, rule sup-ty-opt-trans*)

6.1.5 State Type

lemma *sup-state-conv* [*iff*]:

$P \vdash (ST, LT) \leq_i (ST', LT') = (P \vdash ST \llbracket \leq \rrbracket ST' \wedge P \vdash LT \llbracket \leq_{\top} \rrbracket LT')$

by (*auto simp add: sup-state-def stk-convert lesub-def Product.le-def sup-loc-def*)

lemma *sup-state-conv2*:

$P \vdash s1 \leq_i s2 = (P \vdash fst s1 \llbracket \leq \rrbracket fst s2 \wedge P \vdash snd s1 \llbracket \leq_{\top} \rrbracket snd s2)$

by (*cases s1, cases s2*) *simp*

lemma *sup-state-refl* [*iff*]: $P \vdash s \leq_i s$

by (*auto simp add: sup-state-conv2 intro: list-all2-refl*)

lemma *sup-state-trans* [*intro?*, *trans*]:

$\llbracket P \vdash a \leq_i b; P \vdash b \leq_i c \rrbracket \implies P \vdash a \leq_i c$

by (*auto intro: sup-loc-trans widens-trans simp add: sup-state-conv2*)

lemma *sup-state-opt-None-any* [*iff*]:

$P \vdash None \leq' s$


```

by (simp add: sup-state-opt-def Opt.le-def)

lemma sup-state-opt-any-None [iff]:
   $P \vdash s \leq' \text{None} = (s = \text{None})$ 
by (simp add: sup-state-opt-def Opt.le-def)

lemma sup-state-opt-Some-Some [iff]:
   $P \vdash \text{Some } a \leq' \text{Some } b = P \vdash a \leq_i b$ 
by (simp add: sup-state-opt-def Opt.le-def lesub-def)

lemma sup-state-opt-any-Some:
   $P \vdash (\text{Some } s) \leq' X = (\exists s'. X = \text{Some } s' \wedge P \vdash s \leq_i s')$ 
by (simp add: sup-state-opt-def Opt.le-def lesub-def)

lemma sup-state-opt-refl [iff]:  $P \vdash s \leq' s$ 
by (simp add: sup-state-opt-def Opt.le-def lesub-def)

lemma sup-state-opt-trans [intro?, trans]:
   $\llbracket P \vdash a \leq' b; P \vdash b \leq' c \rrbracket \implies P \vdash a \leq' c$ 
  apply (unfold sup-state-opt-def Opt.le-def lesub-def)
  apply (simp del: split-paired-All)
  apply (rule sup-state-trans, assumption+)
  done

end

```

6.2 Effect of Instructions on the State Type

```

theory Effect
imports
  JVM-SemiType
  ../JVM/JVMExceptions
begin

locale jvm-method = prog +
  fixes mxs :: nat
  fixes mxl0 :: nat
  fixes Ts :: ty list
  fixes Tτ :: ty
  fixes is :: 'addr instr list
  fixes xt :: ex-table

  fixes mxl :: nat
  defines mxl-def:  $mxl \equiv 1 + \text{size } Ts + mxl_0$ 

  Program counter of successor instructions:

primrec succs :: 'addr instr  $\Rightarrow$  tyi  $\Rightarrow$  pc  $\Rightarrow$  pc list
where
  succs (Load idx) τ pc = [pc+1]
| succs (Store idx) τ pc = [pc+1]
| succs (Push v) τ pc = [pc+1]
| succs (Getfield F C) τ pc = [pc+1]
| succs (Putfield F C) τ pc = [pc+1]
| succs (CAS F C) τ pc = [pc+1]

```

```

| succs (New C) τ pc      = [pc+1]
| succs (NewArray T) τ pc = [pc+1]
| succs ALoad τ pc      = (if (fst τ)!1 = NT then [] else [pc+1])
| succs AStore τ pc     = (if (fst τ)!2 = NT then [] else [pc+1])
| succs ALength τ pc    = (if (fst τ)!0 = NT then [] else [pc+1])
| succs (Checkcast C) τ pc = [pc+1]
| succs (Instanceof T) τ pc = [pc+1]
| succs Pop τ pc        = [pc+1]
| succs Dup τ pc        = [pc+1]
| succs Swap τ pc       = [pc+1]
| succs (BinOpInstr b) τ pc = [pc+1]
| succs-IfFalse:
  succs (IfFalse b) τ pc = [pc+1, nat (int pc + b)]
| succs-Goto:
  succs (Goto b) τ pc    = [nat (int pc + b)]
| succs-Return:
  succs Return τ pc     = []
| succs-Invoke:
  succs (Invoke M n) τ pc = (if (fst τ)!n = NT then [] else [pc+1])
| succs-Throw:
  succs ThrowExc τ pc   = []
| succs MEnter τ pc     = (if (fst τ)!0 = NT then [] else [pc+1])
| succs MExit τ pc     = (if (fst τ)!0 = NT then [] else [pc+1])

```

Effect of instruction on the state type:

```

fun effi :: 'addr instr × 'm prog × tyi ⇒ tyi
where
  effi-Load:
    effi (Load n, P, (ST, LT))      = (ok-val (LT ! n) # ST, LT)

| effi-Store:
  effi (Store n, P, (T#ST, LT))    = (ST, LT[n:= OK T])

| effi-Push:
  effi (Push v, P, (ST, LT))      = (the (typeof v) # ST, LT)

| effi-Getfield:
  effi (Getfield F C, P, (T#ST, LT)) = (fst (snd (field P C F)) # ST, LT)

| effi-Putfield:
  effi (Putfield F C, P, (T1#T2#ST, LT)) = (ST,LT)

| effi-CAS:
  effi (CAS F C, P, (T1#T2#T3#ST, LT)) = (Boolean # ST, LT)

| effi-New:
  effi (New C, P, (ST,LT))        = (Class C # ST, LT)

| effi-NewArray:
  effi (NewArray Ty, P, (T#ST,LT)) = (Ty[] # ST,LT)

| effi-ALoad:
  effi (ALoad, P, (T1#T2#ST,LT)) = (the-Array T2# ST,LT)

```

| *eff_i-AStore*:
 $eff_i (AStore, P, (T1 \# T2 \# T3 \# ST, LT)) = (ST, LT)$

| *eff_i-ALength*:
 $eff_i (ALength, P, (T1 \# ST, LT)) = (Integer \# ST, LT)$

| *eff_i-Checkcast*:
 $eff_i (Checkcast \ Ty, P, (T \# ST, LT)) = (Ty \ \# \ ST, LT)$

| *eff_i-Instanceof*:
 $eff_i (Instanceof \ Ty, P, (T \# ST, LT)) = (Boolean \ \# \ ST, LT)$

| *eff_i-Pop*:
 $eff_i (Pop, P, (T \# ST, LT)) = (ST, LT)$

| *eff_i-Dup*:
 $eff_i (Dup, P, (T \# ST, LT)) = (T \ \# \ T \ \# \ ST, LT)$

| *eff_i-Swap*:
 $eff_i (Swap, P, (T1 \ \# \ T2 \ \# \ ST, LT)) = (T2 \ \# \ T1 \ \# \ ST, LT)$

| *eff_i-BinOpInstr*:
 $eff_i (BinOpInstr \ bop, P, (T2 \ \# \ T1 \ \# \ ST, LT)) = ((THE \ T. \ P \vdash \ T1 \ \ll bop \gg \ T2 : T) \ \# \ ST, LT)$

| *eff_i-IfFalse*:
 $eff_i (IfFalse \ b, P, (T_1 \ \# \ ST, LT)) = (ST, LT)$

| *eff_i-Invoke*:
 $eff_i (Invoke \ M \ n, P, (ST, LT)) =$
 $(let \ U = fst \ (snd \ (snd \ (method \ P \ (the \ (class\text{-}type\text{-}of' \ (ST \ ! \ n))) \ M)))$
 $in \ (U \ \# \ drop \ (n+1) \ ST, LT))$

| *eff_i-Goto*:
 $eff_i (Goto \ n, P, s) = s$

| *eff_i-MEnter*:
 $eff_i (MEnter, P, (T1 \ \# \ ST, LT)) = (ST, LT)$

| *eff_i-MExit*:
 $eff_i (MExit, P, (T1 \ \# \ ST, LT)) = (ST, LT)$

fun *is-relevant-class* :: 'addr instr \Rightarrow 'm prog \Rightarrow cname \Rightarrow bool

where

rel-Getfield:
 $is-relevant-class (Getfield \ F \ D) = (\lambda P \ C. \ P \vdash \ NullPointer \ \preceq^* \ C)$

| *rel-Putfield*:
 $is-relevant-class (Putfield \ F \ D) = (\lambda P \ C. \ P \vdash \ NullPointer \ \preceq^* \ C)$

| *rel-CAS*:
 $is-relevant-class (CAS \ F \ D) = (\lambda P \ C. \ P \vdash \ NullPointer \ \preceq^* \ C)$

| *rel-Checkcast*:
 $is-relevant-class (Checkcast \ T) = (\lambda P \ C. \ P \vdash \ ClassCast \ \preceq^* \ C)$

| *rel-New*:
 $is-relevant-class (New \ D) = (\lambda P \ C. \ P \vdash \ OutOfMemory \ \preceq^* \ C)$

$| \text{rel-Throw:}$
 $\text{is-relevant-class ThrowExc} = (\lambda P C. \text{True})$
 $| \text{rel-Invoke:}$
 $\text{is-relevant-class (Invoke M n)} = (\lambda P C. \text{True})$
 $| \text{rel-NewArray:}$
 $\text{is-relevant-class (NewArray T)} = (\lambda P C. (P \vdash \text{OutOfMemory} \preceq^* C) \vee (P \vdash \text{NegativeArraySize} \preceq^* C))$
 $| \text{rel-ALoad:}$
 $\text{is-relevant-class ALoad} = (\lambda P C. P \vdash \text{ArrayIndexOutOfBounds} \preceq^* C \vee P \vdash \text{NullPointer} \preceq^* C)$
 $| \text{rel-ASStore:}$
 $\text{is-relevant-class ASStore} = (\lambda P C. P \vdash \text{ArrayIndexOutOfBounds} \preceq^* C \vee P \vdash \text{ArrayStore} \preceq^* C \vee P \vdash \text{NullPointer} \preceq^* C)$
 $| \text{rel-ALength:}$
 $\text{is-relevant-class ALength} = (\lambda P C. P \vdash \text{NullPointer} \preceq^* C)$
 $| \text{rel-MEnter:}$
 $\text{is-relevant-class MEnter} = (\lambda P C. P \vdash \text{IllegalMonitorState} \preceq^* C \vee P \vdash \text{NullPointer} \preceq^* C)$
 $| \text{rel-MExit:}$
 $\text{is-relevant-class MExit} = (\lambda P C. P \vdash \text{IllegalMonitorState} \preceq^* C \vee P \vdash \text{NullPointer} \preceq^* C)$
 $| \text{rel-BinOp:}$
 $\text{is-relevant-class (BinOpInstr bop)} = \text{binop-relevant-class bop}$
 $| \text{rel-default:}$
 $\text{is-relevant-class } i = (\lambda P C. \text{False})$

definition $\text{is-relevant-entry} :: 'm \text{ prog} \Rightarrow 'addr \text{ instr} \Rightarrow pc \Rightarrow ex\text{-entry} \Rightarrow bool$

where

$\text{is-relevant-entry } P \ i \ pc \ e \equiv$
 $\text{let } (f,t,C,h,d) = e$
 $\text{in } (\text{case } C \text{ of } \text{None} \Rightarrow \text{True} \mid [C'] \Rightarrow \text{is-relevant-class } i \ P \ C') \wedge pc \in \{f..<t\}$

definition $\text{relevant-entries} :: 'm \text{ prog} \Rightarrow 'addr \text{ instr} \Rightarrow pc \Rightarrow ex\text{-table} \Rightarrow ex\text{-table}$

where

$\text{relevant-entries } P \ i \ pc \equiv \text{filter } (\text{is-relevant-entry } P \ i \ pc)$

definition $\text{xcpt-eff} :: 'addr \text{ instr} \Rightarrow 'm \text{ prog} \Rightarrow pc \Rightarrow ty_i \Rightarrow ex\text{-table} \Rightarrow (pc \times ty_i') \text{ list}$

where

$\text{xcpt-eff } i \ P \ pc \ \tau \ et \equiv \text{let } (ST,LT) = \tau \ \text{in}$
 $\text{map } (\lambda(f,t,C,h,d). (h, \text{Some } ((\text{case } C \text{ of } \text{None} \Rightarrow \text{Class Throwable} \mid \text{Some } C' \Rightarrow \text{Class } C')\#\text{drop}$
 $(\text{size } ST - d) \ ST, LT))) (\text{relevant-entries } P \ i \ pc \ et)$

definition $\text{norm-eff} :: 'addr \text{ instr} \Rightarrow 'm \text{ prog} \Rightarrow nat \Rightarrow ty_i \Rightarrow (pc \times ty_i') \text{ list}$

where $\text{norm-eff } i \ P \ pc \ \tau \equiv \text{map } (\lambda pc'. (pc', \text{Some } (\text{eff}_i (i, P, \tau)))) (\text{succs } i \ \tau \ pc)$

definition $\text{eff} :: 'addr \text{ instr} \Rightarrow 'm \text{ prog} \Rightarrow pc \Rightarrow ex\text{-table} \Rightarrow ty_i' \Rightarrow (pc \times ty_i') \text{ list}$

where

$\text{eff } i \ P \ pc \ et \ t \equiv$
 $\text{case } t \ \text{of}$
 $\text{None} \Rightarrow []$
 $\mid \text{Some } \tau \Rightarrow (\text{norm-eff } i \ P \ pc \ \tau) \ @ \ (\text{xcpt-eff } i \ P \ pc \ \tau \ et)$

lemma eff-None:

$\text{eff } i \ P \ pc \ xt \ \text{None} = []$

by (simp add: eff-def)

lemma *eff-Some*:

$eff\ i\ P\ pc\ xt\ (Some\ \tau) = norm\text{-}eff\ i\ P\ pc\ \tau\ @\ xcpt\text{-}eff\ i\ P\ pc\ \tau\ xt$
by (*simp add: eff-def*)

Conditions under which *eff* is applicable:

```

fun appi :: 'addr instr × 'm prog × pc × nat × ty × tyi ⇒ bool
where
  appi-Load:
    appi (Load n, P, pc, mxs, Tr, (ST,LT)) =
      (n < length LT ∧ LT ! n ≠ Err ∧ length ST < mxs)
  | appi-Store:
    appi (Store n, P, pc, mxs, Tr, (T#ST, LT)) =
      (n < length LT)
  | appi-Push:
    appi (Push v, P, pc, mxs, Tr, (ST,LT)) =
      (length ST < mxs ∧ typeof v ≠ None)
  | appi-Getfield:
    appi (Getfield F C, P, pc, mxs, Tr, (T#ST, LT)) =
      (∃ Tf fm. P ⊢ C sees F:Tf (fm) in C ∧ P ⊢ T ≤ Class C)
  | appi-Putfield:
    appi (Putfield F C, P, pc, mxs, Tr, (T1#T2#ST, LT)) =
      (∃ Tf fm. P ⊢ C sees F:Tf (fm) in C ∧ P ⊢ T2 ≤ (Class C) ∧ P ⊢ T1 ≤ Tf)
  | appi-CAS:
    appi (CAS F C, P, pc, mxs, Tr, (T3#T2#T1#ST, LT)) =
      (∃ Tf fm. P ⊢ C sees F:Tf (fm) in C ∧ volatile fm ∧ P ⊢ T1 ≤ Class C ∧ P ⊢ T2 ≤ Tf ∧ P ⊢
T3 ≤ Tf)
  | appi-New:
    appi (New C, P, pc, mxs, Tr, (ST,LT)) =
      (is-class P C ∧ length ST < mxs)
  | appi-NewArray:
    appi (NewArray Ty, P, pc, mxs, Tr, (Integer#ST,LT)) =
      is-type P (Ty[])
  | appi-ALoad:
    appi (ALoad, P, pc, mxs, Tr, (T1#T2#ST,LT)) =
      (T1 = Integer ∧ (T2 ≠ NT → (∃ Ty. T2 = Ty[])))
  | appi-AStore:
    appi (AStore, P, pc, mxs, Tr, (T1#T2#T3#ST,LT)) =
      (T2 = Integer ∧ (T3 ≠ NT → (∃ Ty. T3 = Ty[])))
  | appi-ALength:
    appi (ALength, P, pc, mxs, Tr, (T1#ST,LT)) =
      (T1 = NT ∨ (∃ Ty. T1 = Ty[]))
  | appi-Checkcast:
    appi (Checkcast Ty, P, pc, mxs, Tr, (T#ST,LT)) =
      (is-type P Ty)
  | appi-Instanceof:
    appi (Instanceof Ty, P, pc, mxs, Tr, (T#ST,LT)) =
      (is-type P Ty ∧ is-refT T)
  | appi-Pop:
    appi (Pop, P, pc, mxs, Tr, (T#ST,LT)) =
      True
  | appi-Dup:
    appi (Dup, P, pc, mxs, Tr, (T#ST,LT)) =
      (Suc (length ST) < mxs)

```

$|$ *app_i-Swap*:
 $app_i (Swap, P, pc, mxs, T_r, (T1\#T2\#ST,LT)) = True$
 $|$ *app_i-BinOpInstr*:
 $app_i (BinOpInstr\ bop, P, pc, mxs, T_r, (T2\#T1\#ST,LT)) = (\exists T. P \vdash T1 \ll bop \gg T2 : T)$
 $|$ *app_i-IfFalse*:
 $app_i (IfFalse\ b, P, pc, mxs, T_r, (Boolean\#ST,LT)) =$
 $(0 \leq int\ pc + b)$
 $|$ *app_i-Goto*:
 $app_i (Goto\ b, P, pc, mxs, T_r, s) = (0 \leq int\ pc + b)$
 $|$ *app_i-Return*:
 $app_i (Return, P, pc, mxs, T_r, (T\#ST,LT)) = (P \vdash T \leq T_r)$
 $|$ *app_i-Throw*:
 $app_i (ThrowExc, P, pc, mxs, T_r, (T\#ST,LT)) =$
 $(T = NT \vee (\exists C. T = Class\ C \wedge P \vdash C \preceq^* Throwable))$
 $|$ *app_i-Invoke*:
 $app_i (Invoke\ M\ n, P, pc, mxs, T_r, (ST,LT)) =$
 $(n < length\ ST \wedge$
 $(ST!n \neq NT \rightarrow$
 $(\exists C\ D\ Ts\ T\ m. class\ type\ of' (ST\ !\ n) = [C] \wedge P \vdash C\ sees\ M:Ts \rightarrow T = m\ in\ D \wedge P \vdash rev$
 $(take\ n\ ST) [\leq] Ts)))$
 $|$ *app_i-MEnter*:
 $app_i (MEnter, P, pc, mxs, T_r, (T\#ST,LT)) = (is-refT\ T)$
 $|$ *app_i-MExit*:
 $app_i (MExit, P, pc, mxs, T_r, (T\#ST,LT)) = (is-refT\ T)$
 $|$ *app_i-default*:
 $app_i (i, P, pc, mxs, T_r, s) = False$

definition *xcpt-app* :: 'addr instr \Rightarrow 'm prog \Rightarrow pc \Rightarrow nat \Rightarrow ex-table \Rightarrow ty_i \Rightarrow bool

where

xcpt-app *i* *P* *pc* *mxs* *xt* $\tau \equiv \forall (f,t,C,h,d) \in set (relevant-entries\ P\ i\ pc\ xt). (case\ C\ of\ None \Rightarrow True$
 $| Some\ C' \Rightarrow is-class\ P\ C') \wedge d \leq size (fst\ \tau) \wedge d < mxs$

definition *app* :: 'addr instr \Rightarrow 'm prog \Rightarrow nat \Rightarrow ty \Rightarrow nat \Rightarrow nat \Rightarrow ex-table \Rightarrow ty_i' \Rightarrow bool

where

app *i* *P* *mxs* *T_r* *pc* *mpc* *xt* $\equiv case\ t\ of\ None \Rightarrow True\ | Some\ \tau \Rightarrow$
 $app_i (i, P, pc, mxs, T_r, \tau) \wedge xcpt-app\ i\ P\ pc\ mxs\ xt\ \tau \wedge$
 $(\forall (pc', \tau') \in set (eff\ i\ P\ pc\ xt\ t). pc' < mpc)$

lemma *app-Some*:

app *i* *P* *mxs* *T_r* *pc* *mpc* *xt* (Some τ) =
 $(app_i (i, P, pc, mxs, T_r, \tau) \wedge xcpt-app\ i\ P\ pc\ mxs\ xt\ \tau \wedge$
 $(\forall (pc', s') \in set (eff\ i\ P\ pc\ xt\ (Some\ \tau)). pc' < mpc))$

by (*simp* *add*: *app-def*)

locale *eff* = *jvm-method* +

fixes *eff_i* **and** *app_i* **and** *eff* **and** *app*

fixes *norm-eff* **and** *xcpt-app* **and** *xcpt-eff*

fixes *mpc*

defines *mpc* $\equiv size\ is$

defines *eff_i* *i* $\tau \equiv Effect.eff_i (i, P, \tau)$

notes $eff_i\text{-simps}$ [simp] = *Effect.eff_i.simps* [where $P = P$, folded *eff_i-def*]

defines app_i i pc $\tau \equiv$ *Effect.app_i* (i , P , pc , mxs , T_r , τ)

notes $app_i\text{-simps}$ [simp] = *Effect.app_i.simps* [where $P=P$ and $mxs=mxs$ and $T_r=T_r$, folded *app_i-def*]

defines $xcpt\text{-eff}$ i pc $\tau \equiv$ *Effect.xcpt-eff* i P pc τ xt

notes $xcpt\text{-eff}$ = *Effect.xcpt-eff-def* [of - P - - xt , folded *xcpt-eff-def*]

defines $norm\text{-eff}$ i pc $\tau \equiv$ *Effect.norm-eff* i P pc τ

notes $norm\text{-eff}$ = *Effect.norm-eff-def* [of - P , folded *norm-eff-def* *eff_i-def*]

defines eff i $pc \equiv$ *Effect.eff* i P pc xt

notes eff = *Effect.eff-def* [of - P - - xt , folded *eff-def* *norm-eff-def* *xcpt-eff-def*]

defines $xcpt\text{-app}$ i pc $\tau \equiv$ *Effect.xcpt-app* i P pc mxs xt τ

notes $xcpt\text{-app}$ = *Effect.xcpt-app-def* [of - P - - mxs xt , folded *xcpt-app-def*]

defines app i $pc \equiv$ *Effect.app* i P mxs T_r pc mpc xt

notes app = *Effect.app-def* [of - P mxs T_r - - mpc xt , folded *app-def* *xcpt-app-def* *app_i-def* *eff-def*]

lemma *length-cases2*:

assumes $\bigwedge LT. P$ ($[], LT$)

assumes $\bigwedge l ST LT. P$ ($l\#ST, LT$)

shows P s

by (*cases* s , *cases fst* s) (*auto intro!*: *assms*)

lemma *length-cases3*:

assumes $\bigwedge LT. P$ ($[], LT$)

assumes $\bigwedge l LT. P$ ($[l], LT$)

assumes $\bigwedge l l' ST LT. P$ ($l\#l'\#ST, LT$)

shows P s

apply(*rule* *length-cases2*; (*rule* *assms*)?)

subgoal for $l ST LT$ **by**(*cases* ST ; *clarsimp simp*: *assms*)

done

lemma *length-cases4*:

assumes $\bigwedge LT. P$ ($[], LT$)

assumes $\bigwedge l LT. P$ ($[l], LT$)

assumes $\bigwedge l l' LT. P$ ($[l, l'], LT$)

assumes $\bigwedge l l' l'' ST LT. P$ ($l\#l'\#l''\#ST, LT$)

shows P s

apply(*rule* *length-cases3*; (*rule* *assms*)?)

subgoal for $l l' ST LT$ **by**(*cases* ST ; *clarsimp simp*: *assms*)

done

lemma *length-cases5*:

assumes $\bigwedge LT. P$ ($[], LT$)

assumes $\bigwedge l LT. P$ ($[l], LT$)

assumes $\bigwedge l l' LT. P$ ($[l, l'], LT$)

assumes $\bigwedge l l' l'' LT. P$ ($[l, l', l''], LT$)

assumes $\bigwedge l l' l'' l''' ST LT. P$ ($l\#l'\#l''\#l'''\#ST, LT$)

shows $P\ s$
 apply(rule length-cases4; (rule assms)?)
 subgoal for $l\ l'\ l''\ ST\ LT$ by(cases ST ; clarsimp simp: assms)
 done

simp rules for *app*

lemma appNone[simp]: $app\ i\ P\ mxs\ T_r\ pc\ mpc\ et\ None = True$
 by (simp add: app-def)

lemma appLoad[simp]:
 $app_i\ (Load\ idx,\ P,\ T_r,\ mxs,\ pc,\ s) = (\exists\ ST\ LT.\ s = (ST,LT) \wedge idx < length\ LT \wedge LT!idx \neq Err \wedge length\ ST < mxs)$
 by (cases s , simp)

lemma appStore[simp]:
 $app_i\ (Store\ idx,\ P,\ pc,\ mxs,\ T_r,\ s) = (\exists\ ts\ ST\ LT.\ s = (ts\#\ ST,LT) \wedge idx < length\ LT)$
 by (rule length-cases2, auto)

lemma appPush[simp]:
 $app_i\ (Push\ v,\ P,\ pc,\ mxs,\ T_r,\ s) =$
 $(\exists\ ST\ LT.\ s = (ST,LT) \wedge length\ ST < mxs \wedge typeof\ v \neq None)$
 by (cases s , simp)

lemma appGetField[simp]:
 $app_i\ (Getfield\ F\ C,\ P,\ pc,\ mxs,\ T_r,\ s) =$
 $(\exists\ oT\ vT\ ST\ LT\ fm.\ s = (oT\#\ ST, LT) \wedge$
 $P \vdash C\ sees\ F:\ vT\ (fm)\ in\ C \wedge P \vdash oT \leq (Class\ C))$
 by (rule length-cases2 [of - s]) auto

lemma appPutField[simp]:
 $app_i\ (Putfield\ F\ C,\ P,\ pc,\ mxs,\ T_r,\ s) =$
 $(\exists\ vT\ vT'\ oT\ ST\ LT\ fm.\ s = (vT\#\ oT\#\ ST, LT) \wedge$
 $P \vdash C\ sees\ F:\ vT'\ (fm)\ in\ C \wedge P \vdash oT \leq (Class\ C) \wedge P \vdash vT \leq vT')$
 by (rule length-cases4 [of - s], auto)

lemma appCAS[simp]:
 $app_i\ (CAS\ F\ C,\ P,\ pc,\ mxs,\ T_r,\ s) =$
 $(\exists\ T1\ T2\ T3\ T'\ ST\ LT\ fm.\ s = (T3\ \#\ T2\ \#\ T1\ \#\ ST, LT) \wedge$
 $P \vdash C\ sees\ F:\ T'\ (fm)\ in\ C \wedge volatile\ fm \wedge P \vdash T1 \leq Class\ C \wedge P \vdash T2 \leq T' \wedge P \vdash T3 \leq T')$
 by(rule length-cases4[of - s]) auto

lemma appNew[simp]:
 $app_i\ (New\ C,\ P,\ pc,\ mxs,\ T_r,\ s) =$
 $(\exists\ ST\ LT.\ s = (ST,LT) \wedge is-class\ P\ C \wedge length\ ST < mxs)$
 by (cases s , simp)

lemma appNewArray[simp]:
 $app_i\ (NewArray\ Ty,\ P,\ pc,\ mxs,\ T_r,\ s) =$
 $(\exists\ ST\ LT.\ s = (Integer\#\ ST,LT) \wedge is-type\ P\ (Ty[\]))$
 by (cases s , simp, cases $fst\ s$, simp)(cases $hd\ (fst\ s)$, auto)

lemma appALoad[simp]:
 $app_i\ (ALoad,\ P,\ pc,\ mxs,\ T_r,\ s) =$

$(\exists T ST LT. s=(Integer\#T\#ST,LT) \wedge (T \neq NT \longrightarrow (\exists T'. T = T'[])))$

proof –

obtain $ST LT$ **where** $[simp]: s = (ST, LT)$ **by** $(cases\ s)$
have $ST = [] \vee (\exists T. ST = [T]) \vee (\exists T_1 T_2 ST'. ST = T_1\#T_2\#ST')$
by $(cases\ ST, auto, case-tac\ list, auto)$

moreover

{ **assume** $ST = []$ **hence** $?thesis$ **by** $simp$ }

moreover

{ **fix** T **assume** $ST = [T]$ **hence** $?thesis$ **by** $(cases\ T, auto)$ }

moreover

{ **fix** $T_1 T_2 ST'$ **assume** $ST = T_1\#T_2\#ST'$
hence $?thesis$ **by** $(cases\ T_1, auto)$

}

ultimately show $?thesis$ **by** $blast$

qed

lemma $appAStore[simp]:$

$app_i (AStore, P, pc, mxs, T_r, s) =$
 $(\exists T U ST LT. s=(T\#Integer\#U\#ST,LT) \wedge (U \neq NT \longrightarrow (\exists T'. U = T'[])))$

proof –

obtain $ST LT$ **where** $[simp]: s = (ST, LT)$ **by** $(cases\ s)$

have $ST = [] \vee (\exists T. ST = [T]) \vee (\exists T_1 T_2. ST = [T_1, T_2]) \vee (\exists T_1 T_2 T_3 ST'. ST = T_1 \# T_2 \# T_3 \# ST')$

by $(cases\ ST, auto, case-tac\ list, auto, case-tac\ lista, auto)$

moreover

{ **assume** $ST = []$ **hence** $?thesis$ **by** $simp$ }

moreover

{ **fix** T **assume** $ST = [T]$ **hence** $?thesis$ **by** $(simp)$ }

moreover

{ **fix** $T_1 T_2$ **assume** $ST = [T_1, T_2]$ **hence** $?thesis$ **by** $simp$ }

moreover

{ **fix** $T_1 T_2 T_3 ST'$ **assume** $ST = T_1 \# T_2 \# T_3 \# ST'$ **hence** $?thesis$ **by** $(cases\ T_2, auto)$ }

ultimately show $?thesis$ **by** $blast$

qed

lemma $appALength[simp]:$

$app_i (ALength, P, pc, mxs, T_r, s) =$
 $(\exists T ST LT. s=(T\#ST,LT) \wedge (T \neq NT \longrightarrow (\exists T'. T = T'[])))$
by $(cases\ s, cases\ fst\ s, simp\ add: app-def) (cases\ hd\ (fst\ s), auto)$

lemma $appCheckcast[simp]:$

$app_i (Checkcast\ Ty, P, pc, mxs, T_r, s) =$
 $(\exists T ST LT. s = (T\#ST,LT) \wedge is-type\ P\ Ty)$
by $(cases\ s, cases\ fst\ s, simp\ add: app-def) (cases\ hd\ (fst\ s), auto)$

lemma $appInstanceof[simp]:$

$app_i (Instanceof\ Ty, P, pc, mxs, T_r, s) =$
 $(\exists T ST LT. s = (T\#ST,LT) \wedge is-type\ P\ Ty \wedge is-refT\ T)$
by $(cases\ s, cases\ fst\ s, simp\ add: app-def) (cases\ hd\ (fst\ s), auto)$

lemma $app_iPop[simp]:$

$app_i (Pop, P, pc, mxs, T_r, s) = (\exists ts\ ST\ LT. s = (ts\#\#ST,LT))$
by $(rule\ length-cases2, auto)$

lemma *appDup*[*simp*]:
app_i (*Dup*, *P*, *pc*, *maxs*, *T_r*, *s*) =
 ($\exists T ST LT. s = (T\#ST, LT) \wedge \text{Suc} (\text{length } ST) < \text{maxs}$)
by (*cases s*, *cases fst s*, *simp-all*)

lemma *app_iSwap*[*simp*]:
app_i (*Swap*, *P*, *pc*, *maxs*, *T_r*, *s*) = ($\exists T_1 T_2 ST LT. s = (T_1\#T_2\#ST, LT)$)
by(*rule length-cases4*) *auto*

lemma *appBinOp*[*simp*]:
app_i (*BinOpInstr* *bop*, *P*, *pc*, *maxs*, *T_r*, *s*) = ($\exists T_1 T_2 ST LT T. s = (T_2 \# T_1 \# ST, LT) \wedge P \vdash T_1 \ll bop \gg T_2 : T$)

proof –

obtain *ST LT* **where** [*simp*]: *s* = (*ST*, *LT*) **by** (*cases s*)
have *ST* = [] \vee ($\exists T. ST = [T]$) \vee ($\exists T_1 T_2 ST'. ST = T_1\#T_2\#ST'$)
by (*cases ST*, *auto*, *case-tac list*, *auto*)
moreover
 { **assume** *ST* = [] **hence** ?*thesis* **by** *simp* }
moreover
 { **fix** *T* **assume** *ST* = [*T*] **hence** ?*thesis* **by** (*cases T*, *auto*) }
moreover
 { **fix** *T₁ T₂ ST'* **assume** *ST* = *T₁\#T₂\#ST'*
hence ?*thesis* **by** *simp*
 }
ultimately show ?*thesis* **by** *blast*

qed

lemma *appIfFalse* [*simp*]:
app_i (*IfFalse* *b*, *P*, *pc*, *maxs*, *T_r*, *s*) =
 ($\exists ST LT. s = (\text{Boolean}\#ST, LT) \wedge 0 \leq \text{int } pc + b$)
apply (*rule length-cases2*)
apply *simp*
apply (*case-tac l*)
apply *auto*
done

lemma *appReturn*[*simp*]:
app_i (*Return*, *P*, *pc*, *maxs*, *T_r*, *s*) = ($\exists T ST LT. s = (T\#ST, LT) \wedge P \vdash T \leq T_r$)
by (*rule length-cases2*, *auto*)

lemma *appThrow*[*simp*]:
app_i (*ThrowExc*, *P*, *pc*, *maxs*, *T_r*, *s*) = ($\exists T ST LT. s=(T\#ST, LT) \wedge (T = NT \vee (\exists C. T = \text{Class } C \wedge P \vdash C \preceq^* \text{Throwable}))$)
by (*rule length-cases2*, *auto*)

lemma *appMEnter*[*simp*]:
app_i (*MEnter*, *P*, *pc*, *maxs*, *T_r*, *s*) = ($\exists T ST LT. s=(T\#ST, LT) \wedge \text{is-refT } T$)
by (*rule length-cases2*, *auto*)

lemma *appMExit*[*simp*]:
app_i (*MExit*, *P*, *pc*, *maxs*, *T_r*, *s*) = ($\exists T ST LT. s=(T\#ST, LT) \wedge \text{is-refT } T$)
by (*rule length-cases2*, *auto*)

lemma *effNone*:

$(pc', s') \in \text{set } (\text{eff } i \ P \ pc \ \text{et } \text{None}) \implies s' = \text{None}$
by (*auto simp add: eff-def xcpt-eff-def norm-eff-def*)

lemma *relevant-entries-append* [*simp*]:

relevant-entries $P \ i \ pc \ (xt \ @ \ xt') = \text{relevant-entries } P \ i \ pc \ xt \ @ \ \text{relevant-entries } P \ i \ pc \ xt'$
by (*unfold relevant-entries-def simp*)

lemma *xcpt-app-append* [*iff*]:

xcpt-app $i \ P \ pc \ mxs \ (xt@xt') \ \tau = (\text{xcpt-app } i \ P \ pc \ mxs \ xt \ \tau \ \wedge \ \text{xcpt-app } i \ P \ pc \ mxs \ xt' \ \tau)$
unfolding *xcpt-app-def* **by** *force*

lemma *xcpt-eff-append* [*simp*]:

xcpt-eff $i \ P \ pc \ \tau \ (xt@xt') = \text{xcpt-eff } i \ P \ pc \ \tau \ xt \ @ \ \text{xcpt-eff } i \ P \ pc \ \tau \ xt'$
by (*unfold xcpt-eff-def, cases \tau*) *simp*

lemma *app-append* [*simp*]:

app $i \ P \ pc \ T \ mxs \ mpc \ (xt@xt') \ \tau = (\text{app } i \ P \ pc \ T \ mxs \ mpc \ xt \ \tau \ \wedge \ \text{app } i \ P \ pc \ T \ mxs \ mpc \ xt' \ \tau)$
by (*unfold app-def eff-def*) *auto*

6.2.1 Code generator setup

declare *list-all2-Nil* [*code*]

declare *list-all2-Cons* [*code*]

lemma *eff_i-BinOpInstr-code*:

eff_i $(\text{BinOpInstr } \text{bop}, P, (T2\#T1\#ST,LT)) = (\text{Predicate.the } (\text{WTrt-binop-i-i-i-i-o } P \ T1 \ \text{bop } \ T2) \ \# \ ST, \ LT)$
by(*simp add: the-WTrt-binop-code*)

lemmas *eff_i-code*[*code*] =

eff_i-Load *eff_i-Store* *eff_i-Push* *eff_i-Getfield* *eff_i-Putfield* *eff_i-New* *eff_i-NewArray* *eff_i-ALoad*
eff_i-AStore *eff_i-ALength* *eff_i-Checkcast* *eff_i-InstanceOf* *eff_i-Pop* *eff_i-Dup* *eff_i-Swap* *eff_i-BinOpInstr-code*
eff_i-IfFalse *eff_i-Invoke* *eff_i-Goto* *eff_i-MEnter* *eff_i-MExit*

lemma *app_i-Getfield-code*:

app_i $(\text{Getfield } F \ C, P, pc, mxs, T_r, (T\#ST, LT)) \longleftrightarrow$
Predicate.holds $(\text{Predicate.bind } (\text{sees-field-i-i-i-o-o-i } P \ C \ F \ C) \ (\lambda T. \ \text{Predicate.single } ())) \ \wedge \ P \vdash \ T \leq$
Class C
apply(*clarsimp simp add: Predicate.bind-def Predicate.single-def holds-eq eval-sees-field-i-i-i-o-i-conv*)
done

lemma *app_i-Putfield-code*:

app_i $(\text{Putfield } F \ C, P, pc, mxs, T_r, (T_1\#T_2\#ST, LT)) \longleftrightarrow$
 $P \vdash \ T_2 \leq (\text{Class } C) \ \wedge$
Predicate.holds $(\text{Predicate.bind } (\text{sees-field-i-i-i-o-o-i } P \ C \ F \ C) \ (\lambda(T, fm). \ \text{if } P \vdash \ T_1 \leq \ T \ \text{then } \ \text{Predicate.single } () \ \text{else } \ \text{bot}))$
by (*auto simp add: holds-eq eval-sees-field-i-i-i-o-i-conv split: if-splits*)

lemma *app_i-CAS-code*:

app_i $(\text{CAS } F \ C, P, pc, mxs, T_r, (T_3\#T_2\#T_1\#ST, LT)) \longleftrightarrow$
 $P \vdash \ T_1 \leq \text{Class } C \ \wedge$
Predicate.holds $(\text{Predicate.bind } (\text{sees-field-i-i-i-o-o-i } P \ C \ F \ C) \ (\lambda(T, fm). \ \text{if } P \vdash \ T_2 \leq \ T \ \wedge \ P \vdash \ T_3 \leq \ T \ \wedge \ \text{volatile } fm \ \text{then } \ \text{Predicate.single } () \ \text{else } \ \text{bot}))$

by(*auto simp add: holds-eq eval-sees-field-i-i-i-o-i-conv*)

lemma *app_i-ALoad-code*:

$app_i (ALoad, P, pc, mxs, T_r, (T1\#T2\#ST,LT)) =$
 $(T1 = Integer \wedge (case\ T2\ of\ Ty[] \Rightarrow True \mid NT \Rightarrow True \mid - \Rightarrow False))$

by(*simp add: split: ty.split*)

lemma *app_i-AStore-code*:

$app_i (AStore, P, pc, mxs, T_r, (T1\#T2\#T3\#ST,LT)) =$
 $(T2 = Integer \wedge (case\ T3\ of\ Ty[] \Rightarrow True \mid NT \Rightarrow True \mid - \Rightarrow False))$

by(*simp add: split: ty.split*)

lemma *app_i-ALength-code*:

$app_i (ALength, P, pc, mxs, T_r, (T1\#ST,LT)) =$
 $(case\ T1\ of\ Ty[] \Rightarrow True \mid NT \Rightarrow True \mid - \Rightarrow False)$

by(*simp add: split: ty.split*)

lemma *app_i-BinOpInstr-code*:

$app_i (BinOpInstr\ bop, P, pc, mxs, T_r, (T2\#T1\#ST,LT)) =$
 $Predicate.holds (Predicate.bind (WTrt-binop-i-i-i-o\ P\ T1\ bop\ T2) (\lambda T. Predicate.single ()))$

by (*auto simp add: holds-eq eval-WTrt-binop-i-i-i-o*)

lemma *app_i-Invoke-code*:

$app_i (Invoke\ M\ n, P, pc, mxs, T_r, (ST,LT)) =$
 $(n < length\ ST \wedge$
 $(ST!n \neq NT \longrightarrow$
 $(case\ class-type-of'\ (ST\ !\ n)\ of\ Some\ C \Rightarrow$
 $Predicate.holds (Predicate.bind (Method-i-i-i-o-o-o\ P\ C\ M)$
 $(\lambda(Ts, -). if\ P \vdash rev\ (take\ n\ ST) [\leq] Ts\ then\ Predicate.single\ ()\ else$
 $bot))$
 $\mid - \Rightarrow False)))$

proof –

have *bind-Ex*: $\bigwedge P\ f. Predicate.bind\ P\ f = Predicate.Pred\ (\lambda x. (\exists y. Predicate.eval\ P\ y \wedge Predicate.eval\ (f\ y)\ x))$

by (*rule pred-eqI*) *auto*

thus *?thesis*

by (*auto simp add: bind-Ex Predicate.single-def holds-eq eval-Method-i-i-i-o-o-o-conv split: ty.split*)

qed

lemma *app_i-Throw-code*:

$app_i (ThrowExc, P, pc, mxs, T_r, (T\#ST,LT)) =$
 $(case\ T\ of\ NT \Rightarrow True \mid Class\ C \Rightarrow P \vdash C \preceq^* Throwable \mid - \Rightarrow False)$

by(*simp split: ty.split*)

lemmas *app_i-code* [code] =

app_i-Load app_i-Store app_i-Push
app_i-Getfield-code app_i-Putfield-code app_i-CAS-code
app_i-New app_i-NewArray
app_i-ALoad-code app_i-AStore-code app_i-ALength-code
app_i-Checkcast app_i-Instanceof
app_i-Pop app_i-Dup app_i-Swap app_i-BinOpInstr-code app_i-IfFalse app_i-Goto
app_i-Return app_i-Throw-code app_i-Invoke-code app_i-MEnter app_i-MExit
app_i-default

end

6.3 The Bytecode Verifier

theory *BVSpec*

imports

Effect

begin

This theory contains a specification of the BV. The specification describes correct typings of method bodies; it corresponds to type *checking*.

definition *check-types* :: '*m prog* ⇒ *nat* ⇒ *nat* ⇒ *ty_i*' *err list* ⇒ *bool*

where

check-types P mxs mxl τs ≡ *set τs* ⊆ *states P mxs mxl*

— An instruction is welltyped if it is applicable and its effect

— is compatible with the type at all successor instructions:

definition *wt-instr* :: [*m prog, ty, nat, pc, ex-table, 'addr instr, pc, ty_m*] ⇒ *bool*

(⟨*-, -, -, -*, *-* ⊢ *-, -* :: *→* [*60, 0, 0, 0, 0, 0, 61*] *60*)

where

P, T, mxs, mpc, xt ⊢ *i, pc* :: *τs* ≡

app i P mxs T pc mpc xt (τs!pc) ∧

(∀ (*pc', τ'*) ∈ *set (eff i P pc xt (τs!pc))*). *P* ⊢ *τ' ≤' τs!pc'*)

— The type at *pc=0* conforms to the method calling convention:

definition *wt-start* :: [*m prog, cname, ty list, nat, ty_m*] ⇒ *bool*

where

wt-start P C Ts mxl₀ τs ≡

P ⊢ *Some* ([], *OK (Class C)#map OK Ts@replicate mxl₀ Err*) ≤' *τs!0*

— A method is welltyped if the body is not empty,

— if the method type covers all instructions and mentions

— declared classes only, if the method calling convention is respected, and

— if all instructions are welltyped.

definition *wt-method* :: [*m prog, cname, ty list, ty, nat, nat, 'addr instr list, ex-table, ty_m*] ⇒ *bool*

where

wt-method P C Ts T_r mxs mxl₀ is xt τs ≡

0 < size is ∧ *size τs = size is* ∧

check-types P mxs (1+size Ts+mxl₀) (map OK τs) ∧

wt-start P C Ts mxl₀ τs ∧

(∀ *pc < size is*. *P, T_r, mxs, size is, xt* ⊢ *is!pc, pc* :: *τs*)

— A program is welltyped if it is wellformed and all methods are welltyped

definition *wf-jvm-prog-phi* :: *ty_P* ⇒ '*addr jvm-prog* ⇒ *bool* (⟨*wf'-jvm'-prog-*⟩)

where

wf-jvm-prog_Φ ≡

wf-prog (λ*P C (M, Ts, T_r, (mxs, mxl₀, is, xt))*).

wt-method P C Ts T_r mxs mxl₀ is xt (Φ C M))

definition *wf-jvm-prog* :: '*addr jvm-prog* ⇒ *bool*

where

wf-jvm-prog P ≡ ∃ *Φ*. *wf-jvm-prog_Φ P*

lemma *wt-jvm-progD*:

$wf\text{-}jvm\text{-}prog_{\Phi} P \implies \exists wt. wf\text{-}prog\ wt\ P$

lemma *wt-jvm-prog-impl-wt-instr*:

$\llbracket wf\text{-}jvm\text{-}prog_{\Phi} P;$
 $P \vdash C\ sees\ M:Ts \rightarrow T = \llbracket (m\ x s, m\ x l_0, i\ n s, x\ t) \rrbracket\ in\ C; pc < size\ i\ n s \rrbracket$
 $\implies P, T, m\ x s, size\ i\ n s, x\ t \vdash i\ n s!pc, pc :: \Phi\ C\ M$

lemma *wt-jvm-prog-impl-wt-start*:

$\llbracket wf\text{-}jvm\text{-}prog_{\Phi} P;$
 $P \vdash C\ sees\ M:Ts \rightarrow T = \llbracket (m\ x s, m\ x l_0, i\ n s, x\ t) \rrbracket\ in\ C \rrbracket \implies$
 $0 < size\ i\ n s \wedge wt\text{-}start\ P\ C\ T\ s\ m\ x l_0\ (\Phi\ C\ M)$

end

6.4 BV Type Safety Invariant

theory *BVConform*

imports

BVSpec

../JVM/JVMExec

begin

context *JVM-heap-base* **begin**

definition *confT* :: *'c prog* \Rightarrow *'heap* \Rightarrow *'addr val* \Rightarrow *ty err* \Rightarrow *bool*

$(\langle -, - \vdash - : \leq_{\top} \rightarrow [51, 51, 51, 51] 50)$

where

$P, h \vdash v : \leq_{\top} E \equiv case\ E\ of\ Err \Rightarrow True \mid OK\ T \Rightarrow P, h \vdash v : \leq T$

notation (*ASCII*)

confT $(\langle -, - \mid - - : \leq = T \rightarrow [51, 51, 51, 51] 50)$

abbreviation *confTs* :: *'c prog* \Rightarrow *'heap* \Rightarrow *'addr val list* \Rightarrow *ty_l* \Rightarrow *bool*

$(\langle -, - \vdash - : [\leq_{\top}] \rightarrow [51, 51, 51, 51] 50)$

where

$P, h \vdash vs : [\leq_{\top}] Ts \equiv list\text{-}all2\ (confT\ P\ h)\ vs\ Ts$

notation (*ASCII*)

confTs $(\langle -, - \mid - - : [\leq = T] \rightarrow [51, 51, 51, 51] 50)$

definition *conf-f* :: *'addr jvm-prog* \Rightarrow *'heap* \Rightarrow *ty_i* \Rightarrow *'addr bytecode* \Rightarrow *'addr frame* \Rightarrow *bool*

where

$conf\text{-}f\ P\ h \equiv \lambda(ST, LT)\ is\ (stk, loc, C, M, pc). P, h \vdash stk : [\leq] ST \wedge P, h \vdash loc : [\leq_{\top}] LT \wedge pc < size\ is$

primrec *conf-fs* :: [*'addr jvm-prog*, *'heap*, *ty_P*, *mname*, *nat*, *ty*, *'addr frame list*] \Rightarrow *bool*

where

$conf\text{-}fs\ P\ h\ \Phi\ M_0\ n_0\ T_0\ [] = True$

$\mid conf\text{-}fs\ P\ h\ \Phi\ M_0\ n_0\ T_0\ (f\#\text{frs}) =$

$(let\ (stk, loc, C, M, pc) = f\ in$

$(\exists ST\ LT\ Ts\ T\ m\ x s\ m\ x l_0\ is\ x\ t.$

$\Phi\ C\ M\ !\ pc = Some\ (ST, LT) \wedge$

$(P \vdash C\ sees\ M:Ts \rightarrow T = \llbracket (m\ x s, m\ x l_0, is, x\ t) \rrbracket\ in\ C) \wedge$

$(\exists Ts'\ T'\ D\ m\ D'.$

$is!pc = (Invoke\ M_0\ n_0) \wedge class\text{-}type\text{-}of'\ (ST!n_0) = \llbracket D \rrbracket \wedge P \vdash D\ sees\ M_0:Ts' \rightarrow T' = m\ in\ D')$

$\wedge P \vdash T_0 \leq T' \wedge$
 $\text{conf-f } P \ h \ (ST, LT) \text{ is } f \wedge \text{conf-fs } P \ h \ \Phi \ M \ (\text{size } Ts) \ T \ \text{frs})$

primrec $\text{conf-xcp} :: 'addr \ \text{jvm-prog} \Rightarrow 'heap \Rightarrow 'addr \ \text{option} \Rightarrow 'addr \ \text{instr} \Rightarrow \text{bool}$ **where**
 $\text{conf-xcp } P \ h \ \text{None} \ i = \text{True}$
 $| \text{conf-xcp } P \ h \ [a] \ i = (\exists D. \text{typeof-addr } h \ a = \lfloor \text{Class-type } D \rfloor \wedge P \vdash D \preceq^* \text{Throwable} \wedge$
 $(\forall D'. P \vdash D \preceq^* D' \longrightarrow \text{is-relevant-class } i \ P \ D'))$

end

context $\text{JVM-heap-conf-base}$ **begin**

definition $\text{correct-state} :: [ty_P, 'thread-id, ('addr, 'heap) \ \text{jvm-state}] \Rightarrow \text{bool}$

where

$\text{correct-state } \Phi \ t \equiv \lambda(xp, h, \text{frs}).$
 $P, h \vdash t \ \checkmark \wedge \text{hconf } h \wedge \text{preallocated } h \wedge$
 $(\text{case } \text{frs} \ \text{of}$
 $\quad [] \Rightarrow \text{True}$
 $\quad | (f \# \text{fs}) \Rightarrow$
 $\quad (\text{let } (stk, loc, C, M, pc) = f$
 $\quad \text{in } \exists Ts \ T \ \text{mxs} \ \text{mxl}_0 \ \text{is} \ \text{xt} \ \tau.$
 $\quad (P \vdash C \ \text{sees } M: Ts \rightarrow T = \lfloor (\text{mxs}, \text{mxl}_0, \text{is}, \text{xt}) \rfloor \text{ in } C) \wedge$
 $\quad \Phi \ C \ M \ ! \ pc = \text{Some } \tau \wedge$
 $\quad \text{conf-f } P \ h \ \tau \ \text{is } f \wedge \text{conf-fs } P \ h \ \Phi \ M \ (\text{size } Ts) \ T \ \text{fs} \wedge$
 $\quad \text{conf-xcp } P \ h \ xp \ (\text{is} \ ! \ pc) \))$

notation

$\text{correct-state} \ (\langle \vdash \ \cdot \ \checkmark \rangle \ [61, 0, 0] \ 61)$

notation (*ASCII*)

$\text{correct-state} \ (\langle \vdash \ | \ \cdot \ \text{[ok]} \rangle \ [61, 0, 0] \ 61)$

end

context JVM-heap-base **begin**

lemma conf-f-def2 :

$\text{conf-f } P \ h \ (ST, LT) \ \text{is} \ (stk, loc, C, M, pc) \equiv$
 $P, h \vdash \text{stk} \ [:\leq] \ ST \wedge P, h \vdash \text{loc} \ [:\leq_{\top}] \ LT \wedge pc < \text{size} \ \text{is}$
by ($\text{simp add: conf-f-def}$)

6.4.1 Values and \top

lemma confT-Err [*iff*]: $P, h \vdash x \ [:\leq_{\top}] \ \text{Err}$

by ($\text{simp add: confT-def}$)

lemma confT-OK [*iff*]: $P, h \vdash x \ [:\leq_{\top}] \ \text{OK } T = (P, h \vdash x \ [:\leq] \ T)$

by ($\text{simp add: confT-def}$)

lemma confT-cases :

$P, h \vdash x \ [:\leq_{\top}] \ X = (X = \text{Err} \vee (\exists T. X = \text{OK } T \wedge P, h \vdash x \ [:\leq] \ T))$

by ($\text{cases } X$) *auto*

lemma confT-widen [*intro?*, *trans*]:

$\llbracket P, h \vdash x : \leq_{\top} T; P \vdash T \leq_{\top} T' \rrbracket \Longrightarrow P, h \vdash x : \leq_{\top} T'$
by (*cases* T' , *auto intro: conf-widen*)

end

context *JVM-heap* **begin**

lemma *confT-hext* [*intro?*, *trans*]:
 $\llbracket P, h \vdash x : \leq_{\top} T; h \trianglelefteq h' \rrbracket \Longrightarrow P, h' \vdash x : \leq_{\top} T$
by (*cases* T) (*blast intro: conf-hext*)**+**

end

6.4.2 Stack and Registers

context *JVM-heap-base* **begin**

lemma *confTs-Cons1* [*iff*]:
 $P, h \vdash x \# xs \llbracket : \leq_{\top} \rrbracket Ts = (\exists z zs. Ts = z \# zs \wedge P, h \vdash x : \leq_{\top} z \wedge \text{list-all2} (\text{confT } P \ h) \ xs \ zs)$
by(*rule list-all2-Cons1*)

lemma *confTs-confT-sup*:
 $\llbracket P, h \vdash \text{loc} \llbracket : \leq_{\top} \rrbracket LT; n < \text{size } LT; LT!n = \text{OK } T; P \vdash T \leq T' \rrbracket$
 $\Longrightarrow P, h \vdash (\text{loc}!n) : \leq T'$
apply (*frule list-all2-lengthD*)
apply (*drule list-all2-nthD, simp*)
apply *simp*
apply (*erule conf-widen, assumption+*)
done

lemma *confTs-widen* [*intro?*, *trans*]:
 $P, h \vdash \text{loc} \llbracket : \leq_{\top} \rrbracket LT \Longrightarrow P \vdash LT \llbracket \leq_{\top} \rrbracket LT' \Longrightarrow P, h \vdash \text{loc} \llbracket : \leq_{\top} \rrbracket LT'$
by (*rule list-all2-trans, rule confT-widen*)

lemma *confTs-map* [*iff*]:
 $(P, h \vdash vs \llbracket : \leq_{\top} \rrbracket \text{map } \text{OK } Ts) = (P, h \vdash vs \llbracket : \leq \rrbracket Ts)$
by (*induct Ts arbitrary: vs*) (*auto simp add: list-all2-Cons2*)

lemma (**in** $-$) *reg-widen-Err*:
 $(P \vdash \text{replicate } n \ \text{Err} \llbracket \leq_{\top} \rrbracket LT) = (LT = \text{replicate } n \ \text{Err})$
by (*induct n arbitrary: LT*) (*auto simp add: list-all2-Cons1*)

declare *reg-widen-Err* [*iff*]

lemma *confTs-Err* [*iff*]:
 $P, h \vdash \text{replicate } n \ v \llbracket : \leq_{\top} \rrbracket \text{replicate } n \ \text{Err}$
by (*induct n*) *auto*

end

context *JVM-heap* **begin**

lemma *confTs-hext* [*intro?*]:
 $P, h \vdash \text{loc} \llbracket : \leq_{\top} \rrbracket LT \Longrightarrow h \trianglelefteq h' \Longrightarrow P, h' \vdash \text{loc} \llbracket : \leq_{\top} \rrbracket LT$

by (*fast elim: list-all2-mono confT-heat*)

6.4.3 correct-frames

declare *fun-upd-apply*[*simp del*]

lemma *conf-f-heat*:

$\llbracket \text{conf-f } P \ h \ \Phi \ M \ f; \ h \sqsubseteq h' \rrbracket \implies \text{conf-f } P \ h' \ \Phi \ M \ f$
 by(*cases f, cases Φ , auto simp add: conf-f-def intro: confs-heat confTs-heat*)

lemma *conf-fs-heat*:

$\llbracket \text{conf-fs } P \ h \ \Phi \ M \ n \ T_r \ \text{frs}; \ h \sqsubseteq h' \rrbracket \implies \text{conf-fs } P \ h' \ \Phi \ M \ n \ T_r \ \text{frs}$
 apply (*induct frs arbitrary: M n T_r*)
 apply *simp*
 apply *clarify*
 apply (*simp (no-asm-use)*)
 apply *clarify*
 apply (*unfold conf-f-def*)
 apply (*simp (no-asm-use) split: if-split-asm*)
 apply (*fast elim!: confs-heat confTs-heat*)
 done

declare *fun-upd-apply*[*simp*]

lemma *conf-xcp-heat*:

$\llbracket \text{conf-xcp } P \ h \ xcp \ i; \ h \sqsubseteq h' \rrbracket \implies \text{conf-xcp } P \ h' \ xcp \ i$
 by(*cases xcp*)(*auto elim: typeof-addr-heat-mono*)

end

context *JVM-heap-conf-base* begin

lemmas *defs1 = correct-state-def conf-f-def wt-instr-def eff-def norm-eff-def app-def xcpt-app-def*

lemma *correct-state-impl-Some-method*:

$\Phi \vdash t: (\text{None}, h, (\text{stk}, \text{loc}, C, M, \text{pc}) \# \text{frs}) \checkmark$
 $\implies \exists m \ Ts \ T. P \vdash C \ \text{sees } M: Ts \rightarrow T = \lfloor m \rfloor \ \text{in } C$
 by(*fastforce simp add: defs1*)

end

context *JVM-heap-conf-base'* begin

lemma *correct-state-heat-mono*:

$\llbracket \Phi \vdash t: (xcp, h, \text{frs}) \checkmark; \ h \sqsubseteq h'; \ hconf \ h' \rrbracket \implies \Phi \vdash t: (xcp, h', \text{frs}) \checkmark$
 unfolding *correct-state-def*
 by(*fastforce elim: tconf-heat-mono preallocated-heat conf-f-heat conf-fs-heat conf-xcp-heat split: list.split*)

end

end

6.5 BV Type Safety Proof

```

theory BVSpecTypeSafe
imports
  BVConform
  ../Common/ExternalCallWF
begin

```

```

declare listE-length [simp del]

```

This theory contains proof that the specification of the bytecode verifier only admits type safe programs.

6.5.1 Preliminaries

Simp and intro setup for the type safety proof:

```

context JVM-heap-conf-base begin

```

```

lemmas widen-rules [intro] = conf-widen confT-widen confs-widens confTs-widen

```

```

end

```

6.5.2 Exception Handling

For the *Invoke* instruction the BV has checked all handlers that guard the current *pc*.

lemma *Invoke-handlers*:

```

match-ex-table P C pc xt = Some (pc',d')  $\implies$ 
 $\exists (f,t,D,h,d) \in \text{set } (\text{relevant-entries } P (\text{Invoke } n M) pc xt).$ 
  (case D of None  $\implies$  True | Some D'  $\implies$   $P \vdash C \preceq^* D'$ )  $\wedge pc \in \{f..<t\} \wedge pc' = h \wedge d' = d$ 
by (induct xt) (auto simp add: relevant-entries-def matches-ex-entry-def
  is-relevant-entry-def split: if-split-asm)

```

lemma *match-is-relevant*:

```

assumes rv:  $\bigwedge D'. P \vdash D \preceq^* D' \implies \text{is-relevant-class } (ins ! i) P D'$ 
assumes match: match-ex-table P D pc xt = Some (pc',d')
shows  $\exists (f,t,D',h,d) \in \text{set } (\text{relevant-entries } P (ins ! i) pc xt).$  (case D' of None  $\implies$  True | Some D''
 $\implies P \vdash D \preceq^* D''$ )  $\wedge pc \in \{f..<t\} \wedge pc' = h \wedge d' = d$ 
using rv match
by (fastforce simp add: relevant-entries-def is-relevant-entry-def matches-ex-entry-def dest: match-ex-table-SomeD)

```

```

context JVM-heap-conf-base begin

```

lemma *exception-step-conform*:

```

fixes  $\sigma' :: ('addr, 'heap) \text{jvm-state}$ 
assumes wtp: wf-jvm-prog  $\Phi$  P
assumes correct:  $\Phi \vdash t:([\text{xcp}], h, fr \# frs) \checkmark$ 
shows  $\Phi \vdash t:\text{exception-step } P \text{ xcp } h \text{ fr } frs \checkmark$ 
proof –
obtain stk loc C M pc where fr: fr = (stk, loc, C, M, pc) by(cases fr)
from correct obtain Ts T mxs mxl0 ins xt
where meth:  $P \vdash C \text{ sees } M:Ts \rightarrow T = [(\text{mxs}, \text{mxl}_0, \text{ins}, \text{xt})]$  in C
by (simp add: correct-state-def fr) blast

```

```

from correct meth fr obtain  $D$ 
  where  $h\text{xcp}$ : typeof-addr  $h$   $xcp = \lfloor \text{Class-type } D \rfloor$  and  $D\text{subThrowable}$ :  $P \vdash D \preceq^* \text{Throwable}$ 
  and  $rv$ :  $\bigwedge D'. P \vdash D \preceq^* D' \implies \text{is-relevant-class } (\text{instrs-of } P \ C \ M \ ! \ pc) \ P \ D'$ 
  by(fastforce simp add: correct-state-def dest: sees-method-fun)

from meth have [simp]: ex-table-of  $P \ C \ M = xt$  by simp

from correct have  $t\text{conf}$ :  $P, h \vdash t \sqrt{t}$  by(simp add: correct-state-def)

show ?thesis
proof(cases match-ex-table P D pc xt)
  case None
    with correct fr meth hxcp show ?thesis
    by(fastforce simp add: correct-state-def cname-of-def split: list.split)
  next
    case (Some pc-d)
    then obtain  $pc' \ d'$  where  $pc\text{-d}$ :  $pc\text{-d} = (pc', d')$ 
    and  $match$ : match-ex-table  $P \ D \ pc \ xt = \text{Some } (pc', d')$  by (cases pc-d) auto
    from match-is-relevant[OF rv match] meth obtain  $f \ t \ D'$ 
    where  $rv$ :  $(f, t, D', pc', d') \in \text{set } (\text{relevant-entries } P \ (\text{ins } ! \ pc) \ pc \ xt)$ 
    and  $D\text{sub}D'$ : (case  $D'$  of None  $\implies \text{True}$  | Some  $D'' \implies P \vdash D \preceq^* D''$ ) and  $pc$ :  $pc \in \{f..<t\}$ 
by(auto)

from correct meth obtain  $ST \ LT$ 
  where  $h\text{-ok}$ : hconf  $h$ 
  and  $\Phi\text{-pc}$ :  $\Phi \ C \ M \ ! \ pc = \text{Some } (ST, LT)$ 
  and  $frame$ : conf-f  $P \ h \ (ST, LT) \ \text{ins } (stk, loc, C, M, pc)$ 
  and  $frames$ : conf-fs  $P \ h \ \Phi \ M \ (\text{size } Ts) \ T \ \text{frs}$ 
  and  $preh$ : preallocated  $h$ 
  unfolding correct-state-def fr by(auto dest: sees-method-fun)

from frame obtain  $stk$ :  $P, h \vdash stk \ [:\leq] \ ST$ 
  and  $loc$ :  $P, h \vdash loc \ [:\leq\top] \ LT$  and  $pc$ :  $pc < \text{size ins}$ 
  by (unfold conf-f-def) auto

from  $stk$  have [simp]: size  $stk = \text{size } ST \ ..$ 

from wtp meth correct fr have  $wt$ :  $P, T, \text{mxs}, \text{size ins}, xt \vdash \text{ins!pc}, pc :: \Phi \ C \ M$ 
  by (auto simp add: correct-state-def conf-f-def
    dest: sees-method-fun
    elim!: wt-jvm-prog-impl-wt-instr)

from  $wt \ \Phi\text{-pc}$  have
   $\text{eff}$ :  $\forall (pc', s') \in \text{set } (x\text{cpt-eff } (\text{ins!pc}) \ P \ pc \ (ST, LT) \ xt).$ 
   $pc' < \text{size ins} \wedge P \vdash s' \leq' \Phi \ C \ M ! pc'$ 
  by (auto simp add: defs1)

let  $?stk' = \text{Addr } xcp \ \# \ \text{drop } (\text{length } stk - d') \ stk$ 
let  $?f = (?stk', loc, C, M, pc')$ 

have  $conf$ :  $P, h \vdash \text{Addr } xcp \ : \leq \ \text{Class } (\text{case } D' \ \text{of } \text{None} \ \implies \ \text{Throwable} \ | \ \text{Some } D'' \ \implies \ D'')$ 
  using  $D\text{sub}D' \ hxcp \ D\text{subThrowable}$  by(auto simp add: conf-def)

```

```

obtain  $ST' LT'$  where
   $\Phi$ - $pc'$ :  $\Phi C M ! pc' = \text{Some} (ST', LT')$  and
   $pc'$ :  $pc' < \text{size } ins$  and
   $less$ :  $P \vdash (\text{Class } D \# \text{drop} (\text{size } ST - d') ST, LT) \leq_i (ST', LT')$ 
proof( $\text{cases } D'$ )
  case  $\text{Some}$ 
    thus  $?thesis$  using  $\text{eff } rv \text{ Dsub}D'$   $\text{conf that}$ 
    by( $\text{fastforce simp add: xcpt-eff-def sup-state-opt-any-Some intro: widen-trans[OF widen-subcls]}$ )
  next
  case  $\text{None}$ 
    with  $\text{that eff } rv \text{ conf DsubThrowable show } ?thesis$ 
    by( $\text{fastforce simp add: xcpt-eff-def sup-state-opt-any-Some intro: widen-trans[OF widen-subcls]}$ )
  qed

with  $\text{conf loc stk h}xcp$  have  $\text{conf-f } P h (ST',LT')$   $ins ?f$ 
  by ( $\text{auto simp add: defs1 conf-def intro: list-all2-dropI}$ )

with  $\text{meth } h\text{-ok frames } \Phi\text{-}pc'$   $\text{fr match } hxcp \text{ tconf } \text{preh}$ 
show  $?thesis$  unfolding  $\text{correct-state-def}$ 
  by( $\text{fastforce dest: sees-method-fun simp add: cname-of-def}$ )
qed
qed
end

```

6.5.3 Single Instructions

In this subsection we prove for each single (welltyped) instruction that the state after execution of the instruction still conforms. Since we have already handled raised exceptions above, we can now assume that no exception has been raised in this step.

```
context  $JVM\text{-conf-read}$  begin
```

```
declare  $\text{defs1}$  [ $\text{simp}$ ]
```

```
lemma  $\text{Invoke-correct}$ :
```

```
  fixes  $\sigma' :: ('addr, 'heap) \text{jvm-state}$ 
```

```
  assumes  $\text{wtprog: wf-jvm-prog}_\Phi P$ 
```

```
  assumes  $\text{meth-C: } P \vdash C \text{ sees } M: Ts \rightarrow T = [(mxs, mxl_0, ins, xt)] \text{ in } C$ 
```

```
  assumes  $\text{ins: } ins ! pc = \text{Invoke } M' n$ 
```

```
  assumes  $\text{wti: } P, T, mxs, \text{size } ins, xt \vdash ins ! pc, pc :: \Phi C M$ 
```

```
  assumes  $\text{approx: } \Phi \vdash t: (\text{None}, h, (\text{stk}, \text{loc}, C, M, pc) \# \text{frs}) \checkmark$ 
```

```
  assumes  $\text{exec: } (tas, \sigma) \in \text{exec-instr} (ins ! pc) P t h \text{ stk loc } C M pc \text{ frs}$ 
```

```
  shows  $\Phi \vdash t: \sigma \checkmark$ 
```

```
proof –
```

```
  note  $\text{split-paired-Ex}$  [ $\text{simp del}$ ]
```

```
from  $\text{wtprog}$  obtain  $\text{wfm}$  where  $\text{wfprog: wf-prog wfm}$   $P$ 
```

```
  by ( $\text{simp add: wf-jvm-prog-phi-def}$ )
```

```
from  $ins \text{ meth-C approx}$  obtain  $ST LT$  where
```

```
   $\text{heap-ok: hconf } h$  and
```

```
   $\text{tconf: } P, h \vdash t \checkmark$  and
```

```
   $\Phi\text{-}pc: \Phi C M ! pc = \text{Some} (ST, LT)$  and
```

frame: $\text{conf-f } P \ h \ (ST, LT) \ \text{ins} \ (stk, loc, C, M, pc) \ \mathbf{and}$
frames: $\text{conf-fs } P \ h \ \Phi \ M \ (\text{size } Ts) \ T \ \text{frs} \ \mathbf{and}$
preh: $\text{preallocated } h$
by ($\text{fastforce dest: sees-method-fun}$)

from $\text{ins wti } \Phi\text{-pc}$
have $n: n < \text{size } ST \ \mathbf{by} \ \text{simp}$

show $?thesis$

proof($\text{cases } stk!n = \text{Null}$)

case True

with $\text{ins heap-ok } \Phi\text{-pc frame frames exec meth-C tconf preh} \ \mathbf{show} \ ?thesis$
by($\text{fastforce elim: wf-preallocatedE}[OF \ \text{wfprog}, \ \mathbf{where} \ C = \text{NullPointer}]$)

next

case False

note $\text{Null} = \text{this}$

have $NT: ST!n \neq NT$

proof

assume $ST!n = NT$

moreover from $\text{frame have } P, h \vdash stk \ [:\leq] \ ST \ \mathbf{by} \ \text{simp}$

with $n \ \mathbf{have} \ P, h \vdash stk!n \ :\leq \ ST!n \ \mathbf{by} \ (\text{simp add: list-all2-conv-all-nth})$

ultimately have $stk!n = \text{Null} \ \mathbf{by} \ \text{simp}$

with $\text{Null} \ \mathbf{show} \ \text{False} \ \mathbf{by} \ \text{contradiction}$

qed

from frame obtain

$stk: P, h \vdash stk \ [:\leq] \ ST \ \mathbf{and}$

$loc: P, h \vdash loc \ [:\leq_{\top}] \ LT \ \mathbf{by} \ \text{simp}$

from $NT \ \text{ins wti } \Phi\text{-pc} \ \mathbf{have} \ pc': pc+1 < \text{size ins} \ \mathbf{by} \ \text{simp}$

from $NT \ \text{ins wti } \Phi\text{-pc} \ \mathbf{obtain} \ ST' \ LT'$

where $pc': pc+1 < \text{size ins}$

and $\Phi': \Phi \ C \ M \ ! \ (pc+1) = \text{Some} \ (ST', \ LT')$

and $LT': P \vdash LT \ [:\leq_{\top}] \ LT'$

by($\text{auto simp add: neq-Nil-conv sup-state-opt-any-Some split: if-split-asm}$)

with $NT \ \text{ins wti } \Phi\text{-pc} \ \mathbf{obtain} \ D \ D' \ TTs \ TT \ m$

where $D: \text{class-type-of}' \ (ST!n) = \lfloor D \rfloor$

and $m\text{-}D: P \vdash D \ \text{sees} \ M': TTs \rightarrow TT = m \ \text{in} \ D'$

and $Ts: P \vdash \text{rev} \ (\text{take } n \ ST) \ [:\leq] \ TTs$

and $ST': P \vdash (TT \ \# \ \text{drop} \ (n+1) \ ST) \ [:\leq] \ ST'$

by(auto)

from $n \ \text{stk } D \ \mathbf{have} \ P, h \vdash stk!n \ :\leq \ ST \ ! \ n$

by ($\text{auto simp add: list-all2-conv-all-nth}$)

from $\langle P, h \vdash stk!n \ :\leq \ ST \ ! \ n \rangle \ \text{Null } D$

obtain $U \ a \ \mathbf{where}$

$\text{Addr: } \text{stk!n} = \text{Addr } a \ \mathbf{and}$

$\text{obj: } \text{typeof-addr } h \ a = \text{Some } U \ \mathbf{and}$

$\text{UsubSTn: } P \vdash \text{ty-of-htype } U \leq ST \ ! \ n$

by($\text{cases } \text{stk} \ ! \ n$)($\text{auto simp add: conf-def widen-Class}$)

from $D \ \text{UsubSTn} \ \mathbf{obtain} \ C' \ \mathbf{where}$

C' : *class-type-of'* (*ty-of-htype* U) = $\lfloor C' \rfloor$ **and** C' *subD*: $P \vdash C' \preceq^* D$
by (*rule widen-is-class-type-of*) *simp*

with *wfprog* m - D
obtain $Ts' T' D'' meth'$ **where**
 m - C' : $P \vdash C'$ *sees* M' : $Ts' \rightarrow T' = meth'$ *in* D'' **and**
 T' : $P \vdash T' \leq TT$ **and**
 Ts' : $P \vdash TTs \leq Ts'$
by (*auto dest: sees-method-mono*)

from Ts n **have** [*simp*]: *size* $TTs = n$
by (*auto dest: list-all2-lengthD simp: min-def*)
with Ts' **have** [*simp*]: *size* $Ts' = n$
by (*auto dest: list-all2-lengthD*)

from m - C' *wfprog*
obtain mD'' : $P \vdash D''$ *sees* M' : $Ts' \rightarrow T' = meth'$ *in* D''
by (*fast dest: sees-method-idemp*)

{ **fix** $mxs' mxl' ins' xt'$
assume [*simp*]: $meth' = \lfloor (mxs', mxl', ins', xt') \rfloor$
let $?loc' = Addr\ a \# rev\ (take\ n\ stk) \textcircled{\small @}$ *replicate* mxl' *undefined-value*
let $?f' = (\ [],\ ?loc',\ D'',\ M',\ 0)$
let $?f = (stk,\ loc,\ C,\ M,\ pc)$

from *Addr obj* m - C' *ins* *meth-C* *exec* C' *False*
have s' : $\sigma = (None,\ h,\ ?f' \# ?f \# frs)$ **by** (*auto split: if-split-asm*)

moreover
from *wtprog* mD''
obtain *start*: *wt-start* $P\ D''\ Ts'\ mxl'$ ($\Phi\ D''\ M'$) **and** *ins'*: $ins' \neq []$
by (*auto dest: wt-jvm-prog-impl-wt-start*)
then obtain LT_0 **where** LT_0 : $\Phi\ D''\ M' ! 0 = Some\ (\ [],\ LT_0)$
by (*clarsimp simp add: wt-start-def defs1 sup-state-opt-any-Some*)
moreover
have *conf-f* $P\ h\ (\ [],\ LT_0)\ ins'\ ?f'$
proof –
let $?LT = OK\ (Class\ D'') \# (map\ OK\ Ts') \textcircled{\small @}$ (*replicate* mxl' *Err*)

from stk **have** $P, h \vdash take\ n\ stk \leq take\ n\ ST ..$
hence $P, h \vdash rev\ (take\ n\ stk) \leq rev\ (take\ n\ ST)$ **by** *simp*
also note Ts **also note** Ts' **finally**
have $P, h \vdash rev\ (take\ n\ stk) \leq map\ OK\ Ts'$ **by** *simp*
also
have $P, h \vdash replicate\ mxl'\ undefined-value \leq replicate\ mxl'\ Err$ **by** *simp*
also from m - C' **have** $P \vdash C' \preceq^* D''$ **by** (*rule sees-method-decl-above*)
from *obj heap-ok* **have** *is-htype* $P\ U$ **by** (*rule typeof-addr-is-type*)
with C' **have** $P \vdash ty-of-htype\ U \leq Class\ C'$
by (*cases* U) (*simp-all add: widen-array-object*)
with $\langle P \vdash C' \preceq^* D'' \rangle$ *obj* C' **have** $P, h \vdash Addr\ a \leq Class\ D''$
by (*auto simp add: conf-def intro: widen-trans*)
ultimately
have $P, h \vdash ?loc' \leq ?LT$ **by** *simp*
also from *start* LT_0 **have** $P \vdash \dots \leq LT_0$ **by** (*simp add: wt-start-def*)

```

finally have  $P, h \vdash ?loc' [:\leq_{\top}] LT_0$  .
thus  $?thesis$  using  $ins'$  by  $simp$ 
qed
ultimately have  $?thesis$  using  $s' \Phi$ -pc approx meth-C m-D  $T'$  ins D tconf  $C' mD''$ 
by (fastforce dest: sees-method-fun [of -  $C'$ ]) }
moreover
{ assume [ $simp$ ]:  $meth' = Native$ 
with  $wfprog$  m- $C'$  have  $D'' \cdot M'(Ts')$  ::  $T'$  by ( $simp$  add: sees-wf-native)
with  $C' m-C'$  have  $nec: is-native P U M'$  by ( $auto$  intro: is-native.intros)

from ins n Addr obj exec m- $C' C'$ 
obtain  $va h' tas'$  where  $va: (tas', va, h') \in red-external-aggr P t a M' (rev (take n stk)) h$ 
and  $\sigma: \sigma = extRet2JVM n h' stk loc C M pc frs va$  by ( $auto$ )
from  $va nec$  obj have  $hext: h \sqsubseteq h'$  by ( $auto$  intro: red-external-aggr-hext)
with frames have  $frames': conf-fs P h' \Phi M (length Ts) T frs$  by ( $rule$  conf-fs-hext)
from  $preh hext$  have  $preh': preallocated h'$  by ( $rule$  preallocated-hext)
from  $va nec$  obj tconf have  $tconf': P, h' \vdash t \sqrt{t}$ 
by ( $auto$  dest: red-external-aggr-preserves-tconf)
from  $hext$  obj have  $obj': typeof-addr h' a = \lfloor U \rfloor$  by ( $rule$  typeof-addr-hext-mono)

from  $stk$  have  $P, h \vdash take n stk [:\leq] take n ST$  by ( $rule$  list-all2-takeI)
then obtain  $Us$  where  $map typeof_h (take n stk) = map Some Us P \vdash Us [:\leq] take n ST$ 
by ( $auto$   $simp$  add: confs-conv-map)
hence  $Us: map typeof_h (rev (take n stk)) = map Some (rev Us) P \vdash rev Us [:\leq] rev (take n ST)$ 
by - ( $simp$  only: rev-map[symmetric],  $simp$ )
from  $\langle P \vdash rev Us [:\leq] rev (take n ST) \rangle Ts Ts'$ 
have  $P \vdash rev Us [:\leq] Ts'$  by ( $blast$  intro: widens-trans)
with obj  $\langle map typeof_h (rev (take n stk)) = map Some (rev Us) \rangle C' m-C'$ 
have  $wtext': P, h \vdash a \cdot M'(rev (take n stk)) : T'$  by ( $simp$  add: external-WT'.intros)
from  $va$  have  $va': P, t \vdash \langle a \cdot M'(rev (take n stk)), h \rangle -tas' \rightarrow ext \langle va, h \rangle$ 
by ( $unfold$  WT-red-external-list-conv[OF  $wfprog$   $wtext' tconf'$ ])
with  $heap-ok wtext' tconf wfprog$  have  $heap-ok': hconf h'$  by ( $auto$  dest: external-call-hconf)

have  $?thesis$ 
proof (cases  $va$ )
case (RetExc  $a'$ )
from frame  $hext$  have  $conf-f P h' (ST, LT) ins (stk, loc, C, M, pc)$  by ( $rule$  conf-f-hext)
with  $\sigma tconf' heap-ok' meth-C \Phi$ -pc frames' RetExc red-external-conf-extRet[OF  $wfprog va'$ 
 $wtext' heap-ok preh tconf'] ins preh'$ 
show  $?thesis$  by ( $fastforce$   $simp$  add: conf-def widen-Class)
next
case RetStaySame
from frame  $hext$  have  $conf-f P h' (ST, LT) ins (stk, loc, C, M, pc)$  by ( $rule$  conf-f-hext)
with  $\sigma heap-ok' meth-C \Phi$ -pc RetStaySame frames'  $tconf' preh'$  show  $?thesis$  by  $fastforce$ 
next
case (RetVal  $v$ )
with  $\sigma$  have  $\sigma: \sigma = (None, h', (v \# drop (n+1) stk, loc, C, M, pc+1) \# frs)$  by  $simp$ 
from  $heap-ok wtext' va' RetVal preh tconf$  have  $P, h' \vdash v : \leq T'$ 
by ( $auto$  dest: red-external-conf-extRet[OF  $wfprog$ ])
from  $stk$  have  $P, h \vdash drop (n + 1) stk [:\leq] drop (n+1) ST$  by ( $rule$  list-all2-dropI)
hence  $P, h' \vdash drop (n + 1) stk [:\leq] drop (n+1) ST$  using  $hext$  by ( $rule$  confs-hext)
with  $\langle P, h' \vdash v : \leq T' \rangle$  have  $P, h' \vdash v \# drop (n + 1) stk [:\leq] T' \# drop (n+1) ST$ 
by ( $auto$   $simp$  add: conf-def intro: widen-trans)
also

```

```

with  $NT$  ins wti  $\Phi$ -pc  $\Phi'$  nec False  $D$   $m$ - $D$   $T'$ 
have  $P \vdash (T' \# \text{drop } (n + 1) ST) [\leq] ST'$ 
  by(auto dest: sees-method-fun intro: widen-trans)
also from loc hext have  $P, h' \vdash \text{loc } [:\leq_{\top}] LT$  by(rule confTs-hext)
hence  $P, h' \vdash \text{loc } [:\leq_{\top}] LT'$  using  $LT'$  by(rule confTs-widen)
ultimately show ?thesis using  $\langle h\text{conf } h' \rangle \sigma$  meth- $C$   $\Phi'$  pc' frames' tconf' preh' by fastforce
qed }
ultimately show ?thesis by(cases meth') auto
qed
qed

```

```

declare list-all2-Cons2 [iff]

```

```

lemma Return-correct:

```

```

  assumes wt-prog: wf-jvm-prog $\Phi$   $P$ 
  assumes meth:  $P \vdash C$  sees  $M:Ts \rightarrow T = [(m\text{xs}, m\text{x}l_0, \text{ins}, \text{xt})]$  in  $C$ 
  assumes ins: ins ! pc = Return
  assumes wt:  $P, T, m\text{xs}, \text{size } \text{ins}, \text{xt} \vdash \text{ins!pc}, \text{pc} :: \Phi C M$ 
  assumes correct:  $\Phi \vdash t:(\text{None}, h, (\text{stk}, \text{loc}, C, M, \text{pc}) \# \text{frs}) \checkmark$ 
  assumes s':  $(\text{tas}, \sigma') \in \text{exec } P t (\text{None}, h, (\text{stk}, \text{loc}, C, M, \text{pc}) \# \text{frs})$ 
  shows  $\Phi \vdash t:\sigma' \checkmark$ 

```

```

proof -

```

```

  from wt-prog

```

```

  obtain wfmb where wf: wf-prog wfmb  $P$  by (simp add: wf-jvm-prog-phi-def)

```

```

  from meth ins s' correct

```

```

  have frs = []  $\implies$  ?thesis by (simp add: correct-state-def)

```

```

  moreover

```

```

  { fix f frs' assume frs': frs = f # frs'

```

```

    moreover obtain stk' loc' C' M' pc' where

```

```

      f: f = (stk', loc', C', M', pc') by (cases f)

```

```

    moreover note meth ins s'

```

```

    ultimately

```

```

    have  $\sigma'$ :

```

```

       $\sigma' = (\text{None}, h, (\text{hd } \text{stk} \# (\text{drop } (1 + \text{size } Ts) \text{stk}'), \text{loc}', C', M', \text{pc}' + 1) \# \text{frs}')$ 

```

```

      (is  $\sigma' = (\text{None}, h, ?f' \# \text{frs}')$ )

```

```

      by simp

```

```

  from correct meth

```

```

  obtain  $ST$   $LT$  where

```

```

    h-ok: hconf  $h$  and

```

```

    tconf:  $P, h \vdash t \sqrt{t}$  and

```

```

     $\Phi$ -pc:  $\Phi C M ! \text{pc} = \text{Some } (ST, LT)$  and

```

```

    frame: conf-f  $P h (ST, LT)$  ins  $(\text{stk}, \text{loc}, C, M, \text{pc})$  and

```

```

    frames: conf-fs  $P h \Phi M (\text{size } Ts) T$  frs and

```

```

    preh: preallocated  $h$ 

```

```

    by (auto dest: sees-method-fun)

```

```

  from  $\Phi$ -pc ins wt

```

```

  obtain  $U$   $ST_0$  where  $ST = U \# ST_0$   $P \vdash U \leq T$ 

```

```

    by (simp add: wt-instr-def app-def) blast

```

```

  with wf frame

```

```

  have hd-stk:  $P, h \vdash \text{hd } \text{stk} : \leq T$  by (auto simp add: conf-f-def)

```


from f frs' frames

obtain $ST' LT' Ts'' T'' mxs' mxl_0' ins' xt' Ts' T'$ where

Φ' : $\Phi C' M' ! pc' = \text{Some } (ST', LT')$ and

meth-C': $P \vdash C'$ sees $M': Ts'' \rightarrow T'' = [(mxs', mxl_0', ins', xt')] in C'$ and

ins': $ins' ! pc' = \text{Invoke } M (size Ts)$ and

$D: \exists D m D'. \text{class-type-of}' (ST' ! (size Ts)) = \text{Some } D \wedge P \vdash D$ sees $M: Ts' \rightarrow T' = m$ in D' and

$T': P \vdash T \leq T'$ and

frame': $\text{conf-f } P h (ST', LT') ins' f$ and

conf-fs: $\text{conf-fs } P h \Phi M' (size Ts'') T'' frs'$

by *clarsimp blast*

from f frame' obtain

stk': $P, h \vdash stk' [:\leq] ST'$ and

loc': $P, h \vdash loc' [:\leq_{\top}] LT'$ and

pc': $pc' < size ins'$

by (*simp add: conf-f-def*)

from *wt-prog meth-C' pc'*

have *wti*: $P, T'', mxs', size ins', xt' \vdash ins' ! pc', pc' :: \Phi C' M'$

by (*rule wt-jvm-prog-impl-wt-instr*)

obtain $aTs ST'' LT''$ where

Φ -suc: $\Phi C' M' ! Suc pc' = \text{Some } (ST'', LT'')$ and

less: $P \vdash (T' \# \text{drop } (size Ts + 1) ST', LT') \leq_i (ST'', LT'')$ and

suc-pc': $Suc pc' < size ins'$

using $ins' \Phi' D T' wti$

by (*fastforce simp add: sup-state-opt-any-Some split: if-split-asm*)

from *hd-stk T'* have *hd-stk'*: $P, h \vdash hd\ stk : \leq T' ..$

have *frame''*:

$\text{conf-f } P h (ST'', LT'') ins' ?f'$

proof –

from *stk'*

have $P, h \vdash \text{drop } (1 + size Ts) stk' [:\leq] \text{drop } (1 + size Ts) ST' ..$

moreover

with *hd-stk' less*

have $P, h \vdash hd\ stk \# \text{drop } (1 + size Ts) stk' [:\leq] ST''$ by *auto*

moreover

from *wf loc' less* have $P, h \vdash loc' [:\leq_{\top}] LT''$ by *auto*

moreover note *suc-pc'*

ultimately show *?thesis* by (*simp add: conf-f-def*)

qed

with $\sigma' frs' f$ meth *h-ok hd-stk* Φ -suc frames *meth-C' Φ' tconf preh*

have *?thesis* by (*fastforce dest: sees-method-fun [of - C']*)

}

ultimately

show *?thesis* by (*cases frs*) *blast+*

qed

declare *sup-state-opt-any-Some* [*iff*]

declare *not-Err-eq* [*iff*]

lemma *Load-correct*:

```
[[ wf-prog wt P;
   P ⊢ C sees M:Ts→T=[(mxs,mxl0,ins,xt)] in C;
   ins!pc = Load idx;
   P,T,mxs,size ins,xt ⊢ ins!pc,pc :: Φ C M;
   Φ ⊢ t:(None, h, (stk,loc,C,M,pc)#frs)√;
   (tas, σ') ∈ exec P t (None, h, (stk,loc,C,M,pc)#frs) ]]
⇒ Φ ⊢ t:σ' √
  by (fastforce dest: sees-method-fun [of - C] elim!: confTs-confT-sup)
```

declare [[simproc del: list-to-set-comprehension]]

lemma *Store-correct*:

```
[[ wf-prog wt P;
   P ⊢ C sees M:Ts→T=[(mxs,mxl0,ins,xt)] in C;
   ins!pc = Store idx;
   P,T,mxs,size ins,xt ⊢ ins!pc,pc :: Φ C M;
   Φ ⊢ t:(None, h, (stk,loc,C,M,pc)#frs)√;
   (tas, σ') ∈ exec P t (None, h, (stk,loc,C,M,pc)#frs) ]]
⇒ Φ ⊢ t:σ' √
  apply clarsimp
  apply (drule (1) sees-method-fun)
  apply clarsimp
  apply (blast intro!: list-all2-update-cong)
  done
```

lemma *Push-correct*:

```
[[ wf-prog wt P;
   P ⊢ C sees M:Ts→T=[(mxs,mxl0,ins,xt)] in C;
   ins!pc = Push v;
   P,T,mxs,size ins,xt ⊢ ins!pc,pc :: Φ C M;
   Φ ⊢ t:(None, h, (stk,loc,C,M,pc)#frs)√;
   (tas, σ') ∈ exec P t (None, h, (stk,loc,C,M,pc)#frs) ]]
⇒ Φ ⊢ t:σ' √
  apply clarsimp
  apply (drule (1) sees-method-fun)
  apply clarsimp
  apply (blast dest: typeof-lit-conf)
  done
```

declare [[simproc add: list-to-set-comprehension]]

lemma *Checkcast-correct*:

```
[[ wf-jvm-progΦ P;
   P ⊢ C sees M:Ts→T=[(mxs,mxl0,ins,xt)] in C;
   ins!pc = Checkcast D;
   P,T,mxs,size ins,xt ⊢ ins!pc,pc :: Φ C M;
   Φ ⊢ t:(None, h, (stk,loc,C,M,pc)#frs)√;
   (tas, σ) ∈ exec-instr (ins!pc) P t h stk loc C M pc frs ]]
⇒ Φ ⊢ t:σ √
  using wf-preallocatedD[of λP C (M, Ts, Tr, mxs, mxl0, is, xt). wt-method P C Ts Tr mxs mxl0 is xt
  (Φ C M) P h ClassCast]
  apply (clarsimp simp add: wf-jvm-prog-phi-def split: if-split-asm)
  apply (drule (1) sees-method-fun)
```

apply(*fastforce simp add: conf-def intro: widen-trans*)
apply (*drule (1) sees-method-fun*)
apply(*fastforce simp add: conf-def intro: widen-trans*)
done

lemma *Instanceof-correct:*

\llbracket *wf-jvm-prog* Φ *P*;
 $P \vdash C$ *sees* $M:Ts \rightarrow T = [(m\ xs, m\ xl_0, ins, xt)]$ *in* C ;
 $ins!pc = Instanceof$ Ty ;
 $P, T, m\ xs, size\ ins, xt \vdash ins!pc, pc :: \Phi\ C\ M$;
 $\Phi \vdash t: (None, h, (stk, loc, C, M, pc) \# frs) \checkmark$;
 $(tas, \sigma) \in exec-instr\ (ins!pc)\ P\ t\ h\ stk\ loc\ C\ M\ pc\ frs$ \rrbracket
 $\implies \Phi \vdash t:\sigma \checkmark$
apply (*clarsimp simp add: wf-jvm-prog-phi-def split: if-split-asm*)
apply (*drule (1) sees-method-fun*)
apply *fastforce*
done

declare *split-paired-All* [*simp del*]

end

lemma *widens-Cons* [*iff*]:

$P \vdash (T \# Ts) [\leq] Us = (\exists z\ zs. Us = z \# zs \wedge P \vdash T \leq z \wedge P \vdash Ts [\leq] zs)$
by(*rule list-all2-Cons1*)

context *heap-conf-base* **begin**

end

context *JVM-conf-read* **begin**

lemma *Getfield-correct:*

assumes *wf*: *wf-prog wt P*
assumes *mC*: $P \vdash C$ *sees* $M:Ts \rightarrow T = [(m\ xs, m\ xl_0, ins, xt)]$ *in* C
assumes *i*: $ins!pc = Getfield\ F\ D$
assumes *wt*: $P, T, m\ xs, size\ ins, xt \vdash ins!pc, pc :: \Phi\ C\ M$
assumes *cf*: $\Phi \vdash t: (None, h, (stk, loc, C, M, pc) \# frs) \checkmark$
assumes *xc*: $(tas, \sigma') \in exec-instr\ (ins!pc)\ P\ t\ h\ stk\ loc\ C\ M\ pc\ frs$

shows $\Phi \vdash t:\sigma' \checkmark$

proof –

from *mC cf* **obtain** $ST\ LT$ **where**

$h \checkmark$: *hconf h* **and**

tconf: $P, h \vdash t \checkmark$ **and**

Φ : $\Phi\ C\ M ! pc = Some\ (ST, LT)$ **and**

stk: $P, h \vdash stk [\leq] ST$ **and** *loc*: $P, h \vdash loc [\leq_{\top}] LT$ **and**

pc: $pc < size\ ins$ **and**

fs: *conf-fs* $P\ h\ \Phi\ M\ (size\ Ts)\ T\ frs$ **and**

preh: *preallocated h*

by (*fastforce dest: sees-method-fun*)

from *i* Φ *wt* **obtain** $oT\ ST''\ vT\ ST'\ LT'\ vT'\ fm$ **where**

$oT: P \vdash oT \leq \text{Class } D$ **and**
 $ST: ST = oT \# ST''$ **and**
 $F: P \vdash D \text{ sees } F:vT \text{ (fm) in } D$ **and**
 $pc': pc+1 < \text{size ins}$ **and**
 $\Phi': \Phi \ C \ M \ ! \ (pc+1) = \text{Some } (vT' \# ST', LT')$ **and**
 $ST': P \vdash ST'' [\leq] ST'$ **and** $LT': P \vdash LT [\leq_{\top}] LT'$ **and**
 $vT': P \vdash vT \leq vT'$
by fastforce

from $stk \ ST$ **obtain** $ref \ stk'$ **where**

$stk': stk = ref \# stk'$ **and**
 $ref: P, h \vdash ref : \leq oT$ **and**
 $ST'': P, h \vdash stk' [\leq] ST''$
by auto

show $?thesis$

proof($\text{cases } ref = \text{Null}$)

case True

with $tconf \ h \checkmark \ i \ xc \ stk' \ mC \ fs \ \Phi \ ST'' \ ref \ ST \ loc \ pc'$
 $wf\text{-preallocatedD}[OF \ wf, \ of \ h \ \text{NullPointer}] \ preh$
show $?thesis$ **by**(fastforce)

next

case False

from $ref \ oT$ **have** $P, h \vdash ref : \leq \text{Class } D \ ..$

with False **obtain** $a \ U' \ D'$ **where** $a: ref = \text{Addr } a$

and $h: \text{typeof-addr } h \ a = \text{Some } U'$

and $U': D' = \text{class-type-of } U'$ **and** $D': P \vdash D' \preceq^* D$

by ($\text{blast dest: non-npD2}$)

{ **fix** v

assume $\text{read: heap-read } h \ a \ (CField \ D \ F) \ v$

from $D' \ F$ **have** $\text{has-field: } P \vdash D' \text{ has } F:vT \text{ (fm) in } D$

by ($\text{blast intro: has-field-mono has-visible-field}$)

with h **have** $P, h \vdash a @ CField \ D \ F : vT$ **unfolding** $U' \ ..$

with read **have** $v: P, h \vdash v : \leq vT$ **using** $h \checkmark$

by($\text{rule heap-read-conf}$)

from $ST'' \ ST'$ **have** $P, h \vdash stk' [\leq] ST' \ ..$

moreover

from $v \ vT'$ **have** $P, h \vdash v : \leq vT'$ **by** blast

moreover

from $\text{loc } LT'$ **have** $P, h \vdash \text{loc} [\leq_{\top}] LT' \ ..$

moreover

note $h \checkmark \ mC \ \Phi' \ pc' \ v \ fs \ tconf \ preh$

ultimately **have** $\Phi \vdash t: (\text{None}, h, (v \# stk', \text{loc}, C, M, pc+1) \# \text{frs}) \checkmark$ **by** fastforce }

with $a \ h \ i \ mC \ stk' \ xc$

show $?thesis$ **by** auto

qed

qed

lemma Putfield-correct:

assumes $wf: wf\text{-prog } wt \ P$

assumes $mC: P \vdash C \text{ sees } M: Ts \rightarrow T = [(mrs, mxl_0, ins, xt)] \text{ in } C$

assumes $i: ins!pc = \text{Putfield } F \ D$

assumes $wt: P, T, mcs, size\ ins, xt \vdash ins!pc, pc :: \Phi\ C\ M$
assumes $cf: \Phi \vdash t: (None, h, (stk, loc, C, M, pc) \# frs) \checkmark$
assumes $xc: (tas, \sigma') \in exec\ instr\ (ins!pc)\ P\ t\ h\ stk\ loc\ C\ M\ pc\ frs$
shows $\Phi \vdash t: \sigma' \checkmark$

proof –

from $mC\ cf$ **obtain** $ST\ LT$ **where**
 $h\checkmark: hconf\ h$ **and**
 $tconf: P, h \vdash t \checkmark$ **and**
 $\Phi: \Phi\ C\ M!pc = Some\ (ST, LT)$ **and**
 $stk: P, h \vdash stk\ [: \leq] ST$ **and** $loc: P, h \vdash loc\ [: \leq_{\top}] LT$ **and**
 $pc: pc < size\ ins$ **and**
 $fs: conf\ fs\ P\ h\ \Phi\ M\ (size\ Ts)\ T\ frs$ **and**
 $preh: preallocated\ h$
by (*fastforce dest: sees-method-fun*)

from $i\ \Phi\ wt$ **obtain** $vT\ vT'\ oT\ ST''\ ST'\ LT'\ fm$ **where**
 $ST: ST = vT \# oT \# ST''$ **and**
 $field: P \vdash D\ sees\ F: vT'\ (fm)\ in\ D$ **and**
 $oT: P \vdash oT \leq Class\ D$ **and** $vT: P \vdash vT \leq vT'$ **and**
 $pc': pc+1 < size\ ins$ **and**
 $\Phi': \Phi\ C\ M!(pc+1) = Some\ (ST', LT')$ **and**
 $ST': P \vdash ST''\ [: \leq] ST'$ **and** $LT': P \vdash LT\ [: \leq_{\top}] LT'$
by *clarsimp*

from $stk\ ST$ **obtain** $v\ ref\ stk'$ **where**
 $stk': stk = v \# ref \# stk'$ **and**
 $v: P, h \vdash v : \leq vT$ **and**
 $ref: P, h \vdash ref : \leq oT$ **and**
 $ST'': P, h \vdash stk' [: \leq] ST''$
by *auto*

show *?thesis*

proof(*cases ref = Null*)

case *True*

with $tconf\ h\checkmark\ i\ xc\ stk'\ mC\ fs\ \Phi\ ST''\ ref\ ST\ loc\ pc'\ v$
 $wf\ preallocatedD[OF\ wf,\ of\ h\ NullPointer]\ preh$
show *?thesis* **by**(*fastforce*)

next

case *False*

from $ref\ oT$ **have** $P, h \vdash ref : \leq Class\ D ..$

with *False* **obtain** $a\ U'\ D'$ **where**

$a: ref = Addr\ a$ **and** $h: typeof\ addr\ h\ a = Some\ U'$
and $U': D' = class\ type\ of\ U'$ **and** $D': P \vdash D' \preceq^* D$
by (*blast dest: non-npD2*)

from $v\ vT$ **have** $vT': P, h \vdash v : \leq vT' ..$

from $field\ D'$ **have** $has\ field: P \vdash D'\ has\ F: vT'\ (fm)\ in\ D$

by (*blast intro: has-field-mono has-visible-field*)

with h **have** $al: P, h \vdash a @ CField\ D\ F : vT'\ unfolding\ U' ..$

let $?f' = (stk', loc, C, M, pc+1)$

{ **fix** h'

assume $write: heap\ write\ h\ a\ (CField\ D\ F)\ v\ h'$

hence hex : $h \sqsubseteq h'$ **by**(rule hex -heap-write)
with $preh$ **have** $preallocated\ h'$ **by**(rule $preallocated$ -hex)
moreover
from $write\ h\sqrt{\ } al\ vT'$ **have** $hconf\ h'$ **by**(rule $hconf$ -heap-write-mono)
moreover
from $ST''\ ST'$ **have** $P, h \vdash stk' [\leq] ST' ..$
from $this\ hex$ **have** $P, h' \vdash stk' [\leq] ST'$ **by** (rule $confs$ -hex)
moreover
from $loc\ LT'$ **have** $P, h \vdash loc [\leq_{\top}] LT' ..$
from $this\ hex$ **have** $P, h' \vdash loc [\leq_{\top}] LT'$ **by** (rule $confTs$ -hex)
moreover
from $fs\ hex$
have $conf$ -fs $P\ h'\ \Phi\ M$ (size Ts) $T\ frs$ **by** (rule $conf$ -fs-hex)
moreover
note $mC\ \Phi'\ pc'$
moreover
from $tconf\ hex$ **have** $P, h' \vdash t\ \sqrt{t}$ **by**(rule $tconf$ -hex-mono)
ultimately **have** $\Phi \vdash t:(None, h', ?f'\#frs)\ \sqrt{\ }$ **by** $fastforce\ }$
with $a\ h\ i\ mC\ stk'\ xc$ **show** $?thesis$ **by**(auto $simp\ del$: $correct$ -state-def)
qed
qed

lemma CAS -correct:

assumes wf : wf -prog $wt\ P$
assumes mC : $P \vdash C$ sees $M:Ts \rightarrow T = [(maxs, mxl_0, ins, xt)]$ in C
assumes i : $ins!pc = CAS\ F\ D$
assumes wt : $P, T, maxs, size\ ins, xt \vdash ins!pc, pc :: \Phi\ C\ M$
assumes cf : $\Phi \vdash t:(None, h, (stk, loc, C, M, pc)\#frs)\ \sqrt{\ }$
assumes xc : $(tas, \sigma') \in exec$ -instr $(ins!pc)\ P\ t\ h\ stk\ loc\ C\ M\ pc\ frs$
shows $\Phi \vdash t:\sigma'\ \sqrt{\ }$

proof –

from $mC\ cf$ **obtain** $ST\ LT$ **where**
 $h\sqrt{\}$: $hconf\ h$ **and**
 $tconf$: $P, h \vdash t\ \sqrt{t}$ **and**
 Φ : $\Phi\ C\ M\ !\ pc = Some\ (ST, LT)$ **and**
 stk : $P, h \vdash stk [\leq] ST$ **and** loc : $P, h \vdash loc [\leq_{\top}] LT$ **and**
 pc : $pc < size\ ins$ **and**
 fs : $conf$ -fs $P\ h\ \Phi\ M$ (size Ts) $T\ frs$ **and**
 $preh$: $preallocated\ h$
by ($fastforce\ dest$: $sees$ -method-fun)

from $i\ \Phi\ wt$ **obtain** $T1\ T2\ T3\ T'\ ST''\ ST'\ LT'\ fm$ **where**
 ST : $ST = T3\ \#\ T2\ \#\ T1\ \#\ ST''$ **and**
 $field$: $P \vdash D$ sees $F:T'$ (fm) in D **and**
 oT : $P \vdash T1 \leq Class\ D$ **and** $T2$: $P \vdash T2 \leq T'$ **and** $T3$: $P \vdash T3 \leq T'$ **and**
 pc' : $pc+1 < size\ ins$ **and**
 Φ' : $\Phi\ C\ M!(pc+1) = Some\ (Boolean\ \#\ ST', LT')$ **and**
 ST' : $P \vdash ST'' [\leq] ST'$ **and** LT' : $P \vdash LT [\leq_{\top}] LT'$
by $clarsimp$

from $stk\ ST$ **obtain** $v''\ v'\ v\ stk'$ **where**
 stk' : $stk = v''\ \#\ v'\ \#\ v\ \#\ stk'$ **and**
 v : $P, h \vdash v : \leq T1$ **and**
 v' : $P, h \vdash v' : \leq T2$ **and**

$v'': P, h \vdash v'' \leq T3$ and
 $ST'': P, h \vdash stk' [:\leq] ST''$
 by *auto*

show *?thesis*

proof(*cases v = Null*)

case *True*

with *tconf h√ i xc stk' mC fs Φ ST'' v ST loc pc' v' v''*
wf-preallocatedD[OF wf, of h NullPointer] preh

show *?thesis* **by**(*fastforce*)

next

case *False*

from $v \ oT$ **have** $P, h \vdash v \leq Class\ D \ ..$

with *False* **obtain** $a\ U'\ D'$ **where**

$a: v = Addr\ a$ **and** $h: typeof\ addr\ h\ a = Some\ U'$

and $U': D' = class\ type\ of\ U'$ **and** $D': P \vdash D' \leq^* D$

by (*blast dest: non-npD2*)

from $v'\ T2$ **have** $vT': P, h \vdash v' \leq T' \ ..$

from $v''\ T3$ **have** $vT'': P, h \vdash v'' \leq T' \ ..$

from *field D'* **have** *has-field: P ⊢ D' has F:T' (fm) in D*

by (*blast intro: has-field-mono has-visible-field*)

with h **have** $al: P, h \vdash a @ CField\ D\ F : T'$ **unfolding** $U' \ ..$

from $ST''\ ST'$ **have** $stk'': P, h \vdash stk' [:\leq] ST' \ ..$

from $loc\ LT'$ **have** $loc': P, h \vdash loc [:\leq_{\top}] LT' \ ..$

{ **fix** h'

assume *write: heap-write h a (CField D F) v'' h'*

hence $hext: h \trianglelefteq h'$ **by**(*rule hext-heap-write*)

with *preh* **have** *preallocated h'* **by**(*rule preallocated-hext*)

moreover

from *write h√ al vT''* **have** *hconf h'* **by**(*rule hconf-heap-write-mono*)

moreover

from $stk''\ hext$ **have** $P, h' \vdash stk' [:\leq] ST'$ **by** (*rule confs-hext*)

moreover

from $loc'\ hext$ **have** $P, h' \vdash loc [:\leq_{\top}] LT'$ **by** (*rule confTs-hext*)

moreover

from $fs\ hext$

have *conf-fs P h' Φ M (size Ts) T frs* **by** (*rule conf-fs-hext*)

moreover

note $mC\ \Phi'\ pc'$

moreover

let $?f' = (Bool\ True\ \# stk', loc, C, M, pc+1)$

from *tconf hext* **have** $P, h' \vdash t \sqrt{t}$ **by**(*rule tconf-hext-mono*)

ultimately **have** $\Phi \vdash t: (None, h', ?f' \# frs) \sqrt{\quad}$ **by** *fastforce*

} **moreover** {

let $?f' = (Bool\ False\ \# stk', loc, C, M, pc+1)$

have $\Phi \vdash t: (None, h, ?f' \# frs) \sqrt{\quad}$ **using** *tconf h√ preh mC Φ' stk'' loc' pc' fs*

by *fastforce*

} **ultimately** **show** *?thesis* **using** $a\ h\ i\ mC\ stk'\ xc$ **by**(*auto simp del: correct-state-def*)

qed

qed

lemma *New-correct*:

assumes *wf*: *wf-prog wt P*
assumes *meth*: $P \vdash C \text{ sees } M:Ts \rightarrow T = [(m\text{xs}, m\text{x}l_0, \text{ins}, \text{xt})]$ *in C*
assumes *ins*: $\text{ins!pc} = \text{New } X$
assumes *wt*: $P, T, m\text{xs}, \text{size ins}, \text{xt} \vdash \text{ins!pc}, \text{pc} :: \Phi \ C \ M$
assumes *conf*: $\Phi \vdash t: (\text{None}, h, (\text{stk}, \text{loc}, C, M, \text{pc}) \# \text{frs}) \surd$
assumes *no-x*: $(\text{tas}, \sigma) \in \text{exec-instr } (\text{ins!pc}) \ P \ t \ h \ \text{stk} \ \text{loc} \ C \ M \ \text{pc} \ \text{frs}$
shows $\Phi \vdash t: \sigma \surd$

proof –

from *ins conf meth*
obtain *ST LT* **where**
heap-ok: *hconf h* **and**
tconf: $P, h \vdash t \surd t$ **and**
 $\Phi\text{-pc}$: $\Phi \ C \ M! \text{pc} = \text{Some } (ST, LT)$ **and**
frame: *conf-f P h (ST, LT) ins (stk, loc, C, M, pc)* **and**
frames: *conf-fs P h Φ M (size Ts) T frs* **and**
preh: *preallocated h*
by (*auto dest: sees-method-fun*)

from $\Phi\text{-pc ins wt}$

obtain *ST' LT'* **where**
is-class-X: *is-class P X* **and**
mxs: $\text{size } ST < m\text{xs}$ **and**
suc-pc: $\text{pc} + 1 < \text{size ins}$ **and**
 $\Phi\text{-suc}$: $\Phi \ C \ M!(\text{pc} + 1) = \text{Some } (ST', LT')$ **and**
less: $P \vdash (\text{Class } X \# ST, LT) \leq_i (ST', LT')$
by *auto*

show *?thesis*

proof(*cases allocate h (Class-type X) = {}*)

case *True*

with *frame frames tconf suc-pc no-x ins meth $\Phi\text{-pc}$*
wf-preallocatedD[OF wf, of h OutOfMemory] preh is-class-X heap-ok

show *?thesis*

by(*fastforce intro: tconf-hext-mono confs-hext confTs-hext conf-fs-hext*)

next

case *False*

with *ins meth no-x* **obtain** *h' oref*

where *new*: $(h', \text{oref}) \in \text{allocate } h \ (\text{Class-type } X)$

and $\sigma' : \sigma = (\text{None}, h', (\text{Addr } \text{oref} \# \text{stk}, \text{loc}, C, M, \text{pc} + 1) \# \text{frs})$ (**is** $\sigma = (\text{None}, h', ?f \# \text{frs})$)

by *auto*

from *new* **have** *hext*: $h \trianglelefteq h'$ **by**(*rule hext-allocate*)

with *preh* **have** *preh'*: *preallocated h'* **by**(*rule preallocated-hext*)

from *new heap-ok is-class-X* **have** *heap-ok'*: *hconf h'*

by(*auto intro: hconf-allocate-mono*)

with *new is-class-X* **have** *h'*: *typeof-addr h' oref = [Class-type X]* **by**(*auto dest: allocate-SomeD*)

note *heap-ok' σ'*

moreover

from *frame less suc-pc wf h' hext*

have *conf-f P h' (ST', LT') ins ?f*

apply (*clarsimp simp add: fun-upd-apply conf-def split-beta*)

apply (*auto intro: confs-hext confTs-hext*)

done
moreover
from *frames heat* **have** *conf-fs* $P\ h'\ \Phi\ M\ (\text{size } Ts)\ T\ \text{frs}$ **by** (*rule conf-fs-heat*)
moreover from *tconf heat* **have** $P, h' \vdash t\ \checkmark$ **by** (*rule tconf-heat-mono*)
ultimately
show *?thesis* **using** *meth* $\Phi\text{-suc}\ \text{preh}'$ **by** *fastforce*
qed
qed

lemma *Goto-correct*:

\llbracket *wf-prog* $wt\ P$;
 $P \vdash C\ \text{sees } M: Ts \rightarrow T = [(m\ x_s, m\ x_{l_0}, i\ n_s, x\ t)]\ \text{in } C$;
 $i\ n_s \ !\ pc = \text{Goto branch}$;
 $P, T, m\ x_s, \text{size } i\ n_s, x\ t \vdash i\ n_s!pc, pc :: \Phi\ C\ M$;
 $\Phi \vdash t: (None, h, (stk, loc, C, M, pc) \# frs) \checkmark$;
 $(tas, \sigma') \in \text{exec } P\ t\ (None, h, (stk, loc, C, M, pc) \# frs)\ \llbracket$
 $\implies \Phi \vdash t: \sigma' \checkmark$
apply *clarsimp*
apply (*drule* (1) *sees-method-fun*)
apply *fastforce*
done

declare \llbracket *simproc del: list-to-set-comprehension* \rrbracket

lemma *IfFalse-correct*:

\llbracket *wf-prog* $wt\ P$;
 $P \vdash C\ \text{sees } M: Ts \rightarrow T = [(m\ x_s, m\ x_{l_0}, i\ n_s, x\ t)]\ \text{in } C$;
 $i\ n_s \ !\ pc = \text{IfFalse branch}$;
 $P, T, m\ x_s, \text{size } i\ n_s, x\ t \vdash i\ n_s!pc, pc :: \Phi\ C\ M$;
 $\Phi \vdash t: (None, h, (stk, loc, C, M, pc) \# frs) \checkmark$;
 $(tas, \sigma') \in \text{exec } P\ t\ (None, h, (stk, loc, C, M, pc) \# frs)\ \llbracket$
 $\implies \Phi \vdash t: \sigma' \checkmark$
apply *clarsimp*
apply (*drule* (1) *sees-method-fun*)
apply *fastforce*
done

declare \llbracket *simproc add: list-to-set-comprehension* \rrbracket

lemma *BinOp-correct*:

\llbracket *wf-prog* $wt\ P$;
 $P \vdash C\ \text{sees } M: Ts \rightarrow T = [(m\ x_s, m\ x_{l_0}, i\ n_s, x\ t)]\ \text{in } C$;
 $i\ n_s \ !\ pc = \text{BinOpInstr } bop$;
 $P, T, m\ x_s, \text{size } i\ n_s, x\ t \vdash i\ n_s!pc, pc :: \Phi\ C\ M$;
 $\Phi \vdash t: (None, h, (stk, loc, C, M, pc) \# frs) \checkmark$;
 $(tas, \sigma') \in \text{exec } P\ t\ (None, h, (stk, loc, C, M, pc) \# frs)\ \llbracket$
 $\implies \Phi \vdash t: \sigma' \checkmark$
apply *clarsimp*
apply (*drule* (1) *sees-method-fun*)
apply (*clarsimp simp add: conf-def*)
apply (*drule* (2) *WTrt-binop-widen-mono*)
apply *clarsimp*
apply (*frule* (2) *binop-progress*)
apply (*clarsimp split: sum.split-asm*)

```

apply(frule (5) binop-type)
apply(fastforce intro: widen-trans simp add: conf-def)
apply(frule (5) binop-type)
apply(clarsimp simp add: conf-def)
apply(clarsimp simp add: widen-Class)
apply(fastforce intro: widen-trans dest: binop-relevant-class simp add: cname-of-def conf-def)
done

```

lemma *Pop-correct:*

```

[[ wf-prog wt P;
  P ⊢ C sees M:Ts→T=[(mxs,mxl0,ins,xt)] in C;
  ins ! pc = Pop;
  P,T,mxs,size ins,xt ⊢ ins!pc,pc :: Φ C M;
  Φ ⊢ t:(None, h, (stk,loc,C,M,pc)#frs)√;
  (tas, σ') ∈ exec P t (None, h, (stk,loc,C,M,pc)#frs) ]]
⇒ Φ ⊢ t:σ'√
apply clarsimp
apply (drule (1) sees-method-fun)
apply fastforce
done

```

lemma *Dup-correct:*

```

[[ wf-prog wt P;
  P ⊢ C sees M:Ts→T=[(mxs,mxl0,ins,xt)] in C;
  ins ! pc = Dup;
  P,T,mxs,size ins,xt ⊢ ins!pc,pc :: Φ C M;
  Φ ⊢ t:(None, h, (stk,loc,C,M,pc)#frs)√;
  (tas, σ') ∈ exec P t (None, h, (stk,loc,C,M,pc)#frs) ]]
⇒ Φ ⊢ t:σ'√
apply clarsimp
apply (drule (1) sees-method-fun)
apply fastforce
done

```

lemma *Swap-correct:*

```

[[ wf-prog wt P;
  P ⊢ C sees M:Ts→T=[(mxs,mxl0,ins,xt)] in C;
  ins ! pc = Swap;
  P,T,mxs,size ins,xt ⊢ ins!pc,pc :: Φ C M;
  Φ ⊢ t:(None, h, (stk,loc,C,M,pc)#frs)√;
  (tas, σ') ∈ exec P t (None, h, (stk,loc,C,M,pc)#frs) ]]
⇒ Φ ⊢ t:σ'√
apply clarsimp
apply (drule (1) sees-method-fun)
apply fastforce
done

```

declare [[simproc del: list-to-set-comprehension]]

lemma *Throw-correct:*

```

[[ wf-prog wt P;
  P ⊢ C sees M:Ts→T=[(mxs,mxl0,ins,xt)] in C;
  ins ! pc = ThrowExc;
  P,T,mxs,size ins,xt ⊢ ins!pc,pc :: Φ C M;

```

```

   $\Phi \vdash t:(None, h, (stk, loc, C, M, pc) \# frs) \checkmark$ ;
   $(tas, \sigma') \in exec\text{-instr } (ins!pc) P t h stk loc C M pc frs \ ]$ 
 $\implies \Phi \vdash t:\sigma' \checkmark$ 
using wf-preallocatedD[of wt P h NullPointer]
apply(clarsimp)
apply(drule (1) sees-method-fun)
apply(auto)
  apply fastforce
  apply fastforce
apply(drule (1) non-npD)
apply fastforce+
done

declare [[simproc add: list-to-set-comprehension]]

lemma NewArray-correct:
  assumes wf: wf-prog wt P
  assumes meth:  $P \vdash C \text{ sees } M:Ts \rightarrow T = [(mxs, mxl_0, ins, xt)] \text{ in } C$ 
  assumes ins:  $ins!pc = NewArray X$ 
  assumes wt:  $P, T, mxs, size \ ins, xt \vdash ins!pc, pc :: \Phi C M$ 
  assumes conf:  $\Phi \vdash t:(None, h, (stk, loc, C, M, pc) \# frs) \checkmark$ 
  assumes no-x:  $(tas, \sigma) \in exec\text{-instr } (ins!pc) P t h stk loc C M pc frs$ 
  shows  $\Phi \vdash t:\sigma \checkmark$ 
proof –
  from ins conf meth
  obtain ST LT where
    heap-ok: hconf h and
    tconf:  $P, h \vdash t \checkmark$  and
     $\Phi\text{-pc}$ :  $\Phi C M!pc = Some (ST, LT)$  and
    stk:  $P, h \vdash stk [\leq] ST$  and loc:  $P, h \vdash loc [\leq_{\top}] LT$  and
    pc:  $pc < size \ ins$  and
    frame: conf-f P h (ST, LT) ins (stk, loc, C, M, pc) and
    frames: conf-fs P h  $\Phi M (size \ Ts) T frs$  and
    preh: preallocated h
  by (auto dest: sees-method-fun)

from ins  $\Phi\text{-pc}$  wt obtain ST'' X' ST' LT' where
  ST:  $ST = Integer \# ST''$  and
  pc':  $pc+1 < size \ ins$  and
   $\Phi'$ :  $\Phi C M! (pc+1) = Some (X' \# ST', LT')$  and
  ST':  $P \vdash ST'' [\leq] ST'$  and LT':  $P \vdash LT [\leq_{\top}] LT'$  and
  XX':  $P \vdash X[] \leq X'$  and
  suc-pc:  $pc+1 < size \ ins$  and
  is-type-X: is-type P (X[])
  by(fastforce dest: Array-widen)

from stk ST obtain si stk' where si:  $stk = Intg \ si \# stk'$ 
  by(auto simp add: conf-def)

show ?thesis
proof(cases si < s 0  $\vee$  allocate h (Array-type X (nat (sint si))) = {})
  case True
  with frame frames tconf heap-ok suc-pc no-x ins meth  $\Phi\text{-pc}$  si preh
    wf-preallocatedD[OF wf, of h OutOfMemory] wf-preallocatedD[OF wf, of h NegativeArraySize]

```

show *?thesis*
by(*fastforce intro: tconf-heap-mono confs-heap confTs-heap conf-fs-heap split: if-split-asm*)
next
case *False*
with *ins meth si no-x* **obtain** *h' oref*
where *new: (h', oref) ∈ allocate h (Array-type X (nat (sint si)))*
and *σ': σ = (None, h', (Addr oref#tl stk,loc,C,M,pc+1)#frs) (is σ = (None, h', ?f # frs))*
by(*auto split: if-split-asm*)
from *new* **have** *heap: h ≤ h'* **by**(*rule heap-allocate*)
with *preh* **have** *preh': preallocated h'* **by**(*rule preallocated-heap*)
from *new heap-ok is-type-X* **have** *heap-ok': hconf h'* **by**(*auto intro: hconf-allocate-mono*)
from *False* **have** *si': 0 ≤_s si* **by** *auto*
with *new is-type-X* **have** *h': typeof-addr h' oref = [Array-type X (nat (sint si))]*
by(*auto dest: allocate-SomeD*)

note *σ' heap-ok'*
moreover
from *frame ST' ST LT' suc-pc wf XX' h' heap*
have *conf-f P h' (X' # ST', LT') ins ?f*
by(*clarsimp simp add: fun-upd-apply conf-def split-beta*)(*auto intro: confs-heap confTs-heap*)
moreover
from *frames heap* **have** *conf-fs P h' Φ M (size Ts) T frs* **by** (*rule conf-fs-heap*)
moreover from *tconf heap* **have** *P, h' ⊢ t √_t* **by**(*rule tconf-heap-mono*)
ultimately
show *?thesis using meth Φ' preh' by fastforce*
qed
qed

lemma *ALoad-correct:*

assumes *wf: wf-prog wt P*
assumes *meth: P ⊢ C sees M: Ts → T = [(mxs, mxl₀, ins, xt)] in C*
assumes *ins: ins!pc = ALoad*
assumes *wt: P, T, mxs, size ins, xt ⊢ ins!pc, pc :: Φ C M*
assumes *conf: Φ ⊢ t: (None, h, (stk, loc, C, M, pc)#frs)√*
assumes *no-x: (tas, σ) ∈ exec-instr (ins!pc) P t h stk loc C M pc frs*
shows *Φ ⊢ t:σ √*

proof –

from *ins conf meth*
obtain *ST LT* **where**
heap-ok: hconf h **and**
tconf: P, h ⊢ t √_t **and**
Φ-pc: Φ C M!pc = Some (ST, LT) **and**
stk: P, h ⊢ stk [≤] ST **and** *loc: P, h ⊢ loc [≤_⊤] LT* **and**
pc: pc < size ins **and**
frame: conf-f P h (ST, LT) ins (stk, loc, C, M, pc) **and**
frames: conf-fs P h Φ M (size Ts) T frs **and**
preh: preallocated h
by (*auto dest: sees-method-fun*)

from *ins wt Φ-pc* **have** *lST: length ST > 1* **by**(*auto*)

show *?thesis*

proof(*cases hd (tl stk) = Null*)

case *True*

with *ins no-x heap-ok tconf Φ -pc stk loc frame frames meth wf-preallocatedD[OF wf, of h Null-Pointer]* *preh*

show *?thesis* **by**(*fastforce*)

next

case *False*

note *stkNN = this*

have *STNN: hd (tl ST) \neq NT*

proof

assume *hd (tl ST) = NT*

moreover

from *frame* **have** *P, h \vdash stk [\leq] ST* **by** *simp*

with *lST* **have** *P, h \vdash hd (tl stk) \leq hd (tl ST)*

by (*cases ST, auto, case-tac list, auto*)

ultimately

have *hd (tl stk) = Null* **by** *simp*

with *stkNN* **show** *False* **by** *contradiction*

qed

with *stkNN ins Φ -pc wt* **obtain** *ST'' X X' ST' LT'* **where**

ST: ST = Integer # X[] # ST'' **and**

pc': pc+1 < size ins **and**

Φ' : Φ *C M ! (pc+1) = Some (X'#ST', LT')* **and**

ST': P \vdash ST'' [\leq] ST' **and** *LT': P \vdash LT [\leq_{\top}] LT'* **and**

XX': P \vdash X \leq X' **and**

suc-pc: pc+1 < size ins

by(*fastforce*)

from *stk ST* **obtain** *ref idx stk'* **where**

stk': stk = idx#ref#stk' **and**

idx: P, h \vdash idx \leq Integer **and**

ref: P, h \vdash ref \leq X[] **and**

ST'': P, h \vdash stk' [\leq] ST''

by *auto*

from *stkNN stk'* **have** *ref \neq Null* **by**(*simp*)

with *ref* **obtain** *a Xel n*

where *a: ref = Addr a*

and *ha: typeof-addr h a = [Array-type Xel n]*

and *Xel: P \vdash Xel \leq X*

by(*cases ref*)(*fastforce simp add: conf-def widen-Array*)**+**

from *idx* **obtain** *idxI* **where** *idxI: idx = Intg idxI*

by(*auto simp add: conf-def*)

show *?thesis*

proof(*cases 0 \leq s idxI \wedge sint idxI < int n*)

case *True*

hence *si': 0 \leq s idxI sint idxI < int n* **by** *auto*

hence *nat (sint idxI) < n*

by (*simp add: word-sle-eq nat-less-iff*)

with *ha* **have** *al: P, h \vdash a@ACell (nat (sint idxI)) : Xel ..*

{ **fix** *v*

assume *read: heap-read h a (ACell (nat (sint idxI))) v*

hence *v: P, h \vdash v \leq Xel* **using** *al heap-ok* **by**(*rule heap-read-conf*)

```

let ?f = (v # stk', loc, C, M, pc + 1)

from frame ST' ST LT' suc-pc wf XX' Xel idxI si' v ST''
have conf-f P h (X' # ST', LT') ins ?f
  by(auto intro: widen-trans simp add: conf-def)
hence  $\Phi \vdash t:(None, h, ?f \# frs) \checkmark$ 
  using meth  $\Phi'$  heap-ok  $\Phi$ -pc frames tconf preh by fastforce }
with ins meth si' stk' a ha no-x idxI idx
show ?thesis by(auto simp del: correct-state-def split: if-split-asm)
next
case False
with stk' idxI ins no-x heap-ok tconf meth a ha Xel  $\Phi$ -pc frame frames
  wf-preallocatedD[OF wf, of h ArrayIndexOutOfBounds]
show ?thesis
  by (fastforce simp: preh split: if-split-asm simp del: Listn.lesub-list-impl-same-size)
qed
qed
qed

```

lemma *AStore-correct:*

```

assumes wf: wf-prog wt P
assumes meth:  $P \vdash C$  sees  $M:Ts \rightarrow T = [(mxs, mxl_0, ins, xt)]$  in C
assumes ins:  $ins!pc = AStore$ 
assumes wt:  $P, T, mxs, size\ ins, xt \vdash ins!pc, pc :: \Phi\ C\ M$ 
assumes conf:  $\Phi \vdash t: (None, h, (stk, loc, C, M, pc) \# frs) \checkmark$ 
assumes no-x:  $(tas, \sigma) \in exec-instr (ins!pc) P\ t\ h\ stk\ loc\ C\ M\ pc\ frs$ 
shows  $\Phi \vdash t: \sigma \checkmark$ 

```

proof –

```

from ins conf meth
obtain ST LT where
  heap-ok: hconf h and
  tconf:  $P, h \vdash t \checkmark$  and
   $\Phi$ -pc:  $\Phi\ C\ M!pc = Some\ (ST, LT)$  and
  stk:  $P, h \vdash stk\ [:\leq] ST$  and loc:  $P, h \vdash loc\ [:\leq_{\top}] LT$  and
  pc:  $pc < size\ ins$  and
  frame:  $conf-f\ P\ h\ (ST, LT)\ ins\ (stk, loc, C, M, pc)$  and
  frames:  $conf-fs\ P\ h\ \Phi\ M\ (size\ Ts)\ T\ frs$  and
  preh: preallocated h
by (auto dest: sees-method-fun)

```

from ins wt Φ -pc have lST: $length\ ST > 2$ by(auto)

show ?thesis

proof(cases hd (tl (tl stk)) = Null)

case True

with ins no-x heap-ok tconf Φ -pc stk loc frame frames meth wf-preallocatedD[OF wf, of h Null-Pointer] preh

show ?thesis by(fastforce)

next

case False

note stkNN = this

have STNN: $hd\ (tl\ (tl\ ST)) \neq NT$

proof

assume $hd (tl (tl ST)) = NT$

moreover

from *frame* **have** $P, h \vdash stk [\leq] ST$ **by** *simp*

with *lST* **have** $P, h \vdash hd (tl (tl stk)) \leq hd (tl (tl ST))$

by (*cases ST, auto, case-tac list, auto, case-tac lista, auto*)

ultimately

have $hd (tl (tl stk)) = Null$ **by** *simp*

with *stkNN* **show** *False* **by** *contradiction*

qed

with *ins stkNN* Φ -*pc wt* **obtain** $ST'' Y X ST' LT'$ **where**

$ST: ST = Y \# Integer \# X[] \# ST''$ **and**

$pc': pc+1 < size\ ins$ **and**

$\Phi': \Phi C M ! (pc+1) = Some (ST', LT')$ **and**

$ST': P \vdash ST'' [\leq] ST'$ **and** $LT': P \vdash LT [\leq_{\top}] LT'$ **and**

suc-pc: pc+1 < size ins

by(*fastforce*)

from *stk ST* **obtain** *ref e idx stk'* **where**

$stk': stk = e \# idx \# ref \# stk'$ **and**

$idx: P, h \vdash idx \leq Integer$ **and**

$ref: P, h \vdash ref \leq X[]$ **and**

$e: P, h \vdash e \leq Y$ **and**

$ST'': P, h \vdash stk' [\leq] ST''$

by *auto*

from *stkNN stk'* **have** $ref \neq Null$ **by**(*simp*)

with *ref* **obtain** $a Xel n$

where $a: ref = Addr a$

and $ha: typeof_addr h a = [Array-type Xel n]$

and $Xel: P \vdash Xel \leq X$

by(*cases ref*)(*fastforce simp add: conf-def widen-Array*)**+**

from *idx* **obtain** $idxI$ **where** $idxI: idx = Intg idxI$

by(*auto simp add: conf-def*)

show *?thesis*

proof(*cases* $0 \leq_s idxI \wedge sint\ idxI < int\ n$)

case *True*

hence $si': 0 \leq_s idxI\ sint\ idxI < int\ n$ **by** *simp-all*

from e **obtain** Te **where** $Te: typeof_h e = [Te] P \vdash Te \leq Y$

by(*auto simp add: conf-def*)

show *?thesis*

proof(*cases* $P \vdash Te \leq Xel$)

case *True*

with Te **have** $eXel: P, h \vdash e \leq Xel$

by(*auto simp add: conf-def intro: widen-trans*)

{ **fix** h'

assume *write: heap-write h a (ACell (nat (sint idxI))) e h'*

hence $hext: h \leq h'$ **by**(*rule hext-heap-write*)

```

with preh have preh': preallocated h' by(rule preallocated-heat)

let ?f = (stk', loc, C, M, pc + 1)

from si' have nat (sint idxI) < n
  by (simp add: word-sle-eq nat-less-iff)
with ha have P, h ⊢ a@ACell (nat (sint idxI)) : Xel ..
with write heap-ok have heap-ok': hconf h' using eXel
  by(rule hconf-heap-write-mono)
moreover
from ST stk stk' ST' have P, h ⊢ stk' [⋮] ST' by auto
with heat have stk'': P, h' ⊢ stk' [⋮] ST'
  by– (rule confs-heat)
moreover
from loc LT' have P, h ⊢ loc [⋮] LT' ..
with heat have P, h' ⊢ loc [⋮] LT' by – (rule confTs-heat)
moreover
with frame ST' ST LT' suc-pc wf Xel idxI si' stk''
have conf-f P h' (ST', LT') ins ?f
  by(clarsimp)
with frames heat have conf-fs P h' Φ M (size Ts) T frs by– (rule conf-fs-heat)
moreover from tconf heat have P, h' ⊢ t √t by(rule tconf-heat-mono)
ultimately have Φ ⊢ t:(None, h', ?f # frs) √ using meth Φ' Φ-pc suc-pc preh'
  by(fastforce) }
with True si' ins meth stk' a ha no-x idxI idx Te
show ?thesis
  by(auto split: if-split-asm simp del: correct-state-def intro: widen-trans)
next
case False
with stk' idxI ins no-x heap-ok tconf meth a ha Xel Te Φ-pc frame frames si'
  wf-preallocatedD[OF wf, of h ArrayStore]
show ?thesis
  by (fastforce split: if-splits list.splits simp: preh
      simp del: Listn.lesub-list-impl-same-size)
qed
next
case False
with stk' idxI ins no-x heap-ok tconf meth a ha Xel Φ-pc frame frames preh
  wf-preallocatedD[OF wf, of h ArrayIndexOutOfBounds]
show ?thesis by(fastforce split: if-split-asm)
qed
qed
qed

```

lemma *ALength-correct*:

```

assumes wf: wf-prog wt P
assumes meth: P ⊢ C sees M: Ts → T = [(mxs, mxl0, ins, xt)] in C
assumes ins: ins!pc = ALength
assumes wt: P, T, mxs, size ins, xt ⊢ ins!pc, pc :: Φ C M
assumes conf: Φ ⊢ t: (None, h, (stk, loc, C, M, pc) # frs) √
assumes no-x: (tas, σ) ∈ exec-instr (ins!pc) P t h stk loc C M pc frs
shows Φ ⊢ t: σ √
proof –
  from ins conf meth

```


obtain $ST\ LT$ **where**

heap-ok: $hconf\ h$ **and**

tconf: $P, h \vdash t \sqrt{t}$ **and**

Φ -*pc*: $\Phi\ C\ M!pc = Some\ (ST, LT)$ **and**

stk: $P, h \vdash stk\ [:\leq]\ ST$ **and** *loc*: $P, h \vdash loc\ [:\leq_{\top}]\ LT$ **and**

pc: $pc < size\ ins$ **and**

frame: $conf\text{-}f\ P\ h\ (ST, LT)\ ins\ (stk, loc, C, M, pc)$ **and**

frames: $conf\text{-}fs\ P\ h\ \Phi\ M\ (size\ Ts)\ T\ frs$ **and**

preh: *preallocated* h

by (*auto dest: sees-method-fun*)

from *ins wt* Φ -*pc* **have** $lST: length\ ST > 0$ **by**(*auto*)

show *?thesis*

proof(*cases hd stk = Null*)

case *True*

with *ins no-x heap-ok tconf* Φ -*pc* *stk loc frame frames meth wf-preallocatedD*[*OF wf, of h Null-Pointer*] *preh*

show *?thesis* **by**(*fastforce*)

next

case *False*

note *stkNN = this*

have $STNN: hd\ ST \neq NT$

proof

assume $hd\ ST = NT$

moreover

from *frame* **have** $P, h \vdash stk\ [:\leq]\ ST$ **by** *simp*

with lST **have** $P, h \vdash hd\ stk\ :\leq\ hd\ ST$

by (*cases ST, auto*)

ultimately

have $hd\ stk = Null$ **by** *simp*

with *stkNN* **show** *False* **by** *contradiction*

qed

with *stkNN ins* Φ -*pc* *wt* **obtain** $ST''\ X\ ST'\ LT'$ **where**

$ST: ST = (X[]) \# ST''$ **and**

$pc': pc+1 < size\ ins$ **and**

$\Phi': \Phi\ C\ M!(pc+1) = Some\ (ST', LT')$ **and**

$ST': P \vdash (Integer \# ST'') [:\leq]\ ST'$ **and** $LT': P \vdash LT [:\leq_{\top}]\ LT'$ **and**

suc-pc: $pc+1 < size\ ins$

by(*fastforce*)

from *stk ST* **obtain** *ref stk'* **where**

$stk': stk = ref \# stk'$ **and**

ref: $P, h \vdash ref\ :\leq\ X[]$ **and**

$ST'': P, h \vdash stk' [:\leq]\ ST''$

by *auto*

from *stkNN stk'* **have** *ref* $\neq Null$ **by**(*simp*)

with *ref* **obtain** *a Xel n*

where *a*: *ref = Addr a*

and *ha*: *typeof-addr h a = [Array-type Xel n]*

and *Xel*: $P \vdash Xel \leq X$

by(*cases ref*)(*fastforce simp add: conf-def widen-Array*)**+**

from *ins meth stk' a ha no-x* **have** σ' :
 $\sigma = (None, h, (Intg (word-of-int (int n)) \# stk', loc, C, M, pc + 1) \# frs)$
 (is $\sigma = (None, h, ?f \# frs)$)
by(*auto*)
moreover
from *ST stk stk' ST'* **have** $P, h \vdash Intg si \# stk' [:\leq] ST'$ **by**(*auto*)
with *frame ST' ST LT' suc-pc wf*
have *conf-f P h (ST', LT') ins ?f*
by(*fastforce intro: widen-trans*)
ultimately show *?thesis using meth Φ' heap-ok Φ -pc frames tconf preh* **by** *fastforce*
qed
qed

lemma *MEnter-correct:*

assumes *wf: wf-prog wt P*
assumes *meth: $P \vdash C$ sees $M: Ts \rightarrow T = [(m\bar{x}s, m\bar{x}l_0, ins, xt)]$ in C*
assumes *ins: $ins!pc = MEnter$*
assumes *wt: $P, T, m\bar{x}s, size\ ins, xt \vdash ins!pc, pc :: \Phi\ C\ M$*
assumes *conf: $\Phi \vdash t: (None, h, (stk, loc, C, M, pc) \# frs) \checkmark$*
assumes *no-x: $(tas, \sigma) \in exec-instr (ins!pc) P\ t\ h\ stk\ loc\ C\ M\ pc\ frs$*
shows $\Phi \vdash t: \sigma \checkmark$

proof –

from *ins conf meth*
obtain *ST LT* **where**
heap-ok: hconf h and
tconf: $P, h \vdash t \checkmark$ and
 Φ -pc: $\Phi\ C\ M!pc = Some\ (ST, LT)$ and
stk: $P, h \vdash stk [:\leq] ST$ and $loc: P, h \vdash loc [:\leq_{\top}] LT$ and
pc: $pc < size\ ins$ and
frame: $conf-f\ P\ h\ (ST, LT)\ ins\ (stk, loc, C, M, pc)$ and
frames: $conf-fs\ P\ h\ \Phi\ M\ (size\ Ts)\ T\ frs$ and
preh: preallocated h
by (*auto dest: sees-method-fun*)

from *ins wt Φ -pc* **have** *lST: length ST > 0* **by**(*auto*)

show *?thesis*

proof(*cases hd stk = Null*)

case *True*

with *ins no-x heap-ok tconf Φ -pc stk loc frame frames meth wf-preallocatedD[OF wf, of h Null-Pointer] preh*

show *?thesis* **by**(*fastforce*)

next

case *False*

note *stkNN = this*

have *STNN: hd ST \neq NT*

proof

assume *hd ST = NT*

moreover

from *frame* **have** $P, h \vdash stk [:\leq] ST$ **by** *simp*

with *lST* **have** $P, h \vdash hd\ stk :\leq\ hd\ ST$

by (*cases ST, auto*)

ultimately
 have $hd\ stk = Null$ by *simp*
 with *stkNN* show *False* by *contradiction*
 qed

with *stkNN* ins Φ -pc wt obtain $ST''\ X\ ST'\ LT'$ where
 $ST: ST = X \# ST''$ and
 $refT: is-refT\ X$ and
 $pc': pc+1 < size\ ins$ and
 $\Phi': \Phi\ C\ M!\ (pc+1) = Some\ (ST',\ LT')$ and
 $ST': P \vdash ST''\ [:\leq]\ ST'$ and $LT': P \vdash LT\ [:\leq_{\top}]\ LT'$ and
 $suc-pc: pc+1 < size\ ins$
 by(*fastforce*)

from *stk* ST obtain *ref* stk' where
 $stk': stk = ref\#\ stk'$ and
 $ref: P, h \vdash ref\ [:\leq]\ X$
 by *auto*

from *stkNN* stk' have $ref \neq Null$ by(*simp*)

moreover

from *loc* LT' have $P, h \vdash loc\ [:\leq_{\top}]\ LT' ..$

moreover

from $ST\ stk\ stk'\ ST'$

have $P, h \vdash stk'\ [:\leq]\ ST'$ by(*auto*)

ultimately show *?thesis* using *meth* Φ' *heap-ok* Φ -pc *suc-pc* frames *loc* LT' *no-x* ins $stk'\ ST'$

tconf *preh*

by(*fastforce*)

qed

qed

lemma *MExit-correct*:

assumes *wf*: *wf-prog* wt P

assumes *meth*: $P \vdash C\ sees\ M: Ts \rightarrow T = [(m\ x_s, m\ x_l_0, ins, xt)]$ in C

assumes *ins*: $ins!pc = MExit$

assumes *wt*: $P, T, m\ x_s, size\ ins, xt \vdash ins!pc, pc :: \Phi\ C\ M$

assumes *conf*: $\Phi \vdash t: (None, h, (stk, loc, C, M, pc) \# frs) \checkmark$

assumes *no-x*: $(tas, \sigma) \in exec-instr\ (ins!pc)\ P\ t\ h\ stk\ loc\ C\ M\ pc\ frs$

shows $\Phi \vdash t: \sigma \checkmark$

proof –

from *ins* *conf* *meth*

obtain $ST\ LT$ where

heap-ok: *hconf* h and

tconf: $P, h \vdash t \checkmark t$ and

Φ -pc: $\Phi\ C\ M!pc = Some\ (ST, LT)$ and

stk: $P, h \vdash stk\ [:\leq]\ ST$ and *loc*: $P, h \vdash loc\ [:\leq_{\top}]\ LT$ and

pc: $pc < size\ ins$ and

frame: *conf-f* $P\ h\ (ST, LT)\ ins\ (stk, loc, C, M, pc)$ and

frames: *conf-fs* $P\ h\ \Phi\ M\ (size\ Ts)\ T\ frs$ and

preh: *preallocated* h

by (*auto* *dest*: *sees-method-fun*)

from *ins* wt Φ -pc have *lST*: $length\ ST > 0$ by(*auto*)

```

show ?thesis
proof(cases hd stk = Null)
  case True
    with ins no-x heap-ok tconf  $\Phi$ -pc stk loc frame frames meth wf-preallocatedD[OF wf, of h Null-Pointer] preh
    show ?thesis by(fastforce)
  next
    case False
    note stkNN = this
    have STNN: hd ST  $\neq$  NT
    proof
      assume hd ST = NT
      moreover
        from frame have P,h  $\vdash$  stk  $[:\leq]$  ST by simp
        with lST have P,h  $\vdash$  hd stk  $:\leq$  hd ST
          by (cases ST, auto)
        ultimately
          have hd stk = Null by simp
          with stkNN show False by contradiction
    qed

with stkNN ins  $\Phi$ -pc wt obtain ST'' X ST' LT' where
  ST: ST = X # ST'' and
  refT: is-refT X and
  pc': pc+1 < size ins and
   $\Phi'$ :  $\Phi$  C M ! (pc+1) = Some (ST', LT') and
  ST': P  $\vdash$  ST''  $[:\leq]$  ST' and LT': P  $\vdash$  LT  $[:\leq_{\top}]$  LT' and
  suc-pc: pc+1 < size ins
  by(fastforce)

from stk ST obtain ref stk' where
  stk': stk = ref # stk' and
  ref: P,h  $\vdash$  ref  $:\leq$  X
  by auto

from stkNN stk' have ref  $\neq$  Null by(simp)
moreover
from loc LT' have P,h  $\vdash$  loc  $[:\leq_{\top}]$  LT' ..
moreover
from ST stk stk' ST'
have P,h  $\vdash$  stk'  $[:\leq]$  ST' by(auto)
ultimately
show ?thesis using meth  $\Phi'$  heap-ok  $\Phi$ -pc suc-pc frames loc LT' no-x ins stk' ST' tconf frame preh
  wf-preallocatedD[OF wf, of h IllegalMonitorState]
  by(fastforce)
qed
qed

```

The next theorem collects the results of the sections above, i.e. exception handling and the execution step for each instruction. It states type safety for single step execution: in welltyped programs, a conforming state is transformed into another conforming state when one instruction is executed.

theorem instr-correct:
 \llbracket wf-jvm-prog Φ P;

```

  P ⊢ C sees M:Ts→T=[(mxs,mxl0,ins,xt)] in C;
  (tas, σ') ∈ exec P t (None, h, (stk,loc,C,M,pc)#frs);
  Φ ⊢ t: (None, h, (stk,loc,C,M,pc)#frs)√ ]
⇒ Φ ⊢ t: σ'√
apply (subgoal-tac P,T,mxs,size ins,xt ⊢ ins!pc,pc :: Φ C M)
prefer 2
apply (erule wt-jvm-prog-impl-wt-instr, assumption)
apply clarsimp
apply (drule (1) sees-method-fun)
apply simp
apply(unfold exec.simps Let-def set-map)
apply (frule wt-jvm-progD, erule exE)
apply (cases ins ! pc)
apply (rule Load-correct, assumption+, fastforce)
apply (rule Store-correct, assumption+, fastforce)
apply (rule Push-correct, assumption+, fastforce)
apply (rule New-correct, assumption+, fastforce)
apply (rule NewArray-correct, assumption+, fastforce)
apply (rule ALoad-correct, assumption+, fastforce)
apply (rule AStore-correct, assumption+, fastforce)
apply (rule ALength-correct, assumption+, fastforce)
apply (rule Getfield-correct, assumption+, fastforce)
apply (rule Putfield-correct, assumption+, fastforce)
apply (rule CAS-correct, assumption+, fastforce)
apply (rule Checkcast-correct, assumption+, fastforce)
apply (rule Instanceof-correct, assumption+, fastforce)
apply (rule Invoke-correct, assumption+, fastforce)
apply (rule Return-correct, assumption+, fastforce simp add: split-beta)
apply (rule Pop-correct, assumption+, fastforce)
apply (rule Dup-correct, assumption+, fastforce)
apply (rule Swap-correct, assumption+, fastforce)
apply (rule BinOp-correct, assumption+, fastforce)
apply (rule Goto-correct, assumption+, fastforce)
apply (rule IfFalse-correct, assumption+, fastforce)
apply (rule Throw-correct, assumption+, fastforce)
apply (rule MEnter-correct, assumption+, fastforce)
apply (rule MExit-correct, assumption+, fastforce)
done

declare defs1 [simp del]

end

```

6.5.4 Main

```

lemma (in JVM-conf-read) BV-correct-1 [rule-format]:
  ∧σ. [ wf-jvm-progΦ P; Φ ⊢ t: σ√ ] ⇒ P,t ⊢ σ -tas-jvm→ σ' → Φ ⊢ t: σ'√
apply (simp only: split-tupled-all exec-1-iff)
apply (rename-tac xp h frs)
apply (case-tac xp)
apply (case-tac frs)
apply simp
apply (simp only: split-tupled-all)
apply hypsubst

```

```

apply (frule correct-state-impl-Some-method)
apply clarify
apply (rule instr-correct)
apply assumption+
apply clarify
apply(case-tac frs)
apply simp
apply(clarsimp simp only: exec.simps set-simps)
apply(erule (1) exception-step-conform)
done

```

theorem (*in JVM-progress*) *progress*:

```

assumes wt: wf-jvm-prog  $\Phi$  P
and cs:  $\Phi \vdash t: (xcp, h, f \# frs) \surd$ 
shows  $\exists ta \sigma'. P, t \vdash (xcp, h, f \# frs) \text{--}ta\text{--}jvm \rightarrow \sigma'$ 

```

proof –

```

obtain stk loc C M pc where f: f = (stk, loc, C, M, pc) by(cases f)
with cs obtain Ts T mxs mxl0 is xt ST LT
where hconf: hconf h
and sees: P  $\vdash$  C sees M: Ts  $\rightarrow$  T = [(mxs, mxl0, is, xt)] in C
and  $\Phi$ -pc:  $\Phi$  C M ! pc = [(ST, LT)]
and ST: P, h  $\vdash$  stk [: $\leq$ ] ST
and LT: P, h  $\vdash$  loc [: $\leq$  $\top$ ] LT
and pc: pc < length is
by(auto simp add: defs1)

```

show *?thesis*

proof(*cases xcp*)

```

case Some thus ?thesis
unfolding f exec-1-iff by auto

```

next

```

case [simp]: None
note [simp del] = split-paired-Ex
note [simp] = defs1 list-all2-Cons2

```

```

from wt obtain wf-md where wf: wf-prog wf-md P by(auto dest: wt-jvm-progD)
from wt sees pc have wt: P, T, mxs, size is, xt  $\vdash$  is!pc, pc ::  $\Phi$  C M
by(rule wt-jvm-prog-impl-wt-instr)

```

have $\exists ta \sigma'. (ta, \sigma') \in \text{exec-instr } (is \! pc) P t h \text{ stk loc } C M pc \text{ frs}$

proof(*cases is ! pc*)

case [*simp*]: *ALoad*

with *wt Φ -pc* **have** *lST: length ST > 1* **by**(*auto*)

show *?thesis*

proof(*cases hd (tl stk) = Null*)

case *True* **thus** *?thesis* **by** *simp*

next

case *False*

have *STNN: hd (tl ST) \neq NT*

proof

assume *hd (tl ST) = NT*

moreover

from *ST lST* **have** *P, h \vdash hd (tl stk) : \leq hd (tl ST)*

```

    by (cases ST)(auto, case-tac list, auto)
    ultimately have hd (tl stk) = Null by simp
    with False show False by contradiction
qed

with False  $\Phi$ -pc wt obtain ST'' X where ST = Integer # X[] # ST'' by auto
with ST obtain ref idx stk' where stk': stk = idx#ref#stk' and idx: P,h  $\vdash$  idx  $\leq$  Integer
and ref: P,h  $\vdash$  ref  $\leq$  X[] by(auto)

from False stk' have ref  $\neq$  Null by(simp)
with ref obtain a Xel n where a: ref = Addr a
and ha: typeof-addr h a = [Array-type Xel n]
and Xel: P  $\vdash$  Xel  $\leq$  X
by(cases ref)(fastforce simp add: conf-def widen-Array)+

from idx obtain idxI where idxI: idx = Intg idxI
by(auto simp add: conf-def)
show ?thesis
proof(cases 0  $\leq$  s idxI  $\wedge$  sint idxI < int n)
case True
hence si': 0  $\leq$  s idxI sint idxI < int n by auto
hence nat (sint idxI) < n
by (simp add: word-sle-eq nat-less-iff)
with ha have a!: P,h  $\vdash$  a@ACell (nat (sint idxI)) : Xel ..
from heap-read-total[OF hconf this] True False ha stk' idxI a
show ?thesis by auto
next
case False with ha stk' idxI a show ?thesis by auto
qed
qed
next
case [simp]: AStore
from wt  $\Phi$ -pc have lST: length ST > 2 by(auto)

show ?thesis
proof(cases hd (tl (tl stk)) = Null)
case True thus ?thesis by(fastforce)
next
case False
note stkNN = this
have STNN: hd (tl (tl ST))  $\neq$  NT
proof
assume hd (tl (tl ST)) = NT
moreover
from ST lST have P,h  $\vdash$  hd (tl (tl stk))  $\leq$  hd (tl (tl ST))
by (cases ST, auto, case-tac list, auto, case-tac lista, auto)
ultimately have hd (tl (tl stk)) = Null by simp
with stkNN show False by contradiction
qed

with stkNN  $\Phi$ -pc wt obtain ST'' Y X
where ST = Y # Integer # X[] # ST'' by(fastforce)

with ST obtain ref e idx stk' where stk': stk = e#idx#ref#stk'

```

```

and  $idx: P, h \vdash idx \leq Integer$  and  $ref: P, h \vdash ref \leq X[]$ 
and  $e: P, h \vdash e \leq Y$  by auto

from  $stkNN\ stk'$  have  $ref \neq Null$  by (simp)
with  $ref$  obtain  $a\ Xel\ n$  where  $a: ref = Addr\ a$ 
  and  $ha: typeof-addr\ h\ a = [Array-type\ Xel\ n]$ 
  and  $Xel: P \vdash Xel \leq X$ 
  by (cases\ ref)(fastforce\ simp\ add: conf-def\ widen-Array)+

from  $idx$  obtain  $idxI$  where  $idxI: idx = Intg\ idxI$ 
  by (auto\ simp\ add: conf-def)

show ?thesis
proof(cases\ 0 <=s\ idxI \wedge\ sint\ idxI < int\ n)
  case True
    hence  $si': 0 <=s\ idxI\ sint\ idxI < int\ n$  by simp-all
    hence  $nat\ (sint\ idxI) < n$ 
      by (simp\ add: word-sle-eq\ nat-less-iff)
    with  $ha$  have  $adal: P, h \vdash a@ACell\ (nat\ (sint\ idxI)) : Xel ..$ 

  show ?thesis
  proof(cases\ P \vdash the\ (typeof_h\ e) \leq Xel)
    case False
      with  $ha\ stk'\ idxI\ a$  show ?thesis by auto
    next
      case True
        hence  $P, h \vdash e \leq Xel$  using  $e$  by (auto\ simp\ add: conf-def)
        from heap-write-total[OF\ hconf\ adal\ this]  $ha\ stk'\ idxI\ a$  show ?thesis by auto
      qed
    next
      case False with  $ha\ stk'\ idxI\ a$  show ?thesis by auto
    qed
  qed
next
  case [simp]: (Getfield\ F\ D)

from  $\Phi\text{-pc}\ wt$  obtain  $oT\ ST''\ vT\ fm$  where  $oT: P \vdash oT \leq Class\ D$ 
  and  $ST = oT \# ST''$  and  $F: P \vdash D\ sees\ F:vT\ (fm)\ in\ D$ 
  by fastforce

with  $ST$  obtain  $ref\ stk'$  where  $stk': stk = ref \# stk'$ 
  and  $ref: P, h \vdash ref \leq oT$  by auto

show ?thesis
proof(cases\ ref = Null)
  case True thus ?thesis using  $stk'$  by auto
next
  case False
    from  $ref\ oT$  have  $P, h \vdash ref \leq Class\ D ..$ 
    with False obtain  $a\ U'\ D'$  where
       $a: ref = Addr\ a$  and  $h: typeof-addr\ h\ a = Some\ U'$ 
      and  $U': D' = class-type-of\ U'$  and  $D': P \vdash D' \preceq^* D$ 
      by (blast\ dest: non-npD2)

```



```

from  $D' F$  have has-field:  $P \vdash D'$  has  $F:vT$  (fm) in  $D$ 
  by (blast intro: has-field-mono has-visible-field)
with  $h$  have  $P, h \vdash a @ CField D F : vT$  unfolding  $U' ..$ 
from heap-read-total[ $OF$  hconf this]
show ?thesis using  $stk'$   $a$  by auto
qed
next
case [simp]: (Putfield  $F D$ )

from  $\Phi$ -pc wt obtain  $vT vT' oT ST''$  fm where  $ST = vT \# oT \# ST''$ 
  and field:  $P \vdash D$  sees  $F:vT'$  (fm) in  $D$ 
  and  $oT$ :  $P \vdash oT \leq Class D$ 
  and  $vT'$ :  $P \vdash vT \leq vT'$  by fastforce
with  $ST$  obtain  $v$  ref  $stk'$  where  $stk'$ :  $stk = v \# ref \# stk'$ 
  and ref:  $P, h \vdash ref : \leq oT$ 
  and  $v$ :  $P, h \vdash v : \leq vT$  by auto

show ?thesis
proof(cases ref = Null)
  case True with  $stk'$  show ?thesis by auto
next
  case False
  from ref oT have  $P, h \vdash ref : \leq Class D ..$ 
  with False obtain  $a U' D'$  where
     $a$ : ref = Addr a and  $h$ : typeof-addr h a = Some U' and
     $U'$ :  $D' = class-type-of U'$  and  $D'$ :  $P \vdash D' \preceq^* D$ 
    by (blast dest: non-npD2)

  from field D' have has-field:  $P \vdash D'$  has  $F:vT'$  (fm) in  $D$ 
    by (blast intro: has-field-mono has-visible-field)
  with  $h$  have  $a$ :  $P, h \vdash a @ CField D F : vT'$  unfolding  $U' ..$ 
  from  $v vT'$  have  $P, h \vdash v : \leq vT'$  by auto
  from heap-write-total[ $OF$  hconf a this]  $v a stk' h$  show ?thesis by auto
qed
next
case [simp]: (CAS  $F D$ )
from  $\Phi$ -pc wt obtain  $T' T1 T2 T3 ST''$  fm where  $ST = T3 \# T2 \# T1 \# ST''$ 
  and field:  $P \vdash D$  sees  $F:T'$  (fm) in  $D$ 
  and  $oT$ :  $P \vdash T1 \leq Class D$ 
  and  $vT'$ :  $P \vdash T2 \leq T' P \vdash T3 \leq T'$  by fastforce
with  $ST$  obtain  $v v' v''$   $stk'$  where  $stk'$ :  $stk = v'' \# v' \# v \# stk'$ 
  and  $v$ :  $P, h \vdash v : \leq T1$ 
  and  $v'$ :  $P, h \vdash v' : \leq T2$ 
  and  $v''$ :  $P, h \vdash v'' : \leq T3$  by auto
show ?thesis
proof(cases v = Null)
  case True with  $stk'$  show ?thesis by auto
next
  case False
  from  $v oT$  have  $P, h \vdash v : \leq Class D ..$ 
  with False obtain  $a U' D'$  where
     $a$ :  $v = Addr a$  and  $h$ : typeof-addr h a = Some U' and
     $U'$ :  $D' = class-type-of U'$  and  $D'$ :  $P \vdash D' \preceq^* D$ 
    by (blast dest: non-npD2)

```

```

from field D' have has-field: P ⊢ D' has F:T' (fm) in D
  by (blast intro: has-field-mono has-visible-field)
with h have al: P, h ⊢ a@CField D F : T' unfolding U' ..
from v' vT' have P, h ⊢ v' :≤ T' by auto
from heap-read-total[OF hconf al] obtain v''' where v''': heap-read h a (CField D F) v''' by
blast
show ?thesis
proof(cases v''' = v')
  case True
    from v'' vT' have P, h ⊢ v'' :≤ T' by auto
    from heap-write-total[OF hconf al this] v a stk' h v''' True show ?thesis by auto
  next
    case False
    from v''' v a stk' h False show ?thesis by auto
  qed
qed
next
case [simp]: (Invoke M' n)

from wt Φ-pc have n: n < size ST by simp

show ?thesis
proof(cases stk!n = Null)
  case True thus ?thesis by simp
next
  case False
  note Null = this
  have NT: ST!n ≠ NT
  proof
    assume ST!n = NT
    moreover from ST n have P, h ⊢ stk!n :≤ ST!n by (simp add: list-all2-conv-all-nth)
    ultimately have stk!n = Null by simp
    with Null show False by contradiction
  qed

from NT wt Φ-pc obtain D D' Ts T m
  where D: class-type-of' (ST!n) = Some D
  and m-D: P ⊢ D sees M': Ts → T = m in D'
  and Ts: P ⊢ rev (take n ST) [≤] Ts
  by auto

from n ST D have P, h ⊢ stk!n :≤ ST!n
  by (auto simp add: list-all2-conv-all-nth)

from ⟨P, h ⊢ stk!n :≤ ST!n⟩ Null D
obtain a T' where
  Addr: stk!n = Addr a and
  obj: typeof-addr h a = Some T' and
  T'subSTn: P ⊢ ty-of-htype T' ≤ ST ! n
  by(cases stk ! n)(auto simp add: conf-def widen-Class)

from D T'subSTn obtain C' where
  C': class-type-of' (ty-of-htype T') = [C'] and C'subD: P ⊢ C' ≤* D

```

```

by(rule widen-is-class-type-of) simp

from Call-lemma[OF m-D C'subD wf]
obtain D' Ts' T' m'
  where Call': P ⊢ C' sees M': Ts' → T' = m' in D' P ⊢ Ts [≤] Ts'
    P ⊢ T' ≤ T P ⊢ C' ≲* D' is-type P T' ∀ T ∈ set Ts'. is-type P T
  by blast

show ?thesis
proof(cases m')
  case Some with Call' C' obj Addr C' C'subD show ?thesis by(auto)
next
  case [simp]: None
  from ST have P, h ⊢ take n stk [≤] take n ST by(rule list-all2-takeI)
  then obtain Us where map typeofh (take n stk) = map Some Us P ⊢ Us [≤] take n ST
    by(auto simp add: confs-conv-map)
  hence Us: map typeofh (rev (take n stk)) = map Some (rev Us) P ⊢ rev Us [≤] rev (take n
ST)

  by- (simp only: rev-map[symmetric], simp)
  with Ts ⟨P ⊢ Ts [≤] Ts'⟩ have P ⊢ rev Us [≤] Ts' by(blast intro: widens-trans)
  with obj Us Call' C' have P, h ⊢ a.M'(rev (take n stk)) : T'
    by(auto intro!: external-WT'.intros)
  from external-call-progress[OF wf this hconf, of t] obj Addr Call' C'
  show ?thesis by(auto dest!: red-external-imp-red-external-aggr)
qed
qed
qed(auto 4 4 simp add: split-beta split: if-split-asm)
thus ?thesis using sees None
  unfolding f exec-1-iff by(simp del: split-paired-Ex)
qed
qed

```

lemma (in JVM-heap-conf) BV-correct-initial:

```

shows [ wf-jvm-progΦ P; start-heap-ok; P ⊢ C sees M: Ts → T = [m] in D; P, start-heap ⊢ vs [≤]
Ts ]
⇒ Φ ⊢ start-tid: JVM-start-state' P C M vs √
  apply (cases m)
  apply (unfold JVM-start-state'-def)
  apply (unfold correct-state-def)
  apply (clarsimp)
  apply (frule wt-jvm-progD)
  apply (erule exE)
  apply (frule wf-prog-wf-syscls)
  apply (rule conjI)
  apply (erule (1) tconf-start-heap-start-tid)
  apply (rule conjI)
  apply (simp add: wf-jvm-prog-phi-def hconf-start-heap)
  apply (frule sees-method-idemp)
  apply (frule wt-jvm-prog-impl-wt-start, assumption+)
  apply (unfold conf-f-def wt-start-def)
  apply (auto simp add: sup-state-opt-any-Some)
  apply (erule preallocated-start-heap)
  apply (rule exI conjI | assumption)+
  apply (auto simp add: list-all2-append1)

```

```

  apply(auto dest: list-all2-lengthD intro!: exI)
done

```

```

end

```

6.6 Welltyped Programs produce no Type Errors

```

theory BVNoTypeError

```

```

imports

```

```

  ../JVM/JVMDefensive

```

```

  BVSpecTypeSafe

```

```

begin

```

```

lemma wt-jvm-prog-states:

```

```

  [[ wf-jvm-prog $\Phi$  P; P  $\vdash$  C sees M:  $Ts \rightarrow T = [(max, mxl, ins, et)]$  in C;

```

```

     $\Phi$  C M ! pc =  $\tau$ ; pc < size ins ]]

```

```

   $\implies$  OK  $\tau \in$  states P max (1+size Ts+mxl)

```

```

context JVM-heap-conf-base' begin

```

```

declare is-IntI [simp, intro]

```

```

declare is-BoolI [simp, intro]

```

```

declare is-RefI [simp]

```

The main theorem: welltyped programs do not produce type errors if they are started in a conformant state.

```

theorem no-type-error:

```

```

  assumes welltyped: wf-jvm-prog $\Phi$  P and conforms:  $\Phi \vdash t:\sigma \checkmark$ 

```

```

  shows exec-d P t  $\sigma \neq$  TypeError

```

6.7 Progress result for both of the multithreaded JVMs

```

theory BVProgressThreaded

```

```

imports

```

```

  ../Framework/FWProgress

```

```

  ../Framework/FWLTS

```

```

  BVNoTypeError

```

```

  ../JVM/JVMThreaded

```

```

begin

```

```

lemma (in JVM-heap-conf-base') mexec-eq-mexecd:

```

```

  [[ wf-jvm-prog $\Phi$  P;  $\Phi \vdash t: (xcp, h, frs) \checkmark$  ]]  $\implies$  mexec P t ((xcp, frs), h) = mexecd P t ((xcp, frs), h)

```

```

apply(auto intro!: ext)

```

```

apply(unfold exec-1-iff)

```

```

apply(drule no-type-error)

```

```

  apply(assumption)

```

```

apply(clarify)

```

```

apply(rule exec-1-d-NormalI)

```

```

  apply(assumption)

```

```

apply(simp add: exec-d-def split: if-split-asm)

```

```

apply(erule jvmd-NormalE, auto)

```

```

done

```

context *JVM-heap-conf-base* **begin**

abbreviation

correct-state-ts :: *typ* \Rightarrow ('*addr*, '*thread-id*, '*addr jvm-thread-state*) *thread-info* \Rightarrow '*heap* \Rightarrow *bool*

where

correct-state-ts $\Phi \equiv$ *ts-ok* (λt (*xcp*, *frstls*) *h*. $\Phi \vdash t$: (*xcp*, *h*, *frstls*) \surd)

lemma *correct-state-ts-thread-conf*:

correct-state-ts Φ (*thr s*) (*shr s*) \Longrightarrow *thread-conf* *P* (*thr s*) (*shr s*)

by(*erule ts-ok-mono*)(*auto simp add: correct-state-def*)

lemma *invoke-new-thread*:

assumes *wf-jvm-prog* Φ *P*

and $P \vdash C$ *sees* $M: Ts \rightarrow T = [(m\ x s, m\ x l 0, i\ n s, x\ t)]$ *in* *C*

and $i\ n s \ ! \ p\ c =$ *Invoke* *Type.start* 0

and $P, T, m\ x s, s\ i\ z\ e \ i\ n s, x\ t \vdash i\ n s \ ! \ p\ c, p\ c :: \Phi \ C \ M$

and $\Phi \vdash t$: (*None*, *h*, (*stk*, *loc*, *C*, *M*, *pc*) $\#$ *frs*) \surd

and *typeof-addr* *h* (*thread-id2addr* *a*) = [*Class-type* *D*]

and $P \vdash D \preceq^* \text{Thread}$

and $P \vdash D$ *sees* $run: [] \rightarrow \text{Void} = [(m\ x s', m\ x l 0', i\ n s', x\ t')]$ *in* D'

shows $\Phi \vdash a$: (*None*, *h*, ([], *Addr* (*thread-id2addr* *a*) $\#$ *replicate* *m\ x l 0'* *undefined-value*, D' , *run*, 0)) \surd

proof –

from $\langle \Phi \vdash t$: (*None*, *h*, (*stk*, *loc*, *C*, *M*, *pc*) $\#$ *frs*) \surd \rangle

have *hconf* *h* **and** *preallocated* *h* **by**(*simp-all add: correct-state-def*)

moreover

from $\langle P \vdash D$ *sees* $run: [] \rightarrow \text{Void} = [(m\ x s', m\ x l 0', i\ n s', x\ t')]$ *in* D' \rangle

have $P \vdash D'$ *sees* $run: [] \rightarrow \text{Void} = [(m\ x s', m\ x l 0', i\ n s', x\ t')]$ *in* D'

by(*rule sees-method-idemp*)

with $\langle \text{wf-jvm-prog}\Phi \ P \rangle$

have *wt-start* $P \ D' \ [] \ m\ x l 0' (\Phi \ D' \ run)$ **and** $i\ n s' \neq []$

by(*auto dest: wt-jvm-prog-impl-wt-start*)

then obtain LT' **where** $LT': \Phi \ D' \ run \ ! \ 0 = \text{Some} ([], LT')$

by (*clarsimp simp add: wt-start-def defs1 sup-state-opt-any-Some*)

moreover

have *conf-f* $P \ h \ ([], LT') \ i\ n s' \ ([], \text{Addr} (\text{thread-id2addr} \ a) \ \# \ \text{replicate} \ m\ x l 0' \ \text{undefined-value}, D', \text{run}, 0)$

proof –

let $?LT = \text{OK} (\text{Class} \ D') \ \# \ (\text{replicate} \ m\ x l 0' \ \text{Err})$

have $P, h \vdash \text{replicate} \ m\ x l 0' \ \text{undefined-value} \ [:\leq_{\top}] \ \text{replicate} \ m\ x l 0' \ \text{Err}$ **by** *simp*

also from $\langle P \vdash D$ *sees* $run: [] \rightarrow \text{Void} = [(m\ x s', m\ x l 0', i\ n s', x\ t')]$ *in* D' \rangle

have $P \vdash D \preceq^* D'$ **by**(*rule sees-method-decl-above*)

with $\langle \text{typeof-addr} \ h \ (\text{thread-id2addr} \ a) = [\text{Class-type} \ D] \rangle$

have $P, h \vdash \text{Addr} (\text{thread-id2addr} \ a) \ :\leq \ \text{Class} \ D'$

by(*simp add: conf-def*)

ultimately have $P, h \vdash \text{Addr} (\text{thread-id2addr} \ a) \ \# \ \text{replicate} \ m\ x l 0' \ \text{undefined-value} \ [:\leq_{\top}] \ ?LT$

by(*simp*)

also from $\langle \text{wt-start} \ P \ D' \ [] \ m\ x l 0' \ (\Phi \ D' \ run) \rangle \ LT'$

have $P \vdash \dots [:\leq_{\top}] \ LT'$ **by**(*simp add: wt-start-def*)

finally have $P, h \vdash \text{Addr} (\text{thread-id2addr} \ a) \ \# \ \text{replicate} \ m\ x l 0' \ \text{undefined-value} \ [:\leq_{\top}] \ LT'$.

with $\langle i\ n s' \neq [] \rangle$ **show** $?thesis$ **by**(*simp add: conf-f-def*)

qed

moreover from $\langle \text{typeof-addr} \ h \ (\text{thread-id2addr} \ a) = [\text{Class-type} \ D] \rangle \langle P \vdash D \preceq^* \text{Thread} \rangle$

have $P, h \vdash a \sqrt{t}$ **by**(rule tconfI)
ultimately show $?thesis$ **using** $\langle P \vdash D' \text{ sees run: } [] \rightarrow Void = [(m\acute{x}s', m\acute{x}l0', ins', xt')] \text{ in } D' \rangle$
by(fastforce simp add: correct-state-def)
qed

lemma *exec-new-threadE*:

assumes $wf\text{-jvm-prog}_{\Phi} P$
and $P, t \vdash Normal \sigma \text{ --ta--jvmd} \rightarrow Normal \sigma'$
and $\Phi \vdash t: \sigma \sqrt{\quad}$
and $\{\!\{ta}\!\}_t \neq []$
obtains $h \text{ frs } a \text{ stk } loc \ C \ M \ pc \ Ts \ T \ m\acute{x}s \ m\acute{x}l0 \ ins \ xt \ M' \ n \ Ta \ ta' \ va \ Us \ Us' \ U \ m' \ D'$
where $\sigma = (None, h, (stk, loc, C, M, pc) \# frs)$
and $(ta, \sigma') \in exec \ P \ t \ (None, h, (stk, loc, C, M, pc) \# frs)$
and $P \vdash C \text{ sees } M: Ts \rightarrow T = [(m\acute{x}s, m\acute{x}l0, ins, xt)] \text{ in } C$
and $stk \ ! \ n = Addr \ a$
and $ins \ ! \ pc = Invoke \ M' \ n$
and $n < length \ stk$
and $typeof\text{-addr} \ h \ a = [Ta]$
and $is\text{-native} \ P \ Ta \ M'$
and $ta = extTA2JVM \ P \ ta'$
and $\sigma' = extRet2JVM \ n \ m' \ stk \ loc \ C \ M \ pc \ frs \ va$
and $(ta', va, m') \in red\text{-external-aggr} \ P \ t \ a \ M' \ (rev \ (take \ n \ stk)) \ h$
and $map \ typeof_h \ (rev \ (take \ n \ stk)) = map \ Some \ Us$
and $P \vdash class\text{-type-of} \ Ta \text{ sees } M': Us' \rightarrow U = Native \text{ in } D'$
and $D'.M'(Us') :: U$
and $P \vdash Us \ [\leq] \ Us'$

proof –

from $\langle P, t \vdash Normal \sigma \text{ --ta--jvmd} \rightarrow Normal \sigma' \rangle$ **obtain** $h \ f \ Frs \ xcp$
where $check: check \ P \ \sigma$
and $exec: (ta, \sigma') \in exec \ P \ t \ \sigma$
and $[simp]: \sigma = (xcp, h, f \# Frs)$
by(rule jvmd-NormalE)
obtain $stk \ loc \ C \ M \ pc$ **where** $[simp]: f = (stk, loc, C, M, pc)$
by(cases f, blast)
from $\langle \{\!\{ta}\!\}_t \neq [] \rangle$ **exec have** $[simp]: xcp = None$ **by**(cases xcp) **auto**
from $\langle \Phi \vdash t: \sigma \sqrt{\quad} \rangle$
obtain $Ts \ T \ m\acute{x}s \ m\acute{x}l0 \ ins \ xt \ ST \ LT$
where $hconf \ h \ preallocated \ h$
and $sees: P \vdash C \text{ sees } M: Ts \rightarrow T = [(m\acute{x}s, m\acute{x}l0, ins, xt)] \text{ in } C$
and $\Phi \ C \ M \ ! \ pc = [(ST, LT)]$
and $conf\text{-f} \ P \ h \ (ST, LT) \ ins \ (stk, loc, C, M, pc)$
and $conf\text{-fs} \ P \ h \ \Phi \ M \ (length \ Ts) \ T \ Frs$
by(fastforce simp add: correct-state-def)
from $check \ \langle \Phi \ C \ M \ ! \ pc = [(ST, LT)] \rangle$ **sees**
have $checkins: check\text{-instr} \ (ins \ ! \ pc) \ P \ h \ stk \ loc \ C \ M \ pc \ Frs$
by(clarsimp simp add: check-def)
from $sees \ \langle \{\!\{ta}\!\}_t \neq [] \rangle$ **exec obtain** $M' \ n$ **where** $[simp]: ins \ ! \ pc = Invoke \ M' \ n$
by(cases ins ! pc, auto split: if-split-asm simp add: split-beta ta-upd-simps)
from $\langle wf\text{-jvm-prog}_{\Phi} \ P \rangle$ **obtain** $wfmd$ **where** $wfp: wf\text{-prog} \ wfmd \ P$ **by**(auto dest: wt-jvm-progD)

from $checkins$ **have** $n < length \ stk$ $is\text{-Ref} \ (stk \ ! \ n)$ **by** auto
moreover from $exec \ sees \ \langle \{\!\{ta}\!\}_t \neq [] \rangle$ **have** $stk \ ! \ n \neq Null$ **by** auto
with $\langle is\text{-Ref} \ (stk \ ! \ n) \rangle$ **obtain** a **where** $stk \ ! \ n = Addr \ a$
by(auto simp add: is-Ref-def elim: is-AddrE)

moreover with *checkins* **obtain** Ta **where** $Ta: \text{typeof-addr } h \ a = \lfloor Ta \rfloor$ **by**(*fastforce*)
moreover with *checkins* *exec sees* $\langle n < \text{length } stk \rangle \langle \{ta\}_t \neq [] \rangle \langle stk ! n = \text{Addr } a \rangle$
obtain $Us \ Us' \ U \ D'$ **where** $\text{map } \text{typeof}_h (\text{rev } (\text{take } n \ stk)) = \text{map } \text{Some } Us$
and $P \vdash \text{class-type-of } Ta \text{ sees } M':Us' \rightarrow U = \text{Native in } D' \text{ and } D'.M'(Us') :: U$
and $P \vdash Us \leq Us'$
by(*auto simp add: confs-conv-map min-def split-beta has-method-def external-WT'-iff split: if-split-asm*)
moreover with $\langle \text{typeof-addr } h \ a = \lfloor Ta \rfloor \rangle \langle n < \text{length } stk \rangle$ *exec sees* $\langle stk ! n = \text{Addr } a \rangle$
obtain $ta' \ va \ h'$ **where** $ta = \text{extTA2JVM } P \ ta' \ \sigma' = \text{extRet2JVM } n \ h' \ stk \ \text{loc } C \ M \ pc \ Frs \ va$
 $(ta', va, h') \in \text{red-external-aggr } P \ t \ a \ M' (\text{rev } (\text{take } n \ stk)) \ h$
by(*fastforce simp add: min-def*)
ultimately show thesis using *exec sees*
by-(*rule that, auto intro!: is-native.intros*)
qed
end
context *JVM-conf-read* **begin**
lemma *correct-state-new-thread*:
assumes $wf: wf\text{-jvm-prog} \Phi \ P$
and $\text{red}: P, t \vdash \text{Normal } \sigma \text{ -ta-jvmd} \rightarrow \text{Normal } \sigma'$
and $cs: \Phi \vdash t: \sigma \checkmark$
and $nt: \text{NewThread } t'' (xcp, frs) \ h'' \in \text{set } \{ta\}_t$
shows $\Phi \vdash t'': (xcp, h'', frs) \checkmark$
proof –
from wf **obtain** wt **where** $wfp: wf\text{-prog } wt \ P$ **by**(*blast dest: wt-jvm-progD*)
from nt **have** $\{ta\}_t \neq []$ **by** *auto*
with $wf \ \text{red} \ cs$
obtain $h \ Frs \ a \ stk \ \text{loc } C \ M \ pc \ Ts \ T \ m\grave{x}s \ m\grave{x}l0 \ \text{ins} \ xt \ M' \ n \ Ta \ ta' \ va \ h' \ Us \ Us' \ U \ D'$
where $[simp]: \sigma = (\text{None}, h, (stk, \text{loc}, C, M, pc) \# Frs)$
and $\text{exec}: (ta, \sigma') \in \text{exec } P \ t \ (\text{None}, h, (stk, \text{loc}, C, M, pc) \# Frs)$
and $\text{sees}: P \vdash C \ \text{sees } M: Ts \rightarrow T = \lfloor (m\grave{x}s, m\grave{x}l0, \text{ins}, xt) \rfloor \text{ in } C$
and $[simp]: stk ! n = \text{Addr } a$
and $[simp]: \text{ins} ! pc = \text{Invoke } M' \ n$
and $n: n < \text{length } stk$
and $Ta: \text{typeof-addr } h \ a = \lfloor Ta \rfloor$
and $\text{iec}: \text{is-native } P \ Ta \ M'$
and $ta: ta = \text{extTA2JVM } P \ ta'$
and $\sigma': \sigma' = \text{extRet2JVM } n \ h' \ stk \ \text{loc } C \ M \ pc \ Frs \ va$
and $\text{rel}: (ta', va, h') \in \text{red-external-aggr } P \ t \ a \ M' (\text{rev } (\text{take } n \ stk)) \ h$
and $Us: \text{map } \text{typeof}_h (\text{rev } (\text{take } n \ stk)) = \text{map } \text{Some } Us$
and $w\text{text}: P \vdash \text{class-type-of } Ta \ \text{sees } M':Us' \rightarrow U = \text{Native in } D' \ D'.M'(Us') :: U$
and $\text{sub}: P \vdash Us \leq Us'$
by(*rule exec-new-threadE*)
from cs **have** $h\text{conf}: h\text{conf } h$ **and** $preh: \text{preallocated } h$
and $t\text{conf}: P, h \vdash t \checkmark$ **by**(*auto simp add: correct-state-def*)
from $Ta \ Us \ w\text{text} \ \text{sub}$ **have** $w\text{text}': P, h \vdash a.M'(\text{rev } (\text{take } n \ stk)) : U$
by(*auto intro!: external-WT'.intros*)
from rel **have** $\text{red}: P, t \vdash \langle a.M'(\text{rev } (\text{take } n \ stk)), h \rangle \text{-ta} \rightarrow \text{ext } \langle va, h' \rangle$
by(*unfold WT-red-external-list-conv[OF wfp wtext' tconf]*)
from $ta \ nt$ **obtain** $D \ M'' \ a'$ **where** $nt': \text{NewThread } t'' (D, M'', a') \ h'' \in \text{set } \{ta\}_t$
 $(xcp, frs) = \text{extNTA2JVM } P (D, M'', a')$ **by** *auto*
with red **have** $[simp]: h'' = h'$ **by**-(*rule red-ext-new-thread-heap*)
from *red-external-new-thread-sub-thread*[*OF red nt'(1)*]

have $h't''$: $\text{typeof-addr } h' a' = \lfloor \text{Class-type } D \rfloor P \vdash D \preceq^* \text{Thread}$ **and** $[\text{simp}]$: $M'' = \text{run}$ **by** *auto*
from *red-external-new-thread-exists-thread-object* $[OF \text{ red } nt'(1)]$
have $tconf'$: $P, h' \vdash t'' \sqrt{t}$ **by** $(\text{auto intro: } tconfI)$
from *sub-Thread-sees-run* $[OF \text{ wfp } \langle P \vdash D \preceq^* \text{Thread} \rangle]$ **obtain** $mxs' \ mxl0' \ ins' \ xt' \ D'$
where $seesrun$: $P \vdash D \text{ sees run: } [] \rightarrow \text{Void} = \lfloor (mxs', mxl0', ins', xt') \rfloor$ **in** D' **by** *auto*
with nt' **ta** nt **have** $xcp = \text{None}$ $frs = \lfloor ([], \text{Addr } a' \# \text{replicate } mxl0' \text{ undefined-value}, D', \text{run}, 0) \rfloor$
by $(\text{auto simp add: extNTA2JVM-def split-beta})$
moreover
have $\Phi \vdash t''$: $(\text{None}, h', \lfloor ([], \text{Addr } a' \# \text{replicate } mxl0' \text{ undefined-value}, D', \text{run}, 0) \rfloor) \sqrt{}$
proof –
from *red wtext'* $\langle hconf \ h \rangle$ **have** $hconf \ h'$
by $(\text{rule external-call-hconf})$
moreover from *red* **have** $h \leq h'$ **by** $(\text{rule red-external-heat})$
with *preh* **have** *preallocated* h' **by** $(\text{rule preallocated-heat})$
moreover from *seesrun*
have $seesrun'$: $P \vdash D' \text{ sees run: } [] \rightarrow \text{Void} = \lfloor (mxs', mxl0', ins', xt') \rfloor$ **in** D'
by $(\text{rule sees-method-idemp})$
moreover with $\langle \text{wf-jvm-prog}_{\Phi} \ P \rangle$
obtain $wt\text{-start } P \ D' \ [] \ mxl0' \ (\Phi \ D' \ \text{run}) \ ins' \neq []$
by $(\text{auto dest: wt-jvm-prog-impl-wt-start})$
then obtain LT' **where** $\Phi \ D' \ \text{run} ! 0 = \text{Some } ([], LT')$
by $(\text{clarsimp simp add: wt-start-def defs1 sup-state-opt-any-Some})$
moreover
have $conf\text{-f } P \ h' \ ([], LT') \ ins' \ ([], \text{Addr } a' \# \text{replicate } mxl0' \text{ undefined-value}, D', \text{run}, 0)$
proof –
let $?LT = OK \ (\text{Class } D') \# \ (\text{replicate } mxl0' \ \text{Err})$
from *seesrun* **have** $P \vdash D \preceq^* D'$ **by** $(\text{rule sees-method-decl-above})$
hence $P, h' \vdash \text{Addr } a' \# \text{replicate } mxl0' \text{ undefined-value} \lfloor \leq_{\top} \rfloor ?LT$
using $h't''$ **by** $(\text{simp add: conf-def})$
also from $\langle \text{wt-start } P \ D' \ [] \ mxl0' \ (\Phi \ D' \ \text{run}) \rangle \langle \Phi \ D' \ \text{run} ! 0 = \text{Some } ([], LT') \rangle$
have $P \vdash ?LT \lfloor \leq_{\top} \rfloor LT'$ **by** $(\text{simp add: wt-start-def})$
finally have $P, h' \vdash \text{Addr } a' \# \text{replicate } mxl0' \text{ undefined-value} \lfloor \leq_{\top} \rfloor LT'$.
with $\langle ins' \neq [] \rangle$ **show** $?thesis$ **by** $(\text{simp add: conf-f-def})$
qed
ultimately show $?thesis$ **using** $tconf'$ **by** $(\text{fastforce simp add: correct-state-def})$
qed
ultimately show $?thesis$ **by** (clarsimp)
qed

lemma *correct-state-heap-change*:

assumes $\text{wf: wf-jvm-prog}_{\Phi} \ P$
and $\text{red: } P, t \vdash \text{Normal } (xcp, h, frs) \text{ --ta--jvmd--} \rightarrow \text{Normal } (xcp', h', frs')$
and cs : $\Phi \vdash t$: $(xcp, h, frs) \sqrt{}$
and cs'' : $\Phi \vdash t''$: $(xcp'', h, frs'') \sqrt{}$
shows $\Phi \vdash t''$: $(xcp'', h', frs'') \sqrt{}$

proof $(\text{cases } xcp)$

case *None*

from cs **have** $P, h \vdash t \sqrt{t}$ **by** $(\text{simp add: correct-state-def})$
with *red* **have** *hext* $h \ h'$ **by** $(\text{auto intro: exec-1-d-hext simp add: tconf-def})$
from $\langle \text{wf-jvm-prog}_{\Phi} \ P \rangle$ cs *red* **have** $\Phi \vdash t$: $(xcp', h', frs') \sqrt{}$
by $(\text{auto elim!: jvmd-NormalE intro: BV-correct-1 simp add: exec-1-iff})$
from cs'' **have** $P, h \vdash t'' \sqrt{t}$ **by** $(\text{simp add: correct-state-def})$
with $\langle h \leq h' \rangle$ **have** $tconf'$: $P, h' \vdash t'' \sqrt{t}$ **by** $(\text{rule tconf-hext-mono})$


```

from  $\langle \Phi \vdash t: (xcp', h', frs') \checkmark \rangle$ 
have  $hconf'$ :  $hconf\ h'\ preallocated\ h'$  by(simp-all add: correct-state-def)

show ?thesis
proof(cases frs'')
  case Nil thus ?thesis using tconf' hconf' by(simp add: correct-state-def)
next
  case (Cons f'' Frs'')
  obtain  $stk''\ loc''\ C0''\ M0''\ pc''$ 
    where  $f'' = (stk'', loc'', C0'', M0'', pc'')$ 
    by(cases f'', blast)
  with  $\langle frs'' = f'' \# Frs'' \rangle\ cs''$ 
  obtain  $Ts''\ T''\ mxs''\ mxl_0''\ ins''\ xt''\ ST''\ LT''$ 
    where  $hconf\ h$ 
    and  $sees''$ :  $P \vdash C0''\ sees\ M0''$ :  $Ts'' \rightarrow T'' = [(mxs'', mxl_0'', ins'', xt'')] \text{ in } C0''$ 
    and  $\Phi\ C0''\ M0''! pc'' = [(ST'', LT'')]$ 
    and  $conf\text{-}f\ P\ h\ (ST'', LT'')\ ins''\ (stk'', loc'', C0'', M0'', pc'')$ 
    and  $conf\text{-}fs\ P\ h\ \Phi\ M0''\ (length\ Ts'')\ T''\ Frs''$ 
    by(fastforce simp add: correct-state-def)

  show ?thesis using Cons  $\langle \Phi \vdash t'': (xcp'', h, frs'') \checkmark \rangle\ \langle heat\ h\ h' \rangle\ hconf'\ tconf'$ 
    apply(cases xcp'')
    apply(auto simp add: correct-state-def)
    apply(blast dest: heat-objD intro: conf-fs-heat conf-f-heat)+
    done
  qed
next
  case (Some a)
  with  $\langle P, t \vdash Normal\ (xcp, h, frs) \text{ --ta--} jvmd \rightarrow Normal\ (xcp', h', frs') \rangle$ 
  have  $h = h'$  by(auto elim!: jvmd-NormalE)
  with  $\langle \Phi \vdash t'': (xcp'', h, frs'') \checkmark \rangle$  show ?thesis by simp
qed

lemma lifting-wf-correct-state-d:
   $wf\text{-}jvm\text{-}prog_{\Phi}\ P \implies lifting\text{-}wf\ JVM\text{-}final\ (mexecd\ P)\ (\lambda t\ (xcp, frs)\ h.\ \Phi \vdash t: (xcp, h, frs) \checkmark)$ 
by(unfold-locales)(auto intro: BV-correct-d-1 correct-state-new-thread correct-state-heap-change)

lemma lifting-wf-correct-state:
  assumes  $wf$ :  $wf\text{-}jvm\text{-}prog_{\Phi}\ P$ 
  shows  $lifting\text{-}wf\ JVM\text{-}final\ (mexec\ P)\ (\lambda t\ (xcp, frs)\ h.\ \Phi \vdash t: (xcp, h, frs) \checkmark)$ 
proof(unfold-locales)
  fix  $t\ x\ m\ ta\ x'\ m'$ 
  assume  $mexec\ P\ t\ (x, m)\ ta\ (x', m')$ 
  and  $(\lambda(xcp, frs)\ h.\ \Phi \vdash t: (xcp, h, frs) \checkmark)\ x\ m$ 
  with  $wf$  show  $(\lambda(xcp, frs)\ h.\ \Phi \vdash t: (xcp, h, frs) \checkmark)\ x'\ m'$ 
  by(cases x)(cases x', simp add: welltyped-commute[symmetric, OF  $\langle wf\text{-}jvm\text{-}prog_{\Phi}\ P \rangle]$ , rule BV-correct-d-1)
next
  fix  $t\ x\ m\ ta\ x'\ m'\ t''\ x''$ 
  assume  $mexec\ P\ t\ (x, m)\ ta\ (x', m')$ 
  and  $(\lambda(xcp, frs)\ h.\ \Phi \vdash t: (xcp, h, frs) \checkmark)\ x\ m$ 
  and  $NewThread\ t''\ x''\ m' \in set\ \{ta\}_t$ 
  with  $wf$  show  $(\lambda(xcp, frs)\ h.\ \Phi \vdash t'': (xcp, h, frs) \checkmark)\ x''\ m'$ 
  apply(cases x, cases x', cases x'', clarify, unfold welltyped-commute[symmetric, OF  $\langle wf\text{-}jvm\text{-}prog_{\Phi}\ P \rangle]$ )

```

```

  by(rule correct-state-new-thread)
next
  fix t x m ta x' m' t'' x''
  assume mexec P t (x, m) ta (x', m')
  and ( $\lambda(xcp, frs) h. \Phi \vdash t: (xcp, h, frs) \checkmark$ ) x m
  and ( $\lambda(xcp, frs) h. \Phi \vdash t'': (xcp, h, frs) \checkmark$ ) x'' m
  with wf show ( $\lambda(xcp, frs) h. \Phi \vdash t'': (xcp, h, frs) \checkmark$ ) x'' m'
  by(cases x)(cases x', cases x'', clarify, unfold welltyped-commute[symmetric, OF  $\langle wf-jvm-prog_{\Phi} P \rangle$ ], rule correct-state-heap-change)
qed

lemmas preserves-correct-state = FWLiftingSem.lifting-wf.RedT-preserves[OF lifting-wf-correct-state]
lemmas preserves-correct-state-d = FWLiftingSem.lifting-wf.RedT-preserves[OF lifting-wf-correct-state-d]

end

context JVM-heap-conf-base begin

definition correct-jvm-state ::  $typ \Rightarrow ('addr, 'thread-id, 'addr jvm-thread-state, 'heap, 'addr) state set$ 
where
  correct-jvm-state  $\Phi$ 
  = {s. correct-state-ts  $\Phi$  (thr s) (shr s)  $\wedge$  lock-thread-ok (locks s) (thr s)}

end

context JVM-heap-conf begin

lemma correct-jvm-state-initial:
  assumes wf: wf-jvm-prog $_{\Phi}$  P
  and wf-start: wf-start-state P C M vs
  shows JVM-start-state P C M vs  $\in$  correct-jvm-state  $\Phi$ 
proof -
  from wf-start obtain Ts T m D
  where start-heap-ok and P  $\vdash$  C sees M: Ts  $\rightarrow$  T = [m] in D
  and P, start-heap  $\vdash$  vs [ $\leq$ ] Ts by cases
  with wf BV-correct-initial[OF wf this] show ?thesis
  by(cases m)(auto simp add: correct-jvm-state-def start-state-def JVM-start-state'-def intro: lock-thread-okI
  ts-okI split: if-split-asm)
qed

end

context JVM-conf-read begin

lemma invariant3p-correct-jvm-state-mexecdT:
  assumes wf: wf-jvm-prog $_{\Phi}$  P
  shows invariant3p (mexecdT P) (correct-jvm-state  $\Phi$ )
unfolding correct-jvm-state-def
apply(rule invariant3pI)
apply safe
apply(erule (1) lifting-wf.RedT-preserves[OF lifting-wf-correct-state-d[OF wf]])
apply(erule (1) execd-mthr.RedT-preserves-lock-thread-ok)
done

```

lemma *invariant3p-correct-jvm-state-mexecT*:

assumes *wf*: *wf-jvm-prog* Φ *P*
shows *invariant3p* (*mexecT P*) (*correct-jvm-state* Φ)
unfolding *correct-jvm-state-def*
apply(*rule invariant3pI*)
apply *safe*
apply(*erule* (1) *lifting-wf.redT-preserves*[*OF lifting-wf-correct-state*[*OF wf*]])
apply(*erule* (1) *exec-mthr.redT-preserves-lock-thread-ok*)
done

lemma *correct-jvm-state-preserved*:

assumes *wf*: *wf-jvm-prog* Φ *P*
and *correct*: *s* \in *correct-jvm-state* Φ
and *red*: *P* \vdash *s* \rightarrow *ttas* \rightarrow *jvm** *s'*
shows *s'* \in *correct-jvm-state* Φ
using *wf red correct* **unfolding** *exec-mthr.RedT-def*
by(*rule invariant3p-rtrancl3p*[*OF invariant3p-correct-jvm-state-mexecT*])

theorem *jvm-typesafe*:

assumes *wf*: *wf-jvm-prog* Φ *P*
and *start*: *wf-start-state* *P C M vs*
and *exec*: *P* \vdash *JVM-start-state* *P C M vs* \rightarrow *ttas* \rightarrow *jvm** *s'*
shows *s'* \in *correct-jvm-state* Φ
by(*rule correct-jvm-state-preserved*[*OF wf - exec*])(*rule correct-jvm-state-initial*[*OF wf start*])

end

declare (**in** *JVM-typesafe*) *split-paired-Ex* [*simp del*]

context *JVM-heap-conf-base'* **begin**

lemma *execd-NewThread-Thread-Object*:

assumes *wf*: *wf-jvm-prog* Φ *P*
and *conf*: $\Phi \vdash t': \sigma \checkmark$
and *red*: *P, t'* \vdash *Normal* σ \rightarrow *ta-jvmd* \rightarrow *Normal* σ'
and *nt*: *NewThread* *t x m* \in *set* $\{ta\}_t$
shows $\exists C. \text{typeof-addr} (\text{fst} (\text{snd } \sigma')) (\text{thread-id2addr } t) = \lfloor \text{Class-type } C \rfloor \wedge P \vdash \text{Class } C \leq \text{Class } \text{Thread}$

proof –

from *wf* **obtain** *wfmd* **where** *wfp*: *wf-prog* *wfmd P* **by**(*blast dest: wt-jvm-progD*)

from *red* **obtain** *h f Frs xcp*

where *check*: *check* *P* σ

and *exec*: $(ta, \sigma') \in \text{exec } P t' \sigma$

and [*simp*]: $\sigma = (xcp, h, f \# Frs)$

by(*rule jvmd-NormalE*)

obtain *xcp' h' frs'* **where** [*simp*]: $\sigma' = (xcp', h', frs')$ **by**(*cases* σ' , *auto*)

obtain *stk loc C M pc* **where** [*simp*]: $f = (stk, loc, C, M, pc)$ **by**(*cases* *f*, *blast*)

from *exec nt* **have** [*simp*]: *xcp* = *None* **by**(*cases* *xcp*, *auto*)

from $\langle \Phi \vdash t': \sigma \checkmark \rangle$ **obtain** *Ts T mxs mxl0 ins xt ST LT*

where *hconf* *h*

and *P, h* $\vdash t' \checkmark$

and *sees*: *P* \vdash *C* *sees* *M*: $Ts \rightarrow T = \lfloor (mxs, mxl0, ins, xt) \rfloor$ *in* *C*

and $\Phi C M ! pc = \lfloor (ST, LT) \rfloor$

```

and conf-f  $P$   $h$  ( $ST$ ,  $LT$ ) ins ( $stk$ ,  $loc$ ,  $C$ ,  $M$ ,  $pc$ )
and conf-fs  $P$   $h$   $\Phi$   $M$  (length  $Ts$ )  $T$   $Frs$ 
by(fastforce simp add: correct-state-def)
from wf red conf nt
obtain  $h$   $frs$   $a$   $stk$   $loc$   $C$   $M$   $pc$   $M'$   $n$   $ta'$   $va$   $h'$ 
  where  $ha$ : typeof-addr  $h$   $a \neq None$  and  $ta$ :  $ta = extTA2JVM P ta'$ 
  and  $\sigma'$ :  $\sigma' = extRet2JVM n h' stk loc C M pc frs va$ 
  and  $rel$ :  $(ta', va, h') \in red-external-aggr P t' a M' (rev (take n stk)) h$ 
  by  $-(erule (2) exec-new-threadE, fastforce+)$ 
from  $nt$   $ta$  obtain  $x'$  where NewThread  $t$   $x'$   $m \in set \{\{ta'\}_t\}$  by auto
from red-external-aggr-new-thread-exists-thread-object[ $OF$   $rel$   $ha$   $this$ ]  $\sigma'$ 
show  $?thesis$  by(cases va) auto
qed

```

lemma *mexecdT-NewThread-Thread-Object*:

```

[[ wf-jvm-prog  $\Phi$   $P$ ; correct-state-ts  $\Phi$  ( $thr$   $s$ ) ( $shr$   $s$ );  $P \vdash s -t \triangleright ta \rightarrow_{jvmd} s'$ ; NewThread  $t$   $x$   $m \in set \{\{ta\}_t\}$  ]

```

```

 $\implies \exists C. \text{typeof-addr } (shr s') (thread-id2addr t) = [Class-type C] \wedge P \vdash C \preceq^* Thread$ 

```

```

apply(frule correct-state-ts-thread-conf)

```

```

apply(erule execd-mthr.redT.cases)

```

```

apply(hypsubst)

```

```

apply(frule (2) execd-tconf.redT-updTs-preserves[where  $ln' = redT-updLns (locks s) t'$  no-wait-locks  $\{\{ta\}_l\}$ ])

```

```

apply clarsimp

```

```

apply(clarsimp)

```

```

apply(drule execd-NewThread-Thread-Object)

```

```

  apply(drule (1) ts-okD)

```

```

  apply(fastforce)

```

```

  apply(assumption)

```

```

  apply(fastforce)

```

```

apply(clarsimp)

```

```

apply(simp)

```

```

done

```

```

end

```

```

context JVM-heap begin

```

lemma *exec-ta-satisfiable*:

```

assumes  $P, t \vdash s -ta-jvm \rightarrow s'$ 

```

```

shows  $\exists s. \text{exec-mthr.actions-ok } s t ta$ 

```

```

proof  $-$ 

```

```

obtain  $xcp$   $h$   $frs$  where [simp]:  $s = (xcp, h, frs)$  by(cases s)

```

```

from assms obtain  $stk$   $loc$   $C$   $M$   $pc$   $frs'$  where [simp]:  $frs = (stk, loc, C, M, pc) \# frs'$ 

```

```

  by(cases frs)(auto simp add: exec-1-iff)

```

```

show  $?thesis$ 

```

```

proof(cases xcp)

```

```

  case Some with assms show  $?thesis$  by(auto simp add: exec-1-iff lock-ok-las-def finfun-upd-apply split-paired-Ex)

```

```

  next

```

```

    case None

```

```

    with assms show  $?thesis$ 

```

```

      apply(cases instrs-of P C M ! pc)

```

```

      apply(auto simp add: exec-1-iff lock-ok-las-def finfun-upd-apply split-beta final-thread.actions-ok-iff)

```

```

split: if-split-asm dest: red-external-aggr-ta-satisfiable[where final=JVM-final])
  apply(fastforce simp add: final-thread.actions-ok-iff lock-ok-las-def dest: red-external-aggr-ta-satisfiable[where
final=JVM-final])
    apply(fastforce simp add: finfun-upd-apply intro: exI[where x=K$ None] exI[where x=K$ [(t,
0)]] may-lock.intros)+
    done
  qed
qed

end

context JVM-typesafe begin

lemma execd-wf-progress:
  assumes wf: wf-jvm-prog $\Phi$  P
  shows progress JVM-final (mexecd P) (execd-mthr.wset-Suspend-ok P (correct-jvm-state  $\Phi$ ))
  (is progress - - ?wf-state)
proof
  {
    fix s t x ta x' m' w
    assume mexecd: mexecd P t (x, shr s) ta (x', m')
      and Suspend: Suspend w  $\in$  set {ta}w
    from mexecd-Suspend-Invoke[OF mexecd Suspend]
    show  $\neg$  JVM-final x' by auto
  }
  note Suspend-final = this
  {
    fix s
    assume s: s  $\in$  ?wf-state
    hence lock-thread-ok (locks s) (thr s)
      by(auto dest: execd-mthr.wset-Suspend-okD1 simp add: correct-jvm-state-def)
    moreover
    have exec-mthr.wset-final-ok (wset s) (thr s)
    proof(rule exec-mthr.wset-final-okI)
      fix t w
      assume wset s t = [w]
      from execd-mthr.wset-Suspend-okD2[OF s this]
      obtain x0 ta x m1 w' ln'' and s0 :: ('addr, 'thread-id, 'addr option  $\times$  'addr frame list, 'heap,
'addr) state
      where mexecd: mexecd P t (x0, shr s0) ta (x, m1)
        and Suspend: Suspend w'  $\in$  set {ta}w
        and tst: thr s t = [(x, ln'')] by blast
      from Suspend-final[OF mexecd Suspend] tst
      show  $\exists$  x ln. thr s t = [(x, ln)]  $\wedge$   $\neg$  JVM-final x by blast
    qed
    ultimately show lock-thread-ok (locks s) (thr s)  $\wedge$  exec-mthr.wset-final-ok (wset s) (thr s) ..
  }
next
  fix s t x ta x' m'
  assume wfs: s  $\in$  ?wf-state
    and thr s t = [(x, no-wait-locks)]
    and mexecd P t (x, shr s) ta (x', m')
    and wait:  $\neg$  waiting (wset s t)
  moreover obtain ls ts h ws is where s [simp]: s = (ls, (ts, h), ws, is) by(cases s) fastforce

```

ultimately have $ts\ t = \llbracket(x, \text{no-wait-locks})\rrbracket\ \text{mexecd}\ P\ t\ (x, h)\ ta\ (x', m')$ **by** *auto*
from wfs **have** $\text{correct-state-ts}\ \Phi\ ts\ h$ **by** (*auto dest: execd-mthr.wset-Suspend-okD1 simp add: correct-jvm-state-def*)
from wf **obtain** $wfmd$ **where** $wfp: wf\text{-prog}\ wfmd\ P$ **by** (*auto dest: wt-jvm-progD*)

from $\langle ts\ t = \llbracket(x, \text{no-wait-locks})\rrbracket \rangle\ \langle \text{mexecd}\ P\ t\ (x, h)\ ta\ (x', m') \rangle$
obtain $xcp\ frs\ xcp'\ frs'$
where $P, t \vdash \text{Normal}\ (xcp, h, frs) \text{---}ta\text{---}jvmd \rightarrow \text{Normal}\ (xcp', m', frs')$
and $[simp]: x = (xcp, frs)\ x' = (xcp', frs')$
by (*cases x, auto*)
then obtain $f\ Frs$
where $check: check\ P\ (xcp, h, f\ \# Frs)$
and $[simp]: frs = f\ \# Frs$
and $exec: (ta, xcp', m', frs') \in exec\ P\ t\ (xcp, h, f\ \# Frs)$
by (*auto elim: jvmd-NormalE*)
with $\langle ts\ t = \llbracket(x, \text{no-wait-locks})\rrbracket \rangle\ \langle \text{correct-state-ts}\ \Phi\ ts\ h \rangle$
have $correct: \Phi \vdash t: (xcp, h, f\ \# Frs) \checkmark$ **by** (*auto dest: ts-okD*)
obtain $stk\ loc\ C\ M\ pc$ **where** $f\ [simp]: f = (stk, loc, C, M, pc)$ **by** (*cases f*)
from $correct$ **obtain** $Ts\ T\ mxs\ mxl0\ ins\ xt\ ST\ LT$
where $hconf: hconf\ h$
and $tconf: P, h \vdash t \checkmark$
and $sees: P \vdash C\ sees\ M: Ts \rightarrow T = \llbracket(mxs, mxl0, ins, xt)\rrbracket$ *in* C
and $wt: \Phi\ C\ M\ !\ pc = \llbracket(ST, LT)\rrbracket$
and $conf\text{-}f: conf\text{-}f\ P\ h\ (ST, LT)\ ins\ (stk, loc, C, M, pc)$
and $conf\text{-}fs: conf\text{-}fs\ P\ h\ \Phi\ M\ (\text{length}\ Ts)\ T\ Frs$
and $conf\text{-}xcp: conf\text{-}xcp\ P\ h\ xcp\ (ins\ !\ pc)$
and $preh: preallocated\ h$
by (*fastforce simp add: correct-state-def*)

have $\exists ta'\ \sigma'. P, t \vdash \text{Normal}\ (xcp, h, (stk, loc, C, M, pc)\ \# Frs) \text{---}ta'\text{---}jvmd \rightarrow \text{Normal}\ \sigma' \wedge$
 $(\text{final-thread.actions-ok}\ JVM\text{-final}\ (ls, (ts, h), ws, is)\ t\ ta' \vee$
 $\text{final-thread.actions-ok}'\ (ls, (ts, h), ws, is)\ t\ ta' \wedge \text{final-thread.actions-subset}\ ta'\ ta)$
proof (*cases final-thread.actions-ok' (ls, (ts, h), ws, is) t ta*)
case *True*
have $\text{final-thread.actions-subset}\ ta\ ta\ ..$
with $\text{True}\ \langle P, t \vdash \text{Normal}\ (xcp, h, frs) \text{---}ta\text{---}jvmd \rightarrow \text{Normal}\ (xcp', m', frs') \rangle$
show *?thesis* **by** *auto*
next
case *False*
note $naok = this$
have $ws: wset\ s\ t = \text{None} \vee$
 $(\exists n\ a\ T\ w. ins\ !\ pc = \text{Invoke}\ wait\ n \wedge stk\ !\ n = \text{Addr}\ a \wedge \text{typeof-addr}\ h\ a = [T] \wedge is\text{-native}$
 $P\ T\ wait \wedge wset\ s\ t = \llbracket\text{PostWS}\ w\rrbracket \wedge xcp = \text{None})$
proof (*cases wset s t*)
case *None* **thus** *?thesis* **..**
next
case (*Some w*)
from $\text{execd-mthr.wset-Suspend-okD2}[OF\ wfs\ this]\ \langle ts\ t = \llbracket(x, \text{no-wait-locks})\rrbracket \rangle$
obtain $xcp0\ frs0\ h0\ ta0\ w'\ s1\ tta1$
where $red0: \text{mexecd}\ P\ t\ ((xcp0, frs0), h0)\ ta0\ ((xcp, frs), shr\ s1)$
and $Suspend: Suspend\ w' \in \text{set}\ \{\!|ta0|\!\}_w$
and $s1: P \vdash s1 \text{---}tta1 \rightarrow jvmd^*\ s$
by *auto*
from $\text{mexecd-Suspend-Invoke}[OF\ red0\ Suspend]\ sees$

```

obtain  $n$   $a$   $T$  where  $[simp]: ins ! pc = Invoke$   $wait$   $n$   $xcp = None$   $stk ! n = Addr$   $a$ 
  and  $type: typeof-addr$   $h0$   $a = \lfloor T \rfloor$ 
  and  $iec: is-native$   $P$   $T$   $wait$ 
  by( $auto$   $simp$   $add: is-native.simps$ )  $blast$ 

from  $red0$  have  $h0 \sqsubseteq shr$   $s1$  by( $auto$   $dest: exec-1-d-heap$ )
also from  $s1$  have  $shr$   $s1 \sqsubseteq shr$   $s$  by( $rule$   $Execd-heap$ )
finally have  $typeof-addr$   $(shr$   $s)$   $a = \lfloor T \rfloor$  using  $type$ 
  by( $rule$   $typeof-addr-heap-mono$ )
moreover from  $Some$   $wait$   $s$  obtain  $w'$  where  $ws$   $t = \lfloor PostWS$   $w' \rfloor$ 
  by( $auto$   $simp$   $add: not-waiting-iff$ )
ultimately show  $?thesis$  using  $iec$   $s$  by  $auto$ 
qed

from  $ws$   $naok$   $exec$   $sees$ 
show  $?thesis$ 
proof( $cases$   $ins ! pc$ )
  case ( $Invoke$   $M' n$ )
    from  $ws$   $Invoke$   $check$   $exec$   $sees$   $naok$  obtain  $a$   $Ts$   $U$   $Ta$   $Us$   $D$   $D'$ 
      where  $a: stk ! n = Addr$   $a$ 
      and  $n: n < length$   $stk$ 
      and  $Ta: typeof-addr$   $h$   $a = \lfloor Ta \rfloor$ 
      and  $wtext: P \vdash class-type-of$   $Ta$   $sees$   $M':Us \rightarrow U = Native$   $in$   $D'$   $D'.M'(Us)::U$ 
      and  $sub: P \vdash Ts \sqsubseteq Us$ 
      and  $Ts: map$   $typeof_h$   $(rev$   $(take$   $n$   $stk)) = map$   $Some$   $Ts$ 
      and  $[simp]: xcp = None$ 
      apply( $cases$   $xcp$ )
    apply( $simp$   $add: is-Ref-def$   $has-method-def$   $external-WT'$ - $iff$   $check-def$   $lock-ok-las'$ - $def$   $confs-conv-map$ 
 $split-beta$   $split: if-split-asm$   $option.splits$ )
      apply( $auto$   $simp$   $add: lock-ok-las'-def$ )[2]
      apply( $fastforce$   $simp$   $add: is-native.simps$   $lock-ok-las'-def$   $dest: sees-method-fun$ )+
      done
    from  $exec$   $Ta$   $n$   $a$   $sees$   $Invoke$   $wtext$  obtain  $ta'$   $va$   $m''$ 
      where  $exec': (ta', va, m'') \in red-external-aggr$   $P$   $t$   $a$   $M'(rev$   $(take$   $n$   $stk))$   $h$ 
      and  $ta: ta = extTA2JVM$   $P$   $ta'$ 
      and  $va: (xcp', m', frs') = extRet2JVM$   $n$   $m''$   $stk$   $loc$   $C$   $M$   $pc$   $Frs$   $va$ 
      by( $auto$ )
    from  $va$  have  $[simp]: m'' = m'$  by( $cases$   $va$ )  $simp-all$ 
    from  $Ta$   $Ts$   $wtext$   $sub$  have  $wtext': P, h \vdash a.M'(rev$   $(take$   $n$   $stk)) : U$ 
      by( $auto$   $intro!: external-WT'.intros$   $simp$   $add: is-native.simps$ )
    with  $wfp$   $exec'$   $tconf$  have  $red: P, t \vdash \langle a.M'(rev$   $(take$   $n$   $stk)), h \rangle - ta' \rightarrow ext$   $\langle va, m' \rangle$ 
      by( $simp$   $add: WT-red-external-list-conv$ )
    from  $ws$   $Invoke$  have  $wset$   $s$   $t = None \vee M' = wait \wedge (\exists w. wset$   $s$   $t = \lfloor PostWS$   $w \rfloor)$  by  $auto$ 
    with  $wfp$   $red$   $tconf$   $hconf$  obtain  $ta''$   $va'$   $h''$ 
      where  $red': P, t \vdash \langle a.M'(rev$   $(take$   $n$   $stk)), h \rangle - ta'' \rightarrow ext$   $\langle va', h'' \rangle$ 
      and  $ok': final-thread.actions-ok$   $JVM-final$   $s$   $t$   $ta'' \vee final-thread.actions-ok'$   $s$   $t$   $ta'' \wedge final-thread.actions-subset$   $ta''$   $ta'$ 
      by( $rule$   $red-external-wf-red$ )
    from  $red'$   $a$   $n$   $Ta$   $Invoke$   $sees$   $wtext$ 
    have  $(extTA2JVM$   $P$   $ta''$ ,  $extRet2JVM$   $n$   $h''$   $stk$   $loc$   $C$   $M$   $pc$   $Frs$   $va')$   $\in$   $exec$   $P$   $t$   $(xcp, h, f \# Frs)$ 
      by( $auto$   $intro: red-external-imp-red-external-aggr$ )
    with  $check$  have  $P, t \vdash Normal$   $(xcp, h, (stk, loc, C, M, pc) \# Frs) - extTA2JVM$   $P$   $ta'' - jvmd \rightarrow$ 
 $Normal$   $(extRet2JVM$   $n$   $h''$   $stk$   $loc$   $C$   $M$   $pc$   $Frs$   $va')$ 
      by  $-(rule$   $exec-1-d.exec-1-d-NormalI$ ,  $auto$   $simp$   $add: exec-d-def$ )

```

```

moreover from  $ok' ta$ 
have  $final\text{-thread.actions-ok JVM-final (ls, (ts, h), ws, is) t (extTA2JVM P ta'') \vee$ 
 $final\text{-thread.actions-ok}' (ls, (ts, h), ws, is) t (extTA2JVM P ta'') \wedge final\text{-thread.actions-subset}$ 
 $(extTA2JVM P ta'') ta$ 
by( $auto simp add: final\text{-thread.actions-ok}'\text{-convert-extTA elim: final\text{-thread.actions-subset.cases}$ 
 $del: subsetI$ )
ultimately show  $?thesis$  by  $blast$ 
next
case  $MEnter$ 
with  $exec\ sees\ naok\ ws$  have  $False$ 
by( $cases\ xcp$ )( $auto split: if\text{-split-asm simp add: lock-ok-las}'\text{-def finfun-upd-apply ta-upd-simps}$ )
thus  $?thesis ..$ 
next
case  $MExit$ 
with  $exec\ sees\ False\ check\ ws$  obtain  $a$  where  $[simp]: hd\ stk = Addr\ a\ xcp = None\ ws\ t = None$ 
and  $ta: ta = \{\!| Unlock \rightarrow a, SyncUnlock\ a |\!\} \vee ta = \{\!| UnlockFail \rightarrow a |\!\}$ 
by( $cases\ xcp$ )( $fastforce split: if\text{-split-asm simp add: lock-ok-las}'\text{-def finfun-upd-apply is-Ref-def}$ 
 $check\text{-def}$ ) $+$ 
from  $ta$  show  $?thesis$ 
proof( $rule\ disjE$ )
assume  $ta: ta = \{\!| Unlock \rightarrow a, SyncUnlock\ a |\!\}$ 
let  $?ta' = \{\!| UnlockFail \rightarrow a |\!\}$ 
from  $ta$   $exec\ sees\ MExit$  obtain  $\sigma'$ 
where  $(?ta', \sigma') \in exec\ P\ t\ (xcp, h, f \# Frs)$  by  $auto$ 
with  $check$  have  $P, t \vdash Normal\ (xcp, h, (stk, loc, C, M, pc) \# Frs) - ?ta'\text{-jvmd} \rightarrow Normal\ \sigma'$ 
by  $-(rule\ exec\text{-1-d.exec-1-d-NormalI, auto simp add: exec-d-def})$ 
moreover from  $False\ ta$  have  $has\text{-locks}\ (ls\ \$\ a)\ t = 0$ 
by( $auto simp add: lock-ok-las}'\text{-def finfun-upd-apply ta-upd-simps}$ )
hence  $final\text{-thread.actions-ok}' (ls, (ts, h), ws, is) t\ ?ta'$ 
by( $auto simp add: lock-ok-las}'\text{-def finfun-upd-apply ta-upd-simps}$ )
moreover from  $ta$  have  $final\text{-thread.actions-subset}\ ?ta'\ ta$ 
by( $auto simp add: final\text{-thread.actions-subset-iff collect-locks}'\text{-def finfun-upd-apply ta-upd-simps}$ )
ultimately show  $?thesis$  by( $fastforce simp add: ta-upd-simps$ )
next
assume  $ta: ta = \{\!| UnlockFail \rightarrow a |\!\}$ 
let  $?ta' = \{\!| Unlock \rightarrow a, SyncUnlock\ a |\!\}$ 
from  $ta$   $exec\ sees\ MExit$  obtain  $\sigma'$ 
where  $(?ta', \sigma') \in exec\ P\ t\ (xcp, h, f \# Frs)$  by  $auto$ 
with  $check$  have  $P, t \vdash Normal\ (xcp, h, (stk, loc, C, M, pc) \# Frs) - ?ta'\text{-jvmd} \rightarrow Normal\ \sigma'$ 
by  $-(rule\ exec\text{-1-d.exec-1-d-NormalI, auto simp add: exec-d-def})$ 
moreover from  $False\ ta$  have  $has\text{-lock}\ (ls\ \$\ a)\ t$ 
by( $auto simp add: lock-ok-las}'\text{-def finfun-upd-apply ta-upd-simps}$ )
hence  $final\text{-thread.actions-ok}' (ls, (ts, h), ws, is) t\ ?ta'$ 
by( $auto simp add: lock-ok-las}'\text{-def finfun-upd-apply ta-upd-simps}$ )
moreover from  $ta$  have  $final\text{-thread.actions-subset}\ ?ta'\ ta$ 
by( $auto simp add: final\text{-thread.actions-subset-iff collect-locks}'\text{-def finfun-upd-apply ta-upd-simps}$ )
ultimately show  $?thesis$  by( $fastforce simp add: ta-upd-simps$ )
qed
qed( $case\ tac\ [!]\ xcp, auto simp add: split\text{-beta lock-ok-las}'\text{-def split: if\text{-split-asm}$ )
qed
thus  $\exists ta'\ x' m'. mexecd\ P\ t\ (x, shr\ s)\ ta'\ (x', m') \wedge$ 
 $(final\text{-thread.actions-ok JVM-final}\ s\ t\ ta' \vee$ 
 $final\text{-thread.actions-ok}'\ s\ t\ ta' \wedge final\text{-thread.actions-subset}\ ta'\ ta)$ 
by  $fastforce$ 

```



```

next
  fix s t x
  assume wfs: s ∈ ?wf-state
  and tst: thr s t = [(x, no-wait-locks)]
  and ¬ JVM-final x
  from wfs have correct: correct-state-ts Φ (thr s) (shr s)
  by(auto dest: execd-mthr.wset-Suspend-okD1 simp add: correct-jvm-state-def)
  obtain xcp frs where x: x = (xcp, frs) by (cases x, auto)
  with ⟨¬ JVM-final x⟩ obtain f Frs where frs = f # Frs
  by(fastforce simp add: neq-Nil-conv)
  with tst correct x have Φ ⊢ t: (xcp, shr s, f # Frs) √ by(auto dest: ts-okD)
  with ⟨wf-jvm-progΦ P⟩
  have exec-d P t (xcp, shr s, f # Frs) ≠ TypeError by(auto dest: no-type-error)
  then obtain Σ where exec-d P t (xcp, shr s, f # Frs) = Normal Σ by(auto)
  hence exec P t (xcp, shr s, f # Frs) = Σ
  by(auto simp add: exec-d-def check-def split: if-split-asm)
  with progress[OF wf ⟨Φ ⊢ t: (xcp, shr s, f # Frs) √⟩]
  obtain ta σ where (ta, σ) ∈ Σ unfolding exec-1-iff by blast
  with ⟨x = (xcp, frs)⟩ ⟨frs = f # Frs⟩ ⟨Φ ⊢ t: (xcp, shr s, f # Frs) √⟩
  ⟨wf-jvm-progΦ P⟩ ⟨exec-d P t (xcp, shr s, f # Frs) = Normal Σ⟩
  show ∃ ta x' m'. mexecd P t (x, shr s) ta (x', m')
  by(cases ta, cases σ)(fastforce simp add: split-paired-Ex intro: exec-1-d-NormalI)
qed(fastforce dest: defensive-imp-aggressive-1 mexec-instr-Wakeup-no-Join exec-ta-satisfiable)+

```

end

context JVM-conf-read begin

lemma mexecT-eq-mexecdT:

```

  assumes wf: wf-jvm-progΦ P
  and cs: correct-state-ts Φ (thr s) (shr s)
  shows P ⊢ s -t>ta→jvm s' = P ⊢ s -t>ta→jvmd s'
proof(rule iffI)
  assume P ⊢ s -t>ta→jvm s'
  thus P ⊢ s -t>ta→jvmd s'
proof(cases rule: execd-mthr.redT-elim[consumes 1, case-names normal acquire])
  case (normal x x' m')
  obtain xcp frs where x [simp]: x = (xcp, frs) by(cases x, auto)
  from ⟨thr s t = [(x, no-wait-locks)]⟩ cs
  have Φ ⊢ t: (xcp, shr s, frs) √ by(auto dest: ts-okD)
  from mexec-eq-mexecd[OF wf ⟨Φ ⊢ t: (xcp, shr s, frs) √⟩] ⟨mexec P t (x, shr s) ta (x', m')⟩
  have *: mexecd P t (x, shr s) ta (x', m') by simp
  with lifting-wf.redT-updTs-preserves[OF lifting-wf-correct-state-d[OF wf] cs, OF this ⟨thr s t =
[(x, no-wait-locks)]⟩] ⟨thread-oks (thr s) {ta}_t⟩
  have correct-state-ts Φ ((redT-updTs (thr s) {ta}_t)(t ↦ (x', redT-updLns (locks s) t no-wait-locks
{ta}_l))) m' by simp
  with * show ?thesis using normal
  by(cases s')(erule execd-mthr.redT-normal, auto)
next
  case acquire thus ?thesis
  apply(cases s', clarify)
  apply(rule execd-mthr.redT-acquire, assumption+)
  by(auto)
qed

```

next

assume $P \vdash s \rightarrow ta \rightarrow_{jvmd} s'$

thus $P \vdash s \rightarrow ta \rightarrow_{jvm} s'$

proof (cases rule: *execd-mthr.redT-elim*[consumes 1, case-names normal acquire])

case (normal $x \ x' \ m'$)

obtain $xcp \ frs$ where x [simp]: $x = (xcp, frs)$ by (cases x , auto)

from $\langle thr \ s \ t = [(x, no-wait-locks)] \rangle \ cs$

have $\Phi \vdash t: (xcp, shr \ s, frs) \checkmark$ by (auto dest: *ts-okD*)

from *mexec-eq-mexecd*[*OF wf* $\langle \Phi \vdash t: (xcp, shr \ s, frs) \checkmark \rangle$] $\langle mexecd \ P \ t \ (x, shr \ s) \ ta \ (x', m') \rangle$

have *mexec* $P \ t \ (x, shr \ s) \ ta \ (x', m')$ by simp

moreover from *lifting-wf.redT-updTs-preserved*[*OF lifting-wf-correct-state-d*[*OF wf*] cs , *OF* $\langle mexecd \ P \ t \ (x, shr \ s) \ ta \ (x', m') \rangle \langle thr \ s \ t = [(x, no-wait-locks)] \rangle$] $\langle thread-oks \ (thr \ s) \ \{\{ta\}_t\}$

have *correct-state-ts* $\Phi \ ((redT-updTs \ (thr \ s) \ \{\{ta\}_t\})(t \mapsto (x', redT-updLns \ (locks \ s) \ t \ no-wait-locks \ \{\{ta\}_l\})) \ m'$ by simp

ultimately show ?thesis using normal

by (cases s') (erule *exec-mthr.redT-normal*, auto)

next

case *acquire* thus ?thesis

apply (cases s' , clarify)

apply (rule *exec-mthr.redT-acquire*, assumption+)

by (auto)

qed

qed

lemma *mExecT-eq-mExecdT*:

assumes $wf: wf-jvm-prog_{\Phi} \ P$

and $ct: correct-state-ts \ \Phi \ (thr \ s) \ (shr \ s)$

shows $P \vdash s \rightarrow ttas \rightarrow_{jvm^*} s' = P \vdash s \rightarrow ttas \rightarrow_{jvmd^*} s'$

proof

assume *Red*: $P \vdash s \rightarrow ttas \rightarrow_{jvm^*} s'$

thus $P \vdash s \rightarrow ttas \rightarrow_{jvmd^*} s'$ using *ct*

proof (induct rule: *exec-mthr.RedT-induct*[consumes 1, case-names refl step])

case *refl* thus ?case by auto

next

case (step $s \ ttas \ s' \ t \ ta \ s''$)

hence $P \vdash s \rightarrow ttas \rightarrow_{jvmd^*} s'$ by blast

moreover from $\langle correct-state-ts \ \Phi \ (thr \ s) \ (shr \ s) \rangle \langle P \vdash s \rightarrow ttas \rightarrow_{jvm^*} s' \rangle$

have *correct-state-ts* $\Phi \ (thr \ s') \ (shr \ s')$

by (auto dest: *preserves-correct-state*[*OF wf*])

with $\langle P \vdash s' \rightarrow ta \rightarrow_{jvm} s'' \rangle$ have $P \vdash s' \rightarrow ta \rightarrow_{jvmd} s''$

by (*unfold mexecT-eq-mexecdT*[*OF wf*])

ultimately show ?case

by (*blast intro: execd-mthr.RedTI rtrancl3p-step elim: execd-mthr.RedTE*)

qed

next

assume *Red*: $P \vdash s \rightarrow ttas \rightarrow_{jvmd^*} s'$

thus $P \vdash s \rightarrow ttas \rightarrow_{jvm^*} s'$ using *ct*

proof (induct rule: *execd-mthr.RedT-induct*[consumes 1, case-names refl step])

case *refl* thus ?case by auto

next

case (step $s \ ttas \ s' \ t \ ta \ s''$)

hence $P \vdash s \rightarrow ttas \rightarrow_{jvmd^*} s'$ by blast

moreover from $\langle correct-state-ts \ \Phi \ (thr \ s) \ (shr \ s) \rangle \langle P \vdash s \rightarrow ttas \rightarrow_{jvmd^*} s' \rangle$

```

have correct-state-ts  $\Phi$  (thr s') (shr s')
  by(auto dest: preserves-correct-state-d[OF wf])
with  $\langle P \vdash s' -t \triangleright ta \rightarrow_{jvmd} s'' \rangle$  have  $P \vdash s' -t \triangleright ta \rightarrow_{jvm} s''$ 
  by(unfold mexecT-eq-mexecdT[OF wf])
ultimately show ?case
  by(blast intro: exec-mthr.RedTI rtrancl3p-step elim: exec-mthr.RedTE)
qed
qed

```

```

lemma mexecT-preserves-thread-conf:
   $\llbracket$  wf-jvm-prog $\Phi$  P; correct-state-ts  $\Phi$  (thr s) (shr s);
   $P \vdash s -t \triangleright ta \rightarrow_{jvm} s'$ ; thread-conf P (thr s) (shr s)  $\rrbracket$ 
   $\implies$  thread-conf P (thr s') (shr s')
by(simp only: mexecT-eq-mexecdT)(rule execd-tconf.redT-preserves)

```

```

lemma mExecT-preserves-thread-conf:
   $\llbracket$  wf-jvm-prog $\Phi$  P; correct-state-ts  $\Phi$  (thr s) (shr s);
   $P \vdash s -\triangleright tta \rightarrow_{jvm} s'$ ; thread-conf P (thr s) (shr s)  $\rrbracket$ 
   $\implies$  thread-conf P (thr s') (shr s')
by(simp only: mExecT-eq-mExecdT)(rule execd-tconf.RedT-preserves)

```

```

lemma wset-Suspend-ok-mexecd-mexec:
  assumes wf: wf-jvm-prog $\Phi$  P
  shows exec-mthr.wset-Suspend-ok P (correct-jvm-state  $\Phi$ ) = execd-mthr.wset-Suspend-ok P (correct-jvm-state  $\Phi$ )

```

```

apply(safe)
apply(rule execd-mthr.wset-Suspend-okI)
apply(erule execd-mthr.wset-Suspend-okD1)
apply(drule (1) execd-mthr.wset-Suspend-okD2)
apply(subst (asm) (2) split-paired-Ex)
apply(elim bexE exE conjE)
apply(subst (asm) mexec-eq-mexecd[OF wf])
apply(simp add: correct-jvm-state-def)
apply(blast dest: ts-okD)
apply(subst (asm) mexecT-eq-mexecdT[OF wf])
apply(simp add: correct-jvm-state-def)
apply(subst (asm) mExecT-eq-mExecdT[OF wf])
apply(simp add: correct-jvm-state-def)
apply(rule beXI exI|erule conjI|assumption)+
apply(rule execd-mthr.wset-Suspend-okI)
apply(erule execd-mthr.wset-Suspend-okD1)
apply(drule (1) execd-mthr.wset-Suspend-okD2)
apply(subst (asm) (2) split-paired-Ex)
apply(elim bexE exE conjE)
apply(subst (asm) mexec-eq-mexecd[OF wf, symmetric])
apply(simp add: correct-jvm-state-def)
apply(blast dest: ts-okD)
apply(subst (asm) mexecT-eq-mexecdT[OF wf, symmetric])
apply(simp add: correct-jvm-state-def)
apply(subst (asm) mExecT-eq-mExecdT[OF wf, symmetric])
apply(simp add: correct-jvm-state-def)
apply(rule beXI exI|erule conjI|assumption)+
done

```

end

context *JVM-typesafe* begin

lemma *exec-wf-progress*:

assumes *wf*: *wf-jvm-prog* Φ *P*

shows *progress JVM-final* (*mexec P*) (*exec-mthr.wset-Suspend-ok P* (*correct-jvm-state* Φ))
(is *progress* - - *?wf-state*)

proof –

interpret *progress*: *progress JVM-final mexecd P convert-RA ?wf-state*

using *assms unfolding wset-Suspend-ok-mexecd-mexec*[*OF wf*] **by**(rule *execd-wf-progress*)

show *?thesis*

proof(*unfold-locales*)

fix *s*

assume *s* \in *?wf-state*

thus *lock-thread-ok* (*locks s*) (*thr s*) \wedge *exec-mthr.wset-final-ok* (*wset s*) (*thr s*)

by(rule *progress.wf-stateD*)

next

fix *s t x ta x' m'*

assume *wfs*: *s* \in *?wf-state*

and *tst*: *thr s t* = $\llbracket(x, \text{no-wait-locks})\rrbracket$

and *exec*: *mexec P t* (*x, shr s*) *ta* (*x', m'*)

and *wait*: \neg *waiting* (*wset s t*)

from *wfs tst* **have** *correct*: $\Phi \vdash t$: (*fst x, shr s, snd x*) \checkmark

by(*auto dest!*: *exec-mthr.wset-Suspend-okD1 ts-okD simp add: correct-jvm-state-def*)

with *exec* **have** *mexecd P t* (*x, shr s*) *ta* (*x', m'*)

by(*cases x*)(*simp only: mexec-eq-mexecd*[*OF wf*] *fst-conv snd-conv*)

from *progress.wf-red*[*OF wfs tst this wait*] *correct*

show $\exists ta' x' m'. mexec P t$ (*x, shr s*) *ta'* (*x', m'*) \wedge

final-thread.actions-ok JVM-final s t ta' \vee

final-thread.actions-ok' s t ta' \wedge *final-thread.actions-subset ta' ta*)

by(*cases x*)(*simp only: fst-conv snd-conv mexec-eq-mexecd*[*OF wf*])

next

fix *s t x ta x' m' w*

assume *wfs*: *s* \in *?wf-state*

and *tst*: *thr s t* = $\llbracket(x, \text{no-wait-locks})\rrbracket$

and *exec*: *mexec P t* (*x, shr s*) *ta* (*x', m'*)

and *wait*: \neg *waiting* (*wset s t*)

and *Suspend*: *Suspend w* \in *set* $\{\!|ta|\!\}_w$

from *wfs tst* **have** *correct*: $\Phi \vdash t$: (*fst x, shr s, snd x*) \checkmark

by(*auto dest!*: *exec-mthr.wset-Suspend-okD1 ts-okD simp add: correct-jvm-state-def*)

with *exec* **have** *mexecd P t* (*x, shr s*) *ta* (*x', m'*)

by(*cases x*)(*simp only: mexec-eq-mexecd*[*OF wf*] *fst-conv snd-conv*)

with *wfs tst* **show** \neg *JVM-final x'* **using** *wait Suspend* **by**(rule *progress.red-wait-set-not-final*)

next

fix *s t x*

assume *wfs*: *s* \in *?wf-state*

and *tst*: *thr s t* = $\llbracket(x, \text{no-wait-locks})\rrbracket$

and \neg *JVM-final x*

from *progress.wf-progress*[*OF this*]

show $\exists ta' x' m'. mexec P t$ (*x, shr s*) *ta'* (*x', m'*)

by(*auto dest: defensive-imp-aggressive-1 simp add: split-beta*)

qed(*fastforce dest: mexec-instr-Wakeup-no-Join exec-ta-satisfiable*)+

qed

theorem *mexecd-TypeSafety*:

fixes $ln :: 'addr \Rightarrow f \text{ nat}$

assumes $wf: wf\text{-jvm}\text{-prog}_{\Phi} P$

and $s: s \in \text{execd}\text{-mthr}.\text{wset}\text{-Suspend}\text{-ok} P$ (*correct-jvm-state* Φ)

and $Exec: P \vdash s \text{-}\triangleright\text{ttas}\text{-}\rightarrow\text{jvmd}^* s'$

and $\neg (\exists t \ ta \ s''. P \vdash s' \text{-}t\triangleright\text{ta}\text{-}\rightarrow\text{jvmd} s'')$

and $ts't: thr \ s' \ t = \lfloor ((xcp, frs), ln) \rfloor$

shows $frs \neq [] \vee ln \neq \text{no}\text{-wait}\text{-locks} \implies t \in \text{execd}\text{-mthr}.\text{deadlocked} P \ s'$

and $\Phi \vdash t: (xcp, shr \ s', frs) \checkmark$

proof –

interpret *progress JVM-final mexecd P convert-RA execd-mthr.wset-Suspend-ok P* (*correct-jvm-state* Φ)

by(*rule execd-wf-progress*) *fact+*

from $Exec \ s$ **have** $wfs': s' \in \text{execd}\text{-mthr}.\text{wset}\text{-Suspend}\text{-ok} P$ (*correct-jvm-state* Φ)

unfolding *execd-mthr.RedT-def*

by(*blast intro: invariant3p-rtrancl3p execd-mthr.invariant3p-wset-Suspend-ok invariant3p-correct-jvm-state-mexecdT[OF wf]*)

with $ts't$ **show** $cst: \Phi \vdash t: (xcp, shr \ s', frs) \checkmark$

by(*auto dest: ts-okD execd-mthr.wset-Suspend-okD1 simp add: correct-jvm-state-def*)

assume $nfin: frs \neq [] \vee ln \neq \text{no}\text{-wait}\text{-locks}$

from $nfin \ \langle thr \ s' \ t = \lfloor ((xcp, frs), ln) \rfloor \rangle$ **have** $\text{exec}\text{-mthr}.\text{not}\text{-final}\text{-thread} \ s' \ t$

by(*auto simp: exec-mthr.not-final-thread-iff*)

from $\langle \neg (\exists t \ ta \ s''. P \vdash s' \text{-}t\triangleright\text{ta}\text{-}\rightarrow\text{jvmd} s'') \rangle$

show $t \in \text{execd}\text{-mthr}.\text{deadlocked} P \ s'$

proof(*rule contrapos-np*)

assume $t \notin \text{execd}\text{-mthr}.\text{deadlocked} P \ s'$

with $\langle \text{exec}\text{-mthr}.\text{not}\text{-final}\text{-thread} \ s' \ t \rangle$ **have** $\neg \text{execd}\text{-mthr}.\text{deadlocked}' P \ s'$

by(*auto simp add: execd-mthr.deadlocked'-def*)

hence $\neg \text{execd}\text{-mthr}.\text{deadlock} P \ s'$ **unfolding** *execd-mthr.deadlock-eq-deadlocked'* .

thus $\exists t \ ta \ s''. P \vdash s' \text{-}t\triangleright\text{ta}\text{-}\rightarrow\text{jvmd} s''$ **by**(*rule redT-progress[OF wfs']*)

qed

qed

theorem *mexec-TypeSafety*:

fixes $ln :: 'addr \Rightarrow f \text{ nat}$

assumes $wf: wf\text{-jvm}\text{-prog}_{\Phi} P$

and $s: s \in \text{exec}\text{-mthr}.\text{wset}\text{-Suspend}\text{-ok} P$ (*correct-jvm-state* Φ)

and $Exec: P \vdash s \text{-}\triangleright\text{ttas}\text{-}\rightarrow\text{jvm}^* s'$

and $\neg (\exists t \ ta \ s''. P \vdash s' \text{-}t\triangleright\text{ta}\text{-}\rightarrow\text{jvm} s'')$

and $ts't: thr \ s' \ t = \lfloor ((xcp, frs), ln) \rfloor$

shows $frs \neq [] \vee ln \neq \text{no}\text{-wait}\text{-locks} \implies t \in \text{multithreaded}\text{-base}.\text{deadlocked} \text{JVM}\text{-final} (mexec \ P) \ s'$

and $\Phi \vdash t: (xcp, shr \ s', frs) \checkmark$

proof –

interpret *progress JVM-final mexec P convert-RA exec-mthr.wset-Suspend-ok P* (*correct-jvm-state* Φ)

by(*rule exec-wf-progress*) *fact+*

from $Exec \ s$ **have** $wfs': s' \in \text{exec}\text{-mthr}.\text{wset}\text{-Suspend}\text{-ok} P$ (*correct-jvm-state* Φ)

unfolding *exec-mthr.RedT-def*

by(*blast intro: invariant3p-rtrancl3p exec-mthr.invariant3p-wset-Suspend-ok invariant3p-correct-jvm-state-mexecT[OF wf]*)

with $ts't$ **show** $cst: \Phi \vdash t: (xcp, shr\ s', frs) \checkmark$
by(*auto dest: ts-okD exec-mthr.wset-Suspend-okD1 simp add: correct-jvm-state-def*)

assume $nfin: frs \neq [] \vee ln \neq no\text{-wait-locks}$
from $nfin \langle thr\ s' t = [((xcp, frs), ln)] \rangle$ **have** $exec\text{-mthr.not-final-thread}\ s' t$
by(*auto simp: exec-mthr.not-final-thread-iff*)
from $\langle \neg (\exists t\ ta\ s''. P \vdash s' -t \triangleright ta \rightarrow_{jvm} s'') \rangle$
show $t \in exec\text{-mthr.deadlocked}\ P\ s'$
proof(*rule contrapos-np*)
assume $t \notin exec\text{-mthr.deadlocked}\ P\ s'$
with $\langle exec\text{-mthr.not-final-thread}\ s' t \rangle$ **have** $\neg exec\text{-mthr.deadlocked}'\ P\ s'$
by(*auto simp add: exec-mthr.deadlocked'-def*)
hence $\neg exec\text{-mthr.deadlock}\ P\ s'$ **unfolding** $exec\text{-mthr.deadlock-eq-deadlocked}'$.
thus $\exists t\ ta\ s''. P \vdash s' -t \triangleright ta \rightarrow_{jvm} s''$ **by**(*rule redT-progress[OF wfs[!]]*)
qed
qed

lemma *start-mexec-mexecd-commute:*

assumes $wf: wf\text{-jvm-prog}_{\Phi}\ P$
and $start: wf\text{-start-state}\ P\ C\ M\ vs$
shows $P \vdash JVM\text{-start-state}\ P\ C\ M\ vs \rightarrow ttas \rightarrow_{jvmd} s \longleftrightarrow P \vdash JVM\text{-start-state}\ P\ C\ M\ vs$
 $\rightarrow ttas \rightarrow_{jvm} s$
using *correct-jvm-state-initial[OF assms]*
by(*clarsimp simp add: correct-jvm-state-def*)(*rule mExecT-eq-mExecdT[symmetric, OF wf]*)

theorem *mRtrancl-eq-mRtranclD:*

assumes $wf: wf\text{-jvm-prog}_{\Phi}\ P$
and $ct: correct\text{-state-ts}\ \Phi\ (thr\ s)\ (shr\ s)$
shows $exec\text{-mthr.mthr.Rtrancl3p}\ P\ s\ ttas \longleftrightarrow execd\text{-mthr.mthr.Rtrancl3p}\ P\ s\ ttas$ (**is** $?lhs \longleftrightarrow ?rhs$)
proof

show $?lhs$ **if** $?rhs$ **using** *that ct*
proof(*coinduction arbitrary: s ttas*)
case $Rtrancl3p$
interpret *lifting-wf JVM-final mexecd P convert-RA* $\lambda t\ (xcp, frs)\ h. \Phi \vdash t: (xcp, h, frs) \checkmark$
using wf **by**(*rule lifting-wf-correct-state-d*)
from $Rtrancl3p(1)$ **show** $?case$
proof *cases*
case $stop: Rtrancl3p\text{-stop}$
then show $?thesis$ **using** $mexecT\text{-eq-mexecdT}[OF\ wf\ Rtrancl3p(2)]$ **by** *clarsimp*
next
case $(Rtrancl3p\text{-into-Rtrancl3p}\ s'\ ttas'\ tta)$
then show $?thesis$ **using** $mexecT\text{-eq-mexecdT}[OF\ wf\ Rtrancl3p(2)]\ Rtrancl3p(2)$
by(*cases tta; cases s'*)(*fastforce simp add: split-paired-Ex dest: redT-preserves*)
qed
qed

show $?rhs$ **if** $?lhs$ **using** *that ct*

proof(*coinduction arbitrary: s ttas*)
case $Rtrancl3p$
interpret *lifting-wf JVM-final mexec P convert-RA* $\lambda t\ (xcp, frs)\ h. \Phi \vdash t: (xcp, h, frs) \checkmark$
using wf **by**(*rule lifting-wf-correct-state*)
from $Rtrancl3p(1)$ **show** $?case$
proof *cases*

```

  case stop: Rtrancl3p-stop
  then show ?thesis using mexecT-eq-mexecdT[OF wf Rtrancl3p(2)] by clarsimp
next
  case (Rtrancl3p-into-Rtrancl3p s' ttas' tta)
  then show ?thesis using mexecT-eq-mexecdT[OF wf Rtrancl3p(2)] Rtrancl3p(2)
    by(cases tta; cases s')(fastforce simp add: split-paired-Ex dest: redT-preserves)
qed
qed
qed

```

lemma *start-mRtrancl-mRtrancl-d-commute*:

```

  assumes wf: wf-jvm-prog $\Phi$  P
  and start: wf-start-state P C M vs
  shows exec-mthr.mthr.Rtrancl3p P (JVM-start-state P C M vs) ttas  $\longleftrightarrow$  execd-mthr.mthr.Rtrancl3p
  P (JVM-start-state P C M vs) ttas
  using correct-jvm-state-initial[OF assms] by(clarsimp simp add: correct-jvm-state-def mRtrancl-eq-mRtrancl-d[OF
  wf])

```

end

6.7.1 Determinism

context *JVM-heap-conf* begin

lemma *exec-instr-deterministic*:

```

  assumes wf: wf-prog wf-md P
  and det: deterministic-heap-ops
  and exec1: (ta',  $\sigma'$ )  $\in$  exec-instr i P t (shr s) stk loc C M pc frs
  and exec2: (ta'',  $\sigma''$ )  $\in$  exec-instr i P t (shr s) stk loc C M pc frs
  and check: check-instr i P (shr s) stk loc C M pc frs
  and aok1: final-thread.actions-ok final s t ta'
  and aok2: final-thread.actions-ok final s t ta''
  and tconf: P, shr s  $\vdash$  t  $\surd$  t
  shows ta' = ta''  $\wedge$   $\sigma'$  =  $\sigma''$ 
  using exec1 exec2 aok1 aok2
  proof(cases i)
  case (Invoke M' n)
  { fix T ta''' ta'''' va' va'' h' h''
    assume T: type-of-addr (shr s) (the-Addr (stk ! n)) =  $\lfloor T \rfloor$ 
    and method: snd (snd (snd (method P (class-type-of T) M'))) = None P  $\vdash$  class-type-of T has
    M'
    and params: P, shr s  $\vdash$  rev (take n stk) [ $\leq$ ] fst (snd (method P (class-type-of T) M'))
    and red1: (ta''', va', h')  $\in$  red-external-aggr P t (the-Addr (stk ! n)) M' (rev (take n stk)) (shr s)
    and red2: (ta''', va'', h'')  $\in$  red-external-aggr P t (the-Addr (stk ! n)) M' (rev (take n stk)) (shr
    s)
    and ta': ta' = extTA2JVM P ta'''
    and ta'': ta'' = extTA2JVM P ta''''
  }
  from T method params obtain T' where P, shr s  $\vdash$  the-Addr (stk ! n)  $\cdot$  M' (rev (take n stk)) : T'
  by(fastforce simp add: has-method-def confs-conv-map external-WT'-iff)
  hence P, t  $\vdash$   $\langle$  the-Addr (stk ! n)  $\cdot$  M' (rev (take n stk)), shr s  $\rangle$  -ta'''  $\rightarrow$  ext  $\langle$  va', h'  $\rangle$ 
  and P, t  $\vdash$   $\langle$  the-Addr (stk ! n)  $\cdot$  M' (rev (take n stk)), shr s  $\rangle$  -ta''''  $\rightarrow$  ext  $\langle$  va'', h''  $\rangle$ 
  using red1 red2 tconf
  by-(rule WT-red-external-aggr-imp-red-external[OF wf], assumption+)+
  moreover from aok1 aok2 ta' ta''

```

```

  have final-thread.actions-ok final s t ta'''
    and final-thread.actions-ok final s t ta''''
    by(auto simp add: final-thread.actions-ok-iff)
  ultimately have ta''' = ta''''  $\wedge$  va' = va''  $\wedge$  h' = h''
    by(rule red-external-deterministic[OF det]) }
  with assms Invoke show ?thesis
  by(clarsimp simp add: split-beta split: if-split-asm) blast
next
case MExit
{ assume final-thread.actions-ok final s t {UnlockFail $\rightarrow$ the-Addr (hd stk)}
  and final-thread.actions-ok final s t {Unlock $\rightarrow$ the-Addr (hd stk), SyncUnlock (the-Addr (hd stk))}
  hence False
    by(auto simp add: final-thread.actions-ok-iff lock-ok-las-def finfun-upd-apply elim!: allE[where
x=the-Addr (hd stk)]) }
  with assms MExit show ?thesis by(auto split: if-split-asm)
qed(auto simp add: split-beta split: if-split-asm dest: deterministic-heap-ops-readD[OF det] determin-
istic-heap-ops-writeD[OF det] deterministic-heap-ops-allocateD[OF det])

```

lemma *exec-1-deterministic:*

```

  assumes wf: wf-jvm-prog $\Phi$  P
  and det: deterministic-heap-ops
  and exec1:  $P, t \vdash (xcp, shr s, frs) -ta'-jvm \rightarrow \sigma'$ 
  and exec2:  $P, t \vdash (xcp, shr s, frs) -ta''-jvm \rightarrow \sigma''$ 
  and aok1: final-thread.actions-ok final s t ta'
  and aok2: final-thread.actions-ok final s t ta''
  and conf:  $\Phi \vdash t:(xcp, shr s, frs) \checkmark$ 
  shows  $ta' = ta'' \wedge \sigma' = \sigma''$ 

```

proof –

```

  from wf obtain wf-md where wf': wf-prog wf-md P by(blast dest: wt-jvm-progD)
  from conf have tconf:  $P, shr s \vdash t \checkmark$  by(simp add: correct-state-def)
  from exec1 conf have  $P, t \vdash Normal (xcp, shr s, frs) -ta'-jvmd \rightarrow Normal \sigma'$ 
    by(simp add: welltyped-commute[OF wf])
  hence check P (xcp, shr s, frs) by(rule jvmd-NormalE)
  with exec1 exec2 aok1 aok2 tconf show ?thesis
    by(cases xcp)(case-tac [!]) frs, auto elim!: exec-1.cases dest: exec-instr-deterministic[OF wf' det]
  simp add: check-def split-beta)

```

qed

end

context *JVM-conf-read* **begin**

lemma *invariant3p-correct-state-ts:*

```

  assumes wf-jvm-prog $\Phi$  P
  shows invariant3p (mexecT P) {s. correct-state-ts  $\Phi$  (thr s) (shr s)}
  using assms by(rule lifting-wf.invariant3p-ts-ok[OF lifting-wf-correct-state])

```

lemma *mexec-deterministic:*

```

  assumes wf: wf-jvm-prog $\Phi$  P
  and det: deterministic-heap-ops
  shows exec-mthr.deterministic P {s. correct-state-ts  $\Phi$  (thr s) (shr s)}
proof(rule exec-mthr.deterministicI)
  fix s t x ta' x' m' ta'' x'' m''
  assume tst: thr s t = [(x, no-wait-locks)]

```



```

and red:  $mexec\ P\ t\ (x,\ shr\ s)\ ta'\ (x',\ m')\ mexec\ P\ t\ (x,\ shr\ s)\ ta''\ (x'',\ m'')$ 
and aok:  $exec\text{-}mthr.\text{actions-ok}\ s\ t\ ta'\ exec\text{-}mthr.\text{actions-ok}\ s\ t\ ta''$ 
and correct [simplified]:  $s \in \{s.\ correct\text{-}state\text{-}ts\ \Phi\ (thr\ s)\ (shr\ s)\}$ 
moreover obtain  $xcp\ frs$  where [simp]:  $x = (xcp,\ frs)$  by(cases  $x$ )
moreover obtain  $xcp'\ frs'$  where [simp]:  $x' = (xcp',\ frs')$  by(cases  $x'$ )
moreover obtain  $xcp''\ frs''$  where [simp]:  $x'' = (xcp'',\ frs'')$  by(cases  $x''$ )
ultimately have  $exec1: P, t \vdash (xcp,\ shr\ s,\ frs) \text{-}ta'\text{-}jvm\rightarrow (xcp',\ m',\ frs')$ 
and  $exec1: P, t \vdash (xcp,\ shr\ s,\ frs) \text{-}ta''\text{-}jvm\rightarrow (xcp'',\ m'',\ frs'')$ 
by simp-all
moreover note aok
moreover from correct tst have  $\Phi \vdash t:(xcp,\ shr\ s,\ frs)\checkmark$ 
by(auto dest: ts-okD)
ultimately have  $ta' = ta'' \wedge (xcp',\ m',\ frs') = (xcp'',\ m'',\ frs'')$ 
by(rule exec-1-deterministic[OF wf det])
thus  $ta' = ta'' \wedge x' = x'' \wedge m' = m''$  by simp
qed(rule invariant3p-correct-state-ts[OF wf])

```

end

end

6.8 Preservation of deadlock for the JVMs

theory *JVMDeadlocked*

imports

BVProgressThreaded

begin

context *JVM-progress* begin

lemma *must-sync-preserved-d*:

assumes *wf*: $wf\text{-}jvm\text{-}prog_{\Phi}\ P$

and *ml*: $execd\text{-}mthr.\text{must-sync}\ P\ t\ (xcp,\ frs)\ h$

and *hext*: $hext\ h\ h'$

and *hconf'*: $hconf\ h'$

and *cs*: $\Phi \vdash t:(xcp,\ h,\ frs)\checkmark$

shows $execd\text{-}mthr.\text{must-sync}\ P\ t\ (xcp,\ frs)\ h'$

proof(*rule* *execd-mthr.must-syncI*)

from *ml* **obtain** $ta\ xcp'\ frs'\ m'$

where $red: P, t \vdash Normal\ (xcp,\ h,\ frs) \text{-}ta\text{-}jvmd\rightarrow Normal\ (xcp',\ m',\ frs')$

by(*auto* *elim*: *execd-mthr.must-syncE*)

then obtain $f\ Frs$

where *check*: $check\ P\ (xcp,\ h,\ frs)$

and *exec*: $(ta,\ xcp',\ m',\ frs') \in exec\ P\ t\ (xcp,\ h,\ frs)$

and [*simp*]: $frs = f \# Frs$

by(*auto* *elim*: *jvmd-NormalE*)

from *cs* *hext* *hconf'* **have** $cs': \Phi \vdash t:(xcp,\ h',\ frs)\checkmark$

by(*rule* *correct-state-hext-mono*)

then obtain $ta\ \sigma'$ **where** $exec: P, t \vdash (xcp,\ h',\ frs) \text{-}ta\text{-}jvm\rightarrow \sigma'$

by(*auto* *dest*: *progress*[*OF* *wf*])

hence $P, t \vdash Normal\ (xcp,\ h',\ frs) \text{-}ta\text{-}jvmd\rightarrow Normal\ \sigma'$

unfolding *welltyped-commute*[*OF* *wf* *cs'*].

moreover from *exec* **have** $\exists s.\ exec\text{-}mthr.\text{actions-ok}\ s\ t\ ta$ **by**(*rule* *exec-ta-satisfiable*)

ultimately show $\exists ta\ x'\ m'\ s.\ mexecd\ P\ t\ ((xcp,\ frs),\ h')\ ta\ (x',\ m') \wedge exec\text{-}mthr.\text{actions-ok}\ s\ t\ ta$
by(cases σ')(fastforce simp del: split-paired-Ex)

qed

lemma *can-sync-devreserp-d*:

assumes *wf*: $wf\text{-}jvm\text{-}prog\ \Phi\ P$
and *cl'*: $execd\text{-}mthr.\text{can-sync}\ P\ t\ (xcp,\ frs)\ h'\ L$
and *cs*: $\Phi \vdash t:\ (xcp,\ h,\ frs)\ \checkmark$
and *hext*: $hext\ h\ h'$
and *hconf'*: $hconf\ h'$
shows $\exists L' \subseteq L.\ execd\text{-}mthr.\text{can-sync}\ P\ t\ (xcp,\ frs)\ h\ L'$

proof –

from *cl'* **obtain** $ta\ xcp'\ frs'\ m'$
where *red*: $P, t \vdash Normal\ (xcp,\ h',\ frs) \text{-}ta\text{-}jvmd \rightarrow Normal\ (xcp',\ m',\ frs')$
and *L*: $L = collect\text{-}locks\ \{ta\}_l \langle + \rangle collect\text{-}cond\text{-}actions\ \{ta\}_c \langle + \rangle collect\text{-}interrupts\ \{ta\}_i$
by $\text{-}(erule\ execd\text{-}mthr.\text{can-sync}E,\ auto)$

then obtain $f\ Frs$

where *check*: $check\ P\ (xcp,\ h',\ frs)$
and *exec*: $(ta,\ xcp',\ m',\ frs') \in exec\ P\ t\ (xcp,\ h',\ frs)$
and [*simp*]: $frs = f \# Frs$
by(auto elim: *jvmd-NormalE* simp add: *finfun-upd-apply*)

obtain *stk loc C M pc* **where** [*simp*]: $f = (stk,\ loc,\ C,\ M,\ pc)$ **by** (cases *f*, *blast*)

from *cs* **obtain** *ST LT Ts T mxs mxl ins xt* **where**

hconf: $hconf\ h$ **and**
tconf: $P, h \vdash t \checkmark$ **and**
meth: $P \vdash C\ sees\ M: Ts \rightarrow T = [(mxs,\ mxl,\ ins,\ xt)]\ in\ C$ **and**
 Φ : $\Phi\ C\ M\ !\ pc = Some\ (ST, LT)$ **and**
frame: $conf\text{-}f\ P\ h\ (ST, LT)\ ins\ (stk, loc, C, M, pc)$ **and**
frames: $conf\text{-}fs\ P\ h\ \Phi\ M\ (size\ Ts)\ T\ Frs$
by (fastforce simp add: *correct-state-def* dest: *sees-method-fun*)

from *cs* **have** $exec\ P\ t\ (xcp,\ h,\ f \# Frs) \neq \{\}$

by(auto dest!: *progress[OF wf]* simp add: *exec-1-iff*)

with *no-type-error[OF wf cs]* **have** *check'*: $check\ P\ (xcp,\ h,\ frs)$

by(auto simp add: *exec-d-def* split: *if-split-asm*)

from *wf* **obtain** *wfmd* **where** *wfp*: $wf\text{-}prog\ wfmd\ P$ **by**(auto dest: *wt-jvm-progD*)

from *tconf hext* **have** *tconf'*: $P, h' \vdash t \checkmark$ **by**(rule *tconf-hext-mono*)

show *?thesis*

proof(cases *xcp*)

case [*simp*]: (Some *a*)

with *exec* **have** [*simp*]: $m' = h'$ **by**(auto)

from $\langle \Phi \vdash t:\ (xcp,\ h,\ frs)\ \checkmark \rangle$ **obtain** *D* **where** *D*: $typeof\text{-}addr\ h\ a = [Class\text{-}type\ D]$

by(auto simp add: *correct-state-def*)

with *hext* **have** *cname-of* $h\ a = cname\text{-}of\ h'\ a$ **by**(auto dest: *hext-objD* simp add: *cname-of-def*)

with *exec* **have** $(ta,\ xcp',\ h,\ frs') \in exec\ P\ t\ (xcp,\ h,\ frs)$ **by** auto

moreover from *check D hext* **have** $check\ P\ (xcp,\ h,\ frs)$

by(auto simp add: *check-def* *check-xcpt-def* dest: *hext-objD*)

ultimately have $P, t \vdash Normal\ (xcp,\ h,\ frs) \text{-}ta\text{-}jvmd \rightarrow Normal\ (xcp',\ h,\ frs')$

by $\text{-}(rule\ exec\text{-}1\text{-}d\text{-}NormalI,\ simp\ only:\ exec\text{-}d\text{-}def\ if\ True)$

with *L* **have** $execd\text{-}mthr.\text{can-sync}\ P\ t\ (xcp,\ frs)\ h\ L$

by(auto intro: *execd-mthr.can-syncI*)

thus *?thesis* **by** auto

next

case [*simp*]: None

note $[simp] = defs1\ list\text{-}all2\text{-}Cons2$

from *frame* **have** $ST: P, h \vdash stk\ [: \leq] ST$
and $LT: P, h \vdash loc\ [: \leq_{\top}] LT$
and $pc: pc < length\ ins$ **by** *simp-all*
from *wf meth pc* **have** $wt: P, T, mxs, size\ ins, xt \vdash ins!pc, pc :: \Phi\ C\ M$
by(*rule wt-jvm-prog-impl-wt-instr*)

from $\langle \Phi \vdash t: (xcp, h, frs) \checkmark \rangle$
have $\exists ta\ \sigma'. P, t \vdash (xcp, h, f \# Frs) \text{-}ta\text{-}jvm \rightarrow \sigma'$
by(*auto dest: progress[OF wf] simp del: correct-state-def split-paired-Ex*)
with *exec meth* **have** $\exists ta'\ \sigma'. (ta', \sigma') \in exec\ P\ t\ (xcp, h, frs) \wedge collect\text{-}locks\ \{ta'\}_l \subseteq collect\text{-}locks\ \{ta\}_l \wedge collect\text{-}cond\text{-}actions\ \{ta'\}_c \subseteq collect\text{-}cond\text{-}actions\ \{ta\}_c \wedge collect\text{-}interrupts\ \{ta'\}_i \subseteq collect\text{-}interrupts\ \{ta\}_i$
proof(*cases ins ! pc*)
case (*Invoke M' n*)
show *?thesis*
proof(*cases stk ! n = Null*)
case *True with Invoke exec meth* **show** *?thesis* **by** *simp*
next
case *False*
with *check meth* **obtain** *a* **where** $a: stk\ !\ n = Addr\ a$ **and** $n: n < length\ stk$
by(*auto simp add: check-def is-Ref-def Invoke*)
from *frame* **have** $stk: P, h \vdash stk\ [: \leq] ST$ **by**(*auto simp add: conf-f-def*)
hence $P, h \vdash stk\ !\ n \leq ST\ !\ n$ **using** *n* **by**(*rule list-all2-nthD*)
with *a* **obtain** *ao Ta* **where** $Ta: typeof\text{-}addr\ h\ a = \lfloor Ta \rfloor$
by(*auto simp add: conf-def*)
from *heat Ta* **have** $Ta': typeof\text{-}addr\ h'\ a = \lfloor Ta \rfloor$ **by**(*rule typeof-addr-heat-mono*)
with *check a meth Invoke False* **obtain** *D Ts' T' meth D'*
where $C: D = class\text{-}type\text{-}of\ Ta$
and $sees': P \vdash D\ sees\ M': Ts' \rightarrow T' = meth\ in\ D'$
and $params: P, h' \vdash rev\ (take\ n\ stk)\ [: \leq] Ts'$
by(*auto simp add: check-def has-method-def*)
show *?thesis*
proof(*cases meth*)
case *Some*
with *exec meth a Ta Ta' Invoke n sees' C* **show** *?thesis* **by**(*simp add: split-beta*)
next
case *None*
with *exec meth a Ta Ta' Invoke n sees' C*
obtain $ta'\ va\ h''$ **where** $ta': ta = extTA2JVM\ P\ ta'$
and $va: (xcp', m', frs') = extRet2JVM\ n\ h''\ stk\ loc\ C\ M\ pc\ Frs\ va$
and $exec': (ta', va, h'') \in red\text{-}external\text{-}aggr\ P\ t\ a\ M'\ (rev\ (take\ n\ stk))\ h'$
by(*fastforce*)
from *va* **have** $[simp]: h'' = m'$ **by**(*cases va*) *simp-all*
note *Ta* **moreover** **from** *None sees' wfp* **have** $D'.M'(Ts') :: T'$ **by**(*auto intro: sees-wf-native*)
with *C sees' params Ta' None* **have** $P, h' \vdash a.M'(rev\ (take\ n\ stk)) : T'$
by(*auto simp add: external-WT'-iff confs-conv-map*)
with *wfp exec' tconf'* **have** $red: P, t \vdash \langle a.M'(rev\ (take\ n\ stk)), h' \rangle \text{-}ta' \rightarrow ext\ \langle va, m' \rangle$
by(*simp add: WT-red-external-list-conv*)

from *stk* **have** $P, h \vdash take\ n\ stk\ [: \leq] take\ n\ ST$ **by**(*rule list-all2-takeI*)
then **obtain** *Ts* **where** $map\ typeof_h\ (take\ n\ stk) = map\ Some\ Ts$
by(*auto simp add: confs-conv-map*)

hence $\text{map typeof}_h (\text{rev } (\text{take } n \text{ stk})) = \text{map Some } (\text{rev } Ts)$ **by** (*simp only: rev-map[symmetric]*)
moreover hence $\text{map typeof}_{h'} (\text{rev } (\text{take } n \text{ stk})) = \text{map Some } (\text{rev } Ts)$ **using** *heaxt* **by** (*rule*
map-typeof-heaxt-mono)
with $\langle P, h' \vdash a \cdot M'(\text{rev } (\text{take } n \text{ stk})) : T' \rangle \langle D' \cdot M'(Ts') :: T' \rangle$ *sees'* $C \text{ Ta}' \text{ Ta}$
have $P \vdash \text{rev } Ts \leq Ts'$ **by** *cases* (*auto dest: sees-method-fun*)
ultimately have $P, h \vdash a \cdot M'(\text{rev } (\text{take } n \text{ stk})) : T'$
using $\text{Ta } C \text{ sees' params None } \langle D' \cdot M'(Ts') :: T' \rangle$
by (*auto simp add: external-WT'-iff confs-conv-map*)
from *red-external-wt-hconf-heaxt[OF wfp red heaxt this tconf hconf]*
obtain $ta'' \text{ va}' h'''$ **where** $P, t \vdash \langle a \cdot M'(\text{rev } (\text{take } n \text{ stk})), h \rangle - ta'' \rightarrow \text{ext } \langle \text{va}', h''' \rangle$
and ta'' : *collect-locks* $\{ \{ ta'' \} \}_l = \text{collect-locks } \{ \{ ta' \} \}_l$
collect-cond-actions $\{ \{ ta'' \} \}_c = \text{collect-cond-actions } \{ \{ ta' \} \}_c$
collect-interrupts $\{ \{ ta'' \} \}_i = \text{collect-interrupts } \{ \{ ta' \} \}_i$
by *auto*
with $\text{None } a \text{ Ta}$ *Invoke meth Ta' n C sees'*
have $(\text{extTA2JVM } P \text{ ta}'', \text{extRet2JVM } n \text{ h}''', \text{stk loc } C \text{ M pc Frs } \text{va}') \in \text{exec } P \text{ t } (xcp, h, \text{frs})$
by (*force intro: red-external-imp-red-external-aggr simp del: split-paired-Ex*)
with $ta'' \text{ ta}'$ **show** *?thesis* **by** (*fastforce simp del: split-paired-Ex*)
qed
qed
qed (*auto 4 4 split: if-split-asm simp add: split-beta ta-upd-simps exec-1-iff intro: rev-image-eqI simp*
del: split-paired-Ex)
with *check'* **have** $\exists ta' \sigma'. P, t \vdash \text{Normal } (xcp, h, \text{frs}) - ta' - \text{jvmd} \rightarrow \text{Normal } \sigma' \wedge \text{collect-locks}$
 $\{ \{ ta' \} \}_l \subseteq \text{collect-locks } \{ \{ ta \} \}_l \wedge$
 $\text{collect-cond-actions } \{ \{ ta' \} \}_c \subseteq \text{collect-cond-actions } \{ \{ ta \} \}_c \wedge \text{collect-interrupts } \{ \{ ta' \} \}_i \subseteq \text{collect-interrupts}$
 $\{ \{ ta \} \}_i$
apply *clarify*
apply (*rule exI conjI*) +
apply (*rule exec-1-d.exec-1-d-NormalI, auto simp add: exec-d-def*)
done
with L **show** *?thesis*
apply –
apply (*erule exE conjE | rule exI conjI*) +
prefer 2
apply (*rule-tac x'=(fst σ' , snd (snd σ')) and m'=(fst (snd σ')) in execd-mthr.can-syncI*)
apply *auto*
done
qed
qed
end
context *JVM-typesafe* **begin**
lemma *execd-preserve-deadlocked:*
assumes $wf: wf\text{-jvm-prog } \Phi \text{ } P$
shows *preserve-deadlocked JVM-final (mexecd P) convert-RA (correct-jvm-state Φ)*
proof (*unfold-locales*)
show *invariant3p (mexecdT P) (correct-jvm-state Φ)*
by (*rule invariant3p-correct-jvm-state-mexecdT[OF wf]*)
next
fix $s \text{ t}' \text{ ta}' \text{ s}' \text{ t } x \text{ ln}$
assume $s: s \in \text{correct-jvm-state } \Phi$
and *red: P t s - t' ta' → jvmd s'*

and $tst: thr\ s\ t = \lfloor(x, ln)\rfloor$
and $execd\text{-}mthr.\text{must}\text{-}sync\ P\ t\ x\ (shr\ s)$
moreover obtain $xcp\ frs$ **where** $x\ [simp]: x = (xcp, frs)$ **by**(cases x , $auto$)
ultimately have $ml: execd\text{-}mthr.\text{must}\text{-}sync\ P\ t\ (xcp, frs)\ (shr\ s)$ **by** $simp$
moreover from s **have** $cs': correct\text{-}state\text{-}ts\ \Phi\ (thr\ s)\ (shr\ s)$ **by**($simp\ add: correct\text{-}jvm\text{-}state\text{-}def$)
with tst **have** $\Phi \vdash t: (xcp, shr\ s, frs)\ \checkmark$ **by**($auto\ dest: ts\text{-}okD$)
moreover from red **have** $hext\ (shr\ s)\ (shr\ s')$ **by**(rule $execd\text{-}hext$)
moreover from $wf\ red\ cs'$ **have** $correct\text{-}state\text{-}ts\ \Phi\ (thr\ s')\ (shr\ s')$
by(rule $lifting\text{-}wf.\text{redT}\text{-}preserves[OF\ lifting\text{-}wf\text{-}correct\text{-}state\text{-}d]$)
from $red\ tst$ **have** $thr\ s'\ t \neq None$
by(cases s)(cases s' , rule $notI$, $auto\ dest: execd\text{-}mthr.\text{redT}\text{-}thread\text{-}not\text{-}disappear$)
with $\langle correct\text{-}state\text{-}ts\ \Phi\ (thr\ s')\ (shr\ s') \rangle$ **have** $hconf\ (shr\ s')$
by($auto\ dest: ts\text{-}okD\ simp\ add: correct\text{-}state\text{-}def$)
ultimately have $execd\text{-}mthr.\text{must}\text{-}sync\ P\ t\ (xcp, frs)\ (shr\ s')$
by-(rule $must\text{-}sync\text{-}preserved\text{-}d[OF\ wf]$)
thus $execd\text{-}mthr.\text{must}\text{-}sync\ P\ t\ x\ (shr\ s')$ **by** $simp$
next
fix $s\ t'\ ta'\ s'\ t\ x\ ln\ L$
assume $s: s \in correct\text{-}jvm\text{-}state\ \Phi$
and $red: P \vdash s \text{-}t' \triangleright ta' \rightarrow_{jvmd} s'$
and $tst: thr\ s\ t = \lfloor(x, ln)\rfloor$
and $execd\text{-}mthr.\text{can}\text{-}sync\ P\ t\ x\ (shr\ s')\ L$
moreover obtain $xcp\ frs$ **where** $x\ [simp]: x = (xcp, frs)$ **by**(cases x , $auto$)
ultimately have $ml: execd\text{-}mthr.\text{can}\text{-}sync\ P\ t\ (xcp, frs)\ (shr\ s')\ L$ **by** $simp$
moreover from s **have** $cs': correct\text{-}state\text{-}ts\ \Phi\ (thr\ s)\ (shr\ s)$ **by**($simp\ add: correct\text{-}jvm\text{-}state\text{-}def$)
with tst **have** $\Phi \vdash t: (xcp, shr\ s, frs)\ \checkmark$ **by**($auto\ dest: ts\text{-}okD$)
moreover from red **have** $hext\ (shr\ s)\ (shr\ s')$ **by**(rule $execd\text{-}hext$)
moreover from $red\ tst$ **have** $thr\ s'\ t \neq None$
by(cases s)(cases s' , rule $notI$, $auto\ dest: execd\text{-}mthr.\text{redT}\text{-}thread\text{-}not\text{-}disappear$)
from $red\ cs'$ **have** $correct\text{-}state\text{-}ts\ \Phi\ (thr\ s')\ (shr\ s')$
by(rule $lifting\text{-}wf.\text{redT}\text{-}preserves[OF\ lifting\text{-}wf\text{-}correct\text{-}state\text{-}d[OF\ wf]]$)
with $\langle thr\ s'\ t \neq None \rangle$ **have** $hconf\ (shr\ s')$
by($auto\ dest: ts\text{-}okD\ simp\ add: correct\text{-}state\text{-}def$)
ultimately have $\exists L' \subseteq L. execd\text{-}mthr.\text{can}\text{-}sync\ P\ t\ (xcp, frs)\ (shr\ s)\ L'$
by-(rule $can\text{-}sync\text{-}devreserp\text{-}d[OF\ wf]$)
thus $\exists L' \subseteq L. execd\text{-}mthr.\text{can}\text{-}sync\ P\ t\ x\ (shr\ s)\ L'$ **by** $simp$
qed

end

and now everything again for the aggressive VM

context $JVM\text{-}heap\text{-}conf\text{-}base'$ **begin**

lemma $must\text{-}lock\text{-}d\text{-}eq\text{-}must\text{-}lock$:

$\llbracket wf\text{-}jvm\text{-}prog_{\Phi}\ P; \Phi \vdash t: (xcp, h, frs)\ \checkmark \rrbracket$
 $\implies execd\text{-}mthr.\text{must}\text{-}sync\ P\ t\ (xcp, frs)\ h = exec\text{-}mthr.\text{must}\text{-}sync\ P\ t\ (xcp, frs)\ h$
apply(rule $iffI$)
apply(rule $exec\text{-}mthr.\text{must}\text{-}syncI$)
apply(erule $exec\text{-}mthr.\text{must}\text{-}syncE$)
apply($simp\ only: mexec\text{-}eq\text{-}mexecd$)
apply($blast$)
apply(rule $execd\text{-}mthr.\text{must}\text{-}syncI$)
apply(erule $exec\text{-}mthr.\text{must}\text{-}syncE$)
apply($simp\ only: mexec\text{-}eq\text{-}mexecd[symmetric]$)

apply(blast)
done

lemma *can-lock-d-eq-can-lock*:

$\llbracket wf\text{-jvm-prog}_{\Phi} P; \Phi \vdash t: (xcp, h, frs) \checkmark \rrbracket$
 $\implies \text{execd-mthr.can-sync } P \ t \ (xcp, frs) \ h \ L = \text{exec-mthr.can-sync } P \ t \ (xcp, frs) \ h \ L$

apply(rule *iffI*)

apply(erule *execd-mthr.can-syncE*)

apply(rule *exec-mthr.can-syncI*)

apply(simp only: *mexec-eq-mexecd*)

apply(assumption)+

apply(erule *exec-mthr.can-syncE*)

apply(rule *execd-mthr.can-syncI*)

by(simp only: *mexec-eq-mexecd*)

end

context *JVM-typesafe* **begin**

lemma *exec-preserve-deadlocked*:

assumes *wf*: $wf\text{-jvm-prog}_{\Phi} P$

shows *preserve-deadlocked JVM-final* (*mexec P*) *convert-RA* (*correct-jvm-state* Φ)

proof –

interpret *preserve-deadlocked JVM-final mexecd P convert-RA correct-jvm-state* Φ

by(rule *execd-preserve-deadlocked*) *fact*+

{ **fix** *s t' ta' s' t x*

assume *s*: $s \in \text{correct-jvm-state } \Phi$

and *red*: $P \vdash s -t \triangleright ta' \rightarrow_{jvm} s'$

and *tst*: $\text{thr } s \ t = \llbracket (x, \text{no-wait-locks}) \rrbracket$

obtain *xcp frs* **where** *x* [*simp*]: $x = (xcp, frs)$ **by**(cases *x*, *auto*)

from *s* **have** *css*: *correct-state-ts* Φ (*thr s*) (*shr s*) **by**(simp add: *correct-jvm-state-def*)

with *red* **have** *redd*: $P \vdash s -t \triangleright ta' \rightarrow_{jvmd} s'$ **by**(simp add: *mexecT-eq-mexecdT*[*OF wf*])

from *css tst* **have** *cst*: $\Phi \vdash t: (xcp, shr \ s, frs) \checkmark$ **by**(auto dest: *ts-okD*)

from *redd* **have** *cst'*: $\Phi \vdash t: (xcp, shr \ s', frs) \checkmark$

proof(cases rule: *execd-mthr.redT-elims*)

case *acquire* **with** *cst* **show** *?thesis* **by** *simp*

next

case (*normal X X' M' ws'*)

obtain *XCP FRS* **where** *X* [*simp*]: $X = (XCP, FRS)$ **by**(cases *X*, *auto*)

obtain *XCP' FRS'* **where** *X'* [*simp*]: $X' = (XCP', FRS')$ **by**(cases *X'*, *auto*)

from $\langle mexecd \ P \ t' \ (X, shr \ s) \ ta' \ (X', M') \rangle$

have $P, t' \vdash \text{Normal } (XCP, shr \ s, FRS) -ta' -jvmd \rightarrow \text{Normal } (XCP', M', FRS')$ **by** *simp*

moreover **from** $\langle thr \ s \ t' = \llbracket (X, \text{no-wait-locks}) \rrbracket \rangle$ *css*

have $\Phi \vdash t': (XCP, shr \ s, FRS) \checkmark$ **by**(auto dest: *ts-okD*)

ultimately **have** $\Phi \vdash t': (XCP, M', FRS) \checkmark$ **by** –(rule *correct-state-heap-change*[*OF wf*])

moreover **from** *lifting-wf.redT-updTs-preserves*[*OF lifting-wf.correct-state-d*[*OF wf*] *css*, *OF*
 $\langle mexecd \ P \ t' \ (X, shr \ s) \ ta' \ (X', M') \rangle \langle thr \ s \ t' = \llbracket (X, \text{no-wait-locks}) \rrbracket \rangle$, *of no-wait-locks*] *thread-oks*
 $(thr \ s) \ \{\!\! \{ ta' \!\!\}$

have *correct-state-ts* Φ (*redT-updTs* (*thr s*) $\{\!\! \{ ta' \!\!\}_t$)($t' \mapsto (X', \text{no-wait-locks})$)) *M'* **by** *simp*

ultimately **have** *correct-state-ts* Φ (*redT-updTs* (*thr s*) $\{\!\! \{ ta' \!\!\}_t$) *M'*

using $\langle thr \ s \ t' = \llbracket (X, \text{no-wait-locks}) \rrbracket \rangle$ *thread-oks* (*thr s*) $\{\!\! \{ ta' \!\!\}_t$

apply(auto intro!: *ts-okI* dest: *ts-okD*)

apply(case-tac $t=t'$)

```

    apply(fastforce dest: redT-updTs-Some)
  apply(drule-tac t=t in ts-okD, fastforce+)
done
hence correct-state-ts  $\Phi$  (redT-updTs (thr s)  $\{ta\}_t$ ) (shr s')
  using  $\langle s' = (redT-updLs (locks s) t' \{ta\}_t), ((redT-updTs (thr s) \{ta\}_t)(t' \mapsto (X', redT-updLns$ 
  (locks s) t' no-wait-locks  $\{ta\}_t)$ ),  $M'$ ),  $ws'$ , redT-updLs (interrupts s)  $\{ta\}_i$ )
  by simp
  moreover from tst  $\langle thread-oks (thr s) \{ta\}_t \rangle$ 
  have redT-updTs (thr s)  $\{ta\}_t t = \llbracket (x, no-wait-locks) \rrbracket$  by(auto intro: redT-updTs-Some)
  ultimately show ?thesis by(auto dest: ts-okD)
qed
{ assume exec-mthr.must-sync P t x (shr s)
  hence ml: exec-mthr.must-sync P t (xcp, frs) (shr s) by simp
  with cst have exeecd-mthr.must-sync P t (xcp, frs) (shr s)
    by(auto dest: must-lock-d-eq-must-lock[OF wf])
  with s redd tst have exeecd-mthr.must-sync P t x (shr s')
    unfolding x by(rule can-lock-preserved)
  with cst' have exec-mthr.must-sync P t x (shr s')
    by(auto dest: must-lock-d-eq-must-lock[OF wf]) }
note ml = this
{ fix L
  assume exec-mthr.can-sync P t x (shr s') L
  hence cl: exec-mthr.can-sync P t (xcp, frs) (shr s') L by simp
  with cst' have exeecd-mthr.can-sync P t (xcp, frs) (shr s') L
    by(auto dest: can-lock-d-eq-can-lock[OF wf])
  with s redd tst
  have  $\exists L' \subseteq L. exeecd-mthr.can-sync P t x (shr s) L'$ 
    unfolding x by(rule can-lock-devreserp)
  then obtain L' where exeecd-mthr.can-sync P t x (shr s) L'
    and L':  $L' \subseteq L$  by blast
  with cst have exec-mthr.can-sync P t x (shr s) L'
    by(auto dest: can-lock-d-eq-can-lock[OF wf])
  with L' have  $\exists L' \subseteq L. exeecd-mthr.can-sync P t x (shr s) L'$ 
    by(blast) }
note this ml }
  moreover have invariant3p (mexecT P) (correct-jvm-state  $\Phi$ ) by(rule invariant3p-correct-jvm-state-mexecT[OF wf])
  ultimately show ?thesis by(unfold-locales)
qed

end

end
end

```

6.9 Monotonicity of eff and app

```

theory EffectMono
imports
  Effect
begin

declare not-Err-eq [iff]

```

declare *widens-trans*[*trans*]

lemma *app_i-mono*:

assumes *wf*: *wf-prog p P*

assumes *less*: $P \vdash \tau \leq_i \tau'$

shows $app_i (i, P, mxs, mpc, rT, \tau') \implies app_i (i, P, mxs, mpc, rT, \tau)$

proof –

assume *app*: $app_i (i, P, mxs, mpc, rT, \tau')$

obtain *ST LT ST' LT'* **where**

[*simp*]: $\tau = (ST, LT)$ **and**

[*simp*]: $\tau' = (ST', LT')$

by (*cases* τ , *cases* τ')

from *less* **have** [*simp*]: *size ST = size ST'* **and** [*simp*]: *size LT = size LT'*

by (*auto dest: list-all2-lengthD*)

note [*iff*] = *list-all2-Cons2 widen-Class*

note [*simp*] = *fun-of-def*

from *app less* **show** $app_i (i, P, mxs, mpc, rT, \tau)$

proof (*cases i*)

case *Load*

with *app less* **show** *?thesis* **by** (*auto dest!: list-all2-nthD*)

next

case (*Invoke M n*)

with *app* **have** $n < size ST'$ **by** *simp*

{ **assume** $ST!n = NT$ **hence** *?thesis* **using** n *app Invoke* **by** *simp* }

moreover {

assume $ST!n = NT$

moreover with n *less* **have** $ST!n = NT$

by (*auto dest: list-all2-nthD*)

ultimately have *?thesis* **using** n *app Invoke* **by** *simp* }

moreover {

assume *ST*: $ST!n \neq NT$ **and** *ST'*: $ST!n \neq NT$

from *ST' app Invoke*

obtain *D Ts T m C'*

where *D*: *class-type-of'* ($ST!n$) = [*D*]

and *Ts*: $P \vdash rev (take\ n\ ST') [\leq] Ts$

and *D-M*: $P \vdash D\ sees\ M: Ts \rightarrow T = m\ in\ C'$

by *fastforce*

from *less* **have** $P \vdash ST!n \leq ST!n$

by (*auto dest: list-all2-nthD2[OF - n]*)

with *D* **obtain** *D'* **where** *D'*: *class-type-of'* ($ST!n$) = [*D'*]

and *DsubC*: $P \vdash D' \preceq^* D$

using *ST* **by** (*rule widen-is-class-type-of*)

from *wf D-M DsubC* **obtain** *Ts' T' m' C''* **where**

D'-M: $P \vdash D'\ sees\ M: Ts' \rightarrow T' = m'\ in\ C''$ **and**

Ts': $P \vdash Ts [\leq] Ts'$

by (*blast dest: sees-method-mono*)

from *less* **have** $P \vdash rev (take\ n\ ST) [\leq] rev (take\ n\ ST')$ **by** *simp*


```

    also note  $T_s$  also note  $T_s'$ 
    finally have  $P \vdash \text{rev } (take\ n\ ST) [\leq] T_s'$  .
    with  $D'-M\ D'$  app less Invoke  $D$  have ?thesis by(auto)
  }
  ultimately show ?thesis by blast
next
case Getfield
with app less show ?thesis
  by(fastforce simp add: sees-field-def widen-Array dest: has-fields-fun)
next
case Putfield
with app less show ?thesis
  by (fastforce intro: widen-trans rtrancl-trans simp add: sees-field-def widen-Array dest: has-fields-fun)
next
case CAS
with app less show ?thesis
  by (fastforce intro: widen-trans rtrancl-trans simp add: sees-field-def widen-Array dest: has-fields-fun)
next
case Return
with app less show ?thesis by (fastforce intro: widen-trans)
next
case ALoad
with app less show ?thesis by(auto simp add: widen-Array)
next
case AStore
with app less show ?thesis by(auto simp add: widen-Array)
next
case ALength
with app less show ?thesis by(auto simp add: widen-Array)
next
case (Checkcast T)
with app less show ?thesis
  by(auto elim!: refTE simp: widen-Array)
next
case (Instanceof T)
with app less show ?thesis
  by(auto elim!: refTE simp: widen-Array)
next
case ThrowExc
with app less show ?thesis
  by(auto elim!: refTE simp: widen-Array)
next
case MEnter
with app less show ?thesis
  by(auto elim!: refTE simp: widen-Array)
next
case MExit
with app less show ?thesis
  by(auto elim!: refTE simp: widen-Array)
next
case (BinOpInstr bop)
with app less show ?thesis by(force dest: WTrt-binop-widen-mono)
next
case Dup

```

```

  with app less show ?thesis
    by(auto dest: list-all2-lengthD)
next
  case Swap
  with app less show ?thesis
    by(auto dest: list-all2-lengthD)
qed (auto elim!: refTE not-refTE)
qed

lemma succs-mono:
  assumes wf: wf-prog p P and appi: appi (i,P,maxs,mpc,rT,τ')
  shows P ⊢ τ ≤i τ' ⇒ set (succs i τ pc) ⊆ set (succs i τ' pc)
proof (cases i)
  case (Invoke M n)
  obtain ST LT ST' LT' where
    [simp]: τ = (ST,LT) and [simp]: τ' = (ST',LT') by (cases τ, cases τ')
  assume P ⊢ τ ≤i τ'
  moreover
  with appi Invoke have n < size ST by (auto dest: list-all2-lengthD)
  ultimately
  have P ⊢ ST!n ≤ ST!n by (auto simp add: fun-of-def dest: list-all2-nthD)
  with Invoke show ?thesis by auto
next
  case ALoad
  obtain ST LT ST' LT' where
    [simp]: τ = (ST,LT) and [simp]: τ' = (ST',LT') by (cases τ, cases τ')
  assume P ⊢ τ ≤i τ'
  moreover
  with appi ALoad have 1 < size ST by (auto dest: list-all2-lengthD)
  ultimately
  have P ⊢ ST!1 ≤ ST!1 by (auto simp add: fun-of-def dest: list-all2-nthD)
  with ALoad show ?thesis by auto
next
  case AStore
  obtain ST LT ST' LT' where
    [simp]: τ = (ST,LT) and [simp]: τ' = (ST',LT') by (cases τ, cases τ')
  assume P ⊢ τ ≤i τ'
  moreover
  with appi AStore have 2 < size ST by (auto dest: list-all2-lengthD)
  ultimately
  have P ⊢ ST!2 ≤ ST!2 by (auto simp add: fun-of-def dest: list-all2-nthD)
  with AStore show ?thesis by auto
next
  case ALength
  obtain ST LT ST' LT' where
    [simp]: τ = (ST,LT) and [simp]: τ' = (ST',LT') by (cases τ, cases τ')
  assume P ⊢ τ ≤i τ'
  moreover
  with appi ALength have 0 < size ST by (auto dest: list-all2-lengthD)
  ultimately
  have P ⊢ ST!0 ≤ ST!0 by (auto simp add: fun-of-def dest: list-all2-nthD)
  with ALength show ?thesis by auto
next
  case MEnter

```

obtain $ST\ LT\ ST'\ LT'$ **where**
 $[simp]: \tau = (ST,LT)$ **and** $[simp]: \tau' = (ST',LT')$ **by** (*cases* τ , *cases* τ')
assume $P \vdash \tau \leq_i \tau'$
moreover
with $app_i\ MEnter$ **have** $0 < size\ ST$ **by** (*auto dest: list-all2-lengthD*)
ultimately
have $P \vdash ST!0 \leq ST^!0$ **by** (*auto simp add: fun-of-def dest: list-all2-nthD*)
with $MEnter$ **show** *?thesis* **by** *auto*

next
case $MExit$
obtain $ST\ LT\ ST'\ LT'$ **where**
 $[simp]: \tau = (ST,LT)$ **and** $[simp]: \tau' = (ST',LT')$ **by** (*cases* τ , *cases* τ')
assume $P \vdash \tau \leq_i \tau'$
moreover
with $app_i\ MExit$ **have** $0 < size\ ST$ **by** (*auto dest: list-all2-lengthD*)
ultimately
have $P \vdash ST!0 \leq ST^!0$ **by** (*auto simp add: fun-of-def dest: list-all2-nthD*)
with $MExit$ **show** *?thesis* **by** *auto*

qed *auto*

lemma *app-mono*:
assumes *wf: wf-prog p P*
assumes *less': P \vdash \tau \leq' \tau'*
shows $app\ i\ P\ m\ rT\ pc\ mpc\ xt\ \tau' \implies app\ i\ P\ m\ rT\ pc\ mpc\ xt\ \tau$

proof (*cases* τ)
case $None$ **thus** *?thesis* **by** *simp*

next
case ($Some\ \tau_1$)
moreover
with *less'* **obtain** τ_2 **where** $\tau' = Some\ \tau_2$ **by** (*cases* τ') *auto*
ultimately **have** $P \vdash \tau_1 \leq_i \tau_2$ **using** *less'* **by** *simp*

assume $app\ i\ P\ m\ rT\ pc\ mpc\ xt\ \tau'$
with $Some\ \tau_2$ **obtain**
 $app_i: app_i\ (i, P, pc, m, rT, \tau_2)$ **and**
 $xcpt: xcpt\ app\ i\ P\ pc\ m\ xt\ \tau_2$ **and**
 $succs: \forall (pc',s') \in set\ (eff\ i\ P\ pc\ xt\ (Some\ \tau_2)).\ pc' < mpc$
by (*auto simp add: app-def*)

from *wf less app_i* **have** $app_i\ (i, P, pc, m, rT, \tau_1)$ **by** (*rule app_i-mono*)
moreover
from *less* **have** $size\ (fst\ \tau_1) = size\ (fst\ \tau_2)$
by (*cases* τ_1 , *cases* τ_2) (*auto dest: list-all2-lengthD*)
with $xcpt$ **have** $xcpt\ app\ i\ P\ pc\ m\ xt\ \tau_1$ **by** (*simp add: xcpt-app-def*)
moreover
from *wf app_i less* **have** $\forall pc.\ set\ (succs\ i\ \tau_1\ pc) \subseteq set\ (succs\ i\ \tau_2\ pc)$
by (*blast dest: succs-mono*)
with $succs$
have $\forall (pc',s') \in set\ (eff\ i\ P\ pc\ xt\ (Some\ \tau_1)).\ pc' < mpc$
by (*cases* τ_1 , *cases* τ_2)
(*auto simp add: eff-def norm-eff-def xcpt-eff-def dest: bspec*)
ultimately
show *?thesis* **using** $Some$ **by** (*simp add: app-def*)

qed

lemma *eff_i-mono*:

assumes *wf*: *wf-prog* *p* *P*
assumes *less*: $P \vdash \tau \leq_i \tau'$
assumes *app_i*: *app* *i* *P* *m* *rT* *pc* *mpc* *xt* (Some τ')
assumes *succs*: *succs* *i* τ *pc* $\neq []$ *succs* *i* τ' *pc* $\neq []$
shows $P \vdash \text{eff}_i(i, P, \tau) \leq_i \text{eff}_i(i, P, \tau')$

proof –

obtain *ST* *LT* *ST'* *LT'* **where**

[*simp*]: $\tau = (ST, LT)$ **and**

[*simp*]: $\tau' = (ST', LT')$

by (*cases* τ , *cases* τ')

note [*simp*] = *eff-def* *app-def* *fun-of-def*

from *less* **have** $P \vdash (\text{Some } \tau) \leq' (\text{Some } \tau')$ **by** *simp*

from *wf* *this* *app_i*

have *app*: *app* *i* *P* *m* *rT* *pc* *mpc* *xt* (Some τ) **by** (*rule* *app-mono*)

from *less* *app* *app_i* **show** *?thesis*

proof (*cases* *i*)

case *ThrowExc* **with** *succs* **have** *False* **by** *simp*

thus *?thesis* ..

next

case *Return* **with** *succs* **have** *False* **by** *simp*

thus *?thesis* ..

next

case (*Load* *i*)

from *Load* *app* **obtain** *y* **where**

y: $i < \text{size } LT$ $LT!i = OK$ *y* **by** *clarsimp*

from *Load* *app_i* **obtain** *y'* **where**

y': $i < \text{size } LT'$ $LT'!i = OK$ *y'* **by** *clarsimp*

from *less* **have** $P \vdash LT \llbracket \leq_{\top} \rrbracket LT'$ **by** *simp*

with *y* *y'* **have** $P \vdash y \leq y'$ **by** (*auto* *dest*: *list-all2-nthD*)

with *Load* *less* *y* *y'* *app* *app_i*

show *?thesis* **by** *auto*

next

case *Store* **with** *less* *app* *app_i*

show *?thesis* **by** (*auto* *simp* *add*: *list-all2-update-cong*)

next

case (*Invoke* *M* *n*)

with *app_i* **have** *n*: $n < \text{size } ST'$ **by** *simp*

from *less* **have** [*simp*]: $\text{size } ST = \text{size } ST'$

by (*auto* *dest*: *list-all2-lengthD*)

from *Invoke* *succs* **have** $ST: ST!n \neq NT$ **and** $ST': ST!n \neq NT$ **by** (*auto*)

from ST' *app_i* *Invoke* **obtain** *D* *Ts* *T* *m* *C'*

where *D*: *class-type-of'* ($ST'!n$) = $\lfloor D \rfloor$

and *Ts*: $P \vdash \text{rev}(\text{take } n \text{ } ST') \llbracket \leq \rrbracket Ts$

and *D-M*: $P \vdash D \text{ sees } M: Ts \rightarrow T = m \text{ in } C'$

by *fastforce*

```

from less have  $P \vdash ST!n \leq ST^n$  by (auto dest: list-all2-nthD2[ $OF - n$ ])
with  $D$  obtain  $D'$  where  $D'$ : class-type-of' ( $ST ! n$ ) = [ $D'$ ]
  and  $DsubC$ :  $P \vdash D' \preceq^* D$ 
  using  $ST$  by (rule widen-is-class-type-of)

from wf D-M DsubC obtain  $Ts' T' m' C''$  where
   $D'-M$ :  $P \vdash D'$  sees  $M$ :  $Ts' \rightarrow T' = m'$  in  $C''$  and
   $Ts'$ :  $P \vdash Ts' [\leq] Ts'$  and  $P \vdash T' \leq T$  by (blast dest: sees-method-mono)

show ?thesis using Invoke n D D' D-M less D'-M Ts' (P ⊢ T' ≤ T)
  by (auto intro: list-all2-dropI)
next
  case ALoad with less app appi succs
  show ?thesis by (auto split: if-split-asm dest: Array-Array-widen)
next
  case AStore with less app appi succs
  show ?thesis by (auto split: if-split-asm dest: Array-Array-widen)
next
  case (BinOpInstr bop)
  with less app appi succs show ?thesis
    by auto (force dest: WTrt-binop-widen-mono WTrt-binop-fun)
qed auto
qed
end

```

6.10 The Typing Framework for the JVM

theory *TF-JVM*

imports

../DFA/Typing-Framework-err

EffectMono

BVSpec

../Common/ExternalCallWF

begin

definition *exec* :: '*addr jvm-prog* ⇒ *nat* ⇒ *ty* ⇒ *ex-table* ⇒ '*addr instr list* ⇒ *ty_i' err step-type*

where

exec G mxs rT et bs ≡

err-step (*size bs*) ($\lambda pc. app (bs!pc) G mxs rT pc (size bs) et$) ($\lambda pc. eff (bs!pc) G pc et$)

locale *JVM-sl* =

fixes P :: '*addr jvm-prog* **and** mxs **and** mxl_0

fixes Ts :: *ty list* **and** is :: '*addr instr list* **and** xt **and** T_r

fixes mxl **and** A **and** r **and** f **and** app **and** eff **and** $step$

defines [*simp*]: mxl ≡ $1 + size Ts + mxl_0$

defines [*simp*]: A ≡ *states* $P mxs mxl$

defines [*simp*]: r ≡ *JVM-SemiType.le* $P mxs mxl$

defines [*simp*]: f ≡ *JVM-SemiType.sup* $P mxs mxl$

defines [*simp*]: app ≡ $\lambda pc. Effect.app (is!pc) P mxs T_r pc (size is) xt$

defines [*simp*]: eff ≡ $\lambda pc. Effect.eff (is!pc) P pc xt$

defines [simp]: $step \equiv err\text{-}step (size\ is)\ app\ eff$

locale *start-context* = *JVM-sl* +

fixes *p* **and** *C*

assumes *wf*: *wf-prog p P*

assumes *C*: *is-class P C*

assumes *Ts*: *set Ts \subseteq types P*

fixes *first* :: *ty_i' and start*

defines [simp]:

first \equiv *Some ([], OK (Class C) # map OK Ts @ replicate mxl₀ Err)*

defines [simp]:

start \equiv *OK first # replicate (size is - 1) (OK None)*

6.10.1 Connecting JVM and Framework

lemma (in *JVM-sl*) *step-def-exec*: $step \equiv exec\ P\ mxs\ T_r\ xt\ is$
by (*simp add: exec-def*)

lemma *special-ex-swap-lemma* [iff]:

$(\exists X. (\exists n. X = A\ n \wedge P\ n) \wedge Q\ X) = (\exists n. Q(A\ n) \wedge P\ n)$

by *blast*

lemma *ex-in-list* [iff]:

$(\exists n. ST \in list\ n\ A \wedge n \leq mxs) = (set\ ST \subseteq A \wedge size\ ST \leq mxs)$

by (*unfold list-def*) *auto*

lemma *singleton-list*:

$(\exists n. [Class\ C] \in list\ n\ (types\ P) \wedge n \leq mxs) = (is\ class\ P\ C \wedge 0 < mxs)$

by(*auto*)

lemma *set-drop-subset*:

$set\ xs \subseteq A \implies set\ (drop\ n\ xs) \subseteq A$

by (*auto dest: in-set-dropD*)

lemma *Suc-minus-minus-le*:

$n < mxs \implies Suc\ (n - (n - b)) \leq mxs$

by *arith*

lemma *in-listE*:

$[[\ xs \in list\ n\ A; [size\ xs = n; set\ xs \subseteq A] \implies P] \implies P$

by (*unfold list-def*) *blast*

declare *is-relevant-entry-def* [simp]

declare *set-drop-subset* [simp]

lemma (in *start-context*) [simp, intro!]: *is-class P Throwable*

apply(*rule converse-subcls-is-class[OF wf]*)

apply(*rule xcpt-subcls-Throwable[OF - wf]*)

prefer 2

apply(*rule is-class-xcpt[OF - wf]*)

apply(*fastforce simp add: sys-xcpts-def sys-xcpts-list-def*)+

done

```

declare option.splits[split del]
declare option.case-cong[cong]
declare is-type-array [simp del]

```

```

theorem (in start-context) exec-pres-type:
  pres-type step (size is) A

```

```

declare option.case-cong-weak[cong]
declare option.splits[split]
declare is-type-array[simp]

```

```

declare is-relevant-entry-def [simp del]
declare set-drop-subset [simp del]

```

```

lemma lesubstep-type-simple:
  xs [ $\sqsubseteq_{\text{Product.le}}$  ( $=$ ) r] ys  $\implies$  set xs { $\sqsubseteq_r$ } set ys
declare is-relevant-entry-def [simp del]

```

```

lemma conjI2: [ $\llbracket A; A \implies B \rrbracket \implies A \wedge B$ ] by blast

```

```

lemma (in JVM-sl) eff-mono:
  [ $\llbracket \text{wf-prog } p \ P; \text{pc} < \text{length } is; s \sqsubseteq_{\text{sup-state-opt}} P \ t; \text{app } \text{pc} \ t \rrbracket$ 
 $\implies \text{set } (\text{eff } \text{pc} \ s) \ \{\sqsubseteq_{\text{sup-state-opt}} P\} \ \text{set } (\text{eff } \text{pc} \ t)$ ]

```

```

lemma (in JVM-sl) bounded-step: bounded step (size is)

```

```

theorem (in JVM-sl) step-mono:
  wf-prog wf-mb P  $\implies$  mono r step (size is) A

```

```

lemma (in start-context) first-in-A [iff]: OK first  $\in$  A
using Ts C by (force intro!; list-appendI simp add: JVM-states-unfold)

```

```

lemma (in JVM-sl) wt-method-def2:
  wt-method P C' Ts Tr mxs mxl0 is xt  $\tau$ s =
  (is  $\neq$  []  $\wedge$ 
   size  $\tau$ s = size is  $\wedge$ 
   OK ' set  $\tau$ s  $\subseteq$  states P mxs mxl  $\wedge$ 
   wt-start P C' Ts mxl0  $\tau$ s  $\wedge$ 
   wt-app-eff (sup-state-opt P) app eff  $\tau$ s)

```

```

end

```

6.11 LBV for the JVM

```

theory LBVJVM
imports
  ../DFA/Abstract-BV
  TF-JVM
begin

```

```

type-synonym prog-cert = cname  $\Rightarrow$  mname  $\Rightarrow$  tyi' err list

```

```

definition check-cert :: 'addr jvm-prog  $\Rightarrow$  nat  $\Rightarrow$  nat  $\Rightarrow$  nat  $\Rightarrow$  tyi' err list  $\Rightarrow$  bool

```

where

$$\text{check-cert } P \text{ } m\acute{x}s \text{ } m\acute{x}l \text{ } n \text{ } cert \equiv \text{check-types } P \text{ } m\acute{x}s \text{ } m\acute{x}l \text{ } cert \wedge \text{size } cert = n+1 \wedge \\ (\forall i < n. \text{cert}!i \neq \text{Err}) \wedge \text{cert}!n = \text{OK None}$$

definition $lbvjvm :: 'addr \text{ jvm-prog} \Rightarrow nat \Rightarrow nat \Rightarrow ty \Rightarrow \text{ex-table} \Rightarrow \\ ty_i' \text{ err list} \Rightarrow 'addr \text{ instr list} \Rightarrow ty_i' \text{ err} \Rightarrow ty_i' \text{ err}$

where

$$lbvjvm \text{ } P \text{ } m\acute{x}s \text{ } m\acute{x}r \text{ } T_r \text{ } et \text{ } cert \text{ } bs \equiv \\ \text{wtl-inst-list } bs \text{ } cert \text{ } (\text{JVM-SemiType.sup } P \text{ } m\acute{x}s \text{ } m\acute{x}r) \text{ } (\text{JVM-SemiType.le } P \text{ } m\acute{x}s \text{ } m\acute{x}r) \text{ } \text{Err} \text{ } (\text{OK} \\ \text{None}) \text{ } (\text{exec } P \text{ } m\acute{x}s \text{ } T_r \text{ } et \text{ } bs) \text{ } 0$$

definition $wt-lbv :: 'addr \text{ jvm-prog} \Rightarrow \text{cname} \Rightarrow ty \text{ list} \Rightarrow ty \Rightarrow nat \Rightarrow nat \Rightarrow \\ \text{ex-table} \Rightarrow ty_i' \text{ err list} \Rightarrow 'addr \text{ instr list} \Rightarrow \text{bool}$

where

$$wt-lbv \text{ } P \text{ } C \text{ } Ts \text{ } T_r \text{ } m\acute{x}s \text{ } m\acute{x}l_0 \text{ } et \text{ } cert \text{ } ins \equiv \\ \text{check-cert } P \text{ } m\acute{x}s \text{ } (1 + \text{size } Ts + m\acute{x}l_0) \text{ } (\text{size } ins) \text{ } cert \wedge \\ 0 < \text{size } ins \wedge \\ (\text{let } start = \text{Some } ([], (\text{OK } (\text{Class } C)) \# ((\text{map } \text{OK } Ts)) \text{ @ } (\text{replicate } m\acute{x}l_0 \text{ Err}))); \\ \text{result} = lbvjvm \text{ } P \text{ } m\acute{x}s \text{ } (1 + \text{size } Ts + m\acute{x}l_0) \text{ } T_r \text{ } et \text{ } cert \text{ } ins \text{ } (\text{OK } start) \\ \text{in } \text{result} \neq \text{Err})$$

definition $wt\text{-jvm-prog-lbv} :: 'addr \text{ jvm-prog} \Rightarrow \text{prog-cert} \Rightarrow \text{bool}$

where

$$wt\text{-jvm-prog-lbv } P \text{ } cert \equiv \\ \text{wf-prog } (\lambda P \text{ } C \text{ } (mn, Ts, T_r, (m\acute{x}s, m\acute{x}l_0, b, et)). \text{wt-lbv } P \text{ } C \text{ } Ts \text{ } T_r \text{ } m\acute{x}s \text{ } m\acute{x}l_0 \text{ } et \text{ } (cert \text{ } C \text{ } mn) \text{ } b) \text{ } P$$

definition $mk\text{-cert} :: 'addr \text{ jvm-prog} \Rightarrow nat \Rightarrow ty \Rightarrow \text{ex-table} \Rightarrow 'addr \text{ instr list} \\ \Rightarrow ty_m \Rightarrow ty_i' \text{ err list}$

where

$$mk\text{-cert } P \text{ } m\acute{x}s \text{ } T_r \text{ } et \text{ } bs \text{ } phi \equiv \text{make-cert } (\text{exec } P \text{ } m\acute{x}s \text{ } T_r \text{ } et \text{ } bs) \text{ } (\text{map } \text{OK } phi) \text{ } (\text{OK None})$$

definition $\text{prg-cert} :: 'addr \text{ jvm-prog} \Rightarrow ty_P \Rightarrow \text{prog-cert}$

where

$$\text{prg-cert } P \text{ } phi \text{ } C \text{ } mn \equiv \text{let } (C, Ts, T_r, \text{meth}) = \text{method } P \text{ } C \text{ } mn; (m\acute{x}s, m\acute{x}l_0, ins, et) = \text{the meth} \\ \text{in } mk\text{-cert } P \text{ } m\acute{x}s \text{ } T_r \text{ } et \text{ } ins \text{ } (phi \text{ } C \text{ } mn)$$

lemma check-certD [intro?]:

$$\text{check-cert } P \text{ } m\acute{x}s \text{ } m\acute{x}l \text{ } n \text{ } cert \Longrightarrow \text{cert-ok } cert \text{ } n \text{ } \text{Err} \text{ } (\text{OK None}) \text{ } (\text{states } P \text{ } m\acute{x}s \text{ } m\acute{x}l) \\ \text{by } (\text{unfold } \text{cert-ok-def } \text{check-cert-def } \text{check-types-def}) \text{ auto}$$

lemma (in *start-context*) wt-lbv-wt-step :

assumes lbv : $\text{wt-lbv } P \text{ } C \text{ } Ts \text{ } T_r \text{ } m\acute{x}s \text{ } m\acute{x}l_0 \text{ } xt \text{ } cert \text{ } is$
shows $\exists \tau s \in \text{list } (\text{size } is) \text{ } A. \text{wt-step } r \text{ } \text{Err} \text{ } \text{step } \tau s \wedge \text{OK first } \sqsubseteq_r \tau s!0$

lemma (in *start-context*) wt-lbv-wt-method :

assumes lbv : $\text{wt-lbv } P \text{ } C \text{ } Ts \text{ } T_r \text{ } m\acute{x}s \text{ } m\acute{x}l_0 \text{ } xt \text{ } cert \text{ } is$
shows $\exists \tau s. \text{wt-method } P \text{ } C \text{ } Ts \text{ } T_r \text{ } m\acute{x}s \text{ } m\acute{x}l_0 \text{ } is \text{ } xt \text{ } \tau s$

lemma (in *start-context*) wt-method-wt-lbv :

assumes wt : $\text{wt-method } P \text{ } C \text{ } Ts \text{ } T_r \text{ } m\acute{x}s \text{ } m\acute{x}l_0 \text{ } is \text{ } xt \text{ } \tau s$
defines [simp]: $\text{cert} \equiv mk\text{-cert } P \text{ } m\acute{x}s \text{ } T_r \text{ } xt \text{ } is \text{ } \tau s$

shows $\text{wt-lbv } P \text{ } C \text{ } Ts \text{ } T_r \text{ } m\acute{x}s \text{ } m\acute{x}l_0 \text{ } xt \text{ } cert \text{ } is$

theorem *jvm-lbv-correct*:
 $wt\text{-jvm-prog-lbv } P \text{ Cert} \implies wf\text{-jvm-prog } P$
theorem *jvm-lbv-complete*:
assumes $wt: wf\text{-jvm-prog}_\Phi P$
shows $wt\text{-jvm-prog-lbv } P (prg\text{-cert } P \Phi)$
end

6.12 Kildall for the JVM

theory *BVExec*

imports

../DFA/Abstract-BV

TF-JVM

begin

definition *kiljvm* :: $'addr \text{ jvm-prog} \Rightarrow nat \Rightarrow nat \Rightarrow ty \Rightarrow$
 $'addr \text{ instr list} \Rightarrow ex\text{-table} \Rightarrow ty_i' \text{ err list} \Rightarrow ty_i' \text{ err list}$

where

$kiljvm P mxs mxl T_r is \text{ xt} \equiv$

$kildall (JVM\text{-SemiType.le } P mxs mxl) (JVM\text{-SemiType.sup } P mxs mxl)$

$(exec P mxs T_r \text{ xt } is)$

definition *wt-kildall* :: $'addr \text{ jvm-prog} \Rightarrow cname \Rightarrow ty \text{ list} \Rightarrow ty \Rightarrow nat \Rightarrow nat \Rightarrow$
 $'addr \text{ instr list} \Rightarrow ex\text{-table} \Rightarrow bool$

where

$wt\text{-kildall } P C' Ts T_r mxs mxl_0 is \text{ xt} \equiv$

$0 < size \text{ is} \wedge$

$(let \text{ first} = Some ([, [OK (Class C')]] @ (map OK Ts) @ (replicate mxl_0 Err));$

$\text{ start} = OK \text{ first} \# (replicate (size \text{ is} - 1) (OK None));$

$\text{ result} = kiljvm P mxs (1 + size Ts + mxl_0) T_r is \text{ xt } \text{ start}$

$in \forall n < size \text{ is}. \text{ result}!n \neq Err)$

definition *wf-jvm-prog_k* :: $'addr \text{ jvm-prog} \Rightarrow bool$

where

$wf\text{-jvm-prog}_k P \equiv$

$wf\text{-prog } (\lambda P C' (M, Ts, T_r, (mxs, mxl_0, is, xt)). wt\text{-kildall } P C' Ts T_r mxs mxl_0 is \text{ xt}) P$

theorem (**in** *start-context*) *is-bcv-kiljvm*:

$is\text{-bcv } r \text{ Err } step (size \text{ is}) A (kiljvm P mxs mxl T_r is \text{ xt})$

lemma *subset-replicate* [*intro?*]: $set (replicate n x) \subseteq \{x\}$

by (*induct n*) *auto*

lemma *in-set-replicate*:

assumes $x \in set (replicate n y)$

shows $x = y$

lemma (**in** *start-context*) *start-in-A* [*intro?*]:

$0 < size \text{ is} \implies \text{ start} \in list (size \text{ is}) A$

using $Ts C$

theorem (**in** *start-context*) *wt-kil-correct*:

assumes *wtk*: *wt-kildall* *P C Ts T_r mxs mxl₀* *is xt*
shows $\exists \tau s$. *wt-method* *P C Ts T_r mxs mxl₀* *is xt* τs

theorem (in *start-context*) *wt-kil-complete*:
assumes *wtm*: *wt-method* *P C Ts T_r mxs mxl₀* *is xt* τs
shows *wt-kildall* *P C Ts T_r mxs mxl₀* *is xt*

theorem *jvm-kildall-correct*:
wf-jvm-prog_k *P* = *wf-jvm-prog* *P*
end

6.13 Code generation for the byte code verifier

theory *BCVExec*
imports
BVNoTypeError
BVExec
begin

lemmas [*code-unfold*] = *exec-lub-def*

lemmas [*code*] = *JVM-le-unfold*[*THEN meta-eq-to-obj-eq*]

lemma *err-code* [*code*]:
Err.err *A* = *Collect* (*case-err* *True* (λx . $x \in A$))
by(*auto simp add: err-def split: err.split*)

lemma *list-code* [*code*]:
list *n* *A* = {*xs*. *size xs* = *n* \wedge *list-all* (λx . $x \in A$) *xs*}
unfolding *list-def*
by(*auto intro!: ext simp add: list-all-iff*)

lemma *opt-code* [*code*]:
opt *A* = *Collect* (*case-option* *True* (λx . $x \in A$))
by(*auto simp add: opt-def split: option.split*)

lemma *Times-code* [*code-unfold*]:
Sigma *A* ($\%.$ *B*) = {(*a*, *b*). $a \in A \wedge b \in B$ }
by *auto*

lemma *upto-esl-code* [*code*]:
upto-esl *m* (*A*, *r*, *f*) = (*Union* ((λn . *list* *n* *A*) ‘ {*..m*}), *Listn.le* *r*, *Listn.sup* *f*)
by(*auto simp add: upto-esl-def*)

lemmas [*code*] = *lesub-def* *plussub-def*

lemma *JVM-sup-unfold* [*code*]:
JVM-SemiType.sup *S* *m* *n* =
lift2 (*Opt.sup* (*Product.sup* (*Listn.sup* (*SemiType.sup* *S*)) ($\lambda x y$. *OK* (*map2* (*lift2* (*SemiType.sup* *S*))
x y))))))
unfolding *JVM-SemiType.sup-def* *JVM-SemiType.sl-def* *Opt.esl-def* *Err.sl-def*
stk-esl-def *loc-sl-def* *Product.esl-def* *Listn.sl-def* *upto-esl-def*
SemiType.esl-def *Err.esl-def*

by *simp*

declare *sup-fun-def* [*code*]

lemma [*code*]: *states P mxs mxl = fst(sl P mxs mxl)*

unfolding *states-def* ..

lemma *check-types-code* [*code*]:

check-types P mxs mxl τs = (list-all (λx. x ∈ (states P mxs mxl)) τs)

unfolding *check-types-def* **by**(*auto simp add: list-all-iff*)

lemma *wf-jvm-prog-code* [*code-unfold*]:

wf-jvm-prog = wf-jvm-prog_k

by(*simp add: fun-eq-iff jvm-kildall-correct*)

definition *wf-jvm-prog' = wf-jvm-prog*

ML-val $\langle @\{code\ wf-jvm-prog'\} \rangle$

end

theory *BV-Main*

imports

JVMDeadlocked

LBVJVM

BCVExec

begin

end

Chapter 7

Compilation

7.1 Method calls in expressions

theory *CallExpr* **imports**

../J/Expr

begin

fun *inline-call* :: ('a,'b,'addr) *exp* \Rightarrow ('a,'b,'addr) *exp* \Rightarrow ('a,'b,'addr) *exp*

and *inline-calls* :: ('a,'b,'addr) *exp* \Rightarrow ('a,'b,'addr) *exp list* \Rightarrow ('a,'b,'addr) *exp list*

where

inline-call *f* (*new* *C*) = *new* *C*
| *inline-call* *f* (*newA* *T* [*e*]) = *newA* *T* [*inline-call* *f* *e*]
| *inline-call* *f* (*Cast* *C* *e*) = *Cast* *C* (*inline-call* *f* *e*)
| *inline-call* *f* (*e* *instanceof* *T*) = (*inline-call* *f* *e*) *instanceof* *T*
| *inline-call* *f* (*Val* *v*) = *Val* *v*
| *inline-call* *f* (*Var* *V*) = *Var* *V*
| *inline-call* *f* (*V* := *e*) = *V* := *inline-call* *f* *e*
| *inline-call* *f* (*e* «*bop*» *e'*) = (if *is-val* *e* then (*e* «*bop*» *inline-call* *f* *e'*) else (*inline-call* *f* *e* «*bop*» *e'*))
| *inline-call* *f* (*a* [*i*]) = (if *is-val* *a* then *a* [*inline-call* *f* *i*] else (*inline-call* *f* *a*) [*i*])
| *inline-call* *f* (*AAss* *a* *i* *e*) =
 (if *is-val* *a* then if *is-val* *i* then *AAss* *a* *i* (*inline-call* *f* *e*) else *AAss* *a* (*inline-call* *f* *i*) *e*
 else *AAss* (*inline-call* *f* *a*) *i* *e*)
| *inline-call* *f* (*a* · *length*) = *inline-call* *f* *a* · *length*
| *inline-call* *f* (*e* · *F* {*D*}) = *inline-call* *f* *e* · *F* {*D*}
| *inline-call* *f* (*FAss* *e* *F* *D* *e'*) = (if *is-val* *e* then *FAss* *e* *F* *D* (*inline-call* *f* *e'*) else *FAss* (*inline-call* *f* *e*) *F* *D* *e'*)
| *inline-call* *f* (*CompareAndSwap* *e* *D* *F* *e'* *e''*) =
 (if *is-val* *e* then if *is-val* *e'* then *CompareAndSwap* *e* *D* *F* *e'* (*inline-call* *f* *e''*)
 else *CompareAndSwap* *e* *D* *F* (*inline-call* *f* *e'*) *e''*
 else *CompareAndSwap* (*inline-call* *f* *e*) *D* *F* *e'* *e''*)
| *inline-call* *f* (*e* · *M* (*es*)) =
 (if *is-val* *e* then if *is-vals* *es* \wedge *is-addr* *e* then *f* else *e* · *M* (*inline-calls* *f* *es*) else *inline-call* *f* *e* · *M* (*es*))
| *inline-call* *f* ({*V*:*T*=*vo*; *e*}) = {*V*:*T*=*vo*; *inline-call* *f* *e*}
| *inline-call* *f* (*sync*_{*V*} (*o'*) *e*) = *sync*_{*V*} (*inline-call* *f* *o'*) *e*
| *inline-call* *f* (*insync*_{*V*} (*a*) *e*) = *insync*_{*V*} (*a*) (*inline-call* *f* *e*)
| *inline-call* *f* (*e*; *e'*) = *inline-call* *f* *e*; *e'*
| *inline-call* *f* (if (*b*) *e* else *e'*) = (if (*inline-call* *f* *b*) *e* else *e'*)
| *inline-call* *f* (while (*b*) *e*) = while (*b*) *e*
| *inline-call* *f* (throw *e*) = throw (*inline-call* *f* *e*)
| *inline-call* *f* (try *e*1 catch (*C* *V*) *e*2) = try *inline-call* *f* *e*1 catch (*C* *V*) *e*2

| *inline-calls* f [] = []
 | *inline-calls* f ($e \# es$) = (if *is-val* e then $e \# \text{inline-calls } f \text{ } es$ else *inline-call* f $e \# es$)

fun *collapse* :: 'addr expr × 'addr expr list ⇒ 'addr expr **where**
collapse (e , []) = e
 | *collapse* (e , ($e' \# es$)) = *collapse* (*inline-call* e e' , es)

definition *is-call* :: ('a, 'b, 'addr) exp ⇒ bool
where *is-call* e = (*call* $e \neq \text{None}$)

definition *is-calls* :: ('a, 'b, 'addr) exp list ⇒ bool
where *is-calls* es = (*calls* $es \neq \text{None}$)

lemma *inline-calls-map-Val-append* [*simp*]:
inline-calls f (*map* *Val* vs @ es) = *map* *Val* vs @ *inline-calls* f es
by(*induct* vs , *auto*)

lemma *inline-call-eq-Val-aux*:
inline-call e $E = \text{Val } v \implies \text{call } E = \lfloor aMvs \rfloor \implies e = \text{Val } v$
by(*induct* E)(*auto* *split*: *if-split-asm*)

lemmas *inline-call-eq-Val* [*dest*] = *inline-call-eq-Val-aux* *inline-call-eq-Val-aux*[*OF sym*, *THEN sym*]

lemma *inline-calls-eq-empty* [*simp*]: *inline-calls* e $es = [] \longleftrightarrow es = []$
by(*cases* es , *auto*)

lemma *inline-calls-map-Val* [*simp*]: *inline-calls* e (*map* *Val* vs) = *map* *Val* vs
by(*induct* vs) *auto*

lemma **fixes** E :: ('a, 'b, 'addr) exp **and** Es :: ('a, 'b, 'addr) exp list
shows *inline-call-eq-Throw* [*dest*]: *inline-call* e $E = \text{Throw } a \implies \text{call } E = \lfloor aMvs \rfloor \implies e = \text{Throw } a \vee e = \text{addr } a$
by(*induct* E *rule*: *exp.induct*)(*fastforce* *split*: *if-split-asm*)**+**

lemma *Throw-eq-inline-call-eq* [*dest*]:
inline-call e $E = \text{Throw } a \implies \text{call } E = \lfloor aMvs \rfloor \implies \text{Throw } a = e \vee \text{addr } a = e$
by(*auto* *dest*: *inline-call-eq-Throw*[*OF sym*])

lemma *is-vals-inline-calls* [*dest*]:
 [*is-vals* (*inline-calls* e es); *calls* $es = \lfloor aMvs \rfloor$] ⇒ *is-val* e
by(*induct* es , *auto* *split*: *if-split-asm*)

lemma [*dest*]: [*inline-calls* e $es = \text{map } \text{Val } vs$; *calls* $es = \lfloor aMvs \rfloor$] ⇒ *is-val* e
 [*map* *Val* $vs = \text{inline-calls } e \text{ } es$; *calls* $es = \lfloor aMvs \rfloor$] ⇒ *is-val* e
by(*fastforce* *intro!*: *is-vals-inline-calls* *del*: *is-val.intros* *simp* *add*: *is-vals-conv* *elim*: *sym*)**+**

lemma *inline-calls-eq-Val-Throw* [*dest*]:
 [*inline-calls* e $es = \text{map } \text{Val } vs$ @ $\text{Throw } a \# es'$; *calls* $es = \lfloor aMvs \rfloor$] ⇒ $e = \text{Throw } a \vee \text{is-val } e$
apply(*induct* es *arbitrary*: vs a es')
apply(*auto* *simp* *add*: *Cons-eq-append-conv* *split*: *if-split-asm*)
done

lemma *Val-Throw-eq-inline-calls* [*dest*]:

$\llbracket \text{map Val vs @ Throw a \# es' = inline-calls e es; calls es = [aMvs]} \rrbracket \implies \text{Throw a} = e \vee \text{is-val } e$
by(*auto dest: inline-calls-eq-Val-Throw[OF sym]*)

declare *option.split* [*split del*] *if-split-asm* [*split*] *if-split* [*split del*]

lemma *call-inline-call* [*simp*]:

$\text{call } e = [aMvs] \implies \text{call } (\text{inline-call } \{v:T=vo; e'\} e) = \text{call } e'$

$\text{calls es} = [aMvs] \implies \text{calls } (\text{inline-calls } \{v:T=vo;e'\} es) = \text{call } e'$

apply(*induct e and es rule: call.induct calls.induct*)

apply(*fastforce*)

apply(*fastforce*)

apply(*fastforce*)

apply(*fastforce*)

apply(*fastforce*)

apply(*fastforce split: if-split*)

apply(*fastforce*)

apply(*fastforce*)

apply(*fastforce split: if-split*)

apply(*clarsimp*)

apply(*fastforce split: if-split*)

apply(*fastforce split: if-split*)

apply(*fastforce*)

apply(*fastforce*)

apply(*fastforce split: if-split*)

apply(*fastforce split: if-split*)

apply(*fastforce split: if-split*)

apply(*fastforce*)

apply(*fastforce*)

apply(*fastforce*)

apply(*fastforce*)

apply(*fastforce*)

apply(*fastforce*)

apply(*fastforce*)

apply(*fastforce*)

apply(*fastforce*)

apply(*fastforce split: if-split*)

done

declare *option.split* [*split*] *if-split* [*split*] *if-split-asm* [*split del*]

lemma *fv-inline-call*: $\text{fv } (\text{inline-call } e' e) \subseteq \text{fv } e \cup \text{fv } e'$

and *fvs-inline-calls*: $\text{fvs } (\text{inline-calls } e' es) \subseteq \text{fvs } es \cup \text{fv } e'$

by(*induct e and es rule: call.induct calls.induct*)(*fastforce split: if-split-asm*)+

lemma *contains-insync-inline-call-conv*:

$\text{contains-insync } (\text{inline-call } e e') \iff \text{contains-insync } e \wedge \text{call } e' \neq \text{None} \vee \text{contains-insync } e'$

and *contains-insyncs-inline-calls-conv*:

$\text{contains-insyncs } (\text{inline-calls } e es') \iff \text{contains-insync } e \wedge \text{calls } es' \neq \text{None} \vee \text{contains-insyncs } es'$

by(*induct e' and es' rule: call.induct calls.induct*)(*auto split: if-split-asm simp add: is-vals-conv*)

lemma *contains-insync-inline-call* [*simp*]:

$\text{call } e' = [aMvs] \implies \text{contains-insync } (\text{inline-call } e e') \iff \text{contains-insync } e \vee \text{contains-insync } e'$

and *contains-insyncs-inline-calls* [*simp*]:
calls $es' = [aMvs] \implies \text{contains-insyncs} (\text{inline-calls } e \text{ } es') \longleftrightarrow \text{contains-insync } e \vee \text{contains-insyncs } es'$
by(*simp-all add: contains-insync-inline-call-conv contains-insyncs-inline-calls-conv*)

lemma *collapse-append* [*simp*]:
 $\text{collapse } (e, es @ es') = \text{collapse } (\text{collapse } (e, es), es')$
by(*induct es arbitrary: e, auto*)

lemma *collapse-conv-foldl*:
 $\text{collapse } (e, es) = \text{foldl inline-call } e \text{ } es$
by(*induct es arbitrary: e*) *simp-all*

lemma *fv-collapse*: $\forall e \in \text{set } es. \text{is-call } e \implies \text{fv } (\text{collapse } (e, es)) \subseteq \text{fvs } (e \# es)$
apply(*induct es arbitrary: e*)
apply(*insert fv-inline-call*)
apply(*fastforce dest: subsetD*)
done

lemma *final-inline-callD*: $\llbracket \text{final } (\text{inline-call } E \text{ } e); \text{is-call } e \rrbracket \implies \text{final } E$
by(*induct e*)(*auto simp add: is-call-def split: if-split-asm*)

lemma *collapse-finalD*: $\llbracket \text{final } (\text{collapse } (e, es)); \forall e \in \text{set } es. \text{is-call } e \rrbracket \implies \text{final } e$
by(*induct es arbitrary: e*)(*auto dest: final-inline-callD*)

context *heap-base* **begin**

definition *synthesized-call* :: $'m \text{ prog} \Rightarrow 'heap \Rightarrow ('addr \times \text{mname} \times 'addr \text{ val list}) \Rightarrow \text{bool}$
where
 $\text{synthesized-call } P \text{ } h =$
 $(\lambda(a, M, vs). \exists T \text{ } Ts \text{ } Tr \text{ } D. \text{typeof-addr } h \text{ } a = \lfloor T \rfloor \wedge P \vdash \text{class-type-of } T \text{ sees } M:Ts \rightarrow Tr = \text{Native in } D)$

lemma *synthesized-call-conv*:
 $\text{synthesized-call } P \text{ } h \text{ } (a, M, vs) =$
 $(\exists T \text{ } Ts \text{ } Tr \text{ } D. \text{typeof-addr } h \text{ } a = \lfloor T \rfloor \wedge P \vdash \text{class-type-of } T \text{ sees } M:Ts \rightarrow Tr = \text{Native in } D)$
by(*simp add: synthesized-call-def*)

end

end

7.2 The JinjaThreads source language with explicit call stacks

theory *J0* **imports**
../J/WWellForm
../J/WellType
../J/Threaded
../Framework/FWBisimulation
CallExpr
begin
declare *widen-refT* [*elim*]

abbreviation *final-expr0* :: 'addr expr × 'addr expr list ⇒ bool **where**
final-expr0 ≡ λ(e, es). *final e* ∧ es = []

type-synonym

('addr, 'thread-id, 'heap) *J0-thread-action* =
('addr, 'thread-id, 'addr expr × 'addr expr list, 'heap) *Jinja-thread-action*

type-synonym

('addr, 'thread-id, 'heap) *J0-state* = ('addr, 'thread-id, 'addr expr × 'addr expr list, 'heap, 'addr) *state*

print-translation <

```

let
  fun tr'
    [a1, t
    , Const (@{type-syntax prod}, -) $
      (Const (@{type-syntax exp}, -) $
        Const (@{type-syntax String.literal}, -) $ Const (@{type-syntax unit}, -) $ a2) $
      (Const (@{type-syntax list}, -) $
        (Const (@{type-syntax exp}, -) $
          Const (@{type-syntax String.literal}, -) $
            Const (@{type-syntax unit}, -) $ a3))
    , h] =
  if a1 = a2 andalso a2 = a3 then Syntax.const @{{type-syntax J0-thread-action}} $ a1 $ t $ h
  else raise Match;
  in [(@{type-syntax Jinja-thread-action}, K tr')]
end
>
typ ('addr, 'thread-id, 'heap) J0-thread-action

```

print-translation <

```

let
  fun tr'
    [a1, t
    , Const (@{type-syntax prod}, -) $
      (Const (@{type-syntax exp}, -) $
        Const (@{type-syntax String.literal}, -) $ Const (@{type-syntax unit}, -) $ a2) $
      (Const (@{type-syntax list}, -) $
        (Const (@{type-syntax exp}, -) $
          Const (@{type-syntax String.literal}, -) $
            Const (@{type-syntax unit}, -) $ a3))
    , h, a4] =
  if a1 = a2 andalso a2 = a3 then Syntax.const @{{type-syntax J0-state}} $ a1 $ t $ h
  else raise Match;
  in [(@{type-syntax state}, K tr')]
end
>
typ ('addr, 'thread-id, 'heap) J0-state

```

definition *extNTA2J0* :: 'addr J-prog ⇒ (cname × mname × 'addr) ⇒ ('addr expr × 'addr expr list)
where

extNTA2J0 P = (λ(C, M, a). let (D, -, -, meth) = method P C M; (-, body) = the meth

$in (\{this:Class D=[Addr a]; body\}, [])$

lemma *extNTA2J0-iff* [simp]:

$extNTA2J0 P (C, M, a) =$
 $(\{this:Class (fst (method P C M))=[Addr a]; snd (the (snd (snd (snd (method P C M))))\}, [])$
by(simp add: extNTA2J0-def split-def)

abbreviation *extTA2J0* ::

$'addr J\text{-prog} \Rightarrow ('addr, 'thread\text{-id}, 'heap) \text{ external-thread-action} \Rightarrow ('addr, 'thread\text{-id}, 'heap) J0\text{-thread-action}$
where $extTA2J0 P \equiv \text{convert-extTA} (extNTA2J0 P)$

lemma *obs-a-extTA2J-eq-obs-a-extTA2J0* [simp]: $\{extTA2J P ta\}_o = \{extTA2J0 P ta\}_o$

by(cases ta)(simp add: ta-upd-simps)

lemma *extTA2J0-ε*: $extTA2J0 P \varepsilon = \varepsilon$

by(simp)

context *J-heap-base* **begin**

definition *no-call* :: $'m \text{ prog} \Rightarrow 'heap \Rightarrow ('a, 'b, 'addr) \text{ exp} \Rightarrow \text{bool}$

where $no\text{-call} P h e = (\forall aMvs. \text{call } e = [aMvs] \longrightarrow \text{synthesized-call } P h aMvs)$

definition *no-calls* :: $'m \text{ prog} \Rightarrow 'heap \Rightarrow ('a, 'b, 'addr) \text{ exp list} \Rightarrow \text{bool}$

where $no\text{-calls} P h es = (\forall aMvs. \text{calls } es = [aMvs] \longrightarrow \text{synthesized-call } P h aMvs)$

inductive *red0* ::

$'addr J\text{-prog} \Rightarrow 'thread\text{-id} \Rightarrow 'addr \text{ expr} \Rightarrow 'addr \text{ expr list} \Rightarrow 'heap$
 $\Rightarrow ('addr, 'thread\text{-id}, 'heap) J0\text{-thread-action} \Rightarrow 'addr \text{ expr} \Rightarrow 'addr \text{ expr list} \Rightarrow 'heap \Rightarrow \text{bool}$
 $(\langle -, - \vdash 0 ((1 \langle - / -, - \rangle) \dashrightarrow (1 \langle - / -, - \rangle)) \rangle [51, 0, 0, 0, 0, 0, 0, 0] 81)$

for $P :: 'addr J\text{-prog}$ **and** $t :: 'thread\text{-id}$

where

red0Red:

$\llbracket extTA2J0 P, P, t \vdash \langle e, (h, \text{Map.empty}) \rangle -ta \rightarrow \langle e', (h', xs') \rangle;$
 $\forall aMvs. \text{call } e = [aMvs] \longrightarrow \text{synthesized-call } P h aMvs \rrbracket$
 $\Longrightarrow P, t \vdash 0 \langle e/es, h \rangle -ta \rightarrow \langle e'/es, h' \rangle$

| *red0Call*:

$\llbracket \text{call } e = [(a, M, vs)]; \text{typeof-addr } h a = [U];$
 $P \vdash \text{class-type-of } U \text{ sees } M:Ts \rightarrow T = [(pns, body)] \text{ in } D;$
 $\text{size } vs = \text{size } pns; \text{size } Ts = \text{size } pns \rrbracket$
 $\Longrightarrow P, t \vdash 0 \langle e/es, h \rangle -\varepsilon \rightarrow \langle \text{blocks } (this \# pns) (Class D \# Ts) (Addr a \# vs) \text{ body}/e\#es, h \rangle$

| *red0Return*:

$\text{final } e' \Longrightarrow P, t \vdash 0 \langle e'/e\#es, h \rangle -\varepsilon \rightarrow \langle \text{inline-call } e' e/es, h \rangle$

abbreviation *J0-start-state* :: $'addr J\text{-prog} \Rightarrow \text{cname} \Rightarrow \text{mname} \Rightarrow 'addr \text{ val list} \Rightarrow ('addr, 'thread\text{-id}, 'heap) J0\text{-state}$

where

$J0\text{-start-state} \equiv$
 $\text{start-state } (\lambda C M Ts T (pns, body) \text{ vs. } (\text{blocks } (this \# pns) (Class C \# Ts) (Null \# vs) \text{ body}, []))$

abbreviation *mred0* ::

$'addr J\text{-prog} \Rightarrow ('addr, 'thread\text{-id}, 'addr \text{ expr} \times 'addr \text{ expr list}, 'heap, 'addr, ('addr, 'thread\text{-id}) \text{ obs-event})$

semantics

where $mred0\ P \equiv (\lambda t ((e, es), h)\ ta\ ((e', es'), h')).\ red0\ P\ t\ e\ es\ h\ ta\ e'\ es'\ h'$

end

declare $domIff[iff, simp\ del]$

context *J-heap-base* **begin**

lemma **assumes** $wf: wwf\text{-}J\text{-}prog\ P$

shows $red\text{-}fv\text{-}subset: extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies fv\ e' \subseteq fv\ e$

and $reds\text{-}fus\text{-}subset: extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies fus\ es' \subseteq fus\ es$

proof(*induct rule: red-reds.inducts*)

case ($RedCall\ s\ a\ U\ M\ Ts\ T\ pns\ body\ D\ vs$)

hence $fv\ body \subseteq \{this\} \cup set\ pns$

using wf **by**($fastforce\ dest!: sees\text{-}wf\text{-}mdecl\ simp: wf\text{-}mdecl\text{-}def$)

with $RedCall$ **show** $?case$ **by** $fastforce$

next

case $RedCallExternal$ **thus** $?case$ **by**($auto\ simp\ add: extRet2J\text{-}def\ split: extCallRet.\ split\text{-}asm$)

qed($fastforce$)**+**

end

declare $domIff[iff\ del]$

context *J-heap-base* **begin**

lemma **assumes** $wf: wwf\text{-}J\text{-}prog\ P$

shows $red\text{-}fv\text{-}ok: \llbracket extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; fv\ e \subseteq dom\ (lcl\ s) \rrbracket \implies fv\ e' \subseteq dom\ (lcl\ s')$

and $reds\text{-}fus\text{-}ok: \llbracket extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; fus\ es \subseteq dom\ (lcl\ s) \rrbracket \implies fus\ es' \subseteq dom\ (lcl\ s')$

proof(*induct rule: red-reds.inducts*)

case ($RedCall\ s\ a\ U\ M\ Ts\ T\ pns\ body\ D\ vs$)

from $\langle P \vdash class\text{-}type\text{-}of\ U\ sees\ M: Ts \rightarrow T = [(pns, body)]\ in\ D \rangle$ **have** $wf\text{-}J\text{-}mdecl\ P\ D\ (M, Ts, T, pns, body)$

by($auto\ dest!: sees\text{-}wf\text{-}mdecl[OF\ wf]\ simp\ add: wf\text{-}mdecl\text{-}def$)

with $RedCall$ **show** $?case$ **by**($auto$)

next

case $RedCallExternal$ **thus** $?case$ **by**($auto\ simp\ add: extRet2J\text{-}def\ split: extCallRet.\ split\text{-}asm$)

next

case ($BlockRed\ e\ h\ x\ V\ vo\ ta\ e'\ h'\ x'\ T$)

note $red = \langle extTA, P, t \vdash \langle e, (h, x(V := vo)) \rangle -ta \rightarrow \langle e', (h', x') \rangle \rangle$

hence $fv\ e' \subseteq fv\ e$ **by**($auto\ dest: red\text{-}fv\text{-}subset[OF\ wf]\ del: subsetI$)

moreover **from** $\langle fv\ \{V: T=vo; e\} \subseteq dom\ (lcl\ (h, x)) \rangle$

have $fv\ e - \{V\} \subseteq dom\ x$ **by**($simp$)

ultimately **have** $fv\ e' - \{V\} \subseteq dom\ x - \{V\}$ **by**($auto$)

moreover **from** red **have** $dom\ (x(V := vo)) \subseteq dom\ x'$

by($auto\ dest: red\text{-}lcl\text{-}incr\ del: subsetI$)

ultimately **have** $fv\ e' - \{V\} \subseteq dom\ x' - \{V\}$

by($auto\ split: if\text{-}split\text{-}asm$)

thus $?case$ **by**($auto\ simp\ del: fun\text{-}upd\text{-}apply$)

qed($fastforce\ dest: red\text{-}lcl\text{-}incr\ del: subsetI$)**+**

lemma *is-call-red-state-unchanged:*

$\llbracket extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; call\ e = [aMvs]; \neg\ synthesized\text{-}call\ P\ (hp\ s)\ aMvs \rrbracket \implies s' = s$

$\wedge ta = \varepsilon$

and *is-calls-reds-state-unchanged*:

$\llbracket extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; calls\ es = [aMvs]; \neg synthesized-call\ P\ (hp\ s)\ aMvs \rrbracket \implies s' = s \wedge ta = \varepsilon$

apply(*induct rule: red-reds.inducts*)

apply(*fastforce split: if-split-asm simp add: synthesized-call-def*)**+**

done

lemma *called-methodD*:

$\llbracket extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; call\ e = [(a, M, vs)]; \neg synthesized-call\ P\ (hp\ s)\ (a, M, vs) \rrbracket$
 $\implies \exists hT\ D\ Us\ U\ pns\ body. hp\ s' = hp\ s \wedge typeof-addr\ (hp\ s)\ a = [hT] \wedge$
 $P \vdash class-type-of\ hT\ sees\ M: Us \rightarrow U = [(pns, body)]\ in\ D \wedge$
 $length\ vs = length\ pns \wedge length\ Us = length\ pns$

and *called-methodsD*:

$\llbracket extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; calls\ es = [(a, M, vs)]; \neg synthesized-call\ P\ (hp\ s)\ (a, M, vs) \rrbracket$
 $\implies \exists hT\ D\ Us\ U\ pns\ body. hp\ s' = hp\ s \wedge typeof-addr\ (hp\ s)\ a = [hT] \wedge$
 $P \vdash class-type-of\ hT\ sees\ M: Us \rightarrow U = [(pns, body)]\ in\ D \wedge$
 $length\ vs = length\ pns \wedge length\ Us = length\ pns$

apply(*induct rule: red-reds.inducts*)

apply(*auto split: if-split-asm simp add: synthesized-call-def*)

apply(*fastforce*)

done

7.2.1 Silent moves

primrec $\tau move0 :: 'm\ prog \Rightarrow 'heap \Rightarrow ('a, 'b, 'addr)\ exp \Rightarrow bool$

and $\tau moves0 :: 'm\ prog \Rightarrow 'heap \Rightarrow ('a, 'b, 'addr)\ exp\ list \Rightarrow bool$

where

$\tau move0\ P\ h\ (new\ C) \longleftrightarrow False$
 $\tau move0\ P\ h\ (newA\ T[e]) \longleftrightarrow \tau move0\ P\ h\ e \vee (\exists a. e = Throw\ a)$
 $\tau move0\ P\ h\ (Cast\ U\ e) \longleftrightarrow \tau move0\ P\ h\ e \vee (\exists a. e = Throw\ a) \vee (\exists v. e = Val\ v)$
 $\tau move0\ P\ h\ (e\ instanceof\ T) \longleftrightarrow \tau move0\ P\ h\ e \vee (\exists a. e = Throw\ a) \vee (\exists v. e = Val\ v)$
 $\tau move0\ P\ h\ (e\ \ll bop \gg e') \longleftrightarrow \tau move0\ P\ h\ e \vee (\exists a. e = Throw\ a) \vee (\exists v. e = Val\ v \wedge$
 $(\tau move0\ P\ h\ e' \vee (\exists a. e' = Throw\ a) \vee (\exists v. e' = Val\ v)))$
 $\tau move0\ P\ h\ (Val\ v) \longleftrightarrow False$
 $\tau move0\ P\ h\ (Var\ V) \longleftrightarrow True$
 $\tau move0\ P\ h\ (V := e) \longleftrightarrow \tau move0\ P\ h\ e \vee (\exists a. e = Throw\ a) \vee (\exists v. e = Val\ v)$
 $\tau move0\ P\ h\ (a[i]) \longleftrightarrow \tau move0\ P\ h\ a \vee (\exists ad. a = Throw\ ad) \vee (\exists v. a = Val\ v \wedge (\tau move0\ P\ h\ i$
 $\vee (\exists a. i = Throw\ a)))$
 $\tau move0\ P\ h\ (AAss\ a\ i\ e) \longleftrightarrow \tau move0\ P\ h\ a \vee (\exists ad. a = Throw\ ad) \vee (\exists v. a = Val\ v \wedge$
 $(\tau move0\ P\ h\ i \vee (\exists a. i = Throw\ a) \vee (\exists v. i = Val\ v \wedge (\tau move0\ P\ h\ e \vee (\exists a. e = Throw\ a))))))$
 $\tau move0\ P\ h\ (a.length) \longleftrightarrow \tau move0\ P\ h\ a \vee (\exists ad. a = Throw\ ad)$
 $\tau move0\ P\ h\ (e.F\{D\}) \longleftrightarrow \tau move0\ P\ h\ e \vee (\exists a. e = Throw\ a)$
 $\tau move0\ P\ h\ (FAss\ e\ F\ D\ e') \longleftrightarrow \tau move0\ P\ h\ e \vee (\exists a. e = Throw\ a) \vee (\exists v. e = Val\ v \wedge (\tau move0$
 $P\ h\ e' \vee (\exists a. e' = Throw\ a)))$
 $\tau move0\ P\ h\ (e.compareAndSwap(D.F, e', e'')) \longleftrightarrow \tau move0\ P\ h\ e \vee (\exists a. e = Throw\ a) \vee (\exists v. e =$
 $Val\ v \wedge$
 $(\tau move0\ P\ h\ e' \vee (\exists a. e' = Throw\ a) \vee (\exists v. e' = Val\ v \wedge (\tau move0\ P\ h\ e'' \vee (\exists a. e'' = Throw$
 $a))))))$
 $\tau move0\ P\ h\ (e.M(es)) \longleftrightarrow \tau move0\ P\ h\ e \vee (\exists a. e = Throw\ a) \vee (\exists v. e = Val\ v \wedge$
 $((\tau moves0\ P\ h\ es \vee (\exists vs\ a\ es'. es = map\ Val\ vs\ @\ Throw\ a\ \# es')) \vee$

$(\exists vs. es = \text{map Val } vs \wedge (v = \text{Null} \vee (\forall T C Ts Tr D. \text{typeof}_h v = \lfloor T \rfloor \longrightarrow \text{class-type-of}' T = \lfloor C \rfloor \longrightarrow P \vdash C \text{ sees } M: Ts \rightarrow Tr = \text{Native in } D \longrightarrow \tau \text{external-defs } D M))))$
 $\mid \tau \text{move0 } P h (\{V:T=vo; e\}) \longleftrightarrow \tau \text{move0 } P h e \vee ((\exists a. e = \text{Throw } a) \vee (\exists v. e = \text{Val } v))$
 $\mid \tau \text{move0 } P h (\text{sync}_{V'}(e) e') \longleftrightarrow \tau \text{move0 } P h e \vee (\exists a. e = \text{Throw } a)$
 $\mid \tau \text{move0 } P h (\text{insync}_{V'}(\text{ad}) e) \longleftrightarrow \tau \text{move0 } P h e$
 $\mid \tau \text{move0 } P h (e; e') \longleftrightarrow \tau \text{move0 } P h e \vee (\exists a. e = \text{Throw } a) \vee (\exists v. e = \text{Val } v)$
 $\mid \tau \text{move0 } P h (\text{if } (e) e' \text{ else } e'') \longleftrightarrow \tau \text{move0 } P h e \vee (\exists a. e = \text{Throw } a) \vee (\exists v. e = \text{Val } v)$
 $\mid \tau \text{move0 } P h (\text{while } (e) e') = \text{True}$
 $\mid \text{--- } \text{Throw } a \text{ is no } \tau \text{move0} \text{ because there is no reduction for it. If it were, most defining equations would be simpler. However, } \text{insync}_{V'}(\text{ad}) \text{ Throw } \text{ad} \text{ must not be a } \tau \text{move0}, \text{ but would be if } \text{Throw } a \text{ was.}$

$\tau \text{move0 } P h (\text{throw } e) \longleftrightarrow \tau \text{move0 } P h e \vee (\exists a. e = \text{Throw } a) \vee e = \text{null}$
 $\mid \tau \text{move0 } P h (\text{try } e \text{ catch}(C V) e') \longleftrightarrow \tau \text{move0 } P h e \vee (\exists a. e = \text{Throw } a) \vee (\exists v. e = \text{Val } v)$
 $\mid \tau \text{moves0 } P h [] \longleftrightarrow \text{False}$
 $\mid \tau \text{moves0 } P h (e \# es) \longleftrightarrow \tau \text{move0 } P h e \vee (\exists v. e = \text{Val } v \wedge \tau \text{moves0 } P h es)$

abbreviation $\tau \text{MOVE} :: 'm \text{ prog} \Rightarrow (('addr \text{ expr} \times 'addr \text{ locals}) \times 'heap, ('addr, 'thread-id, 'heap) J\text{-thread-action}) \text{ trsys}$

where $\tau \text{MOVE} \equiv \lambda P ((e, x), h) \text{ ta } s'. \tau \text{move0 } P h e \wedge \text{ta} = \varepsilon$

primrec $\tau \text{Move0} :: 'm \text{ prog} \Rightarrow 'heap \Rightarrow ('addr \text{ expr} \times 'addr \text{ expr list}) \Rightarrow \text{bool}$

where

$\tau \text{Move0 } P h (e, \text{exs}) = (\tau \text{move0 } P h e \vee \text{final } e)$

abbreviation $\tau \text{MOVE0} :: 'm \text{ prog} \Rightarrow (('addr \text{ expr} \times 'addr \text{ expr list}) \times 'heap, ('addr, 'thread-id, 'heap) J0\text{-thread-action}) \text{ trsys}$

where $\tau \text{MOVE0} \equiv \lambda P (es, h) \text{ ta } s. \tau \text{Move0 } P h es \wedge \text{ta} = \varepsilon$

definition $\tau \text{red0} ::$

$(('addr, 'thread-id, 'heap) \text{ external-thread-action} \Rightarrow ('addr, 'thread-id, 'x, 'heap) \text{ Jinja-thread-action})$
 $\Rightarrow 'addr J\text{-prog} \Rightarrow 'thread-id \Rightarrow 'heap \Rightarrow ('addr \text{ expr} \times 'addr \text{ locals}) \Rightarrow ('addr \text{ expr} \times 'addr \text{ locals})$
 $\Rightarrow \text{bool}$

where

$\tau \text{red0 } \text{extTA } P t h \text{ exs } e'xs' =$
 $(\text{extTA}, P, t \vdash \langle \text{fst } \text{exs}, (h, \text{snd } \text{exs}) \rangle \text{---}\varepsilon \rightarrow \langle \text{fst } e'xs', (h, \text{snd } e'xs') \rangle \wedge \tau \text{move0 } P h (\text{fst } \text{exs}) \wedge \text{no-call } P h (\text{fst } \text{exs}))$

definition $\tau \text{reds0} ::$

$(('addr, 'thread-id, 'heap) \text{ external-thread-action} \Rightarrow ('addr, 'thread-id, 'x, 'heap) \text{ Jinja-thread-action})$
 $\Rightarrow 'addr J\text{-prog} \Rightarrow 'thread-id \Rightarrow 'heap \Rightarrow ('addr \text{ expr list} \times 'addr \text{ locals}) \Rightarrow ('addr \text{ expr list} \times 'addr \text{ locals})$
 $\Rightarrow \text{bool}$

where

$\tau \text{reds0 } \text{extTA } P t h \text{ esxs } es'xs' =$
 $(\text{extTA}, P, t \vdash \langle \text{fst } \text{esxs}, (h, \text{snd } \text{esxs}) \rangle \text{---}\varepsilon \rightarrow \langle \text{fst } es'xs', (h, \text{snd } es'xs') \rangle \wedge \tau \text{moves0 } P h (\text{fst } \text{esxs}) \wedge \text{no-calls } P h (\text{fst } \text{esxs}))$

abbreviation $\tau \text{red0t} ::$

$(('addr, 'thread-id, 'heap) \text{ external-thread-action} \Rightarrow ('addr, 'thread-id, 'x, 'heap) \text{ Jinja-thread-action})$
 $\Rightarrow 'addr J\text{-prog} \Rightarrow 'thread-id \Rightarrow 'heap \Rightarrow ('addr \text{ expr} \times 'addr \text{ locals}) \Rightarrow ('addr \text{ expr} \times 'addr \text{ locals})$
 $\Rightarrow \text{bool}$

where $\tau \text{red0t } \text{extTA } P t h \equiv (\tau \text{red0 } \text{extTA } P t h) \hat{+} +$

abbreviation $\tau \text{reds0t} ::$

$((\text{'addr}, \text{'thread-id}, \text{'heap}) \text{external-thread-action} \Rightarrow (\text{'addr}, \text{'thread-id}, \text{'x}, \text{'heap}) \text{Jinja-thread-action})$
 $\Rightarrow \text{'addr J-prog} \Rightarrow \text{'thread-id} \Rightarrow \text{'heap} \Rightarrow (\text{'addr expr list} \times \text{'addr locals}) \Rightarrow (\text{'addr expr list} \times \text{'addr locals}) \Rightarrow \text{bool}$

where $\tau\text{reds0t extTA } P \text{ t h} \equiv (\tau\text{reds0 extTA } P \text{ t h})^{\wedge++}$

abbreviation $\tau\text{red0r} ::$

$((\text{'addr}, \text{'thread-id}, \text{'heap}) \text{external-thread-action} \Rightarrow (\text{'addr}, \text{'thread-id}, \text{'x}, \text{'heap}) \text{Jinja-thread-action})$
 $\Rightarrow \text{'addr J-prog} \Rightarrow \text{'thread-id} \Rightarrow \text{'heap} \Rightarrow (\text{'addr expr} \times \text{'addr locals}) \Rightarrow (\text{'addr expr} \times \text{'addr locals}) \Rightarrow \text{bool}$

where $\tau\text{red0r extTA } P \text{ t h} \equiv (\tau\text{red0 extTA } P \text{ t h})^{\wedge**}$

abbreviation $\tau\text{reds0r} ::$

$((\text{'addr}, \text{'thread-id}, \text{'heap}) \text{external-thread-action} \Rightarrow (\text{'addr}, \text{'thread-id}, \text{'x}, \text{'heap}) \text{Jinja-thread-action})$
 $\Rightarrow \text{'addr J-prog} \Rightarrow \text{'thread-id} \Rightarrow \text{'heap} \Rightarrow (\text{'addr expr list} \times \text{'addr locals}) \Rightarrow (\text{'addr expr list} \times \text{'addr locals}) \Rightarrow \text{bool}$

where $\tau\text{reds0r extTA } P \text{ t h} \equiv (\tau\text{reds0 extTA } P \text{ t h})^{\wedge**}$

definition $\tau\text{Red0} ::$

$\text{'addr J-prog} \Rightarrow \text{'thread-id} \Rightarrow \text{'heap} \Rightarrow (\text{'addr expr} \times \text{'addr expr list}) \Rightarrow (\text{'addr expr} \times \text{'addr expr list}) \Rightarrow \text{bool}$

where $\tau\text{Red0 } P \text{ t h ees } e'es' = (P, t \vdash 0 \langle \text{fst ees}/\text{snd ees}, h \rangle -\varepsilon \rightarrow \langle \text{fst } e'es'/\text{snd } e'es', h \rangle \wedge \tau\text{Move0 } P \text{ h ees})$

abbreviation $\tau\text{Red0r} ::$

$\text{'addr J-prog} \Rightarrow \text{'thread-id} \Rightarrow \text{'heap} \Rightarrow (\text{'addr expr} \times \text{'addr expr list}) \Rightarrow (\text{'addr expr} \times \text{'addr expr list}) \Rightarrow \text{bool}$

where $\tau\text{Red0r } P \text{ t h} \equiv (\tau\text{Red0 } P \text{ t h})^{\wedge**}$

abbreviation $\tau\text{Red0t} ::$

$\text{'addr J-prog} \Rightarrow \text{'thread-id} \Rightarrow \text{'heap} \Rightarrow (\text{'addr expr} \times \text{'addr expr list}) \Rightarrow (\text{'addr expr} \times \text{'addr expr list}) \Rightarrow \text{bool}$

where $\tau\text{Red0t } P \text{ t h} \equiv (\tau\text{Red0 } P \text{ t h})^{\wedge++}$

lemma $\tau\text{move0-}\tau\text{moves0-intros}$:

fixes $e \text{ e1 } e2 \text{ e}' :: (\text{'a}, \text{'b}, \text{'addr}) \text{exp}$ **and** $es :: (\text{'a}, \text{'b}, \text{'addr}) \text{exp list}$

shows $\tau\text{move0NewArray}: \tau\text{move0 } P \text{ h } e \Longrightarrow \tau\text{move0 } P \text{ h } (\text{newA } T[e])$

and $\tau\text{move0Cast}: \tau\text{move0 } P \text{ h } e \Longrightarrow \tau\text{move0 } P \text{ h } (\text{Cast } U \text{ e})$

and $\tau\text{move0CastRed}: \tau\text{move0 } P \text{ h } (\text{Cast } U \text{ (Val } v))$

and $\tau\text{move0InstanceOf}: \tau\text{move0 } P \text{ h } e \Longrightarrow \tau\text{move0 } P \text{ h } (e \text{ instanceof } T)$

and $\tau\text{move0InstanceOfRed}: \tau\text{move0 } P \text{ h } ((\text{Val } v) \text{ instanceof } T)$

and $\tau\text{move0BinOp1}: \tau\text{move0 } P \text{ h } e \Longrightarrow \tau\text{move0 } P \text{ h } (e \ll \text{bop} \gg e')$

and $\tau\text{move0BinOp2}: \tau\text{move0 } P \text{ h } e \Longrightarrow \tau\text{move0 } P \text{ h } (\text{Val } v \ll \text{bop} \gg e)$

and $\tau\text{move0BinOp}: \tau\text{move0 } P \text{ h } (\text{Val } v \ll \text{bop} \gg \text{Val } v')$

and $\tau\text{move0Var}: \tau\text{move0 } P \text{ h } (\text{Var } V)$

and $\tau\text{move0LAss}: \tau\text{move0 } P \text{ h } e \Longrightarrow \tau\text{move0 } P \text{ h } (V := e)$

and $\tau\text{move0LAssRed}: \tau\text{move0 } P \text{ h } (V := \text{Val } v)$

and $\tau\text{move0AAcc1}: \tau\text{move0 } P \text{ h } e \Longrightarrow \tau\text{move0 } P \text{ h } (e[e'])$

and $\tau\text{move0AAcc2}: \tau\text{move0 } P \text{ h } e \Longrightarrow \tau\text{move0 } P \text{ h } (\text{Val } v[e])$

and $\tau\text{move0AAss1}: \tau\text{move0 } P \text{ h } e \Longrightarrow \tau\text{move0 } P \text{ h } (e[e1] := e2)$

and $\tau\text{move0AAss2}: \tau\text{move0 } P \text{ h } e \Longrightarrow \tau\text{move0 } P \text{ h } (\text{Val } v[e] := e')$

and $\tau\text{move0AAss3}: \tau\text{move0 } P \text{ h } e \Longrightarrow \tau\text{move0 } P \text{ h } (\text{Val } v[\text{Val } v'] := e)$

and $\tau\text{move0ALength}: \tau\text{move0 } P \text{ h } e \Longrightarrow \tau\text{move0 } P \text{ h } (e.\text{length})$

and $\tau\text{move0FAcc}: \tau\text{move0 } P \text{ h } e \Longrightarrow \tau\text{move0 } P \text{ h } (e.F\{D\})$

and $\tau\text{move0FAss1}: \tau\text{move0 } P \text{ h } e \Longrightarrow \tau\text{move0 } P \text{ h } (\text{FAss } e \text{ F } D \text{ e}')$

and $\tau\text{move0FAss2}$: $\tau\text{move0 } P \ h \ e \implies \tau\text{move0 } P \ h \ (Val \ v \cdot F\{D\} := e)$
and $\tau\text{move0CAS1}$: $\tau\text{move0 } P \ h \ e \implies \tau\text{move0 } P \ h \ (e \cdot \text{compareAndSwap}(D \cdot F, e', e''))$
and $\tau\text{move0CAS2}$: $\tau\text{move0 } P \ h \ e' \implies \tau\text{move0 } P \ h \ (Val \ v \cdot \text{compareAndSwap}(D \cdot F, e', e''))$
and $\tau\text{move0CAS3}$: $\tau\text{move0 } P \ h \ e'' \implies \tau\text{move0 } P \ h \ (Val \ v \cdot \text{compareAndSwap}(D \cdot F, Val \ v', e''))$
and $\tau\text{move0CallObj}$: $\tau\text{move0 } P \ h \ e \implies \tau\text{move0 } P \ h \ (e \cdot M(es))$
and $\tau\text{move0CallParams}$: $\tau\text{moves0 } P \ h \ es \implies \tau\text{move0 } P \ h \ (Val \ v \cdot M(es))$
and $\tau\text{move0Call}$: $(\bigwedge T \ C \ Ts \ Tr \ D. \llbracket \text{typeof}_h v = \lfloor T \rfloor; \text{class-type-of}' T = \lfloor C \rfloor; P \vdash C \text{ sees } M: Ts \rightarrow Tr = \text{Native in } D \rrbracket \implies \tau\text{external-defs } D \ M) \implies \tau\text{move0 } P \ h \ (Val \ v \cdot M(\text{map } Val \ vs))$
and $\tau\text{move0Block}$: $\tau\text{move0 } P \ h \ e \implies \tau\text{move0 } P \ h \ \{V: T=vo; e\}$
and $\tau\text{move0BlockRed}$: $\tau\text{move0 } P \ h \ \{V: T=vo; Val \ v\}$
and $\tau\text{move0Sync}$: $\tau\text{move0 } P \ h \ e \implies \tau\text{move0 } P \ h \ (\text{sync}_{V'}(e) \ e')$
and $\tau\text{move0InSync}$: $\tau\text{move0 } P \ h \ e \implies \tau\text{move0 } P \ h \ (\text{insync}_{V'}(a) \ e)$
and $\tau\text{move0Seq}$: $\tau\text{move0 } P \ h \ e \implies \tau\text{move0 } P \ h \ (e;; e')$
and $\tau\text{move0SeqRed}$: $\tau\text{move0 } P \ h \ (Val \ v;; e')$
and $\tau\text{move0Cond}$: $\tau\text{move0 } P \ h \ e \implies \tau\text{move0 } P \ h \ (\text{if } (e) \ e1 \ \text{else } e2)$
and $\tau\text{move0CondRed}$: $\tau\text{move0 } P \ h \ (\text{if } (Val \ v) \ e1 \ \text{else } e2)$
and $\tau\text{move0WhileRed}$: $\tau\text{move0 } P \ h \ (\text{while } (e) \ e')$
and $\tau\text{move0Throw}$: $\tau\text{move0 } P \ h \ e \implies \tau\text{move0 } P \ h \ (\text{throw } e)$
and $\tau\text{move0ThrowNull}$: $\tau\text{move0 } P \ h \ (\text{throw } \text{null})$
and $\tau\text{move0Try}$: $\tau\text{move0 } P \ h \ e \implies \tau\text{move0 } P \ h \ (\text{try } e \ \text{catch}(C \ V) \ e')$
and $\tau\text{move0TryRed}$: $\tau\text{move0 } P \ h \ (\text{try } Val \ v \ \text{catch}(C \ V) \ e)$
and $\tau\text{move0TryThrow}$: $\tau\text{move0 } P \ h \ (\text{try } Throw \ a \ \text{catch}(C \ V) \ e)$
and $\tau\text{move0NewArrayThrow}$: $\tau\text{move0 } P \ h \ (\text{newA } T \lfloor Throw \ a \rfloor)$
and $\tau\text{move0CastThrow}$: $\tau\text{move0 } P \ h \ (\text{Cast } T \ (Throw \ a))$
and $\tau\text{move0CInstanceOfThrow}$: $\tau\text{move0 } P \ h \ ((Throw \ a) \ \text{instanceof } T)$
and $\tau\text{move0BinOpThrow1}$: $\tau\text{move0 } P \ h \ (Throw \ a \ \llbracket \text{bop} \rrbracket \ e')$
and $\tau\text{move0BinOpThrow2}$: $\tau\text{move0 } P \ h \ (Val \ v \ \llbracket \text{bop} \rrbracket \ Throw \ a)$
and $\tau\text{move0LAssThrow}$: $\tau\text{move0 } P \ h \ (V := (Throw \ a))$
and $\tau\text{move0AAccThrow1}$: $\tau\text{move0 } P \ h \ (Throw \ a \lfloor e \rfloor)$
and $\tau\text{move0AAccThrow2}$: $\tau\text{move0 } P \ h \ (Val \ v \lfloor Throw \ a \rfloor)$
and $\tau\text{move0AAssThrow1}$: $\tau\text{move0 } P \ h \ (AAss \ (Throw \ a) \ e \ e')$
and $\tau\text{move0AAssThrow2}$: $\tau\text{move0 } P \ h \ (AAss \ (Val \ v) \ (Throw \ a) \ e')$
and $\tau\text{move0AAssThrow3}$: $\tau\text{move0 } P \ h \ (AAss \ (Val \ v) \ (Val \ v') \ (Throw \ a))$
and $\tau\text{move0ALengthThrow}$: $\tau\text{move0 } P \ h \ (Throw \ a \cdot \text{length})$
and $\tau\text{move0FAccThrow}$: $\tau\text{move0 } P \ h \ (Throw \ a \cdot F\{D\})$
and $\tau\text{move0FAssThrow1}$: $\tau\text{move0 } P \ h \ (Throw \ a \cdot F\{D\} := e)$
and $\tau\text{move0FAssThrow2}$: $\tau\text{move0 } P \ h \ (FAss \ (Val \ v) \ F \ D \ (Throw \ a))$
and $\tau\text{move0CallThrowObj}$: $\tau\text{move0 } P \ h \ (Throw \ a \cdot M(es))$
and $\tau\text{move0CallThrowParams}$: $\tau\text{move0 } P \ h \ (Val \ v \cdot M(\text{map } Val \ vs \ @ \ Throw \ a \ \# \ es))$
and $\tau\text{move0BlockThrow}$: $\tau\text{move0 } P \ h \ \{V: T=vo; Throw \ a\}$
and $\tau\text{move0SyncThrow}$: $\tau\text{move0 } P \ h \ (\text{sync}_{V'}(Throw \ a) \ e)$
and $\tau\text{move0SeqThrow}$: $\tau\text{move0 } P \ h \ (Throw \ a;; e)$
and $\tau\text{move0CondThrow}$: $\tau\text{move0 } P \ h \ (\text{if } (Throw \ a) \ e1 \ \text{else } e2)$
and $\tau\text{move0ThrowThrow}$: $\tau\text{move0 } P \ h \ (\text{throw } (Throw \ a))$

and $\tau\text{moves0Hd}$: $\tau\text{move0 } P \ h \ e \implies \tau\text{moves0 } P \ h \ (e \ \# \ es)$
and $\tau\text{moves0Tl}$: $\tau\text{moves0 } P \ h \ es \implies \tau\text{moves0 } P \ h \ (Val \ v \ \# \ es)$

by *auto*

lemma $\tau\text{moves0-map-Val}$ [*iff*]:

$\neg \tau\text{moves0 } P \ h \ (\text{map } Val \ vs)$

by(*induct vs*) *auto*

lemma $\tau\text{moves0-map-Val-append}$ [*simp*]:

$\tau moves0 P h (map Val vs @ es) = \tau moves0 P h es$
by(*induct vs*)(*auto*)

lemma *no-reds-map-Val-Throw* [*simp*]:

$extTA, P, t \vdash \langle map Val vs @ Throw a \# es, s \rangle [-ta \rightarrow] \langle es', s' \rangle = False$
by(*induct vs arbitrary: es'*)(*auto elim: reds.cases*)

lemma *assumes* [*simp*]: $extTA \ \varepsilon = \varepsilon$

shows *red- τ -taD*: $\llbracket extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \tau move0 P (hp s) e \rrbracket \Longrightarrow ta = \varepsilon$

and *reds- τ -taD*: $\llbracket extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \tau moves0 P (hp s) es \rrbracket \Longrightarrow ta = \varepsilon$

apply(*induct rule: red-reds.inducts*)

apply(*fastforce simp add: map-eq-append-conv $\tau external'$ -def $\tau external$ -def dest: $\tau external'$ -red-external-TA-empty*)
done

lemma *$\tau move0$ -heap-unchanged*: $\llbracket extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \tau move0 P (hp s) e \rrbracket \Longrightarrow hp s' = hp s$

and *$\tau moves0$ -heap-unchanged*: $\llbracket extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \tau moves0 P (hp s) es \rrbracket \Longrightarrow hp s' = hp s$

apply(*induct rule: red-reds.inducts*)

apply(*auto*)

apply(*fastforce simp add: map-eq-append-conv $\tau external'$ -def $\tau external$ -def dest: $\tau external'$ -red-external-heap-unch*)
done

lemma *$\tau Move0$ -iff*:

$\tau Move0 P h ees \longleftrightarrow (let (e, -) = ees in \tau move0 P h e \vee final e)$

by(*cases ees*)(*simp*)

lemma *no-call-simps* [*simp*]:

$no-call P h (new C) = True$

$no-call P h (newA T[e]) = no-call P h e$

$no-call P h (Cast T e) = no-call P h e$

$no-call P h (e instanceof T) = no-call P h e$

$no-call P h (Val v) = True$

$no-call P h (Var V) = True$

$no-call P h (V := e) = no-call P h e$

$no-call P h (e \ll bop \gg e') = (if is-val e then no-call P h e' else no-call P h e)$

$no-call P h (a[i]) = (if is-val a then no-call P h i else no-call P h a)$

$no-call P h (AAss a i e) = (if is-val a then (if is-val i then no-call P h e else no-call P h i) else no-call P h a)$

$no-call P h (a.length) = no-call P h a$

$no-call P h (e.F\{D\}) = no-call P h e$

$no-call P h (FAss e F D e') = (if is-val e then no-call P h e' else no-call P h e)$

$no-call P h (e.compareAndSwap(D.F, e', e'')) = (if is-val e then (if is-val e' then no-call P h e'' else no-call P h e') else no-call P h e)$

$no-call P h (e.M(es)) = (if is-val e then (if is-vals es \wedge is-addr e then synthesized-call P h (THE a. e = addr a, M, THE vs. es = map Val vs) else no-calls P h es) else no-call P h e)$

$no-call P h (\{V:T=vo; e\}) = no-call P h e$

$no-call P h (sync_{V'}(e) e') = no-call P h e$

$no-call P h (insync_{V'}(ad) e) = no-call P h e$

$no-call P h (e;;e') = no-call P h e$

$no-call P h (if (e) e1 else e2) = no-call P h e$

$no-call P h (while(e) e') = True$

$no-call P h (throw e) = no-call P h e$

$no-call P h (try e catch(C V) e') = no-call P h e$

by(*auto simp add: no-call-def no-calls-def*)

lemma *no-calls-simps* [*simp*]:

no-calls P h [] = True

no-calls P h (e # es) = (if is-val e then no-calls P h es else no-call P h e)

by(*simp-all add: no-call-def no-calls-def*)

lemma *no-calls-map-Val* [*simp*]:

no-calls P h (map Val vs)

by(*induct vs*) *simp-all*

lemma *assumes nfin*: \neg *final e'*

shows *inline-call- τ move0-inv*: $\text{call } e = \lfloor aMvs \rfloor \implies \tau\text{move0 } P h (\text{inline-call } e' e) = \tau\text{move0 } P h e'$

and *inline-calls- τ moves0-inv*: $\text{calls } es = \lfloor aMvs \rfloor \implies \tau\text{moves0 } P h (\text{inline-calls } e' es) = \tau\text{move0 } P h e'$

apply(*induct e and es rule: $\tau\text{move0.induct } \tau\text{moves0.induct}$*)

apply(*insert nfin*)

apply *simp-all*

apply *auto*

done

lemma *$\tau\text{red0-iff}$* [*iff*]:

$\tau\text{red0 extTA } P t h (e, xs) (e', xs') = (\text{extTA}, P, t \vdash \langle e, (h, xs) \rangle \rightarrow \langle e', (h, xs') \rangle) \wedge \tau\text{move0 } P h e \wedge \text{no-call } P h e$

by(*simp add: $\tau\text{red0-def}$*)

lemma *$\tau\text{reds0-iff}$* [*iff*]:

$\tau\text{reds0 extTA } P t h (es, xs) (es', xs') =$

$(\text{extTA}, P, t \vdash \langle es, (h, xs) \rangle \rightarrow \langle es', (h, xs') \rangle) \wedge \tau\text{moves0 } P h es \wedge \text{no-calls } P h es$

by(*simp add: $\tau\text{reds0-def}$*)

lemma *$\tau\text{red0t-1step}$* :

$\llbracket \text{extTA}, P, t \vdash \langle e, (h, xs) \rangle \rightarrow \langle e', (h, xs') \rangle; \tau\text{move0 } P h e; \text{no-call } P h e \rrbracket$

$\implies \tau\text{red0t extTA } P t h (e, xs) (e', xs')$

by(*blast intro: tranclp.r-into-trancl*)

lemma *$\tau\text{red0t-2step}$* :

$\llbracket \text{extTA}, P, t \vdash \langle e, (h, xs) \rangle \rightarrow \langle e', (h, xs') \rangle; \tau\text{move0 } P h e; \text{no-call } P h e;$

$\text{extTA}, P, t \vdash \langle e', (h, xs') \rangle \rightarrow \langle e'', (h, xs'') \rangle; \tau\text{move0 } P h e'; \text{no-call } P h e' \rrbracket$

$\implies \tau\text{red0t extTA } P t h (e, xs) (e'', xs'')$

by(*blast intro: tranclp.trancl-into-trancl[OF $\tau\text{red0t-1step}$]*)

lemma *$\tau\text{red1t-3step}$* :

$\llbracket \text{extTA}, P, t \vdash \langle e, (h, xs) \rangle \rightarrow \langle e', (h, xs') \rangle; \tau\text{move0 } P h e; \text{no-call } P h e;$

$\text{extTA}, P, t \vdash \langle e', (h, xs') \rangle \rightarrow \langle e'', (h, xs'') \rangle; \tau\text{move0 } P h e'; \text{no-call } P h e';$

$\text{extTA}, P, t \vdash \langle e'', (h, xs'') \rangle \rightarrow \langle e''', (h, xs''') \rangle; \tau\text{move0 } P h e''; \text{no-call } P h e'' \rrbracket$

$\implies \tau\text{red0t extTA } P t h (e, xs) (e''', xs''')$

by(*blast intro: tranclp.trancl-into-trancl[OF $\tau\text{red0t-2step}$]*)

lemma *$\tau\text{reds0t-1step}$* :

$\llbracket \text{extTA}, P, t \vdash \langle es, (h, xs) \rangle \rightarrow \langle es', (h, xs') \rangle; \tau\text{moves0 } P h es; \text{no-calls } P h es \rrbracket$

$\implies \tau\text{reds0t extTA } P t h (es, xs) (es', xs')$

by(*blast intro: tranclp.r-into-trancl*)

lemma $\tau\text{reds0t-2step}$:

$$\begin{aligned} & \llbracket \text{extTA}, P, t \vdash \langle es, (h, xs) \rangle [-\varepsilon \rightarrow] \langle es', (h, xs') \rangle; \tau\text{moves0 } P \text{ h } es; \text{no-calls } P \text{ h } es; \\ & \quad \text{extTA}, P, t \vdash \langle es', (h, xs') \rangle [-\varepsilon \rightarrow] \langle es'', (h, xs'') \rangle; \tau\text{moves0 } P \text{ h } es'; \text{no-calls } P \text{ h } es' \rrbracket \\ & \implies \tau\text{reds0t extTA } P \text{ t h } (es, xs) (es'', xs'') \end{aligned}$$

by(blast intro: tranclp.trancl-into-trancl[OF $\tau\text{reds0t-1step}$])

lemma $\tau\text{reds0t-3step}$:

$$\begin{aligned} & \llbracket \text{extTA}, P, t \vdash \langle es, (h, xs) \rangle [-\varepsilon \rightarrow] \langle es', (h, xs') \rangle; \tau\text{moves0 } P \text{ h } es; \text{no-calls } P \text{ h } es; \\ & \quad \text{extTA}, P, t \vdash \langle es', (h, xs') \rangle [-\varepsilon \rightarrow] \langle es'', (h, xs'') \rangle; \tau\text{moves0 } P \text{ h } es'; \text{no-calls } P \text{ h } es'; \\ & \quad \text{extTA}, P, t \vdash \langle es'', (h, xs'') \rangle [-\varepsilon \rightarrow] \langle es''', (h, xs''') \rangle; \tau\text{moves0 } P \text{ h } es''; \text{no-calls } P \text{ h } es'' \rrbracket \\ & \implies \tau\text{reds0t extTA } P \text{ t h } (es, xs) (es''', xs''') \end{aligned}$$

by(blast intro: tranclp.trancl-into-trancl[OF $\tau\text{reds0t-2step}$])

lemma $\tau\text{red0r-1step}$:

$$\begin{aligned} & \llbracket \text{extTA}, P, t \vdash \langle e, (h, xs) \rangle -\varepsilon \rightarrow \langle e', (h, xs') \rangle; \tau\text{move0 } P \text{ h } e; \text{no-call } P \text{ h } e \rrbracket \\ & \implies \tau\text{red0r extTA } P \text{ t h } (e, xs) (e', xs') \end{aligned}$$

by(blast intro: r-into-rtranclp)

lemma $\tau\text{red0r-2step}$:

$$\begin{aligned} & \llbracket \text{extTA}, P, t \vdash \langle e, (h, xs) \rangle -\varepsilon \rightarrow \langle e', (h, xs') \rangle; \tau\text{move0 } P \text{ h } e; \text{no-call } P \text{ h } e; \\ & \quad \text{extTA}, P, t \vdash \langle e', (h, xs') \rangle -\varepsilon \rightarrow \langle e'', (h, xs'') \rangle; \tau\text{move0 } P \text{ h } e'; \text{no-call } P \text{ h } e' \rrbracket \\ & \implies \tau\text{red0r extTA } P \text{ t h } (e, xs) (e'', xs'') \end{aligned}$$

by(blast intro: rtranclp.rtrancl-into-rtrancl[OF $\tau\text{red0r-1step}$])

lemma $\tau\text{red0r-3step}$:

$$\begin{aligned} & \llbracket \text{extTA}, P, t \vdash \langle e, (h, xs) \rangle -\varepsilon \rightarrow \langle e', (h, xs') \rangle; \tau\text{move0 } P \text{ h } e; \text{no-call } P \text{ h } e; \\ & \quad \text{extTA}, P, t \vdash \langle e', (h, xs') \rangle -\varepsilon \rightarrow \langle e'', (h, xs'') \rangle; \tau\text{move0 } P \text{ h } e'; \text{no-call } P \text{ h } e'; \\ & \quad \text{extTA}, P, t \vdash \langle e'', (h, xs'') \rangle -\varepsilon \rightarrow \langle e''', (h, xs''') \rangle; \tau\text{move0 } P \text{ h } e''; \text{no-call } P \text{ h } e'' \rrbracket \\ & \implies \tau\text{red0r extTA } P \text{ t h } (e, xs) (e''', xs''') \end{aligned}$$

by(blast intro: rtranclp.rtrancl-into-rtrancl[OF $\tau\text{red0r-2step}$])

lemma $\tau\text{reds0r-1step}$:

$$\begin{aligned} & \llbracket \text{extTA}, P, t \vdash \langle es, (h, xs) \rangle [-\varepsilon \rightarrow] \langle es', (h, xs') \rangle; \tau\text{moves0 } P \text{ h } es; \text{no-calls } P \text{ h } es \rrbracket \\ & \implies \tau\text{reds0r extTA } P \text{ t h } (es, xs) (es', xs') \end{aligned}$$

by(blast intro: r-into-rtranclp)

lemma $\tau\text{reds0r-2step}$:

$$\begin{aligned} & \llbracket \text{extTA}, P, t \vdash \langle es, (h, xs) \rangle [-\varepsilon \rightarrow] \langle es', (h, xs') \rangle; \tau\text{moves0 } P \text{ h } es; \text{no-calls } P \text{ h } es; \\ & \quad \text{extTA}, P, t \vdash \langle es', (h, xs') \rangle [-\varepsilon \rightarrow] \langle es'', (h, xs'') \rangle; \tau\text{moves0 } P \text{ h } es'; \text{no-calls } P \text{ h } es' \rrbracket \\ & \implies \tau\text{reds0r extTA } P \text{ t h } (es, xs) (es'', xs'') \end{aligned}$$

by(blast intro: rtranclp.rtrancl-into-rtrancl[OF $\tau\text{reds0r-1step}$])

lemma $\tau\text{reds0r-3step}$:

$$\begin{aligned} & \llbracket \text{extTA}, P, t \vdash \langle es, (h, xs) \rangle [-\varepsilon \rightarrow] \langle es', (h, xs') \rangle; \tau\text{moves0 } P \text{ h } es; \text{no-calls } P \text{ h } es; \\ & \quad \text{extTA}, P, t \vdash \langle es', (h, xs') \rangle [-\varepsilon \rightarrow] \langle es'', (h, xs'') \rangle; \tau\text{moves0 } P \text{ h } es'; \text{no-calls } P \text{ h } es'; \\ & \quad \text{extTA}, P, t \vdash \langle es'', (h, xs'') \rangle [-\varepsilon \rightarrow] \langle es''', (h, xs''') \rangle; \tau\text{moves0 } P \text{ h } es''; \text{no-calls } P \text{ h } es'' \rrbracket \\ & \implies \tau\text{reds0r extTA } P \text{ t h } (es, xs) (es''', xs''') \end{aligned}$$

by(blast intro: rtranclp.rtrancl-into-rtrancl[OF $\tau\text{reds0r-2step}$])

lemma $\tau\text{red0t-inj-}\tau\text{reds0t}$:

$$\begin{aligned} & \tau\text{red0t extTA } P \text{ t h } (e, xs) (e', xs') \\ & \implies \tau\text{reds0t extTA } P \text{ t h } (e \# es, xs) (e' \# es, xs') \end{aligned}$$

by(induct rule: tranclp-induct2)(auto intro: tranclp.trancl-into-trancl ListRed1)

lemma $\tau\text{reds0t-cons-}\tau\text{reds0t}$:

$$\begin{aligned} & \tau\text{reds0t extTA } P \text{ t h } (es, xs) (es', xs') \\ & \implies \tau\text{reds0t extTA } P \text{ t h } (Val \ v \ \# \ es, xs) (Val \ v \ \# \ es', xs') \end{aligned}$$

by(*induct rule*: tranclp-induct2)(*auto intro*: $\text{tranclp.trancl-into-trancl ListRed2}$)

lemma $\tau\text{red0r-inj-}\tau\text{reds0r}$:

$$\begin{aligned} & \tau\text{red0r extTA } P \text{ t h } (e, xs) (e', xs') \\ & \implies \tau\text{reds0r extTA } P \text{ t h } (e \ \# \ es, xs) (e' \ \# \ es, xs') \end{aligned}$$

by(*induct rule*: rtranclp-induct2)(*auto intro*: $\text{rtranclp.rtrancl-into-rtrancl ListRed1}$)

lemma $\tau\text{reds0r-cons-}\tau\text{reds0r}$:

$$\begin{aligned} & \tau\text{reds0r extTA } P \text{ t h } (es, xs) (es', xs') \\ & \implies \tau\text{reds0r extTA } P \text{ t h } (Val \ v \ \# \ es, xs) (Val \ v \ \# \ es', xs') \end{aligned}$$

by(*induct rule*: rtranclp-induct2)(*auto intro*: $\text{rtranclp.rtrancl-into-rtrancl ListRed2}$)

lemma $\text{NewArray-}\tau\text{red0t-xt}$:

$$\begin{aligned} & \tau\text{red0t extTA } P \text{ t h } (e, xs) (e', xs') \\ & \implies \tau\text{red0t extTA } P \text{ t h } (\text{newA } \ T[e], xs) (\text{newA } \ T[e'], xs') \end{aligned}$$

by(*induct rule*: tranclp-induct2)(*auto intro*: $\text{tranclp.trancl-into-trancl NewArrayRed}$)

lemma $\text{Cast-}\tau\text{red0t-xt}$:

$$\tau\text{red0t extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA } P \text{ t h } (\text{Cast } \ T \ e, xs) (\text{Cast } \ T \ e', xs')$$

by(*induct rule*: tranclp-induct2)(*auto intro*: $\text{tranclp.trancl-into-trancl CastRed}$)

lemma $\text{InstanceOf-}\tau\text{red0t-xt}$:

$$\tau\text{red0t extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA } P \text{ t h } (e \ \text{instanceof } \ T, xs) (e' \ \text{instanceof } \ T, xs')$$

by(*induct rule*: tranclp-induct2)(*auto intro*: $\text{tranclp.trancl-into-trancl InstanceOfRed}$)

lemma $\text{BinOp-}\tau\text{red0t-xt1}$:

$$\tau\text{red0t extTA } P \text{ t h } (e1, xs) (e1', xs') \implies \tau\text{red0t extTA } P \text{ t h } (e1 \ \llbracket \text{bop} \rrbracket \ e2, xs) (e1' \ \llbracket \text{bop} \rrbracket \ e2, xs')$$

by(*induct rule*: tranclp-induct2)(*auto intro*: $\text{tranclp.trancl-into-trancl BinOpRed1}$)

lemma $\text{BinOp-}\tau\text{red0t-xt2}$:

$$\tau\text{red0t extTA } P \text{ t h } (e2, xs) (e2', xs') \implies \tau\text{red0t extTA } P \text{ t h } (Val \ v \ \llbracket \text{bop} \rrbracket \ e2, xs) (Val \ v \ \llbracket \text{bop} \rrbracket \ e2', xs')$$

by(*induct rule*: tranclp-induct2)(*auto intro*: $\text{tranclp.trancl-into-trancl BinOpRed2}$)

lemma $\text{LAss-}\tau\text{red0t}$:

$$\tau\text{red0t extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA } P \text{ t h } (V := e, xs) (V := e', xs')$$

by(*induct rule*: tranclp-induct2)(*auto intro*: $\text{tranclp.trancl-into-trancl LAssRed}$)

lemma $\text{AAcc-}\tau\text{red0t-xt1}$:

$$\tau\text{red0t extTA } P \text{ t h } (a, xs) (a', xs') \implies \tau\text{red0t extTA } P \text{ t h } (a[i], xs) (a'[i], xs')$$

by(*induct rule*: tranclp-induct2)(*auto intro*: $\text{tranclp.trancl-into-trancl AAccRed1}$)

lemma $\text{AAcc-}\tau\text{red0t-xt2}$:

$$\tau\text{red0t extTA } P \text{ t h } (i, xs) (i', xs') \implies \tau\text{red0t extTA } P \text{ t h } (Val \ a[i], xs) (Val \ a[i'], xs')$$

by(*induct rule*: tranclp-induct2)(*auto intro*: $\text{tranclp.trancl-into-trancl AAccRed2}$)

lemma $\text{AAss-}\tau\text{red0t-xt1}$:

$$\tau\text{red0t extTA } P \text{ t h } (a, xs) (a', xs') \implies \tau\text{red0t extTA } P \text{ t h } (a[i] := e, xs) (a'[i] := e, xs')$$

by(*induct rule*: tranclp-induct2)(*auto intro*: $\text{tranclp.trancl-into-trancl AAssRed1}$)

lemma $\text{AAss-}\tau\text{red0t-xt2}$:

$\tau\text{red0t extTA P t h } (i, xs) (i', xs') \implies \tau\text{red0t extTA P t h } (\text{Val } a[i] := e, xs) (\text{Val } a[i'] := e, xs')$
by(*induct rule: tranclp-induct2*)(*auto intro: tranclp.trancl-into-trancl AAssRed2*)

lemma *AAss- $\tau\text{red0t-xt3}$:*

$\tau\text{red0t extTA P t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA P t h } (\text{Val } a[\text{Val } i] := e, xs) (\text{Val } a[\text{Val } i'] := e', xs')$

by(*induct rule: tranclp-induct2*)(*auto intro: tranclp.trancl-into-trancl AAssRed3*)

lemma *ALength- $\tau\text{red0t-xt}$:*

$\tau\text{red0t extTA P t h } (a, xs) (a', xs') \implies \tau\text{red0t extTA P t h } (a.\text{length}, xs) (a'.\text{length}, xs')$

by(*induct rule: tranclp-induct2*)(*auto intro: tranclp.trancl-into-trancl ALengthRed*)

lemma *FAcc- $\tau\text{red0t-xt}$:*

$\tau\text{red0t extTA P t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA P t h } (e.F\{D\}, xs) (e'.F\{D\}, xs')$

by(*induct rule: tranclp-induct2*)(*auto intro: tranclp.trancl-into-trancl FAccRed*)

lemma *FAss- $\tau\text{red0t-xt1}$:*

$\tau\text{red0t extTA P t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA P t h } (e.F\{D\} := e2, xs) (e'.F\{D\} := e2, xs')$

by(*induct rule: tranclp-induct2*)(*auto intro: tranclp.trancl-into-trancl FAssRed1*)

lemma *FAss- $\tau\text{red0t-xt2}$:*

$\tau\text{red0t extTA P t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA P t h } (\text{Val } v.F\{D\} := e, xs) (\text{Val } v.F\{D\} := e', xs')$

by(*induct rule: tranclp-induct2*)(*auto intro: tranclp.trancl-into-trancl FAssRed2*)

lemma *CAS- $\tau\text{red0t-xt1}$:*

$\tau\text{red0t extTA P t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA P t h } (e.\text{compareAndSwap}(D.F, e2, e3), xs) (e'.\text{compareAndSwap}(D.F, e2, e3), xs')$

by(*induct rule: tranclp-induct2*)(*auto intro: tranclp.trancl-into-trancl CASRed1*)

lemma *CAS- $\tau\text{red0t-xt2}$:*

$\tau\text{red0t extTA P t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA P t h } (\text{Val } v.\text{compareAndSwap}(D.F, e, e3), xs) (\text{Val } v.\text{compareAndSwap}(D.F, e', e3), xs')$

by(*induct rule: tranclp-induct2*)(*auto intro: tranclp.trancl-into-trancl CASRed2*)

lemma *CAS- $\tau\text{red0t-xt3}$:*

$\tau\text{red0t extTA P t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA P t h } (\text{Val } v.\text{compareAndSwap}(D.F, \text{Val } v', e), xs) (\text{Val } v.\text{compareAndSwap}(D.F, \text{Val } v', e'), xs')$

by(*induct rule: tranclp-induct2*)(*auto intro: tranclp.trancl-into-trancl CASRed3*)

lemma *Call- $\tau\text{red0t-obj}$:*

$\tau\text{red0t extTA P t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA P t h } (e.M(ps), xs) (e'.M(ps), xs')$

by(*induct rule: tranclp-induct2*)(*auto intro: tranclp.trancl-into-trancl CallObj*)

lemma *Call- $\tau\text{red0t-param}$:*

$\tau\text{red0t extTA P t h } (es, xs) (es', xs') \implies \tau\text{red0t extTA P t h } (\text{Val } v.M(es), xs) (\text{Val } v.M(es'), xs')$

by(*induct rule: tranclp-induct2*)(*fastforce intro: tranclp.trancl-into-trancl CallParams*)**+**

lemma *Block- $\tau\text{red0t-xt}$:*

$\tau\text{red0t extTA P t h } (e, xs(V := vo)) (e', xs') \implies \tau\text{red0t extTA P t h } (\{V:T=vo; e\}, xs) (\{V:T=xs' V; e'\}, xs'(V := xs V))$

proof(*induct rule: tranclp-induct2*)

case base thus ?*case* **by**(*auto intro: BlockRed simp del: fun-upd-apply*)

next

case (*step* $e' xs' e'' xs''$)
from $\langle \tau red0 extTA P t h (e', xs') (e'', xs'') \rangle$
have $extTA, P, t \vdash \langle e', (h, xs') \rangle -\varepsilon \rightarrow \langle e'', (h, xs'') \rangle \tau move0 P h e' no-call P h e'$ **by** *auto*
hence $extTA, P, t \vdash \langle e', (h, xs'(V := xs V, V := xs' V)) \rangle -\varepsilon \rightarrow \langle e'', (h, xs'') \rangle$ **by** *simp*
from *BlockRed[OF this, of T]* $\langle \tau move0 P h e' \rangle \langle no-call P h e' \rangle$
have $\tau red0 extTA P t h (\{V:T=xs' V; e'\}, xs'(V := xs V)) (\{V:T=xs'' V; e''\}, xs''(V := xs V))$
by (*auto*)
with $\langle \tau red0t extTA P t h (\{V:T=vo; e\}, xs) (\{V:T=xs' V; e'\}, xs'(V := xs V)) \rangle$ **show** *?case ..*
qed

lemma *Sync- $\tau red0t$ -xt:*

$\tau red0t extTA P t h (e, xs) (e', xs') \implies \tau red0t extTA P t h (sync_V (e) e2, xs) (sync_V (e') e2, xs')$
by (*induct rule: tranclp-induct2*)(*auto intro: tranclp.trancl-into-trancl SynchronizedRed1*)

lemma *InSync- $\tau red0t$ -xt:*

$\tau red0t extTA P t h (e, xs) (e', xs') \implies \tau red0t extTA P t h (insync_V (a) e, xs) (insync_V (a) e', xs')$
by (*induct rule: tranclp-induct2*)(*auto intro: tranclp.trancl-into-trancl SynchronizedRed2*)

lemma *Seq- $\tau red0t$ -xt:*

$\tau red0t extTA P t h (e, xs) (e', xs') \implies \tau red0t extTA P t h (e;;e2, xs) (e';;e2, xs')$
by (*induct rule: tranclp-induct2*)(*auto intro: tranclp.trancl-into-trancl SeqRed*)

lemma *Cond- $\tau red0t$ -xt:*

$\tau red0t extTA P t h (e, xs) (e', xs') \implies \tau red0t extTA P t h (if (e) e1 else e2, xs) (if (e') e1 else e2, xs')$
by (*induct rule: tranclp-induct2*)(*auto intro: tranclp.trancl-into-trancl CondRed*)

lemma *Throw- $\tau red0t$ -xt:*

$\tau red0t extTA P t h (e, xs) (e', xs') \implies \tau red0t extTA P t h (throw e, xs) (throw e', xs')$
by (*induct rule: tranclp-induct2*)(*auto intro: tranclp.trancl-into-trancl ThrowRed*)

lemma *Try- $\tau red0t$ -xt:*

$\tau red0t extTA P t h (e, xs) (e', xs') \implies \tau red0t extTA P t h (try e catch (C V) e2, xs) (try e' catch (C V) e2, xs')$
by (*induct rule: tranclp-induct2*)(*auto intro: tranclp.trancl-into-trancl TryRed*)

lemma *NewArray- $\tau red0r$ -xt:*

$\tau red0r extTA P t h (e, xs) (e', xs') \implies \tau red0r extTA P t h (newA T[e], xs) (newA T[e'], xs')$
by (*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl NewArrayRed*)

lemma *Cast- $\tau red0r$ -xt:*

$\tau red0r extTA P t h (e, xs) (e', xs') \implies \tau red0r extTA P t h (Cast T e, xs) (Cast T e', xs')$
by (*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl CastRed*)

lemma *InstanceOf- $\tau red0r$ -xt:*

$\tau red0r extTA P t h (e, xs) (e', xs') \implies \tau red0r extTA P t h (e instanceof T, xs) (e' instanceof T, xs')$
by (*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl InstanceOfRed*)

lemma *BinOp- $\tau red0r$ -xt1:*

$\tau red0r extTA P t h (e1, xs) (e1', xs') \implies \tau red0r extTA P t h (e1 \ll bop \gg e2, xs) (e1' \ll bop \gg e2, xs')$
by (*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl BinOpRed1*)

lemma *BinOp- $\tau red0r$ -xt2:*

$\tau red0r extTA P t h (e2, xs) (e2', xs') \implies \tau red0r extTA P t h (Val v \ll bop \gg e2, xs) (Val v \ll bop \gg e2', xs')$

$e2', xs')$

by(*induct rule*: rtranclp-induct2)(*auto intro*: rtranclp.rtrancl-into-rtrancl BinOpRed2)

lemma *LAss- τ red0r*:

$\tau red0r extTA P t h (e, xs) (e', xs') \implies \tau red0r extTA P t h (V := e, xs) (V := e', xs')$

by(*induct rule*: rtranclp-induct2)(*auto intro*: rtranclp.rtrancl-into-rtrancl LAssRed)

lemma *AAcc- τ red0r-xt1*:

$\tau red0r extTA P t h (a, xs) (a', xs') \implies \tau red0r extTA P t h (a[i], xs) (a'[i], xs')$

by(*induct rule*: rtranclp-induct2)(*auto intro*: rtranclp.rtrancl-into-rtrancl AAccRed1)

lemma *AAcc- τ red0r-xt2*:

$\tau red0r extTA P t h (i, xs) (i', xs') \implies \tau red0r extTA P t h (Val a[i], xs) (Val a[i'], xs')$

by(*induct rule*: rtranclp-induct2)(*auto intro*: rtranclp.rtrancl-into-rtrancl AAccRed2)

lemma *AAss- τ red0r-xt1*:

$\tau red0r extTA P t h (a, xs) (a', xs') \implies \tau red0r extTA P t h (a[i] := e, xs) (a'[i] := e, xs')$

by(*induct rule*: rtranclp-induct2)(*auto intro*: rtranclp.rtrancl-into-rtrancl AAssRed1)

lemma *AAss- τ red0r-xt2*:

$\tau red0r extTA P t h (i, xs) (i', xs') \implies \tau red0r extTA P t h (Val a[i] := e, xs) (Val a[i'] := e, xs')$

by(*induct rule*: rtranclp-induct2)(*auto intro*: rtranclp.rtrancl-into-rtrancl AAssRed2)

lemma *AAss- τ red0r-xt3*:

$\tau red0r extTA P t h (e, xs) (e', xs') \implies \tau red0r extTA P t h (Val a[Val i] := e, xs) (Val a[Val i'] := e', xs')$

by(*induct rule*: rtranclp-induct2)(*auto intro*: rtranclp.rtrancl-into-rtrancl AAssRed3)

lemma *ALength- τ red0r-xt*:

$\tau red0r extTA P t h (a, xs) (a', xs') \implies \tau red0r extTA P t h (a.length, xs) (a'.length, xs')$

by(*induct rule*: rtranclp-induct2)(*auto intro*: rtranclp.rtrancl-into-rtrancl ALengthRed)

lemma *FAcc- τ red0r-xt*:

$\tau red0r extTA P t h (e, xs) (e', xs') \implies \tau red0r extTA P t h (e.F\{D\}, xs) (e'.F\{D\}, xs')$

by(*induct rule*: rtranclp-induct2)(*auto intro*: rtranclp.rtrancl-into-rtrancl FAccRed)

lemma *FAss- τ red0r-xt1*:

$\tau red0r extTA P t h (e, xs) (e', xs') \implies \tau red0r extTA P t h (e.F\{D\} := e2, xs) (e'.F\{D\} := e2, xs')$

by(*induct rule*: rtranclp-induct2)(*auto intro*: rtranclp.rtrancl-into-rtrancl FAssRed1)

lemma *FAss- τ red0r-xt2*:

$\tau red0r extTA P t h (e, xs) (e', xs') \implies \tau red0r extTA P t h (Val v.F\{D\} := e, xs) (Val v.F\{D\} := e', xs')$

by(*induct rule*: rtranclp-induct2)(*auto intro*: rtranclp.rtrancl-into-rtrancl FAssRed2)

lemma *CAS- τ red0r-xt1*:

$\tau red0r extTA P t h (e, xs) (e', xs') \implies \tau red0r extTA P t h (e.compareAndSwap(D.F, e2, e3), xs) (e'.compareAndSwap(D.F, e2, e3), xs')$

by(*induct rule*: rtranclp-induct2)(*auto intro*: rtranclp.rtrancl-into-rtrancl CASRed1)

lemma *CAS- τ red0r-xt2*:

$\tau red0r extTA P t h (e, xs) (e', xs') \implies \tau red0r extTA P t h (Val v.compareAndSwap(D.F, e, e3), xs) (Val v.compareAndSwap(D.F, e', e3), xs')$

by(*induct rule*: rtranclp-induct2)(*auto intro*: rtranclp.rtrancl-into-rtrancl CASRed2)

lemma *CAS- τ red0r-xt3*:

$\tau\text{red0r extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0r extTA } P \text{ t h } (Val \ v \cdot compareAndSwap(D \cdot F, Val \ v', e), xs) (Val \ v \cdot compareAndSwap(D \cdot F, Val \ v', e'), xs')$

by(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl CASRed3*)

lemma *Call- τ red0r-obj*:

$\tau\text{red0r extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0r extTA } P \text{ t h } (e \cdot M(ps), xs) (e' \cdot M(ps), xs')$

by(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl CallObj*)

lemma *Call- τ red0r-param*:

$\tau\text{red0r extTA } P \text{ t h } (es, xs) (es', xs') \implies \tau\text{red0r extTA } P \text{ t h } (Val \ v \cdot M(es), xs) (Val \ v \cdot M(es'), xs')$

by(*induct rule: rtranclp-induct2*)(*fastforce intro: rtranclp.rtrancl-into-rtrancl CallParams*)⁺

lemma *Block- τ red0r-xt*:

$\tau\text{red0r extTA } P \text{ t h } (e, xs(V := vo)) (e', xs') \implies \tau\text{red0r extTA } P \text{ t h } (\{V:T=vo; e\}, xs) (\{V:T=xs' V; e'\}, xs'(V := xs V))$

proof(*induct rule: rtranclp-induct2*)

case refl thus ?*case by*(*simp del: fun-upd-apply*)(*auto simp add: fun-upd-apply*)

next

case (*step* $e' \ xs' \ e'' \ xs''$)

from $\langle \tau\text{red0 extTA } P \text{ t h } (e', xs') (e'', xs'') \rangle$

have $\text{extTA}, P, t \vdash \langle e', (h, xs') \rangle -\varepsilon \rightarrow \langle e'', (h, xs'') \rangle \tau\text{move0 } P \text{ h } e' \text{ no-call } P \text{ h } e' \text{ by auto}$

hence $\text{extTA}, P, t \vdash \langle e', (h, xs'(V := xs V, V := xs' V)) \rangle -\varepsilon \rightarrow \langle e'', (h, xs'') \rangle \text{ by simp}$

from *BlockRed*[*OF this, of T*] $\langle \tau\text{move0 } P \text{ h } e' \rangle \langle \text{no-call } P \text{ h } e' \rangle$

have $\tau\text{red0 extTA } P \text{ t h } (\{V:T=xs' V; e'\}, xs'(V := xs V)) (\{V:T=xs'' V; e''\}, xs''(V := xs V))$

by auto

with $\langle \tau\text{red0r extTA } P \text{ t h } (\{V:T=vo; e\}, xs) (\{V:T=xs' V; e'\}, xs'(V := xs V)) \rangle \text{ show } ?\text{case ..}$

qed

lemma *Sync- τ red0r-xt*:

$\tau\text{red0r extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0r extTA } P \text{ t h } (\text{sync}_V(e) \ e2, xs) (\text{sync}_V(e') \ e2, xs')$

by(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl SynchronizedRed1*)

lemma *InSync- τ red0r-xt*:

$\tau\text{red0r extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0r extTA } P \text{ t h } (\text{insync}_V(a) \ e, xs) (\text{insync}_V(a) \ e', xs')$

by(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl SynchronizedRed2*)

lemma *Seq- τ red0r-xt*:

$\tau\text{red0r extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0r extTA } P \text{ t h } (e;;e2, xs) (e';;e2, xs')$

by(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl SeqRed*)

lemma *Cond- τ red0r-xt*:

$\tau\text{red0r extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0r extTA } P \text{ t h } (\text{if } (e) \ e1 \ \text{else } e2, xs) (\text{if } (e') \ e1 \ \text{else } e2, xs')$

by(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl CondRed*)

lemma *Throw- τ red0r-xt*:

$\tau\text{red0r extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0r extTA } P \text{ t h } (\text{throw } e, xs) (\text{throw } e', xs')$

by(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl ThrowRed*)

lemma *Try- τ red0r-xt*:

$\tau\text{red0r extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0r extTA } P \text{ t h } (\text{try } e \ \text{catch}(C \ V) \ e2, xs) (\text{try } e' \ \text{catch}(C \ V) \ e2, xs')$

by(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl TryRed*)

lemma $\tau\text{Red0-conv}$ [*iff*]:

$\tau\text{Red0 } P \ t \ h \ (e, \ es) \ (e', \ es') = (P, t \vdash 0 \ \langle e/es, h \rangle \ -\varepsilon \rightarrow \langle e'/es', h \rangle \wedge \tau\text{Move0 } P \ h \ (e, \ es))$

by(*simp add: $\tau\text{Red0-def}$*)

lemma $\tau\text{red0r-lcl-incr}$:

$\tau\text{red0r extTA } P \ t \ h \ (e, \ xs) \ (e', \ xs') \Longrightarrow \text{dom } xs \subseteq \text{dom } xs'$

by(*induct rule: rtranclp-induct2*)(*auto dest: red-lcl-incr del: subsetI*)

lemma $\tau\text{red0t-lcl-incr}$:

$\tau\text{red0t extTA } P \ t \ h \ (e, \ xs) \ (e', \ xs') \Longrightarrow \text{dom } xs \subseteq \text{dom } xs'$

by(*rule $\tau\text{red0r-lcl-incr}$*)(*rule tranclp-into-rtranclp*)

lemma $\tau\text{red0r-dom-lcl}$:

assumes *wwf: wwf-J-prog P*

shows $\tau\text{red0r extTA } P \ t \ h \ (e, \ xs) \ (e', \ xs') \Longrightarrow \text{dom } xs' \subseteq \text{dom } xs \cup \text{fv } e$

apply(*induct rule: converse-rtranclp-induct2*)

apply *blast*

apply(*clarsimp del: subsetI*)

apply(*frule red-dom-lcl*)

apply(*drule red-fv-subset[OF wwf]*)

apply *auto*

done

lemma $\tau\text{red0t-dom-lcl}$:

assumes *wwf: wwf-J-prog P*

shows $\tau\text{red0t extTA } P \ t \ h \ (e, \ xs) \ (e', \ xs') \Longrightarrow \text{dom } xs' \subseteq \text{dom } xs \cup \text{fv } e$

by(*rule $\tau\text{red0r-dom-lcl}[OF wwf]$*)(*rule tranclp-into-rtranclp*)

lemma $\tau\text{red0r-fv-subset}$:

assumes *wwf: wwf-J-prog P*

shows $\tau\text{red0r extTA } P \ t \ h \ (e, \ xs) \ (e', \ xs') \Longrightarrow \text{fv } e' \subseteq \text{fv } e$

by(*induct rule: converse-rtranclp-induct2*)(*auto dest: red-fv-subset[OF wwf]*)

lemma $\tau\text{red0t-fv-subset}$:

assumes *wwf: wwf-J-prog P*

shows $\tau\text{red0t extTA } P \ t \ h \ (e, \ xs) \ (e', \ xs') \Longrightarrow \text{fv } e' \subseteq \text{fv } e$

by(*rule $\tau\text{red0r-fv-subset}[OF wwf]$*)(*rule tranclp-into-rtranclp*)

lemma **fixes** $e :: ('a, 'b, 'addr) \text{ exp}$ **and** $es :: ('a, 'b, 'addr) \text{ exp list}$

shows $\tau\text{move0-callD: call } e = \lfloor (a, M, vs) \rfloor \Longrightarrow \tau\text{move0 } P \ h \ e \longleftrightarrow (\text{synthesized-call } P \ h \ (a, M, vs) \longrightarrow \tau\text{external}' P \ h \ a \ M)$

and $\tau\text{moves0-callsD: calls } es = \lfloor (a, M, vs) \rfloor \Longrightarrow \tau\text{moves0 } P \ h \ es \longleftrightarrow (\text{synthesized-call } P \ h \ (a, M, vs) \longrightarrow \tau\text{external}' P \ h \ a \ M)$

apply(*induct e and es rule: call.induct calls.induct*)

apply(*auto split: if-split-asm simp add: is-vals-conv*)

apply(*fastforce simp add: synthesized-call-def map-eq-append-conv $\tau\text{external}'\text{-def}$ $\tau\text{external}\text{-def}$ *dest: sees-method-fun*)**+***

done

lemma **fixes** $e :: ('a, 'b, 'addr) \text{ exp}$ **and** $es :: ('a, 'b, 'addr) \text{ exp list}$

shows $\tau\text{move0-not-call: } \llbracket \tau\text{move0 } P \ h \ e; \text{ call } e = \lfloor (a, M, vs) \rfloor; \text{ synthesized-call } P \ h \ (a, M, vs) \rrbracket \Longrightarrow \tau\text{external}' P \ h \ a \ M$

and $\tau\text{moves0-not-calls}$: $\llbracket \tau\text{moves0 } P \ h \ es; \text{calls } es = \llbracket (a, M, vs) \rrbracket; \text{synthesized-call } P \ h \ (a, M, vs) \rrbracket$
 $\implies \tau\text{external}' \ P \ h \ a \ M$
apply(*drule* $\tau\text{move0-callD}$ [**where** $P=P$ **and** $h=h$], *simp*)
apply(*drule* $\tau\text{moves0-callsD}$ [**where** $P=P$ **and** $h=h$], *simp*)
done

lemma $\tau\text{red0-into-}\tau\text{Red0}$:

assumes red : $\tau\text{red0 } (\text{extTA2J0 } P) \ P \ t \ h \ (e, \text{Map.empty}) \ (e', \text{xs}')$
shows $\tau\text{Red0 } P \ t \ h \ (e, es) \ (e', es)$

proof –

from red **have** red : $\text{extTA2J0 } P, P, t \vdash \langle e, (h, \text{Map.empty}) \rangle -\varepsilon \rightarrow \langle e', (h, \text{xs}') \rangle$
and $\tau\text{move0 } P \ h \ e$ **and** $\text{no-call } P \ h \ e$ **by** *auto*
hence $P, t \vdash 0 \langle e/es, h \rangle -\varepsilon \rightarrow \langle e'/es, h \rangle$
by–(*erule red0Red, auto simp add: no-call-def*)
thus *?thesis* **using** $\langle \tau\text{move0 } P \ h \ e \rangle$ **by**(*auto*)

qed

lemma $\tau\text{red0r-into-}\tau\text{Red0r}$:

assumes wvf : $\text{wvf-J-prog } P$

shows

$\llbracket \tau\text{red0r } (\text{extTA2J0 } P) \ P \ t \ h \ (e, \text{Map.empty}) \ (e'', \text{Map.empty}); \text{fv } e = \{\} \rrbracket$
 $\implies \tau\text{Red0r } P \ t \ h \ (e, es) \ (e'', es)$

proof(*induct e xs* \equiv *Map.empty* :: '*addr locals rule: converse-rtranclp-induct2*)

case refl **show** *?case* **by** *blast*

next

case (*step e e' xs'*)
from $\langle \tau\text{red0 } (\text{extTA2J0 } P) \ P \ t \ h \ (e, \text{Map.empty}) \ (e', \text{xs}') \rangle$
have red : $\text{extTA2J0 } P, P, t \vdash \langle e, (h, \text{Map.empty}) \rangle -\varepsilon \rightarrow \langle e', (h, \text{xs}') \rangle$
and $\tau\text{move0 } P \ h \ e$ **and** $\text{no-call } P \ h \ e$ **by** *auto*
from $\text{red-dom-lcl}[\text{OF } \text{red}] \ \langle \text{fv } e = \{\} \rangle$
have $\text{dom } \text{xs}' = \{\}$ **by**(*auto split:if-split-asm*)
hence $\text{xs}' = \text{Map.empty}$ **by**(*auto*)
moreover
from $\text{wvf } \text{red}$ **have** $\text{fv } e' \subseteq \text{fv } e$ **by**(*rule red-fv-subset*)
with $\langle \text{fv } e = \{\} \rangle$ **have** $\text{fv } e' = \{\}$ **by** *blast*
ultimately have $\tau\text{Red0r } P \ t \ h \ (e', es) \ (e'', es)$ **by**(*rule step*)
moreover from red $\langle \tau\text{move0 } P \ h \ e \rangle \ \langle \text{xs}' = \text{Map.empty} \rangle \ \langle \text{no-call } P \ h \ e \rangle$
have $\tau\text{Red0 } P \ t \ h \ (e, es) \ (e', es)$ **by**(*auto simp add: no-call-def intro!: red0Red*)
ultimately show *?case* **by**(*blast intro: converse-rtranclp-into-rtranclp*)

qed

lemma $\tau\text{red0t-into-}\tau\text{Red0t}$:

assumes wvf : $\text{wvf-J-prog } P$

shows

$\llbracket \tau\text{red0t } (\text{extTA2J0 } P) \ P \ t \ h \ (e, \text{Map.empty}) \ (e'', \text{Map.empty}); \text{fv } e = \{\} \rrbracket$
 $\implies \tau\text{Red0t } P \ t \ h \ (e, es) \ (e'', es)$

proof(*induct e xs* \equiv *Map.empty* :: '*addr locals rule: converse-tranclp-induct2*)

case base **thus** *?case*

by(*blast intro!: tranclp.r-into-tranclp-into-rtranclp-into-rtranclp*)

next

case (*step e e' xs'*)
from $\langle \tau\text{red0 } (\text{extTA2J0 } P) \ P \ t \ h \ (e, \text{Map.empty}) \ (e', \text{xs}') \rangle$
have red : $\text{extTA2J0 } P, P, t \vdash \langle e, (h, \text{Map.empty}) \rangle -\varepsilon \rightarrow \langle e', (h, \text{xs}') \rangle$ **and** $\tau\text{move0 } P \ h \ e$ **and** no-call

P h e **by** *auto*

from *red-dom-lcl*[*OF red*] $\langle fv\ e = \{\} \rangle$
have *dom* $xs' = \{\}$ **by** (*auto split:if-split-asm*)
hence $xs' = Map.empty$ **by** *auto*
moreover from *wf red* **have** $fv\ e' \subseteq fv\ e$ **by** (*rule red-fv-subset*)
with $\langle fv\ e = \{\} \rangle$ **have** $fv\ e' = \{\}$ **by** *blast*
ultimately have $\tau Red0t\ P\ t\ h\ (e',\ es)\ (e'',\ es)$ **by** (*rule step*)
moreover from *red* $\langle \tau move0\ P\ h\ e \rangle\ \langle xs' = Map.empty \rangle\ \langle no-call\ P\ h\ e \rangle$
have $\tau Red0\ P\ t\ h\ (e,\ es)\ (e',\ es)$ **by** (*auto simp add: no-call-def intro!: red0Red*)
ultimately show *?case* **by** (*blast intro: tranclp-into-tranclp2*)

qed

lemma *$\tau red0r$ -Val*:

$\tau red0r\ extTA\ P\ t\ h\ (Val\ v,\ xs)\ s' \longleftrightarrow s' = (Val\ v,\ xs)$

proof

assume $\tau red0r\ extTA\ P\ t\ h\ (Val\ v,\ xs)\ s'$
thus $s' = (Val\ v,\ xs)$ **by** *induct(auto)*

qed *auto*

lemma *$\tau red0t$ -Val*:

$\tau red0t\ extTA\ P\ t\ h\ (Val\ v,\ xs)\ s' \longleftrightarrow False$

proof

assume $\tau red0t\ extTA\ P\ t\ h\ (Val\ v,\ xs)\ s'$
thus *False* **by** *induct auto*

qed *auto*

lemma *$\tau reds0r$ -map-Val*:

$\tau reds0r\ extTA\ P\ t\ h\ (map\ Val\ vs,\ xs)\ s' \longleftrightarrow s' = (map\ Val\ vs,\ xs)$

proof

assume $\tau reds0r\ extTA\ P\ t\ h\ (map\ Val\ vs,\ xs)\ s'$
thus $s' = (map\ Val\ vs,\ xs)$ **by** *induct auto*

qed *auto*

lemma *$\tau reds0t$ -map-Val*:

$\tau reds0t\ extTA\ P\ t\ h\ (map\ Val\ vs,\ xs)\ s' \longleftrightarrow False$

proof

assume $\tau reds0t\ extTA\ P\ t\ h\ (map\ Val\ vs,\ xs)\ s'$
thus *False* **by** *induct auto*

qed *auto*

lemma *Red-Suspend-is-call*:

$\llbracket P, t \vdash 0\ \langle e/exs,\ h \rangle -ta \rightarrow \langle e'/exs',\ h' \rangle; Suspend\ w \in set\ \{\{ta\}_w\} \rrbracket \implies is-call\ e'$
by (*auto elim!: red0.cases dest: red-Suspend-is-call simp add: is-call-def*)

lemma *red0-mthr: multithreaded final-expr0* (*mred0 P*)

by (*unfold-locales*)(*auto elim!: red0.cases dest: red-new-thread-heap*)

lemma *red0- τ mthr-wf: τ multithreaded-wf final-expr0* (*mred0 P*) (*$\tau MOVE0 P$*)

proof –

interpret *multithreaded final-expr0 mred0 P* **by** (*rule red0-mthr*)

show *?thesis*

proof

fix $x1\ m1\ t\ ta1\ x1'\ m1'$

```

  assume mred0 P t (x1, m1) ta1 (x1', m1')  $\tau$ MOVE0 P (x1, m1) ta1 (x1', m1')
  thus m1 = m1' by(cases x1)(fastforce elim!: red0.cases dest:  $\tau$ move0-heap-unchanged)
qed(simp add: split-beta)
qed

```

lemma *red- τ mthr-wf*: τ multithreaded-wf final-expr (mred P) (τ MOVE P)

proof

```

  fix x1 m1 t ta1 x1' m1'
  assume mred P t (x1, m1) ta1 (x1', m1')  $\tau$ MOVE P (x1, m1) ta1 (x1', m1')
  thus m1 = m1' by(auto dest:  $\tau$ move0-heap-unchanged simp add: split-def)
qed(simp add: split-beta)

```

end

sublocale *J-heap-base* < *red-mthr*:

```

   $\tau$ multithreaded-wf
  final-expr
  mred P
  convert-RA
   $\tau$ MOVE P
  for P
by(rule red- $\tau$ mthr-wf)

```

sublocale *J-heap-base* < *red0-mthr*:

```

   $\tau$ multithreaded-wf
  final-expr0
  mred0 P
  convert-RA
   $\tau$ MOVE0 P
  for P
by(rule red0- $\tau$ mthr-wf)

```

context *J-heap-base* **begin**

lemma *τ Red0r-into-red0- τ mthr-silent-moves*:

```

   $\tau$ Red0r P t h (e, es) (e'', es'')  $\implies$  red0-mthr.silent-moves P t ((e, es), h) ((e'', es''), h)
apply(induct rule: rtranclp-induct2)
apply blast
apply(erule rtranclp.rtrancl-into-rtrancl)
apply(simp add: red0-mthr.silent-move-iff)
done

```

lemma *τ Red0t-into-red0- τ mthr-silent-movet*:

```

   $\tau$ Red0t P t h (e, es) (e'', es'')  $\implies$  red0-mthr.silent-movet P t ((e, es), h) ((e'', es''), h)
apply(induct rule: tranclp-induct2)
apply(fastforce simp add: red0-mthr.silent-move-iff elim: tranclp.trancl-into-trancl)+
done

```

end

end

7.3 Bisimulation proof for between source code small step semantics with and without callstacks for single threads

theory *J0Bisim* **imports**

J0
../J/JWellForm
../Common/ExternalCallWF

begin

inductive *wf-state* :: '*addr expr* × '*addr expr list* ⇒ *bool*

where

$\llbracket fvs (e \# es) = \{\}; \forall e \in set\ es.\ is-call\ e \rrbracket$
 $\implies wf-state\ (e, es)$

inductive *bisim-red-red0* :: ('*addr expr* × '*addr locals*) × '*heap* ⇒ ('*addr expr* × '*addr expr list*) × '*heap* ⇒ *bool*

where

$wf-state\ ees \implies bisim-red-red0\ ((collapse\ ees, Map.empty), h)\ (ees, h)$

abbreviation *ta-bisim0* :: ('*addr*, '*thread-id*, '*heap*) *J-thread-action* ⇒ ('*addr*, '*thread-id*, '*heap*) *J0-thread-action* ⇒ *bool*

where $ta-bisim0 \equiv ta-bisim\ (\lambda t.\ bisim-red-red0)$

lemma *wf-state-iff* [*simp*, *code*]:

$wf-state\ (e, es) \longleftrightarrow fvs\ (e \# es) = \{\} \wedge (\forall e \in set\ es.\ is-call\ e)$

by(*simp add: wf-state.simps*)

lemma *bisim-red-red0I* [*intro*]:

$\llbracket e' = collapse\ ees; xs = Map.empty; h' = h; wf-state\ ees \rrbracket \implies bisim-red-red0\ ((e', xs), h')\ (ees, h)$

by(*simp add: bisim-red-red0.simps del: split-paired-Ex*)

lemma *bisim-red-red0-final0D*:

$\llbracket bisim-red-red0\ (x1, m1)\ (x2, m2); final-expr0\ x2 \rrbracket \implies final-expr\ x1$

by(*erule bisim-red-red0.cases auto*)

context *J-heap-base* **begin**

lemma *red0-preserves-wf-state*:

assumes *wf: wwf-J-prog P*

and *red: P, t ⊢ 0 ⟨e / es, h⟩ -ta→ ⟨e' / es', h'⟩*

and *wf-state: wf-state (e, es)*

shows *wf-state (e', es')*

using *wf-state*

proof(*cases*)

assume *fvs (e # es) = {} and icl: ∀ e ∈ set es. is-call e*

hence *fv e = {} fvs es = {} by auto*

show *?thesis*

proof

from *red show fvs (e' # es') = {}*

proof *cases*

case (*red0Red xs'*)

hence [*simp*]: *es' = es*

and *red: extTA2J0 P, P, t ⊢ ⟨e, (h, Map.empty)⟩ -ta→ ⟨e', (h', xs')⟩ by auto*

from *red-fv-subset[OF wf red] fv have fv e' = {} by auto*

```

with fv show ?thesis by simp
next
case (red0Call a M vs U Ts T pns body D)
hence [simp]: ta = ε
e' = blocks (this # pns) (Class D # Ts) (Addr a # vs) body
es' = e # es h' = h
and sees: P ⊢ class-type-of U sees M: Ts→T = [(pns, body)] in D by auto
from sees-wf-mdecl[OF wf sees]
have fv body ⊆ insert this (set pns) length Ts = length pns by (simp-all add: wf-mdecl-def)
thus ?thesis using fv ⟨length vs = length pns⟩ by auto
next
case (red0Return E)
with fv-inline-call[of e E] show ?thesis using fv by auto
qed
next
from red icl show ∀ e∈set es'. is-call e
by cases(simp-all add: is-call-def)
qed
qed

```

lemma *new-thread-bisim0-extNTA2J-extNTA2J0*:

```

assumes wf: wwf-J-prog P
and red: P, t ⊢ ⟨a'·M'(vs), h⟩ -ta→ext ⟨va, h'⟩
and nt: NewThread t' CMa m ∈ set {ta}_t
shows bisim-red-red0 (extNTA2J P CMa, m) (extNTA2J0 P CMa, m)

```

proof –

```

obtain C M a where CMa [simp]: CMa = (C, M, a) by (cases CMa)
from red nt have [simp]: m = h' by (rule red-ext-new-thread-heap)
from red-external-new-thread-sees[OF wf red nt[unfolded CMa]]
obtain T pns body D where h'a: typeof-addr h' a = [Class-type C]
and sees: P ⊢ C sees M: []→T = [(pns, body)] in D by auto
from sees-wf-mdecl[OF wf sees] have fv body ⊆ {this} by (auto simp add: wf-mdecl-def)
with red nt h'a sees show ?thesis by (fastforce simp add: is-call-def intro: bisim-red-red0.intros)

```

qed

lemma *ta-bisim0-extNTA2J-extNTA2J0*:

```

[[ wwf-J-prog P; P, t ⊢ ⟨a'·M'(vs), h⟩ -ta→ext ⟨va, h'⟩ ]]
⇒ ta-bisim0 (extTA2J P ta) (extTA2J0 P ta)
apply (auto simp add: ta-bisim-def intro!: list-all2-all-nthI)
apply (case-tac {ta}_t ! n)
apply (simp-all)
apply (erule (1) new-thread-bisim0-extNTA2J-extNTA2J0)
apply (auto simp add: in-set-conv-nth)
done

```

lemma *assumes wf: wwf-J-prog P*

```

shows red-red0-tabisim0:
P, t ⊢ ⟨e, s⟩ -ta→ ⟨e', s'⟩ ⇒ ∃ ta'. extTA2J0 P, P, t ⊢ ⟨e, s⟩ -ta'→ ⟨e', s'⟩ ∧ ta-bisim0 ta ta'
and reds-reds0-tabisim0:
P, t ⊢ ⟨es, s⟩ [-ta→] ⟨es', s'⟩ ⇒ ∃ ta'. extTA2J0 P, P, t ⊢ ⟨es, s⟩ [-ta'→] ⟨es', s'⟩ ∧ ta-bisim0 ta ta'
proof (induct rule: red-reds.inducts)
case (RedCallExternal s a T M Ts Tr D vs ta va h' ta' e' s')
note red = ⟨P, t ⊢ ⟨a·M(vs), hp s⟩ -ta→ext ⟨va, h'⟩⟩
note T = ⟨typeof-addr (hp s) a = [T]⟩

```

from $T \langle P \vdash \text{class-type-of } T \text{ sees } M: Ts \rightarrow Tr = \text{Native in } D \rangle \text{ red}$
have $\text{extTA2J0 } P, P, t \vdash \langle \text{addr } a \cdot M(\text{map Val } vs), s \rangle - \text{ta} \rightarrow \langle e', (h', \text{lcl } s) \rangle$
by $(\text{rule red-reds.RedCallExternal})(\text{simp-all add: } \langle e' = \text{extRet2J } (\text{addr } a \cdot M(\text{map Val } vs)) \text{ va} \rangle)$
moreover from $\langle ta' = \text{extTA2J } P \text{ ta} \rangle T \text{ red wf}$
have $\text{ta-bisim0 } ta' (\text{extTA2J0 } P \text{ ta})$ **by** $(\text{auto intro: ta-bisim0-extNTA2J-extNTA2J0})$
ultimately show $?case \text{ unfolding } \langle s' = (h', \text{lcl } s) \rangle$ **by** blast
next
case RedTryFail **thus** $?case$ **by** $(\text{force intro: red-reds.RedTryFail})$
qed $(\text{fastforce intro: red-reds.intros simp add: ta-bisim-def ta-upd-simps})+$

lemma assumes $\text{wf: wwf-J-prog } P$
shows $\text{red0-red-tabisim0:}$
 $\text{extTA2J0 } P, P, t \vdash \langle e, s \rangle - \text{ta} \rightarrow \langle e', s' \rangle \implies \exists ta'. P, t \vdash \langle e, s \rangle - \text{ta}' \rightarrow \langle e', s' \rangle \wedge \text{ta-bisim0 } ta' \text{ ta}$
and $\text{reds0-reds-tabisim0:}$
 $\text{extTA2J0 } P, P, t \vdash \langle es, s \rangle [-\text{ta} \rightarrow] \langle es', s' \rangle \implies \exists ta'. P, t \vdash \langle es, s \rangle [-\text{ta}' \rightarrow] \langle es', s' \rangle \wedge \text{ta-bisim0 } ta' \text{ ta}$
proof $(\text{induct rule: red-reds.inducts})$
case $(\text{RedCallExternal } s \ a \ T \ M \ Ts \ Tr \ D \ vs \ ta \ va \ h' \ ta' \ e' \ s')$
note $\text{red} = \langle P, t \vdash \langle a \cdot M(vs), hp \ s \rangle - \text{ta} \rightarrow \text{ext } \langle va, h' \rangle \rangle$
note $T = \langle \text{typeof-addr } (hp \ s) \ a = \lfloor T \rfloor \rangle$
from $T \langle P \vdash \text{class-type-of } T \text{ sees } M: Ts \rightarrow Tr = \text{Native in } D \rangle \text{ red}$
have $P, t \vdash \langle \text{addr } a \cdot M(\text{map Val } vs), s \rangle - \text{extTA2J } P \text{ ta} \rightarrow \langle e', (h', \text{lcl } s) \rangle$
by $(\text{rule red-reds.RedCallExternal})(\text{simp-all add: } \langle e' = \text{extRet2J } (\text{addr } a \cdot M(\text{map Val } vs)) \text{ va} \rangle)$
moreover from $\langle ta' = \text{extTA2J0 } P \text{ ta} \rangle T \text{ red wf}$
have $\text{ta-bisim0 } (\text{extTA2J } P \text{ ta}) \ ta'$ **by** $(\text{auto intro: ta-bisim0-extNTA2J-extNTA2J0})$
ultimately show $?case \text{ unfolding } \langle s' = (h', \text{lcl } s) \rangle$ **by** blast
next
case RedTryFail **thus** $?case$ **by** $(\text{force intro: red-reds.RedTryFail})$
qed $(\text{fastforce intro: red-reds.intros simp add: ta-bisim-def ta-upd-simps})+$

lemma red-inline-call-red:
assumes $\text{red: } P, t \vdash \langle e, (h, \text{Map.empty}) \rangle - \text{ta} \rightarrow \langle e', (h', \text{Map.empty}) \rangle$
shows $\text{call } E = \lfloor aMvs \rfloor \implies P, t \vdash \langle \text{inline-call } e \ E, (h, x) \rangle - \text{ta} \rightarrow \langle \text{inline-call } e' \ E, (h', x) \rangle$
(is - \implies ?concl $E \ x$)

and
 $\text{calls } Es = \lfloor aMvs \rfloor \implies P, t \vdash \langle \text{inline-calls } e \ Es, (h, x) \rangle [-\text{ta} \rightarrow] \langle \text{inline-calls } e' \ Es, (h', x) \rangle$
(is - \implies ?concls $Es \ x$)
proof $(\text{induct } E \text{ and } Es \text{ arbitrary: } x \text{ and } x \text{ rule: call.induct calls.induct})$
case $(\text{Call obj } M \ pns \ x)$
note $IHobj = \langle \bigwedge x. \text{call obj} = \lfloor aMvs \rfloor \implies ?concl \text{ obj } x \rangle$
note $IHpns = \langle \bigwedge x. \text{calls pns} = \lfloor aMvs \rfloor \implies ?concls \text{ pns } x \rangle$
obtain $a \ M' \ vs$ **where** $[\text{simp}]: aMvs = (a, M', vs)$ **by** $(\text{cases } aMvs, \text{auto})$
from $\langle \text{call } (\text{obj} \cdot M(pns)) = \lfloor aMvs \rfloor \rangle$ **have** $\text{call } (\text{obj} \cdot M(pns)) = \lfloor (a, M', vs) \rfloor$ **by** simp
thus $?case$
proof $(\text{induct rule: call-callE})$
case CallObj
with $IHobj[\text{of } x]$ **show** $?case$ **by** $(\text{fastforce intro: red-reds.CallObj})$
next
case $(\text{CallParams } v')$
with $IHpns[\text{of } x]$ **show** $?case$ **by** $(\text{fastforce intro: red-reds.CallParams})$
next
case Call
from $\text{red-lcl-add}[OF \ \text{red}, \text{ where } ?l0.0=x]$
have $P, t \vdash \langle e, (h, x) \rangle - \text{ta} \rightarrow \langle e', (h', x) \rangle$ **by** simp

```

  with Call show ?case by(fastforce dest: BlockRed)
qed
next
case (Block V T' vo exp x)
note IH = ⟨ $\bigwedge x. \text{call } \text{exp} = \lfloor aMvs \rfloor \implies ?\text{concl } \text{exp } x$ ⟩
from IH[of  $x(V := vo)$ ] ⟨ $\text{call } \{V:T'=vo; \text{exp}\} = \lfloor aMvs \rfloor$ ⟩
show ?case by(clarsimp simp del: fun-upd-apply)(drule BlockRed, auto)
next
case (Cons-exp exp exps x)
show ?case
proof(cases is-val exp)
  case True
  with ⟨ $\text{calls } (\text{exp} \# \text{exps}) = \lfloor aMvs \rfloor$ ⟩ have  $\text{calls } \text{exps} = \lfloor aMvs \rfloor$  by auto
  with ⟨ $\text{calls } \text{exps} = \lfloor aMvs \rfloor \implies ?\text{concls } \text{exps } x$ ⟩ True
  show ?thesis by(fastforce intro: ListRed2)
next
case False
  with ⟨ $\text{calls } (\text{exp} \# \text{exps}) = \lfloor aMvs \rfloor$ ⟩ have  $\text{call } \text{exp} = \lfloor aMvs \rfloor$  by auto
  with ⟨ $\text{call } \text{exp} = \lfloor aMvs \rfloor \implies ?\text{concl } \text{exp } x$ ⟩
  show ?thesis by(fastforce intro: ListRed1)
qed
qed(fastforce intro: red-reds.intros)+

lemma
  assumes  $P \vdash \text{class-type-of } T \text{ sees } M:Us \rightarrow U = \lfloor (pns, \text{body}) \rfloor$  in  $D$  length  $vs = \text{length } pns$  length  $Us = \text{length } pns$ 
  shows is-call-red-inline-call:
     $\llbracket \text{call } e = \lfloor (a, M, vs) \rfloor; \text{typeof-addr } (hp \ s) \ a = \lfloor T \rfloor \rrbracket$ 
 $\implies P, t \vdash \langle e, s \rangle \text{--}\varepsilon \rightarrow \langle \text{inline-call } (\text{blocks } (\text{this} \# \text{pns}) (\text{Class } D \# Us) (\text{Addr } a \# vs) \text{body}) \ e, s \rangle$ 
    (is -  $\implies$  -  $\implies$  ?red  $e \ s$ )
  and is-calls-reds-inline-calls:
     $\llbracket \text{calls } es = \lfloor (a, M, vs) \rfloor; \text{typeof-addr } (hp \ s) \ a = \lfloor T \rfloor \rrbracket$ 
 $\implies P, t \vdash \langle es, s \rangle \text{[-}\varepsilon \rightarrow] \langle \text{inline-calls } (\text{blocks } (\text{this} \# \text{pns}) (\text{Class } D \# Us) (\text{Addr } a \# vs) \text{body}) \ es, s \rangle$ 
    (is -  $\implies$  -  $\implies$  ?reds  $es \ s$ )
proof(induct e and es arbitrary: s and s rule: call.induct calls.induct)
  case (Call obj M' params s)
  note IHObj = ⟨ $\bigwedge s. \llbracket \text{call } \text{obj} = \lfloor (a, M, vs) \rfloor; \text{typeof-addr } (hp \ s) \ a = \lfloor T \rfloor \rrbracket \implies ?\text{red } \text{obj } s$ ⟩
  note IHParams = ⟨ $\bigwedge s. \llbracket \text{calls } \text{params} = \lfloor (a, M, vs) \rfloor; \text{typeof-addr } (hp \ s) \ a = \lfloor T \rfloor \rrbracket \implies ?\text{reds } \text{params}$ ⟩
  from ⟨ $\text{call } (\text{obj} \cdot M'(\text{params})) = \lfloor (a, M, vs) \rfloor$ ⟩
  show ?case
  proof(induct rule: call-callE)
    case CallObj
    from IHObj[OF CallObj] ⟨ $\text{typeof-addr } (hp \ s) \ a = \lfloor T \rfloor$ ⟩ have ?red obj s by blast
    moreover from CallObj have  $\neg \text{is-val } \text{obj}$  by auto
    ultimately show ?case by(auto intro: red-reds.CallObj)
  next
  case (CallParams v)
  from IHParams[OF CallParams] ⟨ $\text{calls } \text{params} = \lfloor (a, M, vs) \rfloor$ ⟩ ⟨ $\text{typeof-addr } (hp \ s) \ a = \lfloor T \rfloor$ ⟩
  have ?reds params s by blast
  moreover from CallParams have  $\neg \text{is-vals } \text{params}$  by auto
  ultimately show ?case using ⟨ $\text{obj} = \text{Val } v$ ⟩ by(auto intro: red-reds.CallParams)
  next
  case Call

```

with *RedCall*[**where** $s=s$, *simplified*, *OF* $\langle \text{typeof-addr } (hp\ s)\ a = \lfloor T \rfloor \rangle \langle P \vdash \text{class-type-of } T \text{ sees } M:Us \rightarrow U = \lfloor (pns, \text{body}) \rfloor \text{ in } D \rangle \langle \text{length } vs = \text{length } pns \rangle \langle \text{length } Us = \text{length } pns \rangle$
show *?thesis* **by**(*simp*)
qed
next
case (*Block* $V\ ty\ vo\ exp\ s$)
note $IH = \langle \bigwedge s. \llbracket \text{call } exp = \lfloor (a, M, vs) \rfloor; \text{typeof-addr } (hp\ s)\ a = \lfloor T \rfloor \rrbracket \implies ?red\ exp\ s \rangle$
from $\langle \text{call } \{V:ty=vo; exp\} = \lfloor (a, M, vs) \rfloor \rangle IH[\text{of } (hp\ s, (lcl\ s)(V := vo))] \langle \text{typeof-addr } (hp\ s)\ a = \lfloor T \rfloor \rangle$
show *?case* **by**(*cases* s , *simp del: fun-upd-apply*)(*drule red-reds.BlockRed*, *simp*)
qed(*fastforce intro: red-reds.intros*)+

lemma *red-inline-call-red'*:

assumes *fv: fv ee = {}*
and *eefin: $\neg \text{final } ee$*
shows $\llbracket \text{call } E = \lfloor aMvs \rfloor; P, t \vdash \langle \text{inline-call } ee\ E, (h, x) \rangle \text{ --ta--} \langle E', (h', x') \rangle \rrbracket$
 $\implies \exists ee'. E' = \text{inline-call } ee'\ E \wedge P, t \vdash \langle ee, (h, \text{Map.empty}) \rangle \text{ --ta--} \langle ee', (h', \text{Map.empty}) \rangle \wedge$
 $x = x'$
(is $\llbracket -; - \rrbracket \implies ?concl\ E\ E'\ x\ x'$ **)**
and $\llbracket \text{calls } Es = \lfloor aMvs \rfloor; P, t \vdash \langle \text{inline-calls } ee\ Es, (h, x) \rangle \text{ [-ta-]} \langle Es', (h', x') \rangle \rrbracket$
 $\implies \exists ee'. Es' = \text{inline-calls } ee'\ Es \wedge P, t \vdash \langle ee, (h, \text{Map.empty}) \rangle \text{ --ta--} \langle ee', (h', \text{Map.empty}) \rangle$
 $\wedge x = x'$
(is $\llbracket -; - \rrbracket \implies ?concls\ Es\ Es'\ x\ x'$ **)**
proof(*induct* E **and** Es *arbitrary: $E' x x'$ and $Es' x x'$ rule: call.induct calls.induct*)
case *new* **thus** *?case* **by** *simp*
next
case (*newArray* $T\ exp\ E'\ x\ x'$)
thus *?case* **using** *eefin* **by**(*auto elim!: red-cases*)
next
case *Cast* **thus** *?case* **using** *eefin* **by**(*auto elim!: red-cases*)
next
case *InstanceOf* **thus** *?case* **using** *eefin* **by**(*auto elim!: red-cases*)
next
case *Val* **thus** *?case* **by** *simp*
next
case *Var* **thus** *?case* **by** *simp*
next
case *LAss*
thus *?case* **using** *eefin* **by**(*auto elim!: red-cases*)
next
case *BinOp*
thus *?case* **using** *eefin* **by**(*auto elim!: red-cases split: if-split-asm*)
next
case *AAcc*
thus *?case* **using** *eefin* **by**(*auto elim!: red-cases split: if-split-asm*)
next
case *AAss* **thus** *?case* **using** *eefin* **by**(*auto elim!: red-cases split: if-split-asm*)
next
case *ALen* **thus** *?case* **using** *eefin* **by**(*auto elim!: red-cases split: if-split-asm*)
next
case *FAcc* **thus** *?case* **using** *eefin* **by**(*auto elim!: red-cases*)
next
case *FAss* **thus** *?case* **using** *eefin* **by**(*auto elim!: red-cases split: if-split-asm*)
next

case *CompareAndSwap* **thus** *?case using eefin by(auto elim!: red-cases split: if-split-asm)*
next
case (*Call obj M pns E' x x'*)
note $IHobj = \langle \bigwedge x E' x'. \llbracket call\ obj = [aMvs]; P, t \vdash \langle inline\ call\ ee\ obj, (h, x) \rangle -ta \rightarrow \langle E', (h', x') \rangle \rrbracket$
 $\implies ?concl\ obj\ E' x x'$
note $IHpns = \langle \bigwedge Es' x x'. \llbracket calls\ pns = [aMvs]; P, t \vdash \langle inline\ calls\ ee\ pns, (h, x) \rangle [-ta \rightarrow] \langle Es', (h', x') \rangle \rrbracket$
 $\implies ?concls\ pns\ Es' x x'$
note $red = \langle P, t \vdash \langle inline\ call\ ee\ (obj \cdot M(pns)), (h, x) \rangle -ta \rightarrow \langle E', (h', x') \rangle \rangle$
obtain $a\ M' vs$ **where** [*simp*]: $aMvs = (a, M', vs)$ **by** (*cases aMvs, auto*)
from $\langle call\ (obj \cdot M(pns)) = [aMvs] \rangle$ **have** $call\ (obj \cdot M(pns)) = [(a, M', vs)]$ **by** *simp*
thus *?case*
proof (*cases rule: call-callE*)
case *CallObj*
hence $\neg is\ val\ obj$ **by** *auto*
with $red\ CallObj\ eefin$ **obtain** obj' **where** $E' = obj' \cdot M(pns)$
and $red': P, t \vdash \langle inline\ call\ ee\ obj, (h, x) \rangle -ta \rightarrow \langle obj', (h', x') \rangle$
by (*auto elim!: red-cases*)
from $IHobj[OF - red']\ CallObj$ **obtain** ee'
where $inline\ call\ ee'\ obj = obj' x = x'$
and $P, t \vdash \langle ee, (h, Map.empty) \rangle -ta \rightarrow \langle ee', (h', Map.empty) \rangle$ **by** (*auto simp del: fun-upd-apply*)
with $\langle E' = obj' \cdot M(pns) \rangle\ CallObj\ red'$ **show** *?thesis* **by** (*fastforce simp del: fun-upd-apply*)
next
case (*CallParams v'*)
hence $\neg is\ vals\ pns$ **by** *auto*
with $red\ CallParams\ eefin$ **obtain** pns' **where** $E' = obj \cdot M(pns')$
and $red': P, t \vdash \langle inline\ calls\ ee\ pns, (h, x) \rangle [-ta \rightarrow] \langle pns', (h', x') \rangle$
by (*auto elim!: red-cases*)
from $IHpns[OF - red']\ CallParams$ **obtain** ee'
where $inline\ calls\ ee'\ pns = pns' x = x'$
and $P, t \vdash \langle ee, (h, Map.empty) \rangle -ta \rightarrow \langle ee', (h', Map.empty) \rangle$
by (*auto simp del: fun-upd-apply*)
with $\langle E' = obj \cdot M(pns') \rangle\ CallParams\ red'$ $\langle \neg is\ vals\ pns \rangle$
show *?thesis* **by** (*auto simp del: fun-upd-apply*)
next
case *Call*
with red **have** $red': P, t \vdash \langle ee, (h, x) \rangle -ta \rightarrow \langle E', (h', x') \rangle$ **by** (*auto*)
from $red\ lcl\ sub[OF\ red', of\ \{\}] fv$
have $P, t \vdash \langle ee, (h, Map.empty) \rangle -ta \rightarrow \langle E', (h', Map.empty) \rangle$ **by** *simp*
moreover **have** $x' = x$
proof (*rule ext*)
fix V
from $red\ notfree\ unchanged[OF\ red', of\ V] fv$
show $x' V = x V$ **by** *simp*
qed
ultimately **show** *?thesis* **using** *Call* **by** *simp*
qed
next
case (*Block V ty voo exp E' x x'*)
note $IH = \langle \bigwedge x E' x'. \llbracket call\ exp = [aMvs]; P, t \vdash \langle inline\ call\ ee\ exp, (h, x) \rangle -ta \rightarrow \langle E', (h', x') \rangle \rrbracket$
 $\implies ?concl\ exp\ E' x x'$
from $\langle call\ \{V:ty=voo; exp\} = [aMvs] \rangle$ **have** $ic: call\ exp = [aMvs]$ **by** *simp*
note $red = \langle P, t \vdash \langle inline\ call\ ee\ \{V:ty=voo; exp\}, (h, x) \rangle -ta \rightarrow \langle E', (h', x') \rangle \rangle$
hence $P, t \vdash \langle \{V:ty=voo; inline\ call\ ee\ exp\}, (h, x) \rangle -ta \rightarrow \langle E', (h', x') \rangle$ **by** *simp*

```

with ic eefin obtain exp' x'' where E' = {V:ty=x'' V; exp'}
and red': P,t ⊢ ⟨inline-call ee exp,(h, fun-upd x V voo)⟩ -ta→ ⟨exp',(h', x')⟩
and x' = fun-upd x'' V (x V)
by -(erule red.cases,auto dest: inline-call-eq-Val)
from IH[OF ic red'] obtain ee' vo'
where icl: inline-call ee' exp = exp' x'' = fun-upd x V voo
and red'': P,t ⊢ ⟨ee,(h, Map.empty)⟩ -ta→ ⟨ee',(h', Map.empty)⟩ by blast
from ⟨x'' = fun-upd x V voo⟩ have x'' V = voo by (simp add: fun-eq-iff)
with icl red'' ⟨E' = {V:ty=x'' V; exp'}⟩ ⟨x' = fun-upd x'' V (x V)⟩ red'
show ?case by (auto simp del: fun-upd-apply)
next
case Synchronized thus ?case using eefin by (auto elim!: red-cases)
next
case InSynchronized thus ?case using eefin by (auto elim!: red-cases)
next
case Seq
thus ?case using eefin by (auto elim!: red-cases)
next
case Cond thus ?case using eefin by (auto elim!: red-cases)
next
case While thus ?case by simp
next
case throw
thus ?case using eefin by (auto elim!: red-cases)
next
case TryCatch
thus ?case using eefin by (auto elim!: red-cases)
next
case Nil-exp thus ?case by simp
next
case Cons-exp
thus ?case using eefin by (auto elim!: reds-cases split: if-split-asm)
qed

lemma assumes sees: P ⊢ class-type-of T sees M:Us→U = [(pns, body)] in D
shows is-call-red-inline-callD:
[[ P,t ⊢ ⟨e, s⟩ -ta→ ⟨e', s^⟩; call e = [(a, M, vs)]; typeof-addr (hp s) a = [T] ]]
⇒ e' = inline-call (blocks (this # pns) (Class D # Us) (Addr a # vs) body) e
and is-calls-reds-inline-callsD:
[[ P,t ⊢ ⟨es, s⟩ [-ta→] ⟨es', s^⟩; calls es = [(a, M, vs)]; typeof-addr (hp s) a = [T] ]]
⇒ es' = inline-calls (blocks (this # pns) (Class D # Us) (Addr a # vs) body) es
proof (induct rule: red-reds.inducts)
case RedCall with sees show ?case by (auto dest: sees-method-fun)
next
case RedCallExternal
with sees show ?case by (auto dest: sees-method-fun)
next
case (BlockRed e h x V vo ta e' h' x' T')
from ⟨call {V:T'=vo; e} = [(a, M, vs)]⟩ ⟨typeof-addr (hp (h, x)) a = [T]⟩ sees
have call e = [(a, M, vs)] and ¬ synthesized-call P h (a, M, vs)
by (auto simp add: synthesized-call-conv dest: sees-method-fun)
with ⟨P,t ⊢ ⟨e,(h, x(V := vo))⟩ -ta→ ⟨e',(h', x')⟩⟩
have x(V := vo) = x' by (auto dest: is-call-red-state-unchanged)
hence x' V = vo by auto

```

with *BlockRed* **show** *?case* **by** (*simp*)
qed(*fastforce split: if-split-asm*)+

lemma (**in** $-$) *wf-state-ConsD*: *wf-state* ($e, e' \# es$) \implies *wf-state* (e', es)
by (*simp*)

lemma *red-fold-exs*:

$\llbracket P, t \vdash \langle e, (h, \text{Map.empty}) \rangle -ta \rightarrow \langle e', (h', \text{Map.empty}) \rangle; \text{wf-state } (e, es) \rrbracket$
 $\implies P, t \vdash \langle \text{collapse } (e, es), (h, \text{Map.empty}) \rangle -ta \rightarrow \langle \text{collapse } (e', es), (h', \text{Map.empty}) \rangle$
(**is** $\llbracket -; - \rrbracket \implies ?\text{concl } e \ e'$)

proof(*induction es arbitrary: e e'*)

case *Nil* **thus** *?case* **by** *simp*

next

case (*Cons E es*)

note $wf = \langle \text{wf-state } (e, E \# es) \rangle$

note $red = \langle P, t \vdash \langle e, (h, \text{Map.empty}) \rangle -ta \rightarrow \langle e', (h', \text{Map.empty}) \rangle \rangle$

from wf **obtain** $a \ M$ **vs** *arrobj* **where** *call*: $\text{call } E = \llbracket (a, M, vs) \rrbracket$

by(*auto simp add: is-call-def*)

from red *call* **have** $P, t \vdash \langle \text{inline-call } e \ E, (h, \text{Map.empty}) \rangle -ta \rightarrow \langle \text{inline-call } e' \ E, (h', \text{Map.empty}) \rangle$

by(*rule red-inline-call-red*)

hence $P, t \vdash \langle \text{collapse } (\text{inline-call } e \ E, es), (h, \text{Map.empty}) \rangle -ta \rightarrow \langle \text{collapse } (\text{inline-call } e' \ E, es), (h', \text{Map.empty}) \rangle$

proof(*rule Cons.IH*)

from wf **have** $\text{fv } E = \{\}$ $\text{fv } e = \{\}$ **by** *auto*

with $\text{fv-inline-call}[\text{of } e \ E]$ **have** $\text{fv } (\text{inline-call } e \ E) = \{\}$ **by** *auto*

thus *wf-state* ($\text{inline-call } e \ E, es$) **using** wf **by** *auto*

qed

thus *?case* **by** *simp*

qed

lemma *red-fold-exs'*:

$\llbracket P, t \vdash \langle \text{collapse } (e, es), (h, \text{Map.empty}) \rangle -ta \rightarrow \langle e', (h', x') \rangle; \text{wf-state } (e, es); \neg \text{final } e \rrbracket$
 $\implies \exists E'. e' = \text{collapse } (E', es) \wedge P, t \vdash \langle e, (h, \text{Map.empty}) \rangle -ta \rightarrow \langle E', (h', \text{Map.empty}) \rangle$

(**is** $\llbracket -; -; - \rrbracket \implies ?\text{concl } e \ es$)

proof(*induction es arbitrary: e*)

case *Nil*

hence red' : $P, t \vdash \langle e, (h, \text{Map.empty}) \rangle -ta \rightarrow \langle e', (h', x') \rangle$ **by** *simp*

with $\text{red-dom-lcl}[\text{OF this}] \langle \text{wf-state } (e, []) \rangle$ **show** *?case* **by** *auto*

next

case (*Cons E es*)

note $wf = \langle \text{wf-state } (e, E \# es) \rangle$

note $nfin = \langle \neg \text{final } e \rangle$

from wf **have** $\text{fv } e = \{\}$ **by** *simp*

from wf **obtain** $a \ M$ **vs** **where** *call*: $\text{call } E = \llbracket (a, M, vs) \rrbracket$ **by**(*auto simp add: is-call-def*)

from $\langle P, t \vdash \langle \text{collapse } (e, E \# es), (h, \text{Map.empty}) \rangle -ta \rightarrow \langle e', (h', x') \rangle \rangle$

have $P, t \vdash \langle \text{collapse } (\text{inline-call } e \ E, es), (h, \text{Map.empty}) \rangle -ta \rightarrow \langle e', (h', x') \rangle$ **by** *simp*

moreover **from** $wf \text{fv-inline-call}[\text{of } e \ E]$ **have** *wf-state* ($\text{inline-call } e \ E, es$) **by** *auto*

moreover **from** $nfin$ *call* **have** $\neg \text{final } (\text{inline-call } e \ E)$ **by**(*auto elim!: final.cases*)

ultimately **have** *?concl* ($\text{inline-call } e \ E$) es **by**(*rule Cons.IH*)

then **obtain** E' **where** $e' = \text{collapse } (E', es)$

and red' : $P, t \vdash \langle \text{inline-call } e \ E, (h, \text{Map.empty}) \rangle -ta \rightarrow \langle E', (h', \text{Map.empty}) \rangle$ **by** *blast*

from $\text{red-inline-call-red}'(1)[\text{OF } \langle \text{fv } e = \{\} \rangle \ nfin \ \langle \text{call } E = \llbracket (a, M, vs) \rrbracket \rangle]$ red'

obtain e' **where** $E' = \text{inline-call } e' \ E$ $P, t \vdash \langle e, (h, \text{Map.empty}) \rangle -ta \rightarrow \langle e', (h', \text{Map.empty}) \rangle$ **by** *auto*

thus *?case* **using** e' **by** *auto*

qed

lemma $\tau\text{Red0r-inline-call-not-final}$:

$\exists e' es'. \tau\text{Red0r } P t h (e, es) (e', es') \wedge (\text{final } e' \longrightarrow es' = []) \wedge \text{collapse } (e, es) = \text{collapse } (e', es')$

proof(*induct es arbitrary: e*)

case Nil thus ?case by blast

next

case (*Cons e es E*) **show** ?case

proof(*cases final E*)

case True

hence $\tau\text{Red0 } P t h (E, e \# es) (\text{inline-call } E e, es) \text{ by } (\text{auto intro: red0Return})$

moreover from *Cons*[of *inline-call E e*] **obtain** $e' es'$

where $\tau\text{Red0r } P t h (\text{inline-call } E e, es) (e', es') \text{ final } e' \longrightarrow es' = []$

$\text{collapse } (\text{inline-call } E e, es) = \text{collapse } (e', es') \text{ by blast}$

ultimately show ?thesis **unfolding** *collapse.simps* **by**(*blast intro: converse-rtranclp-into-rtranclp*)

qed *blast*

qed

lemma $\tau\text{Red0-preserves-wf-state}$:

$\llbracket \text{wf-J-prog } P; \tau\text{Red0 } P t h (e, es) (e', es'); \text{wf-state } (e, es) \rrbracket \Longrightarrow \text{wf-state } (e', es')$

by(*auto simp del: wf-state-iff intro: red0-preserves-wf-state*)

lemma $\tau\text{Red0r-preserves-wf-state}$:

assumes *wf: wf-J-prog P*

shows $\llbracket \tau\text{Red0r } P t h (e, es) (e', es'); \text{wf-state } (e, es) \rrbracket \Longrightarrow \text{wf-state } (e', es')$

by(*induct rule: rtranclp-induct2*)(*blast intro: $\tau\text{Red0-preserves-wf-state}[OF wf]$*)⁺

lemma $\tau\text{Red0t-preserves-wf-state}$:

assumes *wf: wf-J-prog P*

shows $\llbracket \tau\text{Red0t } P t h (e, es) (e', es'); \text{wf-state } (e, es) \rrbracket \Longrightarrow \text{wf-state } (e', es')$

by(*induct rule: tranclp-induct2*)(*blast intro: $\tau\text{Red0-preserves-wf-state}[OF wf]$*)⁺

lemma *collapse- $\tau\text{move0-inv}$* :

$\llbracket \forall e \in \text{set } es. \text{is-call } e; \neg \text{final } e \rrbracket \Longrightarrow \tau\text{move0 } P h (\text{collapse } (e, es)) = \tau\text{move0 } P h e$

proof(*induction es arbitrary: e*)

case Nil thus ?case by clarsimp

next

case (*Cons e es e''*)

from $\langle \forall a \in \text{set } (e \# es). \text{is-call } a \rangle$ **obtain** *aMvs* **where** *calls: $\forall e \in \text{set } es. \text{is-call } e$*

and *call: call e = [aMvs]* **by**(*auto simp add: is-call-def*)

note *calls moreover*

from $\langle \neg \text{final } e'' \rangle$ *call* **have** $\neg \text{final } (\text{inline-call } e'' e) \text{ by } (\text{auto simp add: final-iff})$

ultimately have $\tau\text{move0 } P h (\text{collapse } (\text{inline-call } e'' e, es)) = \tau\text{move0 } P h (\text{inline-call } e'' e)$

by(*rule Cons.IH*)

also from *call* $\langle \neg \text{final } e'' \rangle$ **have** $\dots = \tau\text{move0 } P h e'' \text{ by } (\text{auto simp add: inline-call- $\tau\text{move0-inv}$ })$

finally show ?case **by simp**

qed

lemma $\tau\text{Red0r-into-silent-moves}$:

$\tau\text{Red0r } P t h (e, es) (e', es') \Longrightarrow \text{red0-mthr.silent-moves } P t ((e, es), h) ((e', es'), h)$

by(*induct rule: rtranclp-induct2*)(*fastforce intro: $\tau\text{rsys.silent-move.intros elim!: rtranclp.rtrancl-into-rtrancl}$*)⁺

lemma $\tau\text{Red0t-into-silent-movet}$:

$\tau\text{Red0t } P t h (e, es) (e', es') \Longrightarrow \text{red0-mthr.silent-movet } P t ((e, es), h) ((e', es'), h)$

by(*induct rule: tranclp-induct2*)(*fastforce intro: τ trsys.silent-move.intros elim!: tranclp.trancl-into-trancl*)+

lemma *red-simulates-red0*:

assumes *wwf*: *wwf-J-prog P*

and *sim*: *bisim-red-red0 s1 s2 mred0 P t s2 ta2 s2' \neg τ MOVE0 P s2 ta2 s2'*

shows $\exists s1' ta1. mred P t s1 ta1 s1' \wedge \neg \tau MOVE P s1 ta1 s1' \wedge bisim-red-red0 s1' s2' \wedge ta-bisim0 ta1 ta2$

proof –

note *sim*

moreover obtain *e1 h1 x1* where *s1*: *s1 = ((e1, x1), h1)* by(*cases s1, auto*)

moreover obtain *e' es' h2'* where *s2'*: *s2' = ((e', es'), h2')* by(*cases s2', auto*)

moreover obtain *e es h2* where *s2*: *s2 = ((e, es), h2)* by(*cases s2, auto*)

ultimately have *bisim*: *bisim-red-red0 ((e1, x1), h1) ((e, es), h2)*

and *red*: *P, t \vdash 0 $\langle e/es, h2 \rangle -ta2 \rightarrow \langle e'/es', h2' \rangle$*

and τ : $\neg \tau move0 P h2 e \wedge \neg final e \vee ta2 \neq \varepsilon$ by *auto*

from *red τ* have τ : $\neg \tau move0 P h2 e$ and *nfin*: $\neg final e$

by(*cases, auto dest: red- τ -taD[where extTA=extTA2J0 P, OF extTA2J0- ε]*)+

from *bisim* have *heap*: *h1 = h2* and *fold*: *e1 = collapse (e, es)*

and *x1*: *x1 = Map.empty* and *wf-state*: *wf-state (e, es)*

by(*auto elim!: bisim-red-red0.cases*)

from *red wf-state* have *wf-state'*: *wf-state (e', es')* by(*rule red0-preserves-wf-state[OF wwf]*)

from *red* show *?thesis*

proof(*cases*)

case (*red0Red xs'*)

hence [*simp*]: *es' = es*

and *extTA2J0 P, P, t \vdash $\langle e, (h2, Map.empty) \rangle -ta2 \rightarrow \langle e', (h2', xs') \rangle$* by *auto*

from *red0-red-tabisim0[OF wwf this(2)]* obtain *ta1*

where *red'*: *P, t \vdash $\langle e, (h2, Map.empty) \rangle -ta1 \rightarrow \langle e', (h2', xs') \rangle$*

and *tasim*: *ta-bisim0 ta1 ta2* by *auto*

moreover from *wf-state* have *fv e = {}* by *auto*

with *red-dom-lcl[OF red'] red-fv-subset[OF wwf red']* have *xs' = Map.empty* by *auto*

ultimately have *P, t \vdash $\langle e, (h2, Map.empty) \rangle -ta1 \rightarrow \langle e', (h2', Map.empty) \rangle$* by *simp*

with *wf-state* have *P, t \vdash $\langle collapse (e, es), (h2, Map.empty) \rangle -ta1 \rightarrow \langle collapse (e', es), (h2', Map.empty) \rangle$*

by (*erule red-fold-exs, auto*)

moreover from τ *wf-state fold nfin* have $\neg \tau move0 P h2 e1$ by(*auto simp add: collapse- τ move0-inv*)

hence $\neg \tau MOVE P ((collapse (e, es), Map.empty), h2) ta1 ((collapse (e', es), Map.empty), h2')$

unfolding *fold* by *auto*

moreover from *wf-state'* have *bisim-red-red0 ((collapse (e', es), Map.empty), h2')* *s2'*

unfolding *s2'* by(*auto*)

ultimately show *?thesis unfolding heap s1 s2 s2' fold x1*

using *tasim* by(*fastforce intro!: exI rtranclp.rtrancl-refl*)

next

case *red0Call*

with τ have *False*

by(*auto simp add: synthesized-call-def τ external'-def dest!: τ move0-callD[where P=P and h=h2]*
dest: sees-method-fun)

thus *?thesis ..*

next

case *red0Return* with *nfin* show *?thesis* by *simp*

qed

qed

lemma *delay-bisimulation-measure-red-red0*:

assumes $wf: wf\text{-}J\text{-}prog\ P$
shows $delay\text{-}bisimulation\text{-}measure\ (mred\ P\ t)\ (mred0\ P\ t)\ bisim\text{-}red\text{-}red0\ ta\text{-}bisim0\ (\tau MOVE\ P)$
 $(\tau MOVE0\ P)\ (\lambda e\ e'.\ False)\ (\lambda((e, es), h)\ ((e, es'), h).\ length\ es < length\ es')$
proof
show $wfP\ (\lambda e\ e'.\ False)$ **by** $auto$
next
have $wf\ \{(x :: nat, y).\ x < y\}$ **by** $(rule\ wf\text{-}less)$
hence $wf\ (inv\text{-}image\ \{(x :: nat, y).\ x < y\}\ (length\ o\ snd\ o\ fst))$ **by** $(rule\ wf\text{-}inv\text{-}image)$
also have $inv\text{-}image\ \{(x :: nat, y).\ x < y\}\ (length\ o\ snd\ o\ fst) = \{(x, y).\ (\lambda((e, es), h)\ ((e, es'), h).\ length\ es < length\ es')\ x\ y\}$ **by** $auto$
finally show $wfP\ (\lambda((e, es), h)\ ((e, es'), h).\ length\ es < length\ es')$
unfolding $wfp\text{-}def$.
next
fix $s1\ s2\ s1'$
assume $bisim\text{-}red\text{-}red0\ s1\ s2$ **and** $red\text{-}mthr.\text{silent}\text{-}move\ P\ t\ s1\ s1'$
moreover obtain $e1\ h1\ x1$ **where** $s1: s1 = ((e1, x1), h1)$ **by** $(cases\ s1,\ auto)$
moreover obtain $e1'\ h1'\ x1'$ **where** $s1': s1' = ((e1', x1'), h1')$ **by** $(cases\ s1',\ auto)$
moreover obtain $e\ es\ h2$ **where** $s2: s2 = ((e, es), h2)$ **by** $(cases\ s2,\ auto)$
ultimately have $bisim: bisim\text{-}red\text{-}red0\ ((e1, x1), h1)\ ((e, es), h2)$
and $red: P, t \vdash \langle e1, (h1, x1) \rangle -\varepsilon \rightarrow \langle e1', (h1', x1') \rangle$
and $\tau: \tau move0\ P\ h1\ e1$ **by** $(auto\ elim: \tau trsys.\text{silent}\text{-}move.\text{cases})$
from $bisim$ **have** $heap: h1 = h2$
and $fold: e1 = collapse\ (e, es)$
and $x1: x1 = Map.empty$
and $wf\text{-}state: wf\text{-}state\ (e, es)$
by $(auto\ elim!: bisim\text{-}red\text{-}red0.\text{cases})$
from $\tau Red0r\text{-}inline\text{-}call\text{-}not\text{-}final[of\ P\ t\ h1\ e\ es]$
obtain $e'\ es'$ **where** $red1: \tau Red0r\ P\ t\ h1\ (e, es)\ (e', es')$
and $final\ e' \implies es' = []$
and $feq: collapse\ (e, es) = collapse\ (e', es')$ **by** $blast$
have $nfin: \neg final\ e'$
proof
assume $fin: final\ e'$
hence $es' = []$ **by** $(rule\ \langle final\ e' \implies es' = [] \rangle)$
with $fold\ fin\ feq$ **have** $final\ e1$ **by** $simp$
with red **show** $False$ **by** $auto$
qed
from $red1\ wf\text{-}state$ **have** $wf\text{-}state': wf\text{-}state\ (e', es')$ **by** $(rule\ \tau Red0r\text{-}preserves\text{-}wf\text{-}state[OF\ wf])$
hence $fv: fvs\ (e' \# es') = \{\}$ **and** $icl: \forall e \in set\ es'. is\text{-}call\ e$ **by** $auto$
from $red\text{-}fold\text{-}exs'[OF\ red[unfolded\ fold\ x1\ feq]\ wf\text{-}state'\ nfin]$
obtain E' **where** $e1': e1' = collapse\ (E', es')$
and $red': P, t \vdash \langle e', (h1, Map.empty) \rangle -\varepsilon \rightarrow \langle E', (h1', Map.empty) \rangle$ **by** $auto$
from $fv\ fv\text{-}collapse[of\ es\ e]\ wf\text{-}state\ fold\ feq$ **have** $fv\ e1 = \{\}$ **by** $(auto)$
with $red\text{-}dom\text{-}lcl[OF\ red]\ x1$ **have** $x1': x1' = Map.empty$ **by** $simp$
from $red\text{-}red0\text{-}tabisim0[OF\ wf\ red']$
have $red'': extTA2J0\ P, P, t \vdash \langle e', (h1, Map.empty) \rangle -\varepsilon \rightarrow \langle E', (h1', Map.empty) \rangle$ **by** $simp$
show $bisim\text{-}red\text{-}red0\ s1'\ s2 \wedge (\lambda e\ e'. False) \hat{++} s1'\ s1 \vee$
 $(\exists s2'. red0\text{-}mthr.\text{silent}\text{-}movet\ P\ t\ s2\ s2' \wedge bisim\text{-}red\text{-}red0\ s1'\ s2')$
proof $(cases\ no\text{-}call\ P\ h1\ e')$
case $True$
with red'' **have** $P, t \vdash 0\ \langle e'/es', h1 \rangle -\varepsilon \rightarrow \langle E'/es', h1' \rangle$ **unfolding** $no\text{-}call\text{-}def$ **by** $(rule\ red0Red)$
moreover from $red\ \tau$ **have** $[simp]: h1' = h1$ **by** $(auto\ dest: \tau move0\text{-}heap\text{-}unchanged)$
moreover from $\tau\ fold\ feq\ icl\ nfin$ **have** $\tau move0\ P\ h1\ e'$ **by** $(simp\ add: collapse\text{-}\tau move0\text{-}inv)$
ultimately have $\tau Red0\ P\ t\ h1\ (e', es')\ (E', es')$ **using** $\langle \tau move0\ P\ h1\ e' \rangle$ **by** $auto$

with $red1$ **have** $\tau Red0t P t h1 (e, es) (E', es')$ **by**(rule $rtranclp$ -into- $tranclp1$)
moreover hence wf -state (E', es') **using** wf -state **by**(rule $\tau Red0t$ -preserves- wf -state[$OF wf$])
hence $bisim$ -red-red0 $((e1', x1'), h1) ((E', es'), h1)$ **unfolding** $x1' e1'$ **by**($auto$)
ultimately show $?thesis$ **using** $s1 s1' s2$ $heap$ **by** $simp$ (blast intro: $\tau Red0t$ -into-silent-movet)

next
case $False$
then obtain $a M vs$ **where** $call: call e' = \llbracket (a, M, vs) \rrbracket$
and $notsynth: \neg synthesized$ -call $P h1 (a, M, vs)$ **by**($auto simp$ add: no -call-def)
from $notsynth$ called-methodD[$OF red'' call$] **obtain** $T D Us U pns body$
where $h1' = h1$
and $ha: typeof$ -addr $h1 a = \llbracket T \rrbracket$
and $sees: P \vdash class$ -type-of T $sees M: Us \rightarrow U = \llbracket (pns, body) \rrbracket$ in D
and $length: length vs = length pns$ $length Us = length pns$
by($auto$)
let $?e = blocks (this \# pns) (Class D \# Us) (Addr a \# vs) body$
from $call ha$ **have** $P, t \vdash 0 \langle e'/es', h1 \rangle -\varepsilon \rightarrow \langle ?e/e' \# es', h1 \rangle$
using $sees length$ **by**(rule $red0Call$)
moreover from $\tau fold feq icl nfin False$ **have** $\tau move0 P h1 e'$ **by**($simp$ add: $collapse$ - $\tau move0$ -inv)
ultimately have $\tau Red0 P t h1 (e', es') (?e, e' \# es')$ **by** $auto$
with $red1$ **have** $\tau Red0t P t h1 (e, es) (?e, e' \# es')$ **by**(rule $rtranclp$ -into- $tranclp1$)
moreover {
from $\langle P, t \vdash 0 \langle e'/es', h1 \rangle -\varepsilon \rightarrow \langle ?e/e' \# es', h1 \rangle \rangle$ **have** wf -state $(?e, e' \# es')$
using wf -state' **by**(rule $red0$ -preserves- wf -state[$OF wf$])
moreover from is -call-red-inline-callD[$OF sees red' call$] ha
have $E' = inline$ -call $?e e'$ **by** $auto$
ultimately have $bisim$ -red-red0 $s1' ((?e, e' \# es'), h1')$ **unfolding** $s1' e1' x1'$
by($auto del: wf$ -state.cases wf -state.intros) }
moreover from $red' call notsynth$ **have** $h1 = h1'$
by($auto dest: is$ -call-red-state-unchanged)
ultimately show $?thesis$ **unfolding** $heap x1' x1 s2 s1' \langle h1' = h1 \rangle$
by(blast intro: $\tau Red0t$ -into-silent-movet)

qed
next
fix $s1 s2 s2'$
assume $bisim$ -red-red0 $s1 s2$ **and** $red0$ -mthr.silent-move $P t s2 s2'$
moreover obtain $e1 h1 x1$ **where** $s1: s1 = ((e1, x1), h1)$ **by**(cases $s1, auto$)
moreover obtain $e' es' h2'$ **where** $s2': s2' = ((e', es'), h2')$ **by**(cases $s2', auto$)
moreover obtain $e es h2$ **where** $s2: s2 = ((e, es), h2)$ **by**(cases $s2, auto$)
ultimately have $bisim: bisim$ -red-red0 $((e1, x1), h1) ((e, es), h2)$
and $red: P, t \vdash 0 \langle e/es, h2 \rangle -\varepsilon \rightarrow \langle e'/es', h2' \rangle$
and $\tau: \tau move0 P h2 e \vee final e$ **by**($auto elim: \tau trsys$.silent-move.cases)
from $bisim$ **have** $heap: h1 = h2$
and $fold: e1 = collapse (e, es)$
and $x1: x1 = Map.empty$ **and** wf -state: wf -state (e, es)
by($auto elim!: bisim$ -red-red0.cases)
from $red wf$ -state **have** wf -state': wf -state (e', es') **by**(rule $red0$ -preserves- wf -state[$OF wf$])
from red **show** $bisim$ -red-red0 $s1 s2' \wedge (\lambda((e, es), h) ((e, es'), h). length es < length es')^{++} s2' s2$

\vee
 $(\exists s1'. red$ -mthr.silent-movet $P t s1 s1' \wedge bisim$ -red-red0 $s1' s2')$
proof cases
case ($red0Red xs'$)
hence [$simp$]: $es' = es$
and $extTA2J0 P, P, t \vdash (e, (h2, Map.empty)) -\varepsilon \rightarrow (e', (h2', xs'))$ **by** $auto$
from $red0$ -red-tabisim0[$OF wf this(2)$] **have** $red': P, t \vdash (e, (h2, Map.empty)) -\varepsilon \rightarrow (e', (h2', xs'))$

by *auto*

moreover from *wf-state* have $fv\ e = \{\}$ by *auto*
 with *red-dom-lcl*[*OF red'*] *red-fv-subset*[*OF wf red'*] have $xs' = Map.empty$ by *auto*
 ultimately have $P, t \vdash \langle e, (h2, Map.empty) \rangle -\varepsilon \rightarrow \langle e', (h2', Map.empty) \rangle$ by *simp*
 hence $P, t \vdash \langle collapse\ (e, es), (h2, Map.empty) \rangle -\varepsilon \rightarrow \langle collapse\ (e', es), (h2', Map.empty) \rangle$
 using *wf-state* by(*rule red-fold-exs*)
 moreover from *red'* have $\neg\ final\ e$ by *auto*
 with $\tau\ wf\text{-state}\ fold$ have $\tau\ move0\ P\ h2\ e1$ by(*auto simp add: collapse- $\tau\ move0\ inv$*)
 ultimately have *red-mthr.silent-movet* $P\ t\ s1\ ((collapse\ (e', es), Map.empty), h2')$
 using *s1 fold $\tau\ x1\ heap$* by(*auto intro: $\tau\ trsys.silent-move.intros$*)
 moreover from *wf-state'* have *bisim-red-red0* $((collapse\ (e', es), Map.empty), h2')\ s2'$
 unfolding $s2'$ by(*auto*)
 ultimately show *?thesis* by *blast*

next

case (*red0Call a M vs U Ts T pns body D*)
 hence [*simp*]: $es' = e \# es\ h2' = h2\ e' = blocks\ (this\ \# pns)\ (Class\ D\ \# Ts)\ (Addr\ a\ \# vs)\ body$
 and *call*: $call\ e = [(a, M, vs)]$
 and *ha*: *typeof-addr* $h2\ a = [U]$
 and *sees*: $P \vdash class\text{-type-of}\ U\ sees\ M: Ts \rightarrow T = [(pns, body)]$ in *D*
 and *len*: $length\ vs = length\ pns\ length\ Ts = length\ pns$ by *auto*
 from *is-call-red-inline-call(1)*[*OF sees len call, of (h2, Map.empty)*] *ha*
 have $P, t \vdash \langle e, (h2, Map.empty) \rangle -\varepsilon \rightarrow \langle inline\text{-call}\ e'\ e, (h2, Map.empty) \rangle$ by *simp*
 hence $P, t \vdash \langle collapse\ (e, es), (h2, Map.empty) \rangle -\varepsilon \rightarrow \langle collapse\ (inline\text{-call}\ e'\ e, es), (h2, Map.empty) \rangle$
 using *wf-state* by(*rule red-fold-exs*)
 moreover from *call ha wf-state τ* have $\tau\ move0\ P\ h2\ (collapse\ (e, es))$
 by(*subst collapse- $\tau\ move0\ inv$*) *auto*
 hence $\tau\ MOVE\ P\ ((collapse\ (e, es), Map.empty), h2)\ \varepsilon\ ((collapse\ (inline\text{-call}\ e'\ e, es), Map.empty), h2)$ by *auto*
 moreover from *wf-state'*
 have *bisim-red-red0* $((collapse\ (inline\text{-call}\ e'\ e, es), Map.empty), h2)\ ((e', es'), h2')$
 by(*auto*)
 ultimately show *?thesis* unfolding $s1\ s2\ s2'\ fold\ heap\ x1$ by(*fastforce*)

next

case (*red0Return E*)
 hence [*simp*]: $es = E \# es'\ e' = inline\text{-call}\ e\ E\ h2' = h2$ by *auto*
 from *fold wf-state'*
 have *bisim-red-red0* $((e1, Map.empty), h1)\ ((inline\text{-call}\ e\ E, es'), h2)$
 unfolding *heap* by(*auto*)
 thus *?thesis* using $s1\ s2'\ s2\ x1$ by *auto*

qed

next

fix $s1\ s2\ ta1\ s1'$

assume *bisim-red-red0* $s1\ s2$ and *mred* $P\ t\ s1\ ta1\ s1'$ and $\neg\ \tau\ MOVE\ P\ s1\ ta1\ s1'$
 moreover obtain $e1\ h1\ x1$ where $s1: s1 = ((e1, x1), h1)$ by(*cases s1, auto*)
 moreover obtain $e1'\ h1'\ x1'$ where $s1': s1' = ((e1', x1'), h1')$ by(*cases s1', auto*)
 moreover obtain $e\ es\ h2$ where $s2: s2 = ((e, es), h2)$ by(*cases s2, auto*)
 ultimately have *bisim*: *bisim-red-red0* $((e1, x1), h1)\ ((e, es), h2)$
 and *red*: $P, t \vdash \langle e1, (h1, x1) \rangle -ta1 \rightarrow \langle e1', (h1', x1') \rangle$
 and $\tau: \neg\ \tau\ move0\ P\ h1\ e1$ by(*auto dest: red- $\tau\ taD$ [where $extTA=extTA2J\ P, OF\ extTA2J\ \varepsilon$]*)
 from *bisim* have *heap*: $h1 = h2$
 and *fold*: $e1 = collapse\ (e, es)$
 and $x1: x1 = Map.empty$
 and *wf-state*: *wf-state* (e, es)

by(auto elim!: bisim-red-red0.cases)
 from $\tau\text{Red0r-inline-call-not-final}[of\ P\ t\ h1\ e\ es]$
 obtain $e'\ es'$ where $red1: \tau\text{Red0r}\ P\ t\ h1\ (e, es)\ (e', es')$
 and $final\ e' \implies es' = []$ and $feq: collapse\ (e, es) = collapse\ (e', es')$ by blast
 hence $red1': red0\text{-mthr.silent-moves}\ P\ t\ ((e, es), h2)\ ((e', es'), h2)$
 unfolding heap by $-(rule\ \tau\text{Red0r-into-silent-moves})$
 have $nfin: \neg\ final\ e'$
 proof
 assume $fin: final\ e'$
 hence $es' = []$ by(rule $\langle final\ e' \implies es' = [] \rangle$)
 with fold fin feq have final e1 by simp
 with red show False by auto
 qed
 from $red1\ wf\text{-state}$ have $wf\text{-state}': wf\text{-state}\ (e', es')$ by(rule $\tau\text{Red0r-preserves-wf-state}[OF\ wf]$)
 hence $fv: fvs\ (e' \# es') = \{\}$ and $icl: \forall e \in set\ es'. is\text{-call}\ e$ by auto
 from $red\text{-fold-exs}'[OF\ red[unfolded\ fold\ x1\ feq]\ wf\text{-state}'\ nfin]$
 obtain E' where $e1': e1' = collapse\ (E', es')$
 and $red': P, t \vdash \langle e', (h1, Map.empty) \rangle -ta1 \rightarrow \langle E', (h1', Map.empty) \rangle$ by auto
 from $fv\ fv\text{-collapse}[OF\ icl, of\ e']\ fold\ feq$ have $fv\ e1 = \{\}$ by(auto)
 with $red\text{-dom-lcl}[OF\ red]\ x1$ have $x1': x1' = Map.empty$ by simp
 from $red\text{-red0-tabisim0}[OF\ wf\ red']$ obtain $ta2$
 where $red'': extTA2J0\ P, P, t \vdash \langle e', (h1, Map.empty) \rangle -ta2 \rightarrow \langle E', (h1', Map.empty) \rangle$
 and $tasim: ta\text{-bisim0}\ ta1\ ta2$ by auto
 from $\tau\ fold\ feq\ icl\ nfin$ have $\neg\ \tau\text{move0}\ P\ h1\ e'$ by(simp add: collapse- $\tau\text{move0-inv}$)
 hence $\forall aMvs. call\ e' = [aMvs] \longrightarrow synthesized\text{-call}\ P\ h1\ aMvs$
 by(auto dest: $\tau\text{move0-callD}$)
 with red'' have $red''': P, t \vdash \langle e'/es', h1 \rangle -ta2 \rightarrow \langle E'/es', h1' \rangle$ by(rule $red0Red$)
 moreover from $\tau\ fold\ feq\ icl\ nfin$ have $\neg\ \tau\text{move0}\ P\ h1\ e'$ by(simp add: collapse- $\tau\text{move0-inv}$)
 hence $\neg\ \tau\text{MOVE0}\ P\ ((e', es'), h1)\ ta2\ ((E', es'), h1')$ using nfin by auto
 moreover from $red''' wf\text{-state}'$ have $wf\text{-state}\ (E', es')$ by(rule $red0\text{-preserves-wf-state}[OF\ wf]$)
 hence $bisim\text{-red-red0}\ s1'\ ((E', es'), h1')$ unfolding $s1'\ e1'\ x1'$ by(auto)
 ultimately show $\exists s2'\ s2''\ ta2. red0\text{-mthr.silent-moves}\ P\ t\ s2\ s2' \wedge mred0\ P\ t\ s2'\ ta2\ s2'' \wedge$
 $\neg\ \tau\text{MOVE0}\ P\ s2'\ ta2\ s2'' \wedge bisim\text{-red-red0}\ s1'\ s2'' \wedge ta\text{-bisim0}\ ta1\ ta2$
 using $tasim\ red1'\ heap\ unfolding\ s1'\ s2$ by $-(rule\ exI\ conjI|assumption|auto)+$
 next
 fix $s1\ s2\ ta2\ s2'$
 assume $bisim\text{-red-red0}\ s1\ s2$ and $mred0\ P\ t\ s2\ ta2\ s2' \neg\ \tau\text{MOVE0}\ P\ s2\ ta2\ s2'$
 from $red\text{-simulates-red0}[OF\ wf\ this]$
 show $\exists s1'\ s1''\ ta1. red\text{-mthr.silent-moves}\ P\ t\ s1\ s1' \wedge mred\ P\ t\ s1'\ ta1\ s1'' \wedge$
 $\neg\ \tau\text{MOVE}\ P\ s1'\ ta1\ s1'' \wedge bisim\text{-red-red0}\ s1''\ s2' \wedge ta\text{-bisim0}\ ta1\ ta2$
 by(blast intro: rtranclp.rtrancl-refl)
 qed
 lemma *delay-bisimulation-diverge-red-red0*:
 assumes $wf\text{-J-prog}\ P$
 shows $delay\text{-bisimulation-diverge}\ (mred\ P\ t)\ (mred0\ P\ t)\ bisim\text{-red-red0}\ ta\text{-bisim0}\ (\tau\text{MOVE}\ P)$
 $(\tau\text{MOVE0}\ P)$
 proof –
 interpret $delay\text{-bisimulation-measure}$
 $mred\ P\ t\ mred0\ P\ t\ bisim\text{-red-red0}\ ta\text{-bisim0}\ \tau\text{MOVE}\ P\ \tau\text{MOVE0}\ P$
 $\lambda e\ e'. False\ \lambda((e, es), h)\ ((e, es'), h). length\ es < length\ es'$
 using $assms$ by(rule $delay\text{-bisimulation-measure-red-red0}$)
 show ?thesis by $unfold\ locales$
 qed

lemma *bisim-red-red0-finalD*:

assumes *bisim*: *bisim-red-red0* (*x1*, *m1*) (*x2*, *m2*)
and *final-expr* *x1*
shows $\exists x2'. \text{red0-mthr.silent-moves } P \ t \ (x2, m2) \ (x2', m2) \wedge \text{bisim-red-red0} \ (x1, m1) \ (x2', m2)$
 $\wedge \text{final-expr0} \ x2'$
proof –
from *bisim*
obtain *e' e es* **where** *wf-state*: *wf-state* (*e*, *es*)
and [*simp*]: *x1* = (*e'*, *Map.empty*) *x2* = (*e*, *es*) *e'* = *collapse* (*e*, *es*) *m2* = *m1*
by *cases*(*cases* *x2*, *auto*)
from $\langle \text{final-expr } x1 \rangle$ **have** *final* (*collapse* (*e*, *es*)) **by** *simp*
moreover from *wf-state* **have** $\forall e \in \text{set } es. \text{is-call } e$ **by** *auto*
ultimately have *red0-mthr.silent-moves* *P t* ((*e*, *es*), *m1*) ((*collapse* (*e*, *es*), \square), *m1*)
proof(*induction* *es* *arbitrary*: *e*)
case *Nil* **thus** ?*case* **by** *simp*
next
case (*Cons* *e' es*)
from $\langle \text{final} \ (\text{collapse} \ (e, e' \# \text{es})) \rangle$ **have** *final* (*collapse* (*inline-call* *e e'*, *es*)) **by** *simp*
moreover from $\langle \forall e \in \text{set} \ (e' \# \text{es}). \text{is-call } e \rangle$ **have** $\forall e \in \text{set } es. \text{is-call } e$ **by** *simp*
ultimately have *red0-mthr.silent-moves* *P t* ((*inline-call* *e e'*, *es*), *m1*) ((*collapse* (*inline-call* *e e'*, *es*), \square), *m1*)
by(*rule* *Cons.IH*)
moreover from $\langle \text{final} \ (\text{collapse} \ (e, e' \# \text{es})) \rangle \langle \forall e \in \text{set} \ (e' \# \text{es}). \text{is-call } e \rangle$
have *final* *e* **by**(*rule* *collapse-finalD*)
hence *P, t* $\vdash \langle e/e' \# \text{es}, m1 \rangle -\varepsilon \rightarrow \langle \text{inline-call } e \ e'/\text{es}, m1 \rangle$ **by**(*rule* *red0Return*)
with $\langle \text{final } e \rangle$ **have** *red0-mthr.silent-move* *P t* ((*e*, *e' # es*), *m1*) ((*inline-call* *e e'*, *es*), *m1*) **by** *auto*
ultimately show ?*case* **by** –(*erule* *converse-rtranclp-into-rtranclp*, *simp*)
qed
moreover have *bisim-red-red0* ((*collapse* (*e*, *es*), *Map.empty*), *m1*) ((*collapse* (*e*, *es*), \square), *m1*)
using $\langle \text{final} \ (\text{collapse} \ (e, \text{es})) \rangle$ **by**(*auto* *intro!*: *bisim-red-red0I*)
ultimately show ?*thesis* **using** $\langle \text{final} \ (\text{collapse} \ (e, \text{es})) \rangle$ **by** *auto*
qed

lemma *red0-simulates-red-not-final*:

assumes *wf*: *wf-J-prog* *P*
assumes *bisim*: *bisim-red-red0* ((*e*, *xs*), *h*) ((*e0*, *es0*), *h0*)
and *red*: *P, t* $\vdash \langle e, (h, xs) \rangle -\text{ta} \rightarrow \langle e', (h', xs') \rangle$
and *fin*: $\neg \text{final } e0$
and *n τ* : $\neg \tau \text{move0 } P \ h \ e$
shows $\exists e0' \ \text{ta0}. \ P, t \ \vdash \ \langle e0'/\text{es0}, h \rangle -\text{ta0} \rightarrow \langle e0'/\text{es0}, h' \rangle \wedge \text{bisim-red-red0} \ ((e', xs'), h') \ ((e0', \text{es0}), h') \wedge \text{ta-bisim0} \ \text{ta} \ \text{ta0}$
proof –
from *bisim* **have** [*simp*]: *xs* = *Map.empty* *h0* = *h* **and** *e*: *e* = *collapse* (*e0*, *es0*)
and *wfs*: *wf-state* (*e0*, *es0*) **by**(*auto* *elim!*: *bisim-red-red0.cases*)
with *red* **have** *P, t* $\vdash \langle \text{collapse} \ (e0, \text{es0}), (h, \text{Map.empty}) \rangle -\text{ta} \rightarrow \langle e', (h', xs') \rangle$ **by** *simp*
from *wfs* *red-fold-exs'*[*OF* *this*] *fin* **obtain** *e0'* **where** *e'*: *e'* = *collapse* (*e0'*, *es0*)
and *red'*: *P, t* $\vdash \langle e0', (h, \text{Map.empty}) \rangle -\text{ta} \rightarrow \langle e0', (h', \text{Map.empty}) \rangle$ **by**(*auto*)
from *wfs* *fv-collapse*[*of* *es0*, *of* *e0*] *e* **have** *fv* *e* = $\{ \}$ **by**(*auto*)
with *red-dom-lcl*[*OF* *red*] **have** [*simp*]: *xs'* = *Map.empty* **by** *simp*
from *red-red0-tabisim0*[*OF* *wf* *red'*] **obtain** *ta0*
where *red''*: *extTA2J0* *P, P, t* $\vdash \langle e0', (h, \text{Map.empty}) \rangle -\text{ta0} \rightarrow \langle e0', (h', \text{Map.empty}) \rangle$
and *tasim*: *ta-bisim0* *ta* *ta0* **by** *auto*
from *n τ* *e* *wfs* *fin* **have** $\neg \tau \text{move0 } P \ h \ e0$ **by**(*auto* *simp* *add*: *collapse- τ move0-inv*)

hence $\forall aMvs. call\ e0 = \lfloor aMvs \rfloor \longrightarrow synthesized-call\ P\ h\ aMvs$
 by(*auto dest: $\tau move0-callD$*)
 with *red''* have *red'''*: $P, t \vdash 0 \langle e0/es0, h \rangle -ta0 \rightarrow \langle e0'/es0, h' \rangle$ by(*rule red0Red*)
 moreover from *red'''* wfs have *wf-state* ($e0', es0$) by(*rule red0-preserves-wf-state[OF wwf]*)
 hence *bisim-red-red0* ($(e', xs'), h'$) ($(e0', es0), h'$) unfolding *e'* by(*auto*)
 ultimately show *?thesis* using *tasim* by(*auto simp del: split-paired-Ex*)
 qed

lemma *red-red0-FWbisim*:

assumes *wf*: *wf-J-prog P*

shows *FWdelay-bisimulation-diverge final-expr* (*mred P*) *final-expr0* (*mred0 P*)

($\lambda t. bisim-red-red0$) ($\lambda xas\ (e0, es0). \neg final\ e0$) ($\tau MOVE\ P$) ($\tau MOVE0$

P)

proof –

interpret *delay-bisimulation-diverge mred P t mred0 P t bisim-red-red0 ta-bisim0 $\tau MOVE\ P\ \tau MOVE0\ P$*

for *t* by(*rule delay-bisimulation-diverge-red-red0[OF wf]*)

show *?thesis*

proof

fix *t* and *s1* :: ($(\text{'addr expr} \times \text{'addr locals}) \times \text{'heap}$) and *s2* :: ($(\text{'addr expr} \times \text{'addr expr list}) \times \text{'heap}$)

assume *bisim-red-red0 s1 s2* ($\lambda(x1, m). final-expr\ x1$) *s1*

moreover obtain *x1 m1* where [*simp*]: $s1 = (x1, m1)$ by(*cases s1*)

moreover obtain *x2 m2* where [*simp*]: $s2 = (x2, m2)$ by(*cases s2*)

ultimately have *bisim-red-red0* ($x1, m1$) ($x2, m2$) *final-expr x1* by *simp-all*

from *bisim-red-red0-finalD[OF this, of P t]*

show $\exists s2'. red0-mthr.silent-moves\ P\ t\ s2\ s2' \wedge bisim-red-red0\ s1\ s2' \wedge (\lambda(x2, m). final-expr0\ x2)$

$s2'$ by *auto*

next

fix *t* and *s1* :: ($(\text{'addr expr} \times \text{'addr locals}) \times \text{'heap}$) and *s2* :: ($(\text{'addr expr} \times \text{'addr expr list}) \times \text{'heap}$)

assume *bisim-red-red0 s1 s2* ($\lambda(x2, m). final-expr0\ x2$) *s2*

moreover obtain *x1 m1* where [*simp*]: $s1 = (x1, m1)$ by(*cases s1*)

moreover obtain *x2 m2* where [*simp*]: $s2 = (x2, m2)$ by(*cases s2*)

ultimately have *bisim-red-red0* ($x1, m1$) ($x2, m2$) *final-expr0 x2* by *simp-all*

moreover hence *final-expr x1* by(*rule bisim-red-red0-final0D*)

ultimately show $\exists s1'. red-mthr.silent-moves\ P\ t\ s1\ s1' \wedge bisim-red-red0\ s1'\ s2 \wedge (\lambda(x1, m).$

final-expr x1) s1' by *auto*

next

fix $t' x\ m1\ xx\ m2\ t\ x1\ x2\ x1'\ ta1\ x1''\ m1'\ x2'\ ta2\ x2''\ m2'$

assume *b*: *bisim-red-red0* ($x, m1$) ($xx, m2$) and *bo*: *bisim-red-red0* ($x1, m1$) ($x2, m2$)

and *red-mthr.silent-moves P t* ($x1, m1$) ($x1', m1$)

and *red1*: *mred P t* ($x1', m1$) *ta1* ($x1'', m1'$) and $\neg \tau MOVE\ P$ ($x1', m1$) *ta1* ($x1'', m1'$)

and *red0-mthr.silent-moves P t* ($x2, m2$) ($x2', m2$)

and *red2*: *mred0 P t* ($x2', m2$) *ta2* ($x2'', m2'$) and $\neg \tau MOVE0\ P$ ($x2', m2$) *ta2* ($x2'', m2'$)

and *bo'*: *bisim-red-red0* ($x1'', m1'$) ($x2'', m2'$)

and *tb*: *ta-bisim0 ta1 ta2*

from *b* have $m1 = m2$ by(*auto elim: bisim-red-red0.cases*)

moreover from *bo'* have $m1' = m2'$ by(*auto elim: bisim-red-red0.cases*)

ultimately show *bisim-red-red0* ($x, m1'$) ($xx, m2'$) using *b*

by(*auto elim: bisim-red-red0.cases*)

next

fix $t\ x1\ m1\ x2\ m2\ x1'\ ta1\ x1''\ m1'\ x2'\ ta2\ x2''\ m2'\ w$

assume *b*: *bisim-red-red0* ($x1, m1$) ($x2, m2$)

```

and red-mthr.silent-moves  $P\ t\ (x1,\ m1)\ (x1',\ m1)$ 
and  $red1: mred\ P\ t\ (x1',\ m1)\ ta1\ (x1'',\ m1')$  and  $\neg\ \tau MOVE\ P\ (x1',\ m1)\ ta1\ (x1'',\ m1')$ 
and red0-mthr.silent-moves  $P\ t\ (x2,\ m2)\ (x2',\ m2)$ 
and  $red2: mred0\ P\ t\ (x2',\ m2)\ ta2\ (x2'',\ m2')$  and  $\neg\ \tau MOVE0\ P\ (x2',\ m2)\ ta2\ (x2'',\ m2')$ 
and  $b': bisim-red-red0\ (x1'',\ m1')\ (x2'',\ m2')$  and  $ta-bisim0\ ta1\ ta2$ 
and Suspend:  $Suspend\ w\ \in\ set\ \{ta1\}_w\ Suspend\ w\ \in\ set\ \{ta2\}_w$ 
hence  $(\lambda es\ (e0,\ es0).\ is-call\ e0)\ x1''\ x2''$ 
by(cases  $x1'$ )(cases  $x2'$ , auto dest: Red-Suspend-is-call simp add: final-iff)
thus  $(\lambda es\ (e0,\ es0).\ \neg\ final\ e0)\ x1''\ x2''$  by(auto simp add: final-iff is-call-def)
next
fix  $t\ x1\ m1\ x2\ m2\ ta1\ x1'\ m1'$ 
assume  $b: bisim-red-red0\ (x1,\ m1)\ (x2,\ m2)$ 
and  $c: (\lambda(e0,\ es0).\ \neg\ final\ e0)\ x2$ 
and  $red1: mred\ P\ t\ (x1,\ m1)\ ta1\ (x1',\ m1')$ 
and wakeup:  $Notified\ \in\ set\ \{ta1\}_w\ \vee\ WokenUp\ \in\ set\ \{ta1\}_w$ 
from  $c$  have  $\neg\ final\ (fst\ x2)$  by(auto simp add: is-call-def)
moreover from  $red1$  wakeup have  $\neg\ \tau move0\ P\ m1\ (fst\ x1)$ 
by(cases  $x1$ )(auto dest: red- $\tau$ -taD[where extTA=extTA2J P, simplified] simp add: ta-upd-simps)
moreover from  $b$  have  $m2 = m1$  by(cases) auto
ultimately obtain  $e0'\ ta0$  where  $P, t \vdash 0\ \langle fst\ x2/snd\ x2, m2 \rangle - ta0 \rightarrow \langle e0'/snd\ x2, m1' \rangle$ 
 $bisim-red-red0\ ((fst\ x1',\ snd\ x1'),\ m1')\ ((e0',\ snd\ x2),\ m1')$   $ta-bisim0\ ta1\ ta0$ 
using red0-simulates-red-not-final[OF wf, of fst x1 snd x1 m1 fst x2 snd x2 m2 t ta1 fst x1' m1' snd x1']
using  $b\ red1$  by(auto simp add: split-beta)
thus  $\exists ta2\ x2'\ m2'. mred0\ P\ t\ (x2,\ m2)\ ta2\ (x2',\ m2') \wedge bisim-red-red0\ (x1',\ m1')\ (x2',\ m2') \wedge$ 
 $ta-bisim0\ ta1\ ta2$ 
by(cases  $ta0$ )(fastforce simp add: split-beta)
next
fix  $t\ x1\ m1\ x2\ m2\ ta2\ x2'\ m2'$ 
assume  $b: bisim-red-red0\ (x1,\ m1)\ (x2,\ m2)$ 
and  $c: (\lambda(e0,\ es0).\ \neg\ final\ e0)\ x2$ 
and  $red2: mred0\ P\ t\ (x2,\ m2)\ ta2\ (x2',\ m2')$ 
and wakeup:  $Notified\ \in\ set\ \{ta2\}_w\ \vee\ WokenUp\ \in\ set\ \{ta2\}_w$ 
from  $b$  have [simp]:  $m1 = m2$  by cases auto
with red-simulates-red0[OF wf b red2] wakeup obtain  $s1'\ ta1$ 
where  $mred\ P\ t\ (x1,\ m1)\ ta1\ s1'\ bisim-red-red0\ s1'\ (x2',\ m2')\ ta-bisim0\ ta1\ ta2$ 
by(fastforce simp add: split-paired-Ex)
moreover from  $\langle bisim-red-red0\ s1'\ (x2',\ m2') \rangle$  have  $m2' = snd\ s1'$  by cases auto
ultimately
show  $\exists ta1\ x1'\ m1'. mred\ P\ t\ (x1,\ m1)\ ta1\ (x1',\ m1') \wedge bisim-red-red0\ (x1',\ m1')\ (x2',\ m2') \wedge$ 
 $ta-bisim0\ ta1\ ta2$ 
by(cases  $ta1$ )(fastforce simp add: split-beta)
next
show  $(\exists x.\ final-expr\ x) \longleftrightarrow (\exists x.\ final-expr0\ x)$ 
by(auto simp add: final-iff)
qed
qed
end

sublocale J-heap-base  $<$  red-red0:
  FWdelay-bisimulation-base
  final-expr
  mred P

```

```

  final-expr0
  mred0 P
  convert-RA
  λt. bisim-red-red0
  λexs (e0, es0). ¬ final e0
  τMOVE P τMOVE0 P
for P
by(unfold-locales)

context J-heap-base begin

lemma bisim-J-J0-start:
  assumes wf: wwf-J-prog P
  and wf-start: wf-start-state P C M vs
  shows red-red0.mbisim (J-start-state P C M vs) (J0-start-state P C M vs)
proof –
  from wf-start obtain Ts T pns body D
  where sees: P ⊢ C sees M:Ts→T=[(pns,body)] in D
  and conf: P,start-heap ⊢ vs [≤] Ts
  by cases auto

  from conf have vs: length vs = length Ts by(rule list-all2-lengthD)
  from sees-wf-mdecl[OF wf sees]
  have wwfCM: wwf-J-mdecl P D (M, Ts, T, pns, body)
  and len: length pns = length Ts by(auto simp add: wf-mdecl-def)
  from wwfCM have fobody: fv body ⊆ {this} ∪ set pns
  and pns: length pns = length Ts by simp-all
  with vs len have fv: fv (blocks pns Ts vs body) ⊆ {this} by auto
  with len vs sees show ?thesis unfolding start-state-def
  by(auto intro!: red-red0.mbisimI)(auto intro!: bisim-red-red0.intros wset-thread-okI simp add:
  is-call-def split: if-split-asm)
qed

end

end

```

7.4 The intermediate language J1

```

theory J1State imports
  ../J/State
  CallExpr
begin

type-synonym
  'addr expr1 = (nat, nat, 'addr) exp

type-synonym
  'addr J1-prog = 'addr expr1 prog

type-synonym
  'addr locals1 = 'addr val list

```

translations

(*type*) 'addr *expr1* <= (*type*) (*nat*, *nat*, 'addr) *exp*
 (*type*) 'addr *J1-prog* <= (*type*) 'addr *expr1 prog*

type-synonym

'addr *J1state* = ('addr *expr1* × 'addr *locals1*) *list*

type-synonym

('addr, 'thread-id, 'heap) *J1-thread-action* =
 ('addr, 'thread-id, ('addr *expr1* × 'addr *locals1*) × ('addr *expr1* × 'addr *locals1*) *list*, 'heap) *Jinja-thread-action*

type-synonym

('addr, 'thread-id, 'heap) *J1-state* =
 ('addr, 'thread-id, ('addr *expr1* × 'addr *locals1*) × ('addr *expr1* × 'addr *locals1*) *list*, 'heap, 'addr) *state*

print-translation <

```

let
  fun tr'
    [a1, t
    , Const (@{type-syntax prod}, -) $
      (Const (@{type-syntax prod}, -) $
        (Const (@{type-syntax exp}, -) $ Const (@{type-syntax nat}, -) $ Const (@{type-syntax
nat}, -) $ a2) $
          (Const (@{type-syntax list}, -) $ (Const (@{type-syntax val}, -) $ a3))) $
        (Const (@{type-syntax list}, -) $
          (Const (@{type-syntax prod}, -) $
            (Const (@{type-syntax exp}, -) $ Const (@{type-syntax nat}, -) $ Const (@{type-syntax
nat}, -) $ a4) $
              (Const (@{type-syntax list}, -) $ (Const (@{type-syntax val}, -) $ a5))))))
    , h] =
  if a1 = a2 andalso a2 = a3 andalso a3 = a4 andalso a4 = a5
  then Syntax.const @{type-syntax J1-thread-action} $ a1 $ t $ h
  else raise Match;
  in [(@{type-syntax Jinja-thread-action}, K tr')]
end
>
typ ('addr, 'thread-id, 'heap) J1-thread-action

```

print-translation <

```

let
  fun tr'
    [a1, t
    , Const (@{type-syntax prod}, -) $
      (Const (@{type-syntax prod}, -) $
        (Const (@{type-syntax exp}, -) $ Const (@{type-syntax nat}, -) $ Const (@{type-syntax
nat}, -) $ a2) $
          (Const (@{type-syntax list}, -) $ (Const (@{type-syntax val}, -) $ a3))) $
        (Const (@{type-syntax list}, -) $
          (Const (@{type-syntax prod}, -) $
            (Const (@{type-syntax exp}, -) $ Const (@{type-syntax nat}, -) $ Const (@{type-syntax
nat}, -) $ a4) $
              (Const (@{type-syntax list}, -) $ (Const (@{type-syntax val}, -) $ a5))))))
    , h] =
  if a1 = a2 andalso a2 = a3 andalso a3 = a4 andalso a4 = a5
  then Syntax.const @{type-syntax J1-thread-action} $ a1 $ t $ h
  else raise Match;
  in [(@{type-syntax Jinja-thread-action}, K tr')]
end
>
typ ('addr, 'thread-id, 'heap) J1-thread-action

```

```

    , h, a6] =
    if a1 = a2 andalso a2 = a3 andalso a3 = a4 andalso a4 = a5 andalso a5 = a6
    then Syntax.const @{{type-syntax J1-state}} $ a1 $ t $ h
    else raise Match;
    in [(@{{type-syntax state}}, K tr')]
  end
>
typ ('addr, 'thread-id, 'heap) J1-state

```

```

fun blocks1 :: nat ⇒ ty list ⇒ (nat,'b,'addr) exp ⇒ (nat,'b,'addr) exp

```

```

where

```

```

  blocks1 n [] e = e
| blocks1 n (T#Ts) e = {n:T=None; blocks1 (Suc n) Ts e}

```

```

primrec max-vars:: ('a,'b,'addr) exp ⇒ nat

```

```

  and max-varss:: ('a,'b,'addr) exp list ⇒ nat

```

```

where

```

```

  max-vars (new C) = 0
| max-vars (newA T[e]) = max-vars e
| max-vars (Cast C e) = max-vars e
| max-vars (e instanceof T) = max-vars e
| max-vars (Val v) = 0
| max-vars (e «bop» e') = max (max-vars e) (max-vars e')
| max-vars (Var V) = 0
| max-vars (V:=e) = max-vars e
| max-vars (a[i]) = max (max-vars a) (max-vars i)
| max-vars (AAss a i e) = max (max (max-vars a) (max-vars i)) (max-vars e)
| max-vars (a.length) = max-vars a
| max-vars (e.F{D}) = max-vars e
| max-vars (FAss e1 F D e2) = max (max-vars e1) (max-vars e2)
| max-vars (e.compareAndSwap(D.F, e', e'')) = max (max (max-vars e) (max-vars e')) (max-vars e'')
| max-vars (e.M(es)) = max (max-vars e) (max-varss es)
| max-vars ({V:T=vo; e}) = max-vars e + 1

```

— sync and insync will need an extra local variable when compiling to bytecode to store the object that is being synchronized on until its release

```

| max-vars (syncV (e') e) = max (max-vars e') (max-vars e + 1)
| max-vars (insyncV (a) e) = max-vars e + 1
| max-vars (e1;;e2) = max (max-vars e1) (max-vars e2)
| max-vars (if (e) e1 else e2) =
  max (max-vars e) (max (max-vars e1) (max-vars e2))
| max-vars (while (b) e) = max (max-vars b) (max-vars e)
| max-vars (throw e) = max-vars e
| max-vars (try e1 catch(C V) e2) = max (max-vars e1) (max-vars e2 + 1)

```

```

| max-varss [] = 0
| max-varss (e#es) = max (max-vars e) (max-varss es)

```

— Indices in blocks increase by 1

```

primrec B :: 'addr expr1 ⇒ nat ⇒ bool

```

```

  and Bs :: 'addr expr1 list ⇒ nat ⇒ bool

```

```

where

```

```

  B (new C) i = True
| B (newA T[e]) i = B e i

```

$\mathcal{B} (\text{Cast } C \ e) \ i = \mathcal{B} \ e \ i$
 $\mathcal{B} (e \ \text{instanceof } T) \ i = \mathcal{B} \ e \ i$
 $\mathcal{B} (\text{Val } v) \ i = \text{True}$
 $\mathcal{B} (e1 \ \langle\langle \text{bop} \rangle\rangle \ e2) \ i = (\mathcal{B} \ e1 \ i \wedge \mathcal{B} \ e2 \ i)$
 $\mathcal{B} (\text{Var } j) \ i = \text{True}$
 $\mathcal{B} (j := e) \ i = \mathcal{B} \ e \ i$
 $\mathcal{B} (a[j]) \ i = (\mathcal{B} \ a \ i \wedge \mathcal{B} \ j \ i)$
 $\mathcal{B} (a[j] := e) \ i = (\mathcal{B} \ a \ i \wedge \mathcal{B} \ j \ i \wedge \mathcal{B} \ e \ i)$
 $\mathcal{B} (a.\text{length}) \ i = \mathcal{B} \ a \ i$
 $\mathcal{B} (e.F\{D\}) \ i = \mathcal{B} \ e \ i$
 $\mathcal{B} (e1.F\{D\} := e2) \ i = (\mathcal{B} \ e1 \ i \wedge \mathcal{B} \ e2 \ i)$
 $\mathcal{B} (e.\text{compareAndSwap}(D.F, e', e'')) \ i = (\mathcal{B} \ e \ i \wedge \mathcal{B} \ e' \ i \wedge \mathcal{B} \ e'' \ i)$
 $\mathcal{B} (e.M(es)) \ i = (\mathcal{B} \ e \ i \wedge \mathcal{B} s \ es \ i)$
 $\mathcal{B} (\{j:T=vo; e\}) \ i = (i = j \wedge \mathcal{B} \ e \ (i+1))$
 $\mathcal{B} (\text{sync}_V(o') \ e) \ i = (i = V \wedge \mathcal{B} \ o' \ i \wedge \mathcal{B} \ e \ (i+1))$
 $\mathcal{B} (\text{insync}_V(a) \ e) \ i = (i = V \wedge \mathcal{B} \ e \ (i+1))$
 $\mathcal{B} (e1;;e2) \ i = (\mathcal{B} \ e1 \ i \wedge \mathcal{B} \ e2 \ i)$
 $\mathcal{B} (\text{if } (e) \ e1 \ \text{else } e2) \ i = (\mathcal{B} \ e \ i \wedge \mathcal{B} \ e1 \ i \wedge \mathcal{B} \ e2 \ i)$
 $\mathcal{B} (\text{throw } e) \ i = \mathcal{B} \ e \ i$
 $\mathcal{B} (\text{while } (e) \ c) \ i = (\mathcal{B} \ e \ i \wedge \mathcal{B} \ c \ i)$
 $\mathcal{B} (\text{try } e1 \ \text{catch}(C \ j) \ e2) \ i = (\mathcal{B} \ e1 \ i \wedge i=j \wedge \mathcal{B} \ e2 \ (i+1))$

$\mathcal{B} s \ [] \ i = \text{True}$
 $\mathcal{B} s (e\#es) \ i = (\mathcal{B} \ e \ i \wedge \mathcal{B} s \ es \ i)$

Variables for monitor addresses do not occur freely in synchronization blocks

primrec $\text{syncvars} :: ('a, 'a, 'addr) \ \text{exp} \Rightarrow \text{bool}$
and $\text{syncvarss} :: ('a, 'a, 'addr) \ \text{exp list} \Rightarrow \text{bool}$
where

$\text{syncvars} (\text{new } C) = \text{True}$
 $\text{syncvars} (\text{newA } T[e]) = \text{syncvars } e$
 $\text{syncvars} (\text{Cast } T \ e) = \text{syncvars } e$
 $\text{syncvars} (e \ \text{instanceof } T) = \text{syncvars } e$
 $\text{syncvars} (\text{Val } v) = \text{True}$
 $\text{syncvars} (e1 \ \langle\langle \text{bop} \rangle\rangle \ e2) = (\text{syncvars } e1 \wedge \text{syncvars } e2)$
 $\text{syncvars} (\text{Var } V) = \text{True}$
 $\text{syncvars} (V := e) = \text{syncvars } e$
 $\text{syncvars} (a[i]) = (\text{syncvars } a \wedge \text{syncvars } i)$
 $\text{syncvars} (a[i] := e) = (\text{syncvars } a \wedge \text{syncvars } i \wedge \text{syncvars } e)$
 $\text{syncvars} (a.\text{length}) = \text{syncvars } a$
 $\text{syncvars} (e.F\{D\}) = \text{syncvars } e$
 $\text{syncvars} (e.F\{D\} := e2) = (\text{syncvars } e \wedge \text{syncvars } e2)$
 $\text{syncvars} (e.\text{compareAndSwap}(D.F, e', e'')) = (\text{syncvars } e \wedge \text{syncvars } e' \wedge \text{syncvars } e'')$
 $\text{syncvars} (e.M(es)) = (\text{syncvars } e \wedge \text{syncvarss } es)$
 $\text{syncvars} \{V:T=vo;e\} = \text{syncvars } e$
 $\text{syncvars} (\text{sync}_V(e1) \ e2) = (\text{syncvars } e1 \wedge \text{syncvars } e2 \wedge V \notin \text{fv } e2)$
 $\text{syncvars} (\text{insync}_V(a) \ e) = (\text{syncvars } e \wedge V \notin \text{fv } e)$
 $\text{syncvars} (e1;;e2) = (\text{syncvars } e1 \wedge \text{syncvars } e2)$
 $\text{syncvars} (\text{if } (b) \ e1 \ \text{else } e2) = (\text{syncvars } b \wedge \text{syncvars } e1 \wedge \text{syncvars } e2)$
 $\text{syncvars} (\text{while } (b) \ c) = (\text{syncvars } b \wedge \text{syncvars } c)$
 $\text{syncvars} (\text{throw } e) = \text{syncvars } e$
 $\text{syncvars} (\text{try } e1 \ \text{catch}(C \ V) \ e2) = (\text{syncvars } e1 \wedge \text{syncvars } e2)$

$\text{syncvarss} [] = \text{True}$

| $\text{syncvarss } (e\#es) = (\text{syncvars } e \wedge \text{syncvarss } es)$

definition $\text{bsok} :: 'addr \text{ expr1} \Rightarrow \text{nat} \Rightarrow \text{bool}$

where $\text{bsok } e \ n \equiv \mathcal{B} \ e \ n \wedge \text{expr-locks } e = (\lambda ad. 0)$

definition $\text{bsoks} :: 'addr \text{ expr1 list} \Rightarrow \text{nat} \Rightarrow \text{bool}$

where $\text{bsoks } es \ n \equiv \mathcal{B} s \ es \ n \wedge \text{expr-lockss } es = (\lambda ad. 0)$

primrec $\text{call1} :: ('a, 'b, 'addr) \text{ exp} \Rightarrow ('addr \times \text{mname} \times 'addr \text{ val list}) \text{ option}$

and $\text{calls1} :: ('a, 'b, 'addr) \text{ exp list} \Rightarrow ('addr \times \text{mname} \times 'addr \text{ val list}) \text{ option}$

where

$\text{call1 } (\text{new } C) = \text{None}$
| $\text{call1 } (\text{newA } T [e]) = \text{call1 } e$
| $\text{call1 } (\text{Cast } C \ e) = \text{call1 } e$
| $\text{call1 } (e \ \text{instanceof } T) = \text{call1 } e$
| $\text{call1 } (\text{Val } v) = \text{None}$
| $\text{call1 } (\text{Var } V) = \text{None}$
| $\text{call1 } (V := e) = \text{call1 } e$
| $\text{call1 } (e \ \ll\text{bop}\ e') = (\text{if is-val } e \ \text{then } \text{call1 } e' \ \text{else } \text{call1 } e)$
| $\text{call1 } (a [i]) = (\text{if is-val } a \ \text{then } \text{call1 } i \ \text{else } \text{call1 } a)$
| $\text{call1 } (\text{AAss } a \ i \ e) = (\text{if is-val } a \ \text{then } (\text{if is-val } i \ \text{then } \text{call1 } e \ \text{else } \text{call1 } i) \ \text{else } \text{call1 } a)$
| $\text{call1 } (a \cdot \text{length}) = \text{call1 } a$
| $\text{call1 } (e \cdot F \{D\}) = \text{call1 } e$
| $\text{call1 } (\text{FAss } e \ F \ D \ e') = (\text{if is-val } e \ \text{then } \text{call1 } e' \ \text{else } \text{call1 } e)$
| $\text{call1 } (\text{CompareAndSwap } e \ D \ F \ e' \ e'') = (\text{if is-val } e \ \text{then } (\text{if is-val } e' \ \text{then } \text{call1 } e'' \ \text{else } \text{call1 } e') \ \text{else } \text{call1 } e)$
| $\text{call1 } (e \cdot M(es)) = (\text{if is-val } e \ \text{then}$
 $\quad (\text{if is-val } es \wedge \text{is-addr } e \ \text{then } [(THE \ a. \ e = \text{addr } a, M, THE \ vs. \ es = \text{map } \text{Val } vs)])$
 $\text{else } \text{calls1 } es)$
 $\quad \text{else } \text{call1 } e)$
| $\text{call1 } (\{V:T=vo; e\}) = (\text{case } vo \ \text{of } \text{None} \Rightarrow \text{call1 } e \ | \ \text{Some } v \Rightarrow \text{None})$
| $\text{call1 } (\text{sync}_V (o^\wedge) \ e) = \text{call1 } o'$
| $\text{call1 } (\text{insync}_V (a) \ e) = \text{call1 } e$
| $\text{call1 } (e; e') = \text{call1 } e$
| $\text{call1 } (\text{if } (e) \ e1 \ \text{else } e2) = \text{call1 } e$
| $\text{call1 } (\text{while}(b) \ e) = \text{None}$
| $\text{call1 } (\text{throw } e) = \text{call1 } e$
| $\text{call1 } (\text{try } e1 \ \text{catch}(C \ V) \ e2) = \text{call1 } e1$

| $\text{calls1 } [] = \text{None}$
| $\text{calls1 } (e\#es) = (\text{if is-val } e \ \text{then } \text{calls1 } es \ \text{else } \text{call1 } e)$

lemma $\text{expr-locks-blocks1}$ [simp]:

$\text{expr-locks } (\text{blocks1 } n \ Ts \ e) = \text{expr-locks } e$

by(induct $n \ Ts \ e$ rule: blocks1.induct) simp-all

lemma max-varss-append [simp]:

$\text{max-varss } (es \ @ \ es') = \text{max } (\text{max-varss } es) \ (\text{max-varss } es')$

by(induct es , auto)

lemma max-varss-map-Val [simp]: $\text{max-varss } (\text{map } \text{Val } vs) = 0$

by(induct vs) auto

lemma *blocks1-max-vars*:

$max\text{-vars } (blocks1\ n\ Ts\ e) = max\text{-vars } e + length\ Ts$

by(*induct* $n\ Ts\ e$ *rule*: *blocks1.induct*)(*auto*)

lemma *blocks-max-vars*:

$\llbracket length\ vs = length\ pns; length\ Ts = length\ pns \rrbracket$

$\implies max\text{-vars } (blocks\ pns\ Ts\ vs\ e) = max\text{-vars } e + length\ pns$

by(*induct* $pns\ Ts\ vs\ e$ *rule*: *blocks.induct*)(*auto*)

lemma *Bs-append* [*simp*]: $\mathcal{B}s\ (es\ @\ es')\ n \longleftrightarrow \mathcal{B}s\ es\ n \wedge \mathcal{B}s\ es'\ n$

by(*induct* es) *auto*

lemma *Bs-map-Val* [*simp*]: $\mathcal{B}s\ (map\ Val\ vs)\ n$

by(*induct* vs) *auto*

lemma *B-blocks1* [*intro*]: $\mathcal{B}\ body\ (n + length\ Ts) \implies \mathcal{B}\ (blocks1\ n\ Ts\ body)\ n$

by(*induct* $n\ Ts\ body$ *rule*: *blocks1.induct*)(*auto*)

lemma *B-extRet2J* [*simp*]: $\mathcal{B}\ e\ n \implies \mathcal{B}\ (extRet2J\ e\ va)\ n$

by(*cases* va) *auto*

lemma *B-inline-call*: $\llbracket \mathcal{B}\ e\ n; \bigwedge n. \mathcal{B}\ e'\ n \rrbracket \implies \mathcal{B}\ (inline\text{-call}\ e'\ e)\ n$

and *Bs-inline-calls*: $\llbracket \mathcal{B}s\ es\ n; \bigwedge n. \mathcal{B}\ e'\ n \rrbracket \implies \mathcal{B}s\ (inline\text{-calls}\ e'\ es)\ n$

by(*induct* e **and** es *arbitrary*: n **and** n *rule*: *call.induct* *calls.induct*) *auto*

lemma *syncvarss-append* [*simp*]: $syncvarss\ (es\ @\ es') \longleftrightarrow syncvarss\ es \wedge syncvarss\ es'$

by(*induct* es) *auto*

lemma *syncvarss-map-Val* [*simp*]: $syncvarss\ (map\ Val\ vs)$

by(*induct* vs) *auto*

lemma *bsok-simps* [*simp*]:

$bsok\ (new\ C)\ n = True$

$bsok\ (newA\ T[e])\ n = bsok\ e\ n$

$bsok\ (Cast\ T\ e)\ n = bsok\ e\ n$

$bsok\ (e\ instanceof\ T)\ n = bsok\ e\ n$

$bsok\ (e1\ \llbracket bop \rrbracket\ e2)\ n = (bsok\ e1\ n \wedge bsok\ e2\ n)$

$bsok\ (Var\ V)\ n = True$

$bsok\ (Val\ v)\ n = True$

$bsok\ (V := e)\ n = bsok\ e\ n$

$bsok\ (a[i])\ n = (bsok\ a\ n \wedge bsok\ i\ n)$

$bsok\ (a[i] := e)\ n = (bsok\ a\ n \wedge bsok\ i\ n \wedge bsok\ e\ n)$

$bsok\ (a \cdot length)\ n = bsok\ a\ n$

$bsok\ (e \cdot F\{D\})\ n = bsok\ e\ n$

$bsok\ (e \cdot F\{D\} := e')\ n = (bsok\ e\ n \wedge bsok\ e'\ n)$

$bsok\ (e \cdot compareAndSwap(D \cdot F, e', e''))\ n = (bsok\ e\ n \wedge bsok\ e'\ n \wedge bsok\ e''\ n)$

$bsok\ (e \cdot M(ps))\ n = (bsok\ e\ n \wedge bsoks\ ps\ n)$

$bsok\ \{V:T=vo; e\}\ n = (bsok\ e\ (Suc\ n) \wedge V = n)$

$bsok\ (sync_V\ (e)\ e')\ n = (bsok\ e\ n \wedge bsok\ e'\ (Suc\ n) \wedge V = n)$

$bsok\ (insync_V\ (ad)\ e)\ n = False$

$bsok\ (e;; e')\ n = (bsok\ e\ n \wedge bsok\ e'\ n)$

$bsok\ (if\ (e)\ e1\ else\ e2)\ n = (bsok\ e\ n \wedge bsok\ e1\ n \wedge bsok\ e2\ n)$

$bsok\ (while\ (b)\ c)\ n = (bsok\ b\ n \wedge bsok\ c\ n)$

$bsok\ (throw\ e)\ n = bsok\ e\ n$

bsok (try e catch(C V) e') n = (bsok e n ∧ bsok e' (Suc n) ∧ V = n)
and *bsoks-simps [simp]:*
bsoks [] n = True
bsoks (e # es) n = (bsok e n ∧ bsoks es n)
by(*auto simp add: bsok-def bsoks-def fun-eq-iff*)

lemma *call1-callE:*

assumes *call1 (obj·M(pns)) = [(a, M', vs)]*
obtains (*CallObj*) *call1 obj = [(a, M', vs)]*
 | (*CallParams*) *v* **where** *obj = Val v calls1 pns = [(a, M', vs)]*
 | (*Call*) *obj = addr a pns = map Val vs M = M'*
using *assms* **by**(*auto split: if-split-asm simp add: is-vals-conv*)

lemma *calls1-map-Val-append [simp]:*

calls1 (map Val vs @ es) = calls1 es
by(*induct vs*) *simp-all*

lemma *calls1-map-Val [simp]:*

calls1 (map Val vs) = None
by(*induct vs*) *simp-all*

lemma *fixes e :: ('a, 'b, 'addr) exp and es :: ('a, 'b, 'addr) exp list*

shows *call1-imp-call: call1 e = [aMvs] ⇒ call e = [aMvs]*
and *calls1-imp-calls: calls1 es = [aMvs] ⇒ calls es = [aMvs]*
by(*induct e and es rule: call1.induct calls1.induct*) *auto*

lemma *max-vars-inline-call: max-vars (inline-call e' e) ≤ max-vars e + max-vars e'*
and *max-varss-inline-calls: max-varss (inline-calls e' es) ≤ max-varss es + max-vars e'*
by(*induct e and es rule: call1.induct calls1.induct*) *auto*

lemmas *inline-call-max-vars1 = max-vars-inline-call*

lemmas *inline-calls-max-varss1 = max-varss-inline-calls*

end

7.5 Abstract heap locales for J1 programs

theory *J1Heap* **imports**

J1State

../Common/Conform

begin

locale *J1-heap-base = heap-base +*

constrains *addr2thread-id :: ('addr :: addr) ⇒ 'thread-id*

and *thread-id2addr :: 'thread-id ⇒ 'addr*

and *sc-spurious-wakeups :: bool*

and *empty-heap :: 'heap*

and *allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set*

and *typeof-addr :: 'heap ⇒ 'addr → htype*

and *heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool*

and *heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool*

locale *J1-heap = heap +*

```

constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and sc-spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and P :: 'addr J1-prog

```

sublocale *J1-heap* < *J1-heap-base* .

```

locale J1-heap-conf-base = heap-conf-base +
constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and sc-spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool
and P :: 'addr J1-prog

```

sublocale *J1-heap-conf-base* < *J1-heap-base* .

```

locale J1-heap-conf =
  J1-heap-conf-base +
  heap-conf +
constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and sc-spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool
and P :: 'addr J1-prog

```

sublocale *J1-heap-conf* < *J1-heap* **by**(*unfold-locales*)

```

locale J1-conf-read =
  J1-heap-conf +
  heap-conf-read +
constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and sc-spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool

```

and $P :: 'addr\ J1\text{-prog}$

end

7.6 Semantics of the intermediate language

theory $J1$ imports

$J1State$

$J1Heap$

$../Framework/FWBisimulation$

begin

abbreviation $final\text{-}expr1 :: ('addr\ expr1 \times 'addr\ locals1) \times ('addr\ expr1 \times 'addr\ locals1)\ list \Rightarrow bool$

where

$final\text{-}expr1 \equiv \lambda(ex, eks). final\ (fst\ ex) \wedge eks = []$

definition $extNTA2J1 ::$

$'addr\ J1\text{-prog} \Rightarrow (cname \times mname \times 'addr) \Rightarrow (('addr\ expr1 \times 'addr\ locals1) \times ('addr\ expr1 \times 'addr\ locals1)\ list)$

where

$extNTA2J1\ P = (\lambda(C, M, a). let\ (D, -, -, meth) = method\ P\ C\ M; body = the\ meth$
 $in\ ((\{0:Class\ D=None; body\}, Addr\ a\ \# replicate\ (max\text{-}vars\ body)$
 $undefined\text{-}value), []))$

lemma $extNTA2J1\text{-iff}\ [simp]:$

$extNTA2J1\ P\ (C, M, a) = ((\{0:Class\ (fst\ (method\ P\ C\ M))=None; the\ (snd\ (snd\ (snd\ (method\ P\ C\ M))))\}, Addr\ a\ \# replicate\ (max\text{-}vars\ (the\ (snd\ (snd\ (snd\ (method\ P\ C\ M))))))\ undefined\text{-}value), [])$

by ($simp\ add: extNTA2J1\text{-def}\ split\text{-}beta$)

abbreviation $extTA2J1 ::$

$'addr\ J1\text{-prog} \Rightarrow ('addr, 'thread\text{-}id, 'heap)\ external\text{-}thread\text{-}action \Rightarrow ('addr, 'thread\text{-}id, 'heap)\ J1\text{-thread}\text{-}action$

where $extTA2J1\ P \equiv convert\text{-}extTA\ (extNTA2J1\ P)$

abbreviation (*input*) $extRet2J1 :: 'addr\ expr1 \Rightarrow 'addr\ extCallRet \Rightarrow 'addr\ expr1$

where $extRet2J1 \equiv extRet2J$

lemma $max\text{-}vars\text{-}extRet2J1\ [simp]:$

$max\text{-}vars\ e = 0 \Longrightarrow max\text{-}vars\ (extRet2J1\ e\ va) = 0$

by ($cases\ va$) $simp\text{-}all$

context $J1\text{-heap}\text{-}base$ **begin**

abbreviation $J1\text{-start}\text{-}state :: 'addr\ J1\text{-prog} \Rightarrow cname \Rightarrow mname \Rightarrow 'addr\ val\ list \Rightarrow ('addr, 'thread\text{-}id, 'heap)\ J1\text{-state}$

where

$J1\text{-start}\text{-}state \equiv$

$start\text{-}state\ (\lambda C\ M\ Ts\ T\ body\ vs. ((blocks1\ 0\ (Class\ C\ \# Ts)\ body, Null\ \# vs\ @ replicate\ (max\text{-}vars\ body)\ undefined\text{-}value), []))$

inductive $red1 ::$

$bool \Rightarrow 'addr\ J1\text{-prog} \Rightarrow 'thread\text{-}id \Rightarrow 'addr\ expr1 \Rightarrow 'heap \times 'addr\ locals1$

$\Rightarrow ('addr, 'thread\text{-}id, 'heap)\ external\text{-}thread\text{-}action \Rightarrow 'addr\ expr1 \Rightarrow 'heap \times 'addr\ locals1 \Rightarrow bool$

$(\langle -, - \vdash 1 ((1 \langle -, - \rangle) \dashrightarrow / (1 \langle -, - \rangle)) \rangle [51, 51, 0, 0, 0, 0, 0, 0] 81)$
and $reds1 ::$
 $bool \Rightarrow 'addr\ J1\text{-prog} \Rightarrow 'thread\text{-id} \Rightarrow 'addr\ expr1\ list \Rightarrow 'heap \times 'addr\ locals1$
 $\Rightarrow ('addr, 'thread\text{-id}, 'heap)\ external\text{-thread}\text{-action} \Rightarrow 'addr\ expr1\ list \Rightarrow 'heap \times 'addr\ locals1 \Rightarrow$
 $bool$
 $(\langle -, - \vdash 1 ((1 \langle -, - \rangle) [-\dashrightarrow] / (1 \langle -, - \rangle)) \rangle [51, 51, 0, 0, 0, 0, 0, 0] 81)$
for $uf :: bool$ **and** $P :: 'addr\ J1\text{-prog}$ **and** $t :: 'thread\text{-id}$
where
Red1New:
 $(h', a) \in allocate\ h\ (Class\text{-type}\ C)$
 $\Longrightarrow uf, P, t \vdash 1 \langle new\ C, (h, l) \rangle -\{\!\!\{NewHeapElem\ a\ (Class\text{-type}\ C)\!\!\} \rightarrow \langle addr\ a, (h', l) \rangle$
| *Red1NewFail:*
 $allocate\ h\ (Class\text{-type}\ C) = \{\}$
 $\Longrightarrow uf, P, t \vdash 1 \langle new\ C, (h, l) \rangle -\varepsilon \rightarrow \langle THROW\ OutOfMemory, (h, l) \rangle$
| *New1ArrayRed:*
 $uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s^\wedge \rangle$
 $\Longrightarrow uf, P, t \vdash 1 \langle newA\ T[e], s \rangle -ta \rightarrow \langle newA\ T[e^\wedge], s^\wedge \rangle$
| *Red1NewArray:*
 $\llbracket 0 \leq s\ i; (h', a) \in allocate\ h\ (Array\text{-type}\ T\ (nat\ (sint\ i))) \rrbracket$
 $\Longrightarrow uf, P, t \vdash 1 \langle newA\ T[Val\ (Intg\ i)], (h, l) \rangle -\{\!\!\{NewHeapElem\ a\ (Array\text{-type}\ T\ (nat\ (sint\ i)))\!\!\} \rightarrow$
 $\langle addr\ a, (h', l) \rangle$
| *Red1NewArrayNegative:*
 $i < s\ 0 \Longrightarrow uf, P, t \vdash 1 \langle newA\ T[Val\ (Intg\ i)], s \rangle -\varepsilon \rightarrow \langle THROW\ NegativeArraySize, s \rangle$
| *Red1NewArrayFail:*
 $\llbracket 0 \leq s\ i; allocate\ h\ (Array\text{-type}\ T\ (nat\ (sint\ i))) = \{\} \rrbracket$
 $\Longrightarrow uf, P, t \vdash 1 \langle newA\ T[Val\ (Intg\ i)], (h, l) \rangle -\varepsilon \rightarrow \langle THROW\ OutOfMemory, (h, l) \rangle$
| *Cast1Red:*
 $uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s^\wedge \rangle$
 $\Longrightarrow uf, P, t \vdash 1 \langle Cast\ C\ e, s \rangle -ta \rightarrow \langle Cast\ C\ e', s^\wedge \rangle$
| *Red1Cast:*
 $\llbracket typeof_{hp}\ s\ v = \lfloor U \rfloor; P \vdash U \leq T \rrbracket$
 $\Longrightarrow uf, P, t \vdash 1 \langle Cast\ T\ (Val\ v), s \rangle -\varepsilon \rightarrow \langle Val\ v, s \rangle$
| *Red1CastFail:*
 $\llbracket typeof_{hp}\ s\ v = \lfloor U \rfloor; \neg P \vdash U \leq T \rrbracket$
 $\Longrightarrow uf, P, t \vdash 1 \langle Cast\ T\ (Val\ v), s \rangle -\varepsilon \rightarrow \langle THROW\ ClassCast, s \rangle$
| *InstanceOf1Red:*
 $uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s^\wedge \rangle \Longrightarrow uf, P, t \vdash 1 \langle e\ instanceof\ T, s \rangle -ta \rightarrow \langle e'\ instanceof\ T, s^\wedge \rangle$
| *Red1InstanceOf:*
 $\llbracket typeof_{hp}\ s\ v = \lfloor U \rfloor; b \longleftrightarrow v \neq Null \wedge P \vdash U \leq T \rrbracket$
 $\Longrightarrow uf, P, t \vdash 1 \langle (Val\ v)\ instanceof\ T, s \rangle -\varepsilon \rightarrow \langle Val\ (Bool\ b), s \rangle$
| *Bin1OpRed1:*
 $uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s^\wedge \rangle \Longrightarrow uf, P, t \vdash 1 \langle e\ \langle\langle bop \rangle\rangle\ e2, s \rangle -ta \rightarrow \langle e'\ \langle\langle bop \rangle\rangle\ e2, s^\wedge \rangle$

- | *Bin1OpRed2*:
 $uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies uf, P, t \vdash 1 \langle (Val\ v) \ll bop \gg e, s \rangle -ta \rightarrow \langle (Val\ v) \ll bop \gg e', s' \rangle$
- | *Red1BinOp*:
 $binop\ bop\ v1\ v2 = Some\ (Inl\ v) \implies$
 $uf, P, t \vdash 1 \langle (Val\ v1) \ll bop \gg (Val\ v2), s \rangle -\varepsilon \rightarrow \langle Val\ v, s \rangle$
- | *Red1BinOpFail*:
 $binop\ bop\ v1\ v2 = Some\ (Inr\ a) \implies$
 $uf, P, t \vdash 1 \langle (Val\ v1) \ll bop \gg (Val\ v2), s \rangle -\varepsilon \rightarrow \langle Throw\ a, s \rangle$
- | *Red1Var*:
 $\ll (lcl\ s)!V = v; V < size\ (lcl\ s) \gg$
 $\implies uf, P, t \vdash 1 \langle Var\ V, s \rangle -\varepsilon \rightarrow \langle Val\ v, s \rangle$
- | *LAss1Red*:
 $uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle$
 $\implies uf, P, t \vdash 1 \langle V := e, s \rangle -ta \rightarrow \langle V := e', s' \rangle$
- | *Red1LAss*:
 $V < size\ l$
 $\implies uf, P, t \vdash 1 \langle V := (Val\ v), (h, l) \rangle -\varepsilon \rightarrow \langle unit, (h, l[V := v]) \rangle$
- | *AAcc1Red1*:
 $uf, P, t \vdash 1 \langle a, s \rangle -ta \rightarrow \langle a', s' \rangle \implies uf, P, t \vdash 1 \langle a[i], s \rangle -ta \rightarrow \langle a'[i], s' \rangle$
- | *AAcc1Red2*:
 $uf, P, t \vdash 1 \langle i, s \rangle -ta \rightarrow \langle i', s' \rangle \implies uf, P, t \vdash 1 \langle (Val\ a)[i], s \rangle -ta \rightarrow \langle (Val\ a)[i'], s' \rangle$
- | *Red1AAccNull*:
 $uf, P, t \vdash 1 \langle null[Val\ i], s \rangle -\varepsilon \rightarrow \langle THROW\ NullPointer, s \rangle$
- | *Red1AAccBounds*:
 $\ll typeof\ addr\ (hp\ s)\ a = [Array\ type\ T\ n]; i < s\ 0 \vee sint\ i \geq int\ n \gg$
 $\implies uf, P, t \vdash 1 \langle (addr\ a)[Val\ (Intg\ i)], s \rangle -\varepsilon \rightarrow \langle THROW\ ArrayIndexOutOfBounds, s \rangle$
- | *Red1AAcc*:
 $\ll typeof\ addr\ h\ a = [Array\ type\ T\ n]; 0 \leq s\ i; sint\ i < int\ n;$
 $heap\ read\ h\ a\ (ACell\ (nat\ (sint\ i)))\ v \gg$
 $\implies uf, P, t \vdash 1 \langle (addr\ a)[Val\ (Intg\ i)], (h, xs) \rangle -\{ReadMem\ a\ (ACell\ (nat\ (sint\ i)))\ v\} \rightarrow \langle Val\ v, (h, xs) \rangle$
- | *AAss1Red1*:
 $uf, P, t \vdash 1 \langle a, s \rangle -ta \rightarrow \langle a', s' \rangle \implies uf, P, t \vdash 1 \langle a[i] := e, s \rangle -ta \rightarrow \langle a'[i] := e, s' \rangle$
- | *AAss1Red2*:
 $uf, P, t \vdash 1 \langle i, s \rangle -ta \rightarrow \langle i', s' \rangle \implies uf, P, t \vdash 1 \langle (Val\ a)[i] := e, s \rangle -ta \rightarrow \langle (Val\ a)[i'] := e, s' \rangle$
- | *AAss1Red3*:
 $uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies uf, P, t \vdash 1 \langle AAss\ (Val\ a)\ (Val\ i)\ e, s \rangle -ta \rightarrow \langle (Val\ a)[Val\ i] := e', s' \rangle$
- | *Red1AAssNull*:
 $uf, P, t \vdash 1 \langle AAss\ null\ (Val\ i)\ (Val\ e), s \rangle -\varepsilon \rightarrow \langle THROW\ NullPointer, s \rangle$

- | *Red1AAssBounds*:
 $\llbracket \text{typeof-addr } (hp\ s)\ a = \lfloor \text{Array-type } T\ n \rfloor; i <_s 0 \vee \text{sint } i \geq \text{int } n \rrbracket$
 $\implies uf, P, t \vdash 1 \langle AAss\ (\text{addr } a)\ (\text{Val } (Intg\ i))\ (\text{Val } e),\ s \rangle -\varepsilon \rightarrow \langle \text{THROW } \text{ArrayIndexOutOfBounds},\ s \rangle$
- | *Red1AAssStore*:
 $\llbracket \text{typeof-addr } (hp\ s)\ a = \lfloor \text{Array-type } T\ n \rfloor; 0 \leq_s i; \text{sint } i < \text{int } n;$
 $\text{typeof}_{hp\ s}\ w = \lfloor U \rfloor; \neg (P \vdash U \leq T) \rrbracket$
 $\implies uf, P, t \vdash 1 \langle AAss\ (\text{addr } a)\ (\text{Val } (Intg\ i))\ (\text{Val } w),\ s \rangle -\varepsilon \rightarrow \langle \text{THROW } \text{ArrayStore},\ s \rangle$
- | *Red1AAss*:
 $\llbracket \text{typeof-addr } h\ a = \lfloor \text{Array-type } T\ n \rfloor; 0 \leq_s i; \text{sint } i < \text{int } n; \text{typeof}_h\ w = \text{Some } U; P \vdash U \leq T;$
 $\text{heap-write } h\ a\ (\text{ACell } (\text{nat } (\text{sint } i)))\ w\ h' \rrbracket$
 $\implies uf, P, t \vdash 1 \langle AAss\ (\text{addr } a)\ (\text{Val } (Intg\ i))\ (\text{Val } w),\ (h, l) \rangle -\{\!\!-\} \text{WriteMem } a\ (\text{ACell } (\text{nat } (\text{sint } i)))$
 $w \!\!\} \rightarrow \langle \text{unit},\ (h', l) \rangle$
- | *ALength1Red*:
 $uf, P, t \vdash 1 \langle a, s \rangle -ta \rightarrow \langle a', s^\wedge \rangle \implies uf, P, t \vdash 1 \langle a \cdot \text{length}, s \rangle -ta \rightarrow \langle a' \cdot \text{length}, s^\wedge \rangle$
- | *Red1ALength*:
 $\text{typeof-addr } h\ a = \lfloor \text{Array-type } T\ n \rfloor$
 $\implies uf, P, t \vdash 1 \langle \text{addr } a \cdot \text{length}, (h, xs) \rangle -\varepsilon \rightarrow \langle \text{Val } (Intg\ (\text{word-of-nat } n)), (h, xs) \rangle$
- | *Red1ALengthNull*:
 $uf, P, t \vdash 1 \langle \text{null} \cdot \text{length}, s \rangle -\varepsilon \rightarrow \langle \text{THROW } \text{NullPointer}, s \rangle$
- | *FAcc1Red*:
 $uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s^\wedge \rangle \implies uf, P, t \vdash 1 \langle e \cdot F\{D\}, s \rangle -ta \rightarrow \langle e' \cdot F\{D\}, s^\wedge \rangle$
- | *Red1FAcc*:
 $\text{heap-read } h\ a\ (\text{CField } D\ F)\ v$
 $\implies uf, P, t \vdash 1 \langle (\text{addr } a) \cdot F\{D\}, (h, xs) \rangle -\{\!\!-\} \text{ReadMem } a\ (\text{CField } D\ F)\ v \!\!\} \rightarrow \langle \text{Val } v, (h, xs) \rangle$
- | *Red1FAccNull*:
 $uf, P, t \vdash 1 \langle \text{null} \cdot F\{D\}, s \rangle -\varepsilon \rightarrow \langle \text{THROW } \text{NullPointer}, s \rangle$
- | *FAss1Red1*:
 $uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s^\wedge \rangle \implies uf, P, t \vdash 1 \langle e \cdot F\{D\} := e2, s \rangle -ta \rightarrow \langle e' \cdot F\{D\} := e2, s^\wedge \rangle$
- | *FAss1Red2*:
 $uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s^\wedge \rangle \implies uf, P, t \vdash 1 \langle FAss\ (\text{Val } v)\ F\ D\ e, s \rangle -ta \rightarrow \langle \text{Val } v \cdot F\{D\} := e', s^\wedge \rangle$
- | *Red1FAss*:
 $\text{heap-write } h\ a\ (\text{CField } D\ F)\ v\ h' \implies$
 $uf, P, t \vdash 1 \langle FAss\ (\text{addr } a)\ F\ D\ (\text{Val } v), (h, l) \rangle -\{\!\!-\} \text{WriteMem } a\ (\text{CField } D\ F)\ v \!\!\} \rightarrow \langle \text{unit}, (h', l) \rangle$
- | *Red1FAssNull*:
 $uf, P, t \vdash 1 \langle FAss\ \text{null } F\ D\ (\text{Val } v), s \rangle -\varepsilon \rightarrow \langle \text{THROW } \text{NullPointer}, s \rangle$
- | *CAS1Red1*:
 $uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s^\wedge \rangle \implies$
 $uf, P, t \vdash 1 \langle e \cdot \text{compareAndSwap}(D \cdot F, e2, e3), s \rangle -ta \rightarrow \langle e' \cdot \text{compareAndSwap}(D \cdot F, e2, e3), s^\wedge \rangle$
- | *CAS1Red2*:

$$uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies$$

$$uf, P, t \vdash 1 \langle \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, e, e\beta), s \rangle -ta \rightarrow \langle \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, e', e\beta), s' \rangle$$

| *CAS1Red3*:

$$uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies$$

$$uf, P, t \vdash 1 \langle \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', e), s \rangle -ta \rightarrow \langle \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', e'), s' \rangle$$

| *CAS1Null*:

$$uf, P, t \vdash 1 \langle \text{null} \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v, \text{Val } v'), s \rangle -\varepsilon \rightarrow \langle \text{THROW NullPointer}, s \rangle$$

| *Red1CASSucceed*:

$$\llbracket \text{heap-read } h \ a \ (CField \ D \ F) \ v; \text{heap-write } h \ a \ (CField \ D \ F) \ v' \ h' \rrbracket \implies$$

$$uf, P, t \vdash 1 \langle \text{addr } a \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v, \text{Val } v'), (h, l) \rangle$$

$$-\{\!\{ \text{ReadMem } a \ (CField \ D \ F) \ v, \text{WriteMem } a \ (CField \ D \ F) \ v' \}\!\} \rightarrow$$

$$\langle \text{true}, (h', l) \rangle$$

| *Red1CASFail*:

$$\llbracket \text{heap-read } h \ a \ (CField \ D \ F) \ v''; \ v \neq v'' \rrbracket \implies$$

$$uf, P, t \vdash 1 \langle \text{addr } a \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v, \text{Val } v'), (h, l) \rangle$$

$$-\{\!\{ \text{ReadMem } a \ (CField \ D \ F) \ v'' \}\!\} \rightarrow$$

$$\langle \text{false}, (h, l) \rangle$$

| *Call1Obj*:

$$uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies uf, P, t \vdash 1 \langle e \cdot M(es), s \rangle -ta \rightarrow \langle e' \cdot M(es), s' \rangle$$

| *Call1Params*:

$$uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies$$

$$uf, P, t \vdash 1 \langle (\text{Val } v) \cdot M(es), s \rangle -ta \rightarrow \langle (\text{Val } v) \cdot M(es'), s' \rangle$$

| *Red1CallExternal*:

$$\llbracket \text{typeof-addr } (hp \ s) \ a = \lfloor T \rfloor; \ P \vdash \text{class-type-of } T \text{ sees } M:Ts \rightarrow Tr = \text{Native in } D; \ P, t \vdash \langle a \cdot M(vs),$$

$$hp \ s \rangle -ta \rightarrow \text{ext } \langle va, h' \rangle;$$

$$e' = \text{extRet2J1 } ((\text{addr } a) \cdot M(\text{map } \text{Val } vs)) \ va; \ s' = (h', \text{lcl } s) \rrbracket$$

$$\implies uf, P, t \vdash 1 \langle (\text{addr } a) \cdot M(\text{map } \text{Val } vs), s \rangle -ta \rightarrow \langle e', s' \rangle$$

| *Red1CallNull*:

$$uf, P, t \vdash 1 \langle \text{null} \cdot M(\text{map } \text{Val } vs), s \rangle -\varepsilon \rightarrow \langle \text{THROW NullPointer}, s \rangle$$

| *Block1Some*:

$$V < \text{length } x \implies uf, P, t \vdash 1 \langle \{V:T=\lfloor v \rfloor; e\}, (h, x) \rangle -\varepsilon \rightarrow \langle \{V:T=None; e\}, (h, x[V := v]) \rangle$$

| *Block1Red*:

$$uf, P, t \vdash 1 \langle e, (h, x) \rangle -ta \rightarrow \langle e', (h', x') \rangle$$

$$\implies uf, P, t \vdash 1 \langle \{V:T=None; e\}, (h, x) \rangle -ta \rightarrow \langle \{V:T=None; e'\}, (h', x') \rangle$$

| *Red1Block*:

$$uf, P, t \vdash 1 \langle \{V:T=None; \text{Val } u\}, s \rangle -\varepsilon \rightarrow \langle \text{Val } u, s \rangle$$

| *Synchronized1Red1*:

$$uf, P, t \vdash 1 \langle o', s \rangle -ta \rightarrow \langle o'', s' \rangle \implies uf, P, t \vdash 1 \langle \text{sync}_V(o') \ e, s \rangle -ta \rightarrow \langle \text{sync}_V(o'') \ e, s' \rangle$$

| *Synchronized1Null*:

$$V < \text{length } xs \implies uf, P, t \vdash 1 \langle \text{sync}_V(\text{null}) \ e, (h, xs) \rangle -\varepsilon \rightarrow \langle \text{THROW NullPointer}, (h, xs[V :=$$

$\text{Null})\rangle\rangle$

| *Lock1Synchronized:*

$V < \text{length } xs \implies \text{uf}, P, t \vdash 1 \langle \text{sync}_V(\text{addr } a) e, (h, xs) \rangle -\{\!\{ \text{Lock} \rightarrow a, \text{SyncLock } a \}\!\} \rightarrow \langle \text{insync}_V(a) e, (h, xs[V := \text{Addr } a]) \rangle$

| *Synchronized1Red2:*

$\text{uf}, P, t \vdash 1 \langle e, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle \implies \text{uf}, P, t \vdash 1 \langle \text{insync}_V(a) e, s \rangle -\text{ta} \rightarrow \langle \text{insync}_V(a) e', s' \rangle$

| *Unlock1Synchronized:*

$\llbracket xs ! V = \text{Addr } a'; V < \text{length } xs \rrbracket \implies \text{uf}, P, t \vdash 1 \langle \text{insync}_V(a) (\text{Val } v), (h, xs) \rangle -\{\!\{ \text{Unlock} \rightarrow a', \text{SyncUnlock } a' \}\!\} \rightarrow \langle \text{Val } v, (h, xs) \rangle$

| *Unlock1SynchronizedNull:*

$\llbracket xs ! V = \text{Null}; V < \text{length } xs \rrbracket \implies \text{uf}, P, t \vdash 1 \langle \text{insync}_V(a) (\text{Val } v), (h, xs) \rangle -\varepsilon \rightarrow \langle \text{THROW } \text{NullPointer}, (h, xs) \rangle$

| *Unlock1SynchronizedFail:*

$\llbracket \text{uf}; xs ! V = \text{Addr } a'; V < \text{length } xs \rrbracket \implies \text{uf}, P, t \vdash 1 \langle \text{insync}_V(a) (\text{Val } v), (h, xs) \rangle -\{\!\{ \text{UnlockFail} \rightarrow a' \}\!\} \rightarrow \langle \text{THROW } \text{IllegalMonitorState}, (h, xs) \rangle$

| *Seq1Red:*

$\text{uf}, P, t \vdash 1 \langle e, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle \implies \text{uf}, P, t \vdash 1 \langle e;;e2, s \rangle -\text{ta} \rightarrow \langle e';e2, s' \rangle$

| *Red1Seq:*

$\text{uf}, P, t \vdash 1 \langle \text{Seq}(\text{Val } v) e, s \rangle -\varepsilon \rightarrow \langle e, s \rangle$

| *Cond1Red:*

$\text{uf}, P, t \vdash 1 \langle b, s \rangle -\text{ta} \rightarrow \langle b', s' \rangle \implies \text{uf}, P, t \vdash 1 \langle \text{if}(b) e1 \text{ else } e2, s \rangle -\text{ta} \rightarrow \langle \text{if}(b') e1 \text{ else } e2, s' \rangle$

| *Red1CondT:*

$\text{uf}, P, t \vdash 1 \langle \text{if}(\text{true}) e1 \text{ else } e2, s \rangle -\varepsilon \rightarrow \langle e1, s \rangle$

| *Red1CondF:*

$\text{uf}, P, t \vdash 1 \langle \text{if}(\text{false}) e1 \text{ else } e2, s \rangle -\varepsilon \rightarrow \langle e2, s \rangle$

| *Red1While:*

$\text{uf}, P, t \vdash 1 \langle \text{while}(b) c, s \rangle -\varepsilon \rightarrow \langle \text{if}(b) (c;;\text{while}(b) c) \text{ else } \text{unit}, s \rangle$

| *Throw1Red:*

$\text{uf}, P, t \vdash 1 \langle e, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle \implies \text{uf}, P, t \vdash 1 \langle \text{throw } e, s \rangle -\text{ta} \rightarrow \langle \text{throw } e', s' \rangle$

| *Red1ThrowNull:*

$\text{uf}, P, t \vdash 1 \langle \text{throw null}, s \rangle -\varepsilon \rightarrow \langle \text{THROW } \text{NullPointer}, s \rangle$

| *Try1Red:*

$\text{uf}, P, t \vdash 1 \langle e, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle \implies \text{uf}, P, t \vdash 1 \langle \text{try } e \text{ catch}(C V) e2, s \rangle -\text{ta} \rightarrow \langle \text{try } e' \text{ catch}(C V) e2, s' \rangle$

| *Red1Try:*

$\text{uf}, P, t \vdash 1 \langle \text{try}(\text{Val } v) \text{ catch}(C V) e2, s \rangle -\varepsilon \rightarrow \langle \text{Val } v, s \rangle$

| *Red1TryCatch:*

$\llbracket \text{typeof-addr } h \ a = \lfloor \text{Class-type } D \rfloor; P \vdash D \preceq^* C; V < \text{length } x \rrbracket$
 $\implies uf, P, t \vdash 1 \langle \text{try } (\text{Throw } a) \text{ catch}(C \ V) \ e2, (h, x) \rangle -\varepsilon \rightarrow \langle \{V:\text{Class } C=\text{None}; e2\}, (h, x[V := \text{Addr } a]) \rangle$

| *Red1TryFail*:

$\llbracket \text{typeof-addr } (hp \ s) \ a = \lfloor \text{Class-type } D \rfloor; \neg P \vdash D \preceq^* C \rrbracket$
 $\implies uf, P, t \vdash 1 \langle \text{try } (\text{Throw } a) \text{ catch}(C \ V) \ e2, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$

| *List1Red1*:

$uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies$
 $uf, P, t \vdash 1 \langle e \# es, s \rangle [-ta \rightarrow] \langle e' \# es, s' \rangle$

| *List1Red2*:

$uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies$
 $uf, P, t \vdash 1 \langle \text{Val } v \ \# \ es, s \rangle [-ta \rightarrow] \langle \text{Val } v \ \# \ es', s' \rangle$

| *New1ArrayThrow*: $uf, P, t \vdash 1 \langle \text{newA } T \lfloor \text{Throw } a \rfloor, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$

| *Cast1Throw*: $uf, P, t \vdash 1 \langle \text{Cast } C \ (\text{Throw } a), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$

| *InstanceOf1Throw*: $uf, P, t \vdash 1 \langle (\text{Throw } a) \text{ instanceof } T, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$

| *Bin1OpThrow1*: $uf, P, t \vdash 1 \langle (\text{Throw } a) \ \llbracket \text{bop} \rrbracket \ e2, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$

| *Bin1OpThrow2*: $uf, P, t \vdash 1 \langle (\text{Val } v_1) \ \llbracket \text{bop} \rrbracket \ (\text{Throw } a), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$

| *LAss1Throw*: $uf, P, t \vdash 1 \langle V := (\text{Throw } a), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$

| *AAcc1Throw1*: $uf, P, t \vdash 1 \langle (\text{Throw } a)[i], s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$

| *AAcc1Throw2*: $uf, P, t \vdash 1 \langle (\text{Val } v) \lfloor \text{Throw } a \rfloor, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$

| *AAss1Throw1*: $uf, P, t \vdash 1 \langle (\text{Throw } a)[i] := e, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$

| *AAss1Throw2*: $uf, P, t \vdash 1 \langle (\text{Val } v) \lfloor \text{Throw } a \rfloor := e, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$

| *AAss1Throw3*: $uf, P, t \vdash 1 \langle \text{AAss } (\text{Val } v) \ (\text{Val } i) \ (\text{Throw } a), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$

| *ALength1Throw*: $uf, P, t \vdash 1 \langle (\text{Throw } a) \cdot \text{length}, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$

| *FAcc1Throw*: $uf, P, t \vdash 1 \langle (\text{Throw } a) \cdot F\{D\}, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$

| *FAss1Throw1*: $uf, P, t \vdash 1 \langle (\text{Throw } a) \cdot F\{D\} := e2, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$

| *FAss1Throw2*: $uf, P, t \vdash 1 \langle \text{FAss } (\text{Val } v) \ F \ D \ (\text{Throw } a), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$

| *CAS1Throw*: $uf, P, t \vdash 1 \langle \text{Throw } a \cdot \text{compareAndSwap}(D \cdot F, e2, e3), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$

| *CAS1Throw2*: $uf, P, t \vdash 1 \langle \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Throw } a, e3), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$

| *CAS1Throw3*: $uf, P, t \vdash 1 \langle \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', \text{Throw } a), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$

| *Call1ThrowObj*: $uf, P, t \vdash 1 \langle (\text{Throw } a) \cdot M(es), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$

| *Call1ThrowParams*: $\llbracket es = \text{map } \text{Val } vs \ @ \ \text{Throw } a \ \# \ es' \rrbracket \implies uf, P, t \vdash 1 \langle (\text{Val } v) \cdot M(es), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$

| *Block1Throw*: $uf, P, t \vdash 1 \langle \{V:T=\text{None}; \text{Throw } a\}, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$

| *Synchronized1Throw1*: $uf, P, t \vdash 1 \langle \text{sync } V \ (\text{Throw } a) \ e, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$

| *Synchronized1Throw2*:

$\llbracket xs ! V = \text{Addr } a'; V < \text{length } xs \rrbracket$

$\implies uf, P, t \vdash 1 \langle \text{insync } V \ (a) \ \text{Throw } ad, (h, xs) \rangle -\llbracket \text{Unlock} \rightarrow a', \text{SyncUnlock } a' \rrbracket \rightarrow \langle \text{Throw } ad, (h, xs) \rangle$

| *Synchronized1Throw2Fail*:

$\llbracket uf; xs ! V = \text{Addr } a'; V < \text{length } xs \rrbracket$

$\implies uf, P, t \vdash 1 \langle \text{insync } V \ (a) \ \text{Throw } ad, (h, xs) \rangle -\llbracket \text{UnlockFail} \rightarrow a' \rrbracket \rightarrow \langle \text{THROW } \text{IllegalMonitorState}, (h, xs) \rangle$

| *Synchronized1Throw2Null*:

$\llbracket xs ! V = \text{Null}; V < \text{length } xs \rrbracket$

$\implies uf, P, t \vdash 1 \langle \text{insync } V \ (a) \ \text{Throw } ad, (h, xs) \rangle -\varepsilon \rightarrow \langle \text{THROW } \text{NullPointer}, (h, xs) \rangle$

| *Seq1Throw*: $uf, P, t \vdash 1 \langle (\text{Throw } a); e2, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$

| *Cond1Throw*: $uf, P, t \vdash 1 \langle \text{if } (\text{Throw } a) \ e_1 \ \text{else } e_2, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$

| *Throw1Throw*: $uf, P, t \vdash 1 \langle \text{throw}(\text{Throw } a), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$

inductive-cases red1-cases:

$uf, P, t \vdash 1 \langle \text{new } C, s \rangle -ta \rightarrow \langle e', s' \rangle$
 $uf, P, t \vdash 1 \langle \text{new } T[e], s \rangle -ta \rightarrow \langle e', s' \rangle$
 $uf, P, t \vdash 1 \langle e \text{ «bop» } e', s \rangle -ta \rightarrow \langle e'', s' \rangle$
 $uf, P, t \vdash 1 \langle \text{Var } V, s \rangle -ta \rightarrow \langle e', s' \rangle$
 $uf, P, t \vdash 1 \langle V := e, s \rangle -ta \rightarrow \langle e', s' \rangle$
 $uf, P, t \vdash 1 \langle a[i], s \rangle -ta \rightarrow \langle e', s' \rangle$
 $uf, P, t \vdash 1 \langle a[i] := e, s \rangle -ta \rightarrow \langle e', s' \rangle$
 $uf, P, t \vdash 1 \langle a \cdot \text{length}, s \rangle -ta \rightarrow \langle e', s' \rangle$
 $uf, P, t \vdash 1 \langle e \cdot F\{D\}, s \rangle -ta \rightarrow \langle e', s' \rangle$
 $uf, P, t \vdash 1 \langle e \cdot F\{D\} := e2, s \rangle -ta \rightarrow \langle e', s' \rangle$
 $uf, P, t \vdash 1 \langle e \cdot \text{compareAndSwap}(D \cdot F, e', e''), s \rangle -ta \rightarrow \langle e''', s' \rangle$
 $uf, P, t \vdash 1 \langle e \cdot M(es), s \rangle -ta \rightarrow \langle e', s' \rangle$
 $uf, P, t \vdash 1 \langle \{V:T=vo; e\}, s \rangle -ta \rightarrow \langle e', s' \rangle$
 $uf, P, t \vdash 1 \langle \text{sync}_V(o') e, s \rangle -ta \rightarrow \langle e', s' \rangle$
 $uf, P, t \vdash 1 \langle \text{insync}_V(a) e, s \rangle -ta \rightarrow \langle e', s' \rangle$
 $uf, P, t \vdash 1 \langle e;; e', s \rangle -ta \rightarrow \langle e'', s' \rangle$
 $uf, P, t \vdash 1 \langle \text{throw } e, s \rangle -ta \rightarrow \langle e', s' \rangle$
 $uf, P, t \vdash 1 \langle \text{try } e \text{ catch}(C V) e'', s \rangle -ta \rightarrow \langle e', s' \rangle$

inductive *Red1* ::

$\text{bool} \Rightarrow \text{'addr J1-prog} \Rightarrow \text{'thread-id} \Rightarrow (\text{'addr expr1} \times \text{'addr locals1}) \Rightarrow (\text{'addr expr1} \times \text{'addr locals1})$
 $\text{list} \Rightarrow \text{'heap}$
 $\Rightarrow (\text{'addr}, \text{'thread-id}, \text{'heap}) \text{ J1-thread-action}$
 $\Rightarrow (\text{'addr expr1} \times \text{'addr locals1}) \Rightarrow (\text{'addr expr1} \times \text{'addr locals1}) \text{ list} \Rightarrow \text{'heap} \Rightarrow \text{bool}$
 $(\langle -, - \rangle, \vdash 1 ((1 \langle -' / -, - \rangle) \dashrightarrow / (1 \langle -' / -, - \rangle))) \triangleright [51, 51, 0, 0, 0, 0, 0, 0, 0, 0] \ 81$

for $uf :: \text{bool}$ **and** $P :: \text{'addr J1-prog}$ **and** $t :: \text{'thread-id}$

where

red1Red:

$uf, P, t \vdash 1 \langle e, (h, x) \rangle -ta \rightarrow \langle e', (h', x') \rangle$
 $\implies uf, P, t \vdash 1 \langle (e, x) / \text{exs}, h \rangle -\text{extTA2J1 } P \text{ ta} \rightarrow \langle (e', x') / \text{exs}, h' \rangle$

| *red1Call*:

$\llbracket \text{call1 } e = \llbracket (a, M, vs) \rrbracket; \text{typeof-addr } h \ a = \llbracket U \rrbracket;$
 $P \vdash \text{class-type-of } U \text{ sees } M:Ts \rightarrow T = \llbracket \text{body} \rrbracket \text{ in } D;$
 $\text{size } vs = \text{size } Ts \rrbracket$

$\implies uf, P, t \vdash 1 \langle (e, x) / \text{exs}, h \rangle -\varepsilon \rightarrow \langle (\text{blocks1 } 0 (\text{Class } D \# Ts) \text{ body}, \text{Addr } a \# vs @ \text{replicate} (\text{max-vars body}) \text{ undefined-value}) / (e, x) \# \text{exs}, h \rangle$

| *red1Return*:

$\text{final } e' \implies uf, P, t \vdash 1 \langle (e', x') / (e, x) \# \text{exs}, h \rangle -\varepsilon \rightarrow \langle (\text{inline-call } e' \ e, x) / \text{exs}, h \rangle$

abbreviation *mred1g* :: $\text{bool} \Rightarrow \text{'addr J1-prog} \Rightarrow (\text{'addr}, \text{'thread-id}, (\text{'addr expr1} \times \text{'addr locals1}) \times (\text{'addr expr1} \times \text{'addr locals1}) \text{ list}, \text{'heap}, \text{'addr}, (\text{'addr}, \text{'thread-id}) \text{ obs-event}) \text{ semantics}$

where $mred1g \ uf \ P \equiv \lambda t \ ((ex, \text{exs}), h) \ \text{ta} \ ((e', \text{exs}'), h'). \ uf, P, t \vdash 1 \langle ex / \text{exs}, h \rangle -ta \rightarrow \langle e' / \text{exs}', h' \rangle$

abbreviation *mred1'* ::

$\text{'addr J1-prog} \Rightarrow (\text{'addr}, \text{'thread-id}, (\text{'addr expr1} \times \text{'addr locals1}) \times (\text{'addr expr1} \times \text{'addr locals1}) \text{ list}, \text{'heap}, \text{'addr}, (\text{'addr}, \text{'thread-id}) \text{ obs-event}) \text{ semantics}$

where $mred1' \equiv mred1g \ \text{False}$

abbreviation *mred1* ::

$\text{'addr J1-prog} \Rightarrow (\text{'addr}, \text{'thread-id}, (\text{'addr expr1} \times \text{'addr locals1}) \times (\text{'addr expr1} \times \text{'addr locals1}) \text{ list}, \text{'heap}, \text{'addr}, (\text{'addr}, \text{'thread-id}) \text{ obs-event}) \text{ semantics}$

where $mred1 \equiv mred1g \text{ True}$

lemma *red1-preserves-len*: $uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies \text{length } (lcl \ s') = \text{length } (lcl \ s)$
and *reds1-preserves-len*: $uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies \text{length } (lcl \ s') = \text{length } (lcl \ s)$
by(*induct rule: red1-reds1.inducts*)(*auto*)

lemma *reds1-preserves-elen*: $uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies \text{length } es' = \text{length } es$
by(*induct es arbitrary: es'*)(*auto elim: reds1.cases*)

lemma *red1-Val-iff* [*iff*]:
 $\neg uf, P, t \vdash 1 \langle \text{Val } v, s \rangle -ta \rightarrow \langle e', s' \rangle$
by(*auto elim: red1.cases*)

lemma *red1-Throw-iff* [*iff*]:
 $\neg uf, P, t \vdash 1 \langle \text{Throw } a, xs \rangle -ta \rightarrow \langle e', s' \rangle$
by(*auto elim: red1.cases*)

lemma *reds1-Nil-iff* [*iff*]:
 $\neg uf, P, t \vdash 1 \langle [], s \rangle [-ta \rightarrow] \langle es', s' \rangle$
by(*auto elim: reds1.cases*)

lemma *reds1-Val-iff* [*iff*]:
 $\neg uf, P, t \vdash 1 \langle \text{map Val } vs, s \rangle [-ta \rightarrow] \langle es', s' \rangle$
by(*induct vs arbitrary: es'*)(*auto elim: reds1.cases*)

lemma *reds1-map-Val-Throw-iff* [*iff*]:
 $\neg uf, P, t \vdash 1 \langle \text{map Val } vs \ @ \ \text{Throw } a \ \# \ es, s \rangle [-ta \rightarrow] \langle es', s' \rangle$
by(*induct vs arbitrary: es'*)(*auto elim: reds1.cases elim!: red1.cases*)

lemma *red1-max-vars-decr*: $uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies \text{max-vars } e' \leq \text{max-vars } e$
and *reds1-max-varss-decr*: $uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies \text{max-varss } es' \leq \text{max-varss } es$
by(*induct rule: red1-reds1.inducts*)(*auto*)

lemma *red1-new-thread-heap*: $\llbracket uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{NewThread } t' \text{ ex } h \in \text{set } \{\{ta\}_t\} \rrbracket \implies h = hp \ s'$
and *reds1-new-thread-heap*: $\llbracket uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{NewThread } t' \text{ ex } h \in \text{set } \{\{ta\}_t\} \rrbracket \implies h = hp \ s'$
apply(*induct rule: red1-reds1.inducts*)
apply(*fastforce dest: red-ext-new-thread-heap simp add: ta-upd-simps*)
done

lemma *red1-new-threadD*:
 $\llbracket uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{NewThread } t' \text{ x } H \in \text{set } \{\{ta\}_t\} \rrbracket$
 $\implies \exists a \ M \ vs \ va \ T \ Ts \ Tr \ D. P, t \vdash \langle a \cdot M(vs), hp \ s \rangle -ta \rightarrow \text{ext } \langle va, hp \ s' \rangle \wedge \text{typeof-addr } (hp \ s) \ a =$
 $\lfloor T \rfloor \wedge P \vdash \text{class-type-of } T \text{ sees } M:Ts \rightarrow Tr = \text{Native in } D$
and *reds1-new-threadD*:
 $\llbracket uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{NewThread } t' \text{ x } H \in \text{set } \{\{ta\}_t\} \rrbracket$
 $\implies \exists a \ M \ vs \ va \ T \ Ts \ Tr \ D. P, t \vdash \langle a \cdot M(vs), hp \ s \rangle -ta \rightarrow \text{ext } \langle va, hp \ s' \rangle \wedge \text{typeof-addr } (hp \ s) \ a =$
 $\lfloor T \rfloor \wedge P \vdash \text{class-type-of } T \text{ sees } M:Ts \rightarrow Tr = \text{Native in } D$
by(*induct rule: red1-reds1.inducts*)(*fastforce simp add: ta-upd-simps*)
done

lemma *red1-call-synthesized*: $\llbracket uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{call1 } e = \lfloor aMvs \rfloor \rrbracket \implies \text{synthesized-call } P \ (hp \ s) \ aMvs$
and *reds1-calls-synthesized*: $\llbracket uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{calls1 } es = \lfloor aMvs \rfloor \rrbracket \implies \text{synthe-}$

sized-call P (hp s) aMvs
apply(*induct rule: red1-reds1.inducts*)
apply(*auto split: if-split-asm simp add: is-vals-conv append-eq-map-conv synthesized-call-conv*)
apply blast
done

lemma *red1-preserves-B*: $\llbracket uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \mathcal{B} e n \rrbracket \Longrightarrow \mathcal{B} e' n$
and *reds1-preserves-Bs*: $\llbracket uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \mathcal{B} s es n \rrbracket \Longrightarrow \mathcal{B} s es' n$
by(*induct arbitrary: n and n rule: red1-reds1.inducts*)(*auto*)

end

context *J1-heap begin*

lemma *red1-heat-incr*: $uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \Longrightarrow heat (hp s) (hp s')$
and *reds1-heat-incr*: $uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \Longrightarrow heat (hp s) (hp s')$
by(*induct rule: red1-reds1.inducts*)(*auto intro: heat-heap-ops red-external-heat*)

lemma *Red1-heat-incr*: $uf, P, t \vdash 1 \langle ex/exs, h \rangle -ta \rightarrow \langle ex'/exs', h' \rangle \Longrightarrow h \sqsubseteq h'$
by(*auto elim!: Red1.cases dest: red1-heat-incr*)

end

7.6.1 Silent moves

context *J1-heap-base begin*

primrec $\tau move1 :: 'm prog \Rightarrow 'heap \Rightarrow ('a, 'b, 'addr) exp \Rightarrow bool$
and $\tau moves1 :: 'm prog \Rightarrow 'heap \Rightarrow ('a, 'b, 'addr) exp list \Rightarrow bool$
where

- $\tau move1 P h (new C) \longleftrightarrow False$
- $\tau move1 P h (newA T[e]) \longleftrightarrow \tau move1 P h e \vee (\exists a. e = Throw a)$
- $\tau move1 P h (Cast U e) \longleftrightarrow \tau move1 P h e \vee final e$
- $\tau move1 P h (e instanceof T) \longleftrightarrow \tau move1 P h e \vee final e$
- $\tau move1 P h (e \ll bop \gg e') \longleftrightarrow \tau move1 P h e \vee (\exists a. e = Throw a) \vee (\exists v. e = Val v \wedge (\tau move1 P h e' \vee final e'))$
- $\tau move1 P h (Val v) \longleftrightarrow False$
- $\tau move1 P h (Var V) \longleftrightarrow True$
- $\tau move1 P h (V := e) \longleftrightarrow \tau move1 P h e \vee final e$
- $\tau move1 P h (a[i]) \longleftrightarrow \tau move1 P h a \vee (\exists ad. a = Throw ad) \vee (\exists v. a = Val v \wedge (\tau move1 P h i \vee (\exists a. i = Throw a)))$
- $\tau move1 P h (AAss a i e) \longleftrightarrow \tau move1 P h a \vee (\exists ad. a = Throw ad) \vee (\exists v. a = Val v \wedge (\tau move1 P h i \vee (\exists a. i = Throw a) \vee (\exists v. i = Val v \wedge (\tau move1 P h e \vee (\exists a. e = Throw a)))))$
- $\tau move1 P h (a \cdot length) \longleftrightarrow \tau move1 P h a \vee (\exists ad. a = Throw ad)$
- $\tau move1 P h (e \cdot F\{D\}) \longleftrightarrow \tau move1 P h e \vee (\exists a. e = Throw a)$
- $\tau move1 P h (FAss e F D e') \longleftrightarrow \tau move1 P h e \vee (\exists a. e = Throw a) \vee (\exists v. e = Val v \wedge (\tau move1 P h e' \vee (\exists a. e' = Throw a)))$
- $\tau move1 P h (e \cdot compareAndSwap(D \cdot F, e', e'')) \longleftrightarrow \tau move1 P h e \vee (\exists a. e = Throw a) \vee (\exists v. e = Val v \wedge (\tau move1 P h e' \vee (\exists a. e' = Throw a) \vee (\exists v. e' = Val v \wedge (\tau move1 P h e'' \vee (\exists a. e'' = Throw a)))))$
- $\tau move1 P h (e \cdot M(es)) \longleftrightarrow \tau move1 P h e \vee (\exists a. e = Throw a) \vee (\exists v. e = Val v \wedge (\tau moves1 P h es \vee (\exists vs a es'. es = map Val vs @ Throw a \# es') \vee (\exists vs. es = map Val vs \wedge (v = Null \vee (\forall T C Ts Tr D. typeof_h v = [T] \longrightarrow class-type-of' T =$

$ex'expr', h) \wedge \tau Move1 P h exes)$

abbreviation $\tau Red1gt$::

$bool \Rightarrow 'addr J1-prog \Rightarrow 'thread-id \Rightarrow 'heap \Rightarrow ('addr expr1 \times 'addr locals1) \times (('addr expr1 \times 'addr locals1) list)$

$\Rightarrow ('addr expr1 \times 'addr locals1) \times (('addr expr1 \times 'addr locals1) list) \Rightarrow bool$

where $\tau Red1gt uf P t h \equiv (\tau Red1g uf P t h)^{++}$

abbreviation $\tau Red1gr$::

$bool \Rightarrow 'addr J1-prog \Rightarrow 'thread-id \Rightarrow 'heap \Rightarrow ('addr expr1 \times 'addr locals1) \times (('addr expr1 \times 'addr locals1) list)$

$\Rightarrow ('addr expr1 \times 'addr locals1) \times (('addr expr1 \times 'addr locals1) list) \Rightarrow bool$

where $\tau Red1gr uf P t h \equiv (\tau Red1g uf P t h)^{**}$

abbreviation $\tau red1$::

$'addr J1-prog \Rightarrow 'thread-id \Rightarrow 'heap \Rightarrow ('addr expr1 \times 'addr locals1) \Rightarrow ('addr expr1 \times 'addr locals1) \Rightarrow bool$

where $\tau red1 \equiv \tau red1g True$

abbreviation $\tau reds1$::

$'addr J1-prog \Rightarrow 'thread-id \Rightarrow 'heap \Rightarrow ('addr expr1 list \times 'addr locals1) \Rightarrow ('addr expr1 list \times 'addr locals1) \Rightarrow bool$

where $\tau reds1 \equiv \tau reds1g True$

abbreviation $\tau red1t$::

$'addr J1-prog \Rightarrow 'thread-id \Rightarrow 'heap \Rightarrow ('addr expr1 \times 'addr locals1) \Rightarrow ('addr expr1 \times 'addr locals1) \Rightarrow bool$

where $\tau red1t \equiv \tau red1gt True$

abbreviation $\tau reds1t$::

$'addr J1-prog \Rightarrow 'thread-id \Rightarrow 'heap \Rightarrow ('addr expr1 list \times 'addr locals1) \Rightarrow ('addr expr1 list \times 'addr locals1) \Rightarrow bool$

where $\tau reds1t \equiv \tau reds1gt True$

abbreviation $\tau red1r$::

$'addr J1-prog \Rightarrow 'thread-id \Rightarrow 'heap \Rightarrow ('addr expr1 \times 'addr locals1) \Rightarrow ('addr expr1 \times 'addr locals1) \Rightarrow bool$

where $\tau red1r \equiv \tau red1gr True$

abbreviation $\tau reds1r$::

$'addr J1-prog \Rightarrow 'thread-id \Rightarrow 'heap \Rightarrow ('addr expr1 list \times 'addr locals1) \Rightarrow ('addr expr1 list \times 'addr locals1) \Rightarrow bool$

where $\tau reds1r \equiv \tau reds1gr True$

abbreviation $\tau Red1$::

$'addr J1-prog \Rightarrow 'thread-id \Rightarrow 'heap \Rightarrow ('addr expr1 \times 'addr locals1) \times (('addr expr1 \times 'addr locals1) list)$

$\Rightarrow ('addr expr1 \times 'addr locals1) \times (('addr expr1 \times 'addr locals1) list) \Rightarrow bool$

where $\tau Red1 \equiv \tau Red1g True$

abbreviation $\tau Red1t$::

$'addr J1-prog \Rightarrow 'thread-id \Rightarrow 'heap \Rightarrow ('addr expr1 \times 'addr locals1) \times (('addr expr1 \times 'addr locals1) list)$

$\Rightarrow ('addr expr1 \times 'addr locals1) \times (('addr expr1 \times 'addr locals1) list) \Rightarrow bool$

where $\tau_{Red1t} \equiv \tau_{Red1gt} \text{ True}$

abbreviation $\tau_{Red1r} ::$

$'addr \ J1\text{-prog} \Rightarrow 'thread\text{-id} \Rightarrow 'heap \Rightarrow ('addr \ expr1 \times 'addr \ locals1) \times (('addr \ expr1 \times 'addr \ locals1) \ list)$

$\Rightarrow ('addr \ expr1 \times 'addr \ locals1) \times (('addr \ expr1 \times 'addr \ locals1) \ list) \Rightarrow bool$

where $\tau_{Red1r} \equiv \tau_{Red1gr} \text{ True}$

abbreviation $\tau_{red1'} ::$

$'addr \ J1\text{-prog} \Rightarrow 'thread\text{-id} \Rightarrow 'heap \Rightarrow ('addr \ expr1 \times 'addr \ locals1) \Rightarrow ('addr \ expr1 \times 'addr \ locals1) \Rightarrow bool$

where $\tau_{red1'} \equiv \tau_{red1g} \text{ False}$

abbreviation $\tau_{reds1'} ::$

$'addr \ J1\text{-prog} \Rightarrow 'thread\text{-id} \Rightarrow 'heap \Rightarrow ('addr \ expr1 \ list \times 'addr \ locals1) \Rightarrow ('addr \ expr1 \ list \times 'addr \ locals1) \Rightarrow bool$

where $\tau_{reds1'} \equiv \tau_{reds1g} \text{ False}$

abbreviation $\tau_{red1't} ::$

$'addr \ J1\text{-prog} \Rightarrow 'thread\text{-id} \Rightarrow 'heap \Rightarrow ('addr \ expr1 \times 'addr \ locals1) \Rightarrow ('addr \ expr1 \times 'addr \ locals1) \Rightarrow bool$

where $\tau_{red1't} \equiv \tau_{red1gt} \text{ False}$

abbreviation $\tau_{reds1't} ::$

$'addr \ J1\text{-prog} \Rightarrow 'thread\text{-id} \Rightarrow 'heap \Rightarrow ('addr \ expr1 \ list \times 'addr \ locals1) \Rightarrow ('addr \ expr1 \ list \times 'addr \ locals1) \Rightarrow bool$

where $\tau_{reds1't} \equiv \tau_{reds1gt} \text{ False}$

abbreviation $\tau_{red1'r} ::$

$'addr \ J1\text{-prog} \Rightarrow 'thread\text{-id} \Rightarrow 'heap \Rightarrow ('addr \ expr1 \times 'addr \ locals1) \Rightarrow ('addr \ expr1 \times 'addr \ locals1) \Rightarrow bool$

where $\tau_{red1'r} \equiv \tau_{red1gr} \text{ False}$

abbreviation $\tau_{reds1'r} ::$

$'addr \ J1\text{-prog} \Rightarrow 'thread\text{-id} \Rightarrow 'heap \Rightarrow ('addr \ expr1 \ list \times 'addr \ locals1) \Rightarrow ('addr \ expr1 \ list \times 'addr \ locals1) \Rightarrow bool$

where $\tau_{reds1'r} \equiv \tau_{reds1gr} \text{ False}$

abbreviation $\tau_{Red1'} ::$

$'addr \ J1\text{-prog} \Rightarrow 'thread\text{-id} \Rightarrow 'heap \Rightarrow ('addr \ expr1 \times 'addr \ locals1) \times (('addr \ expr1 \times 'addr \ locals1) \ list)$

$\Rightarrow ('addr \ expr1 \times 'addr \ locals1) \times (('addr \ expr1 \times 'addr \ locals1) \ list) \Rightarrow bool$

where $\tau_{Red1'} \equiv \tau_{Red1g} \text{ False}$

abbreviation $\tau_{Red1't} ::$

$'addr \ J1\text{-prog} \Rightarrow 'thread\text{-id} \Rightarrow 'heap \Rightarrow ('addr \ expr1 \times 'addr \ locals1) \times (('addr \ expr1 \times 'addr \ locals1) \ list)$

$\Rightarrow ('addr \ expr1 \times 'addr \ locals1) \times (('addr \ expr1 \times 'addr \ locals1) \ list) \Rightarrow bool$

where $\tau_{Red1't} \equiv \tau_{Red1gt} \text{ False}$

abbreviation $\tau_{Red1'r} ::$

$'addr \ J1\text{-prog} \Rightarrow 'thread\text{-id} \Rightarrow 'heap \Rightarrow ('addr \ expr1 \times 'addr \ locals1) \times (('addr \ expr1 \times 'addr \ locals1) \ list)$

$\Rightarrow ('addr \ expr1 \times 'addr \ locals1) \times (('addr \ expr1 \times 'addr \ locals1) \ list) \Rightarrow bool$

where $\tau Red1' r \equiv \tau Red1gr False$

abbreviation $\tau MOVE1 ::$

$'m prog \Rightarrow (((addr\ expr1 \times 'addr\ locals1) \times (addr\ expr1 \times 'addr\ locals1)\ list) \times 'heap, ('addr,$
 $'thread-id, 'heap)\ J1-thread-action)\ trsys$

where $\tau MOVE1 P \equiv \lambda(execs, h)\ ta\ s.\ \tau Move1 P h execs \wedge ta = \varepsilon$

lemma $\tau move1\text{-}\tau moves1\text{-}intros:$

fixes $e :: ('a, 'b, 'addr)\ exp$ **and** $es :: ('a, 'b, 'addr)\ exp\ list$
shows $\tau move1NewArray: \tau move1 P h e \Longrightarrow \tau move1 P h (newA\ T[e])$
and $\tau move1Cast: \tau move1 P h e \Longrightarrow \tau move1 P h (Cast\ U\ e)$
and $\tau move1CastRed: \tau move1 P h (Cast\ U\ (Val\ v))$
and $\tau move1InstanceOf: \tau move1 P h e \Longrightarrow \tau move1 P h (e\ instanceof\ U)$
and $\tau move1InstanceOfRed: \tau move1 P h ((Val\ v)\ instanceof\ U)$
and $\tau move1BinOp1: \tau move1 P h e \Longrightarrow \tau move1 P h (e\ \ll bop\ e')$
and $\tau move1BinOp2: \tau move1 P h e \Longrightarrow \tau move1 P h (Val\ v\ \ll bop\ e)$
and $\tau move1BinOp: \tau move1 P h (Val\ v\ \ll bop\ Val\ v')$
and $\tau move1Var: \tau move1 P h (Var\ V)$
and $\tau move1LAss: \tau move1 P h e \Longrightarrow \tau move1 P h (V := e)$
and $\tau move1LAssRed: \tau move1 P h (V := Val\ v)$
and $\tau move1AAcc1: \tau move1 P h e \Longrightarrow \tau move1 P h (e[e'])$
and $\tau move1AAcc2: \tau move1 P h e \Longrightarrow \tau move1 P h (Val\ v[e])$
and $\tau move1AAss1: \tau move1 P h e \Longrightarrow \tau move1 P h (AAss\ e\ e'\ e'')$
and $\tau move1AAss2: \tau move1 P h e \Longrightarrow \tau move1 P h (AAss\ (Val\ v)\ e\ e')$
and $\tau move1AAss3: \tau move1 P h e \Longrightarrow \tau move1 P h (AAss\ (Val\ v)\ (Val\ v')\ e)$
and $\tau move1ALength: \tau move1 P h e \Longrightarrow \tau move1 P h (e.length)$
and $\tau move1FAcc: \tau move1 P h e \Longrightarrow \tau move1 P h (e.F\{D\})$
and $\tau move1FAss1: \tau move1 P h e \Longrightarrow \tau move1 P h (FAss\ e\ F\ D\ e')$
and $\tau move1FAss2: \tau move1 P h e \Longrightarrow \tau move1 P h (FAss\ (Val\ v)\ F\ D\ e)$
and $\tau move1CAS1: \tau move1 P h e \Longrightarrow \tau move1 P h (e.compareAndSwap(D.F, e', e''))$
and $\tau move1CAS2: \tau move1 P h e \Longrightarrow \tau move1 P h (Val\ v.compareAndSwap(D.F, e, e''))$
and $\tau move1CAS3: \tau move1 P h e \Longrightarrow \tau move1 P h (Val\ v.compareAndSwap(D.F, Val\ v', e))$
and $\tau move1CallObj: \tau move1 P h\ obj \Longrightarrow \tau move1 P h (obj.M(ps))$
and $\tau move1CallParams: \tau moves1 P h\ ps \Longrightarrow \tau move1 P h (Val\ v.M(ps))$
and $\tau move1Call: (\bigwedge T\ C\ Ts\ Tr\ D.\ \llbracket\ typeof_h\ v = [T];\ class\text{-}type\text{-}of'\ T = [C];\ P \vdash C\ sees\ M:Ts \rightarrow Tr = Native\ in\ D \rrbracket \Longrightarrow \tau external\text{-}defs\ D\ M) \Longrightarrow \tau move1 P h (Val\ v.M(map\ Val\ vs))$
and $\tau move1BlockSome: \tau move1 P h \{V:T=[v];\ e\}$
and $\tau move1Block: \tau move1 P h e \Longrightarrow \tau move1 P h \{V:T=None;\ e\}$
and $\tau move1BlockRed: \tau move1 P h \{V:T=None;\ Val\ v\}$
and $\tau move1Sync: \tau move1 P h e \Longrightarrow \tau move1 P h (sync_{V'}(e)\ e')$
and $\tau move1InSync: \tau move1 P h e \Longrightarrow \tau move1 P h (insync_{V'}(a)\ e)$
and $\tau move1Seq: \tau move1 P h e \Longrightarrow \tau move1 P h (e;;e')$
and $\tau move1SeqRed: \tau move1 P h (Val\ v;;\ e)$
and $\tau move1Cond: \tau move1 P h e \Longrightarrow \tau move1 P h (if\ (e)\ e1\ else\ e2)$
and $\tau move1CondRed: \tau move1 P h (if\ (Val\ v)\ e1\ else\ e2)$
and $\tau move1WhileRed: \tau move1 P h (while\ (c)\ e)$
and $\tau move1Throw: \tau move1 P h e \Longrightarrow \tau move1 P h (throw\ e)$
and $\tau move1ThrowNull: \tau move1 P h (throw\ null)$
and $\tau move1Try: \tau move1 P h e \Longrightarrow \tau move1 P h (try\ e\ catch(C\ V)\ e')$
and $\tau move1TryRed: \tau move1 P h (try\ Val\ v\ catch(C\ V)\ e)$
and $\tau move1TryThrow: \tau move1 P h (try\ Throw\ a\ catch(C\ V)\ e)$
and $\tau move1NewArrayThrow: \tau move1 P h (newA\ T[Throw\ a])$
and $\tau move1CastThrow: \tau move1 P h (Cast\ T\ (Throw\ a))$
and $\tau move1InstanceOfThrow: \tau move1 P h ((Throw\ a)\ instanceof\ T)$

and $\tau\text{move1BinOpThrow1}$: $\tau\text{move1 } P \ h \ (Throw \ a \ \langle\langle\text{bop}\rangle\rangle \ e2)$
and $\tau\text{move1BinOpThrow2}$: $\tau\text{move1 } P \ h \ (Val \ v \ \langle\langle\text{bop}\rangle\rangle \ Throw \ a)$
and $\tau\text{move1LAssThrow}$: $\tau\text{move1 } P \ h \ (V := (Throw \ a))$
and $\tau\text{move1AAccThrow1}$: $\tau\text{move1 } P \ h \ (Throw \ a[e])$
and $\tau\text{move1AAccThrow2}$: $\tau\text{move1 } P \ h \ (Val \ v[Throw \ a])$
and $\tau\text{move1AAssThrow1}$: $\tau\text{move1 } P \ h \ (AAss \ (Throw \ a) \ e \ e')$
and $\tau\text{move1AAssThrow2}$: $\tau\text{move1 } P \ h \ (AAss \ (Val \ v) \ (Throw \ a) \ e')$
and $\tau\text{move1AAssThrow3}$: $\tau\text{move1 } P \ h \ (AAss \ (Val \ v) \ (Val \ v') \ (Throw \ a))$
and $\tau\text{move1ALengthThrow}$: $\tau\text{move1 } P \ h \ (Throw \ a.length)$
and $\tau\text{move1FAccThrow}$: $\tau\text{move1 } P \ h \ (Throw \ a.F\{D\})$
and $\tau\text{move1FAssThrow1}$: $\tau\text{move1 } P \ h \ (Throw \ a.F\{D\} := e)$
and $\tau\text{move1FAssThrow2}$: $\tau\text{move1 } P \ h \ (FAss \ (Val \ v) \ F \ D \ (Throw \ a))$
and $\tau\text{move1CASThrow1}$: $\tau\text{move1 } P \ h \ (CompareAndSwap \ (Throw \ a) \ D \ F \ e \ e')$
and $\tau\text{move1CASThrow2}$: $\tau\text{move1 } P \ h \ (CompareAndSwap \ (Val \ v) \ D \ F \ (Throw \ a) \ e')$
and $\tau\text{move1CASThrow3}$: $\tau\text{move1 } P \ h \ (CompareAndSwap \ (Val \ v) \ D \ F \ (Val \ v') \ (Throw \ a))$
and $\tau\text{move1CallThrowObj}$: $\tau\text{move1 } P \ h \ (Throw \ a.M(es))$
and $\tau\text{move1CallThrowParams}$: $\tau\text{move1 } P \ h \ (Val \ v.M(\text{map } Val \ vs \ @ \ Throw \ a \ \# \ es))$
and $\tau\text{move1BlockThrow}$: $\tau\text{move1 } P \ h \ \{V:T=None; \ Throw \ a\}$
and $\tau\text{move1SyncThrow}$: $\tau\text{move1 } P \ h \ (sync_{V'} \ (Throw \ a) \ e)$
and $\tau\text{move1SeqThrow}$: $\tau\text{move1 } P \ h \ (Throw \ a; e)$
and $\tau\text{move1CondThrow}$: $\tau\text{move1 } P \ h \ (if \ (Throw \ a) \ e1 \ \text{else} \ e2)$
and $\tau\text{move1ThrowThrow}$: $\tau\text{move1 } P \ h \ (throw \ (Throw \ a))$

and $\tau\text{moves1Hd}$: $\tau\text{move1 } P \ h \ e \implies \tau\text{moves1 } P \ h \ (e \ \# \ es)$
and $\tau\text{moves1Tl}$: $\tau\text{moves1 } P \ h \ es \implies \tau\text{moves1 } P \ h \ (Val \ v \ \# \ es)$
by *fastforce+*

lemma $\tau\text{moves1-map-Val} \ [dest!]$:
 $\tau\text{moves1 } P \ h \ (\text{map } Val \ es) \implies False$
by(*induct es*)(*auto*)

lemma $\tau\text{moves1-map-Val-ThrowD} \ [simp]$: $\tau\text{moves1 } P \ h \ (\text{map } Val \ vs \ @ \ Throw \ a \ \# \ es) = False$
by(*induct vs*)(*fastforce+*)

lemma fixes $e :: ('a, 'b, 'addr) \ exp$ **and** $es :: ('a, 'b, 'addr) \ exp \ list$
shows $\tau\text{move1-not-call1}$:
 $call1 \ e = [(a, M, vs)] \implies \tau\text{move1 } P \ h \ e \longleftrightarrow (\text{synthesized-call } P \ h \ (a, M, vs) \longrightarrow \tau\text{external}' \ P \ h \ a \ M)$
and $\tau\text{moves1-not-calls1}$:
 $calls1 \ es = [(a, M, vs)] \implies \tau\text{moves1 } P \ h \ es \longleftrightarrow (\text{synthesized-call } P \ h \ (a, M, vs) \longrightarrow \tau\text{external}' \ P \ h \ a \ M)$
apply(*induct e and es rule: call1.induct calls1.induct*)
apply(*auto split: if-split-asm simp add: is-vals-conv*)
apply(*fastforce simp add: synthesized-call-def map-eq-append-conv $\tau\text{external}'$ -def $\tau\text{external}$ -def dest: sees-method-fun*)
done

lemma $red1-\tau-taD$: $\llbracket uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \tau\text{move1 } P \ (hp \ s) \ e \rrbracket \implies ta = \varepsilon$
and $reds1-\tau-taD$: $\llbracket uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \tau\text{moves1 } P \ (hp \ s) \ es \rrbracket \implies ta = \varepsilon$
apply(*induct rule: red1-reds1.inducts*)
apply(*fastforce simp add: map-eq-append-conv $\tau\text{external}'$ -def $\tau\text{external}$ -def dest: $\tau\text{external}'$ -red-external-TA-empty*)
done

lemma $\tau\text{move1-heap-unchanged}$: $\llbracket uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \tau\text{move1 } P \ (hp \ s) \ e \rrbracket \implies hp \ s' =$

$hp\ s$
and $\tau moves1\text{-heap-unchanged}$: $\llbracket uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \tau moves1\ P\ (hp\ s)\ es \rrbracket \implies hp\ s'$
 $= hp\ s$
apply(*induct rule: red1-reds1.inducts*)
apply(*auto*)
apply(*fastforce simp add: map-eq-append-conv $\tau external'$ -def $\tau external$ -def dest: $\tau external'$ -red-external-heap-unch*)
done

lemma $\tau Move1\text{-iff}$:

$\tau Move1\ P\ h\ exes \longleftrightarrow (let\ ((e, -), -) = exes\ in\ \tau move1\ P\ h\ e \vee final\ e)$
by(*cases exes*)(*auto*)

lemma $\tau red1\text{-iff}$ [*iff*]:

$\tau red1g\ uf\ P\ t\ h\ (e, xs)\ (e', xs') = (uf, P, t \vdash 1 \langle e, (h, xs) \rangle -\varepsilon \rightarrow \langle e', (h, xs') \rangle) \wedge \tau move1\ P\ h\ e$
by(*simp add: $\tau red1g$ -def*)

lemma $\tau reds1\text{-iff}$ [*iff*]:

$\tau reds1g\ uf\ P\ t\ h\ (es, xs)\ (es', xs') = (uf, P, t \vdash 1 \langle es, (h, xs) \rangle [-\varepsilon \rightarrow] \langle es', (h, xs') \rangle) \wedge \tau moves1\ P\ h\ es$
by(*simp add: $\tau reds1g$ -def*)

lemma $\tau red1t\text{-1step}$:

$\llbracket uf, P, t \vdash 1 \langle e, (h, xs) \rangle -\varepsilon \rightarrow \langle e', (h, xs') \rangle; \tau move1\ P\ h\ e \rrbracket$
 $\implies \tau red1gt\ uf\ P\ t\ h\ (e, xs)\ (e', xs')$
by(*blast intro: tranclp.r-into-trancl*)

lemma $\tau red1t\text{-2step}$:

$\llbracket uf, P, t \vdash 1 \langle e, (h, xs) \rangle -\varepsilon \rightarrow \langle e', (h, xs') \rangle; \tau move1\ P\ h\ e;$
 $uf, P, t \vdash 1 \langle e', (h, xs') \rangle -\varepsilon \rightarrow \langle e'', (h, xs'') \rangle; \tau move1\ P\ h\ e' \rrbracket$
 $\implies \tau red1gt\ uf\ P\ t\ h\ (e, xs)\ (e'', xs'')$
by(*blast intro: tranclp.trancl-into-trancl[OF $\tau red1t\text{-1step}$]*)

lemma $\tau red1t\text{-3step}$:

$\llbracket uf, P, t \vdash 1 \langle e, (h, xs) \rangle -\varepsilon \rightarrow \langle e', (h, xs') \rangle; \tau move1\ P\ h\ e;$
 $uf, P, t \vdash 1 \langle e', (h, xs') \rangle -\varepsilon \rightarrow \langle e'', (h, xs'') \rangle; \tau move1\ P\ h\ e';$
 $uf, P, t \vdash 1 \langle e'', (h, xs'') \rangle -\varepsilon \rightarrow \langle e''', (h, xs''') \rangle; \tau move1\ P\ h\ e'' \rrbracket$
 $\implies \tau red1gt\ uf\ P\ t\ h\ (e, xs)\ (e''', xs''')$
by(*blast intro: tranclp.trancl-into-trancl[OF $\tau red1t\text{-2step}$]*)

lemma $\tau reds1t\text{-1step}$:

$\llbracket uf, P, t \vdash 1 \langle es, (h, xs) \rangle [-\varepsilon \rightarrow] \langle es', (h, xs') \rangle; \tau moves1\ P\ h\ es \rrbracket$
 $\implies \tau reds1gt\ uf\ P\ t\ h\ (es, xs)\ (es', xs')$
by(*blast intro: tranclp.r-into-trancl*)

lemma $\tau reds1t\text{-2step}$:

$\llbracket uf, P, t \vdash 1 \langle es, (h, xs) \rangle [-\varepsilon \rightarrow] \langle es', (h, xs') \rangle; \tau moves1\ P\ h\ es;$
 $uf, P, t \vdash 1 \langle es', (h, xs') \rangle [-\varepsilon \rightarrow] \langle es'', (h, xs'') \rangle; \tau moves1\ P\ h\ es' \rrbracket$
 $\implies \tau reds1gt\ uf\ P\ t\ h\ (es, xs)\ (es'', xs'')$
by(*blast intro: tranclp.trancl-into-trancl[OF $\tau reds1t\text{-1step}$]*)

lemma $\tau reds1t\text{-3step}$:

$\llbracket uf, P, t \vdash 1 \langle es, (h, xs) \rangle [-\varepsilon \rightarrow] \langle es', (h, xs') \rangle; \tau moves1\ P\ h\ es;$
 $uf, P, t \vdash 1 \langle es', (h, xs') \rangle [-\varepsilon \rightarrow] \langle es'', (h, xs'') \rangle; \tau moves1\ P\ h\ es';$
 $uf, P, t \vdash 1 \langle es'', (h, xs'') \rangle [-\varepsilon \rightarrow] \langle es''', (h, xs''') \rangle; \tau moves1\ P\ h\ es'' \rrbracket$

$\implies \tau\text{reds1gt } uf P t h (es, xs) (es''', xs''')$
by(blast intro: tranclp.trancl-into-trancl[OF $\tau\text{reds1t-2step}$])

lemma $\tau\text{red1r-1step}$:
 $\llbracket uf, P, t \vdash 1 \langle e, (h, xs) \rangle -\varepsilon \rightarrow \langle e', (h, xs') \rangle; \tau\text{move1 } P h e \rrbracket$
 $\implies \tau\text{red1gr } uf P t h (e, xs) (e', xs')$
by(blast intro: r-into-rtranclp)

lemma $\tau\text{red1r-2step}$:
 $\llbracket uf, P, t \vdash 1 \langle e, (h, xs) \rangle -\varepsilon \rightarrow \langle e', (h, xs') \rangle; \tau\text{move1 } P h e;$
 $uf, P, t \vdash 1 \langle e', (h, xs') \rangle -\varepsilon \rightarrow \langle e'', (h, xs'') \rangle; \tau\text{move1 } P h e' \rrbracket$
 $\implies \tau\text{red1gr } uf P t h (e, xs) (e'', xs'')$
by(blast intro: rtranclp.rtrancl-into-rtrancl[OF $\tau\text{red1r-1step}$])

lemma $\tau\text{red1r-3step}$:
 $\llbracket uf, P, t \vdash 1 \langle e, (h, xs) \rangle -\varepsilon \rightarrow \langle e', (h, xs') \rangle; \tau\text{move1 } P h e;$
 $uf, P, t \vdash 1 \langle e', (h, xs') \rangle -\varepsilon \rightarrow \langle e'', (h, xs'') \rangle; \tau\text{move1 } P h e';$
 $uf, P, t \vdash 1 \langle e'', (h, xs'') \rangle -\varepsilon \rightarrow \langle e''', (h, xs''') \rangle; \tau\text{move1 } P h e'' \rrbracket$
 $\implies \tau\text{red1gr } uf P t h (e, xs) (e''', xs''')$
by(blast intro: rtranclp.rtrancl-into-rtrancl[OF $\tau\text{red1r-2step}$])

lemma $\tau\text{reds1r-1step}$:
 $\llbracket uf, P, t \vdash 1 \langle es, (h, xs) \rangle [-\varepsilon \rightarrow] \langle es', (h, xs') \rangle; \tau\text{moves1 } P h es \rrbracket$
 $\implies \tau\text{reds1gr } uf P t h (es, xs) (es', xs')$
by(blast intro: r-into-rtranclp)

lemma $\tau\text{reds1r-2step}$:
 $\llbracket uf, P, t \vdash 1 \langle es, (h, xs) \rangle [-\varepsilon \rightarrow] \langle es', (h, xs') \rangle; \tau\text{moves1 } P h es;$
 $uf, P, t \vdash 1 \langle es', (h, xs') \rangle [-\varepsilon \rightarrow] \langle es'', (h, xs'') \rangle; \tau\text{moves1 } P h es' \rrbracket$
 $\implies \tau\text{reds1gr } uf P t h (es, xs) (es'', xs'')$
by(blast intro: rtranclp.rtrancl-into-rtrancl[OF $\tau\text{reds1r-1step}$])

lemma $\tau\text{reds1r-3step}$:
 $\llbracket uf, P, t \vdash 1 \langle es, (h, xs) \rangle [-\varepsilon \rightarrow] \langle es', (h, xs') \rangle; \tau\text{moves1 } P h es;$
 $uf, P, t \vdash 1 \langle es', (h, xs') \rangle [-\varepsilon \rightarrow] \langle es'', (h, xs'') \rangle; \tau\text{moves1 } P h es';$
 $uf, P, t \vdash 1 \langle es'', (h, xs'') \rangle [-\varepsilon \rightarrow] \langle es''', (h, xs''') \rangle; \tau\text{moves1 } P h es'' \rrbracket$
 $\implies \tau\text{reds1gr } uf P t h (es, xs) (es''', xs''')$
by(blast intro: rtranclp.rtrancl-into-rtrancl[OF $\tau\text{reds1r-2step}$])

lemma $\tau\text{red1t-preserves-len}$: $\tau\text{red1gt } uf P t h (e, xs) (e', xs') \implies \text{length } xs' = \text{length } xs$
by(induct rule: tranclp-induct2)(auto dest: red1-preserves-len)

lemma $\tau\text{red1r-preserves-len}$: $\tau\text{red1gr } uf P t h (e, xs) (e', xs') \implies \text{length } xs' = \text{length } xs$
by(induct rule: rtranclp-induct2)(auto dest: red1-preserves-len)

lemma $\tau\text{red1t-inj-}\tau\text{reds1t}$: $\tau\text{red1gt } uf P t h (e, xs) (e', xs') \implies \tau\text{reds1gt } uf P t h (e \# es, xs) (e' \# es, xs')$
by(induct rule: tranclp-induct2)(auto intro: tranclp.trancl-into-trancl List1Red1 $\tau\text{moves1Hd}$)

lemma $\tau\text{reds1t-cons-}\tau\text{reds1t}$: $\tau\text{reds1gt } uf P t h (es, xs) (es', xs') \implies \tau\text{reds1gt } uf P t h (Val v \# es, xs) (Val v \# es', xs')$
by(induct rule: tranclp-induct2)(auto intro: tranclp.trancl-into-trancl List1Red2 $\tau\text{moves1Tl}$)

lemma $\tau\text{red1r-inj-}\tau\text{reds1r}$: $\tau\text{red1gr } uf P t h (e, xs) (e', xs') \implies \tau\text{reds1gr } uf P t h (e \# es, xs) (e' \# es, xs')$

es, xs')

by(*induct rule*: *rtranclp-induct2*)(*auto intro*: *rtranclp.rtrancl-into-rtrancl List1Red1 τ moves1Hd*)

lemma τ reds1r-cons- τ reds1r: τ reds1gr uf P t h (es, xs) (es', xs') \implies τ reds1gr uf P t h (Val v # es, xs) (Val v # es', xs')

by(*induct rule*: *rtranclp-induct2*)(*auto intro*: *rtranclp.rtrancl-into-rtrancl List1Red2 τ moves1Tl*)

lemma *NewArray- τ red1t-xt*:

τ red1gt uf P t h (e, xs) (e', xs') \implies τ red1gt uf P t h (newA T[e], xs) (newA T[e'], xs')

by(*induct rule*: *tranclp-induct2*)(*auto intro*: *tranclp.trancl-into-trancl New1ArrayRed τ move1NewArray*)

lemma *Cast- τ red1t-xt*:

τ red1gt uf P t h (e, xs) (e', xs') \implies τ red1gt uf P t h (Cast T e, xs) (Cast T e', xs')

by(*induct rule*: *tranclp-induct2*)(*auto intro*: *tranclp.trancl-into-trancl Cast1Red τ move1Cast*)

lemma *InstanceOf- τ red1t-xt*:

τ red1gt uf P t h (e, xs) (e', xs') \implies τ red1gt uf P t h (e instanceof T, xs) (e' instanceof T, xs')

by(*induct rule*: *tranclp-induct2*)(*auto intro*: *tranclp.trancl-into-trancl InstanceOf1Red τ move1InstanceOf*)

lemma *BinOp- τ red1t-xt1*:

τ red1gt uf P t h (e1, xs) (e1', xs') \implies τ red1gt uf P t h (e1 «bop» e2, xs) (e1' «bop» e2, xs')

by(*induct rule*: *tranclp-induct2*)(*auto intro*: *tranclp.trancl-into-trancl Bin1OpRed1 τ move1BinOp1*)

lemma *BinOp- τ red1t-xt2*:

τ red1gt uf P t h (e2, xs) (e2', xs') \implies τ red1gt uf P t h (Val v «bop» e2, xs) (Val v «bop» e2', xs')

by(*induct rule*: *tranclp-induct2*)(*auto intro*: *tranclp.trancl-into-trancl Bin1OpRed2 τ move1BinOp2*)

lemma *LAss- τ red1t*:

τ red1gt uf P t h (e, xs) (e', xs') \implies τ red1gt uf P t h (V := e, xs) (V := e', xs')

by(*induct rule*: *tranclp-induct2*)(*auto intro*: *tranclp.trancl-into-trancl LAss1Red τ move1LAss*)

lemma *AAcc- τ red1t-xt1*:

τ red1gt uf P t h (a, xs) (a', xs') \implies τ red1gt uf P t h (a[i], xs) (a'[i], xs')

by(*induct rule*: *tranclp-induct2*)(*auto intro*: *tranclp.trancl-into-trancl AAcc1Red1 τ move1AAcc1*)

lemma *AAcc- τ red1t-xt2*:

τ red1gt uf P t h (i, xs) (i', xs') \implies τ red1gt uf P t h (Val a[i], xs) (Val a[i'], xs')

by(*induct rule*: *tranclp-induct2*)(*auto intro*: *tranclp.trancl-into-trancl AAcc1Red2 τ move1AAcc2*)

lemma *AAss- τ red1t-xt1*:

τ red1gt uf P t h (a, xs) (a', xs') \implies τ red1gt uf P t h (a[i] := e, xs) (a'[i] := e, xs')

by(*induct rule*: *tranclp-induct2*)(*auto intro*: *tranclp.trancl-into-trancl AAss1Red1 τ move1AAss1*)

lemma *AAss- τ red1t-xt2*:

τ red1gt uf P t h (i, xs) (i', xs') \implies τ red1gt uf P t h (Val a[i] := e, xs) (Val a[i'] := e, xs')

by(*induct rule*: *tranclp-induct2*)(*auto intro*: *tranclp.trancl-into-trancl AAss1Red2 τ move1AAss2*)

lemma *AAss- τ red1t-xt3*:

τ red1gt uf P t h (e, xs) (e', xs') \implies τ red1gt uf P t h (Val a[Val i] := e, xs) (Val a[Val i] := e', xs')

by(*induct rule*: *tranclp-induct2*)(*auto intro*: *tranclp.trancl-into-trancl AAss1Red3 τ move1AAss3*)

lemma *ALength- τ red1t-xt*:

τ red1gt uf P t h (a, xs) (a', xs') \implies τ red1gt uf P t h (a.length, xs) (a'.length, xs')

by(*induct rule*: *tranclp-induct2*)(*auto intro*: *tranclp.trancl-into-trancl ALength1Red* τ *move1ALength*)

lemma *FAcc- τ red1t-xt*:

τ *red1gt uf P t h* (*e*, *xs*) (*e'*, *xs'*) \implies τ *red1gt uf P t h* (*e*·*F*{*D*}, *xs*) (*e'*·*F*{*D*}, *xs'*)

by(*induct rule*: *tranclp-induct2*)(*auto intro*: *tranclp.trancl-into-trancl FAcc1Red* τ *move1FAcc*)

lemma *FAss- τ red1t-xt1*:

τ *red1gt uf P t h* (*e*, *xs*) (*e'*, *xs'*) \implies τ *red1gt uf P t h* (*e*·*F*{*D*} := *e2*, *xs*) (*e'*·*F*{*D*} := *e2*, *xs'*)

by(*induct rule*: *tranclp-induct2*)(*auto intro*: *tranclp.trancl-into-trancl FAss1Red1* τ *move1FAss1*)

lemma *FAss- τ red1t-xt2*:

τ *red1gt uf P t h* (*e*, *xs*) (*e'*, *xs'*) \implies τ *red1gt uf P t h* (*Val v*·*F*{*D*} := *e*, *xs*) (*Val v*·*F*{*D*} := *e'*, *xs'*)

by(*induct rule*: *tranclp-induct2*)(*auto intro*: *tranclp.trancl-into-trancl FAss1Red2* τ *move1FAss2*)

lemma *CAS- τ red1t-xt1*:

τ *red1gt uf P t h* (*e*, *xs*) (*e'*, *xs'*) \implies τ *red1gt uf P t h* (*e*·*compareAndSwap*(*D*·*F*, *e2*, *e3*), *xs*) (*e'*·*compareAndSwap*(*D*·*F*, *e2*, *e3*), *xs'*)

by(*induct rule*: *tranclp-induct2*)(*auto intro*: *tranclp.trancl-into-trancl CAS1Red1*)

lemma *CAS- τ red1t-xt2*:

τ *red1gt uf P t h* (*e*, *xs*) (*e'*, *xs'*) \implies τ *red1gt uf P t h* (*Val v*·*compareAndSwap*(*D*·*F*, *e*, *e3*), *xs*) (*Val v*·*compareAndSwap*(*D*·*F*, *e'*, *e3*), *xs'*)

by(*induct rule*: *tranclp-induct2*)(*auto intro*: *tranclp.trancl-into-trancl CAS1Red2*)

lemma *CAS- τ red1t-xt3*:

τ *red1gt uf P t h* (*e*, *xs*) (*e'*, *xs'*) \implies τ *red1gt uf P t h* (*Val v*·*compareAndSwap*(*D*·*F*, *Val v'*, *e*), *xs*) (*Val v*·*compareAndSwap*(*D*·*F*, *Val v'*, *e'*), *xs'*)

by(*induct rule*: *tranclp-induct2*)(*auto intro*: *tranclp.trancl-into-trancl CAS1Red3*)

lemma *Call- τ red1t-obj*:

τ *red1gt uf P t h* (*e*, *xs*) (*e'*, *xs'*) \implies τ *red1gt uf P t h* (*e*·*M*(*ps*), *xs*) (*e'*·*M*(*ps*), *xs'*)

by(*induct rule*: *tranclp-induct2*)(*auto intro*: *tranclp.trancl-into-trancl Call1Obj* τ *move1CallObj*)

lemma *Call- τ red1t-param*:

τ *reds1gt uf P t h* (*es*, *xs*) (*es'*, *xs'*) \implies τ *red1gt uf P t h* (*Val v*·*M*(*es*), *xs*) (*Val v*·*M*(*es'*), *xs'*)

by(*induct rule*: *tranclp-induct2*)(*auto intro*: *tranclp.trancl-into-trancl Call1Params* τ *move1CallParams*)

lemma *Block-None- τ red1t-xt*:

τ *red1gt uf P t h* (*e*, *xs*) (*e'*, *xs'*) \implies τ *red1gt uf P t h* (*{ V:T=None; e }*, *xs*) (*{ V:T=None; e' }*, *xs'*)

by(*induct rule*: *tranclp-induct2*)(*auto intro*: *tranclp.trancl-into-trancl* τ *move1Block elim!*: *Block1Red*)

lemma *Block- τ red1t-Some*:

$\llbracket \tau$ *red1gt uf P t h* (*e*, *xs*[*V* := *v*]) (*e'*, *xs'*); *V* < *length xs* \rrbracket

\implies τ *red1gt uf P t h* (*{ V:Ty=[v]; e }*, *xs*) (*{ V:Ty=None; e' }*, *xs'*)

by(*blast intro*: *tranclp-into-tranclp2 Block1Some* τ *move1BlockSome* *Block-None- τ red1t-xt*)

lemma *Sync- τ red1t-xt*:

τ *red1gt uf P t h* (*e*, *xs*) (*e'*, *xs'*) \implies τ *red1gt uf P t h* (*sync*_{*V*} (*e*) *e2*, *xs*) (*sync*_{*V*} (*e'*) *e2*, *xs'*)

by(*induct rule*: *tranclp-induct2*)(*auto intro*: *tranclp.trancl-into-trancl Synchronized1Red1* τ *move1Sync*)

lemma *InSync- τ red1t-xt*:

τ *red1gt uf P t h* (*e*, *xs*) (*e'*, *xs'*) \implies τ *red1gt uf P t h* (*insync*_{*V*} (*a*) *e*, *xs*) (*insync*_{*V*} (*a*) *e'*, *xs'*)

by(*induct rule*: *tranclp-induct2*)(*auto intro*: *tranclp.trancl-into-trancl Synchronized1Red2* τ *move1In-Sync*)

lemma *Seq- τ red1t-xt*:

$\tau\text{red1gt uf P t h } (e, xs) (e', xs') \implies \tau\text{red1gt uf P t h } (e;;e2, xs) (e';;e2, xs')$
by(*induct rule: tranclp-induct2*)(*auto intro: tranclp.trancl-into-trancl Seq1Red τ move1Seq*)

lemma *Cond- τ red1t-xt*:

$\tau\text{red1gt uf P t h } (e, xs) (e', xs') \implies \tau\text{red1gt uf P t h } (\text{if } (e) e1 \text{ else } e2, xs) (\text{if } (e') e1 \text{ else } e2, xs')$
by(*induct rule: tranclp-induct2*)(*auto intro: tranclp.trancl-into-trancl Cond1Red τ move1Cond*)

lemma *Throw- τ red1t-xt*:

$\tau\text{red1gt uf P t h } (e, xs) (e', xs') \implies \tau\text{red1gt uf P t h } (\text{throw } e, xs) (\text{throw } e', xs')$
by(*induct rule: tranclp-induct2*)(*auto intro: tranclp.trancl-into-trancl Throw1Red τ move1Throw*)

lemma *Try- τ red1t-xt*:

$\tau\text{red1gt uf P t h } (e, xs) (e', xs') \implies \tau\text{red1gt uf P t h } (\text{try } e \text{ catch } (C V) e2, xs) (\text{try } e' \text{ catch } (C V) e2, xs')$
by(*induct rule: tranclp-induct2*)(*auto intro: tranclp.trancl-into-trancl Try1Red τ move1Try*)

lemma *NewArray- τ red1r-xt*:

$\tau\text{red1gr uf P t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf P t h } (\text{newA } T[e], xs) (\text{newA } T[e'], xs')$
by(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl New1ArrayRed τ move1NewArray*)

lemma *Cast- τ red1r-xt*:

$\tau\text{red1gr uf P t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf P t h } (\text{Cast } T e, xs) (\text{Cast } T e', xs')$
by(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl Cast1Red τ move1Cast*)

lemma *InstanceOf- τ red1r-xt*:

$\tau\text{red1gr uf P t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf P t h } (e \text{ instanceof } T, xs) (e' \text{ instanceof } T, xs')$
by(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl InstanceOf1Red τ move1InstanceOf*)

lemma *BinOp- τ red1r-xt1*:

$\tau\text{red1gr uf P t h } (e1, xs) (e1', xs') \implies \tau\text{red1gr uf P t h } (e1 \llbracket \text{bop} \rrbracket e2, xs) (e1' \llbracket \text{bop} \rrbracket e2, xs')$
by(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl Bin1OpRed1 τ move1BinOp1*)

lemma *BinOp- τ red1r-xt2*:

$\tau\text{red1gr uf P t h } (e2, xs) (e2', xs') \implies \tau\text{red1gr uf P t h } (\text{Val } v \llbracket \text{bop} \rrbracket e2, xs) (\text{Val } v \llbracket \text{bop} \rrbracket e2', xs')$
by(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl Bin1OpRed2 τ move1BinOp2*)

lemma *LAss- τ red1r*:

$\tau\text{red1gr uf P t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf P t h } (V := e, xs) (V := e', xs')$
by(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl LAss1Red τ move1LAss*)

lemma *AAcc- τ red1r-xt1*:

$\tau\text{red1gr uf P t h } (a, xs) (a', xs') \implies \tau\text{red1gr uf P t h } (a[i], xs) (a'[i], xs')$
by(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl AAcc1Red1 τ move1AAcc1*)

lemma *AAcc- τ red1r-xt2*:

$\tau\text{red1gr uf P t h } (i, xs) (i', xs') \implies \tau\text{red1gr uf P t h } (\text{Val } a[i], xs) (\text{Val } a[i'], xs')$
by(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl AAcc1Red2 τ move1AAcc2*)

lemma *AAss- τ red1r-xt1*:

$\tau red1gr\ uf\ P\ t\ h\ (a,\ xs)\ (a',\ xs') \implies \tau red1gr\ uf\ P\ t\ h\ (a[i] := e,\ xs)\ (a'[i] := e,\ xs')$
by(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl AAss1Red1 $\tau move1AAss1$*)

lemma *AAss- $\tau red1r$ -xt2*:

$\tau red1gr\ uf\ P\ t\ h\ (i,\ xs)\ (i',\ xs') \implies \tau red1gr\ uf\ P\ t\ h\ (Val\ a[i] := e,\ xs)\ (Val\ a[i'] := e,\ xs')$
by(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl AAss1Red2 $\tau move1AAss2$*)

lemma *AAss- $\tau red1r$ -xt3*:

$\tau red1gr\ uf\ P\ t\ h\ (e,\ xs)\ (e',\ xs') \implies \tau red1gr\ uf\ P\ t\ h\ (Val\ a[Val\ i] := e,\ xs)\ (Val\ a[Val\ i] := e',\ xs')$
by(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl AAss1Red3 $\tau move1AAss3$*)

lemma *ALength- $\tau red1r$ -xt*:

$\tau red1gr\ uf\ P\ t\ h\ (a,\ xs)\ (a',\ xs') \implies \tau red1gr\ uf\ P\ t\ h\ (a.length,\ xs)\ (a'.length,\ xs')$
by(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl ALength1Red $\tau move1ALength$*)

lemma *FAcc- $\tau red1r$ -xt*:

$\tau red1gr\ uf\ P\ t\ h\ (e,\ xs)\ (e',\ xs') \implies \tau red1gr\ uf\ P\ t\ h\ (e.F\{D\},\ xs)\ (e'.F\{D\},\ xs')$
by(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl FAcc1Red $\tau move1FAcc$*)

lemma *FAss- $\tau red1r$ -xt1*:

$\tau red1gr\ uf\ P\ t\ h\ (e,\ xs)\ (e',\ xs') \implies \tau red1gr\ uf\ P\ t\ h\ (e.F\{D\} := e2,\ xs)\ (e'.F\{D\} := e2,\ xs')$
by(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl FAss1Red1 $\tau move1FAss1$*)

lemma *FAss- $\tau red1r$ -xt2*:

$\tau red1gr\ uf\ P\ t\ h\ (e,\ xs)\ (e',\ xs') \implies \tau red1gr\ uf\ P\ t\ h\ (Val\ v.F\{D\} := e,\ xs)\ (Val\ v.F\{D\} := e',\ xs')$
by(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl FAss1Red2 $\tau move1FAss2$*)

lemma *CAS- $\tau red1r$ -xt1*:

$\tau red1gr\ uf\ P\ t\ h\ (e,\ xs)\ (e',\ xs') \implies \tau red1gr\ uf\ P\ t\ h\ (e.compareAndSwap(D.F,\ e2,\ e3),\ xs)\ (e'.compareAndSwap(D.F,\ e2,\ e3),\ xs')$
by(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl CAS1Red1*)

lemma *CAS- $\tau red1r$ -xt2*:

$\tau red1gr\ uf\ P\ t\ h\ (e,\ xs)\ (e',\ xs') \implies \tau red1gr\ uf\ P\ t\ h\ (Val\ v.compareAndSwap(D.F,\ e,\ e3),\ xs)\ (Val\ v.compareAndSwap(D.F,\ e',\ e3),\ xs')$
by(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl CAS1Red2*)

lemma *CAS- $\tau red1r$ -xt3*:

$\tau red1gr\ uf\ P\ t\ h\ (e,\ xs)\ (e',\ xs') \implies \tau red1gr\ uf\ P\ t\ h\ (Val\ v.compareAndSwap(D.F,\ Val\ v',\ e),\ xs)\ (Val\ v.compareAndSwap(D.F,\ Val\ v',\ e'),\ xs')$
by(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl CAS1Red3*)

lemma *Call- $\tau red1r$ -obj*:

$\tau red1gr\ uf\ P\ t\ h\ (e,\ xs)\ (e',\ xs') \implies \tau red1gr\ uf\ P\ t\ h\ (e.M(ps),\ xs)\ (e'.M(ps),\ xs')$
by(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl Call1Obj $\tau move1CallObj$*)

lemma *Call- $\tau red1r$ -param*:

$\tau red1gr\ uf\ P\ t\ h\ (es,\ xs)\ (es',\ xs') \implies \tau red1gr\ uf\ P\ t\ h\ (Val\ v.M(es),\ xs)\ (Val\ v.M(es'),\ xs')$
by(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl Call1Params $\tau move1CallParams$*)

lemma *Block-None- $\tau red1r$ -xt*:

$\tau red1gr\ uf\ P\ t\ h\ (e,\ xs)\ (e',\ xs') \implies \tau red1gr\ uf\ P\ t\ h\ (\{V:T=None;\ e\},\ xs)\ (\{V:T=None;\ e'\},\ xs')$
by(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl $\tau move1Block\ elim!$: Block1Red*)

lemma *Block- τ red1r-Some*:

$$\begin{aligned} & \llbracket \tau red1gr\ uf\ P\ t\ h\ (e,\ xs[V := v])\ (e',\ xs') ;\ V < length\ xs \rrbracket \\ & \implies \tau red1gr\ uf\ P\ t\ h\ (\{V:Ty=[v];\ e\},\ xs)\ (\{V:Ty=None;\ e'\},\ xs') \end{aligned}$$

by(blast intro: converse-rtranclp-into-rtranclp Block1Some τ move1BlockSome Block-None- τ red1r-xt)

lemma *Sync- τ red1r-xt*:

$$\tau red1gr\ uf\ P\ t\ h\ (e,\ xs)\ (e',\ xs') \implies \tau red1gr\ uf\ P\ t\ h\ (sync_V\ (e)\ e2,\ xs)\ (sync_V\ (e')\ e2,\ xs')$$

by(induct rule: rtranclp-induct2)(auto intro: rtranclp.rtrancl-into-rtrancl Synchronized1Red1 τ move1Sync)

lemma *InSync- τ red1r-xt*:

$$\tau red1gr\ uf\ P\ t\ h\ (e,\ xs)\ (e',\ xs') \implies \tau red1gr\ uf\ P\ t\ h\ (insync_V\ (a)\ e,\ xs)\ (insync_V\ (a)\ e',\ xs')$$

by(induct rule: rtranclp-induct2)(auto intro: rtranclp.rtrancl-into-rtrancl Synchronized1Red2 τ move1In-Sync)

lemma *Seq- τ red1r-xt*:

$$\tau red1gr\ uf\ P\ t\ h\ (e,\ xs)\ (e',\ xs') \implies \tau red1gr\ uf\ P\ t\ h\ (e;;e2,\ xs)\ (e';;e2,\ xs')$$

by(induct rule: rtranclp-induct2)(auto intro: rtranclp.rtrancl-into-rtrancl Seq1Red τ move1Seq)

lemma *Cond- τ red1r-xt*:

$$\tau red1gr\ uf\ P\ t\ h\ (e,\ xs)\ (e',\ xs') \implies \tau red1gr\ uf\ P\ t\ h\ (if\ (e)\ e1\ else\ e2,\ xs)\ (if\ (e')\ e1\ else\ e2,\ xs')$$

by(induct rule: rtranclp-induct2)(auto intro: rtranclp.rtrancl-into-rtrancl Cond1Red τ move1Cond)

lemma *Throw- τ red1r-xt*:

$$\tau red1gr\ uf\ P\ t\ h\ (e,\ xs)\ (e',\ xs') \implies \tau red1gr\ uf\ P\ t\ h\ (throw\ e,\ xs)\ (throw\ e',\ xs')$$

by(induct rule: rtranclp-induct2)(auto intro: rtranclp.rtrancl-into-rtrancl Throw1Red τ move1Throw)

lemma *Try- τ red1r-xt*:

$$\tau red1gr\ uf\ P\ t\ h\ (e,\ xs)\ (e',\ xs') \implies \tau red1gr\ uf\ P\ t\ h\ (try\ e\ catch\ (C\ V)\ e2,\ xs)\ (try\ e'\ catch\ (C\ V)\ e2,\ xs')$$

by(induct rule: rtranclp-induct2)(auto intro: rtranclp.rtrancl-into-rtrancl Try1Red τ move1Try)

lemma *τ red1t-ThrowD [dest]: τ red1gt uf P t h (Throw a, xs) (e'', xs'') \implies e'' = Throw a \wedge xs'' = xs*

by(induct rule: tranclp-induct2)(auto)

lemma *τ red1r-ThrowD [dest]: τ red1gr uf P t h (Throw a, xs) (e'', xs'') \implies e'' = Throw a \wedge xs'' = xs*

by(induct rule: rtranclp-induct2)(auto)

lemma *τ Red1-conv [iff]*:

$$\tau Red1g\ uf\ P\ t\ h\ (ex,\ exs)\ (ex',\ exs') = (uf,P,t \vdash 1 \langle ex/exs,\ h \rangle -\varepsilon \rightarrow \langle ex'/exs',\ h \rangle \wedge \tau Move1\ P\ h\ (ex,\ exs))$$

by(simp add: τ Red1g-def)

lemma *τ red1t-into- τ Red1t*:

$$\tau red1gt\ uf\ P\ t\ h\ (e,\ xs)\ (e'',\ xs'') \implies \tau Red1gt\ uf\ P\ t\ h\ ((e,\ xs),\ exs)\ ((e'',\ xs''),\ exs)$$

by(induct rule: tranclp-induct2)(fastforce dest: red1Red intro: τ move1Block tranclp.intros)+

lemma *τ red1r-into- τ Red1r*:

$$\tau red1gr\ uf\ P\ t\ h\ (e,\ xs)\ (e'',\ xs'') \implies \tau Red1gr\ uf\ P\ t\ h\ ((e,\ xs),\ exs)\ ((e'',\ xs''),\ exs)$$

by(induct rule: rtranclp-induct2)(fastforce dest: red1Red intro: τ move1Block rtranclp.intros)+

lemma *red1-max-vars: $uf,P,t \vdash 1 \langle e,\ s \rangle -ta \rightarrow \langle e',\ s' \rangle \implies max\ vars\ e' \leq max\ vars\ e$*

$$\text{and } reds1-max-varss: uf,P,t \vdash 1 \langle es,\ s \rangle [-ta \rightarrow] \langle es',\ s' \rangle \implies max\ varss\ es' \leq max\ varss\ es$$

by(*induct rule: red1-reds1.inducts*) auto

lemma $\tau red1t\text{-max}\text{-vars}$: $\tau red1gt\ uf\ P\ t\ h\ (e,\ xs)\ (e',\ xs') \implies max\text{-vars}\ e' \leq max\text{-vars}\ e$
 by(*induct rule: tranclp-induct2*)(auto dest: red1-max-vars)

lemma $\tau red1r\text{-max}\text{-vars}$: $\tau red1gr\ uf\ P\ t\ h\ (e,\ xs)\ (e',\ xs') \implies max\text{-vars}\ e' \leq max\text{-vars}\ e$
 by(*induct rule: rtranclp-induct2*)(auto dest: red1-max-vars)

lemma $\tau red1r\text{-Val}$:

$\tau red1gr\ uf\ P\ t\ h\ (Val\ v,\ xs)\ s' \longleftrightarrow s' = (Val\ v,\ xs)$

proof

assume $\tau red1gr\ uf\ P\ t\ h\ (Val\ v,\ xs)\ s'$

thus $s' = (Val\ v,\ xs)$ **by** *induct*(auto)

qed auto

lemma $\tau red1t\text{-Val}$:

$\tau red1gt\ uf\ P\ t\ h\ (Val\ v,\ xs)\ s' \longleftrightarrow False$

proof

assume $\tau red1gt\ uf\ P\ t\ h\ (Val\ v,\ xs)\ s'$

thus *False* **by** *induct* auto

qed auto

lemma $\tau reds1r\text{-map}\text{-Val}$:

$\tau reds1gr\ uf\ P\ t\ h\ (map\ Val\ vs,\ xs)\ s' \longleftrightarrow s' = (map\ Val\ vs,\ xs)$

proof

assume $\tau reds1gr\ uf\ P\ t\ h\ (map\ Val\ vs,\ xs)\ s'$

thus $s' = (map\ Val\ vs,\ xs)$ **by** *induct* auto

qed auto

lemma $\tau reds1t\text{-map}\text{-Val}$:

$\tau reds1gt\ uf\ P\ t\ h\ (map\ Val\ vs,\ xs)\ s' \longleftrightarrow False$

proof

assume $\tau reds1gt\ uf\ P\ t\ h\ (map\ Val\ vs,\ xs)\ s'$

thus *False* **by** *induct* auto

qed auto

lemma $\tau reds1r\text{-map}\text{-Val}\text{-Throw}$:

$\tau reds1gr\ uf\ P\ t\ h\ (map\ Val\ vs\ @\ Throw\ a\ \#\ es,\ xs)\ s' \longleftrightarrow s' = (map\ Val\ vs\ @\ Throw\ a\ \#\ es,\ xs)$
 (**is** $?lhs \longleftrightarrow ?rhs$)

proof

assume $?lhs$ **thus** $?rhs$ **by** *induct* auto

qed auto

lemma $\tau reds1t\text{-map}\text{-Val}\text{-Throw}$:

$\tau reds1gt\ uf\ P\ t\ h\ (map\ Val\ vs\ @\ Throw\ a\ \#\ es,\ xs)\ s' \longleftrightarrow False$
 (**is** $?lhs \longleftrightarrow ?rhs$)

proof

assume $?lhs$ **thus** $?rhs$ **by** *induct* auto

qed auto

lemma $\tau red1r\text{-Throw}$:

$\tau red1gr\ uf\ P\ t\ h\ (Throw\ a,\ xs)\ s' \longleftrightarrow s' = (Throw\ a,\ xs)$ (**is** $?lhs \longleftrightarrow ?rhs$)

proof

assume $?lhs$ **thus** $?rhs$ **by** *induct* auto

qed *simp*

lemma $\tau red1t\text{-Throw}$:

$\tau red1gt\ uf\ P\ t\ h\ (Throw\ a,\ xs)\ s' \longleftrightarrow False\ (is\ ?lhs \longleftrightarrow ?rhs)$

proof

assume $?lhs$ **thus** $?rhs$ **by** *induct auto*

qed *simp*

lemma *red1-False-into-red1-True*:

$False, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies True, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle$

and *reds1-False-into-reds1-True*:

$False, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies True, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle$

by (*induct rule: red1-reds1.inducts*) (*auto intro: red1-reds1.intros*)

lemma *Red1-False-into-Red1-True*:

assumes $False, P, t \vdash 1 \langle ex/exs, shr\ s \rangle -ta \rightarrow \langle ex'/exs', m' \rangle$

shows $True, P, t \vdash 1 \langle ex/exs, shr\ s \rangle -ta \rightarrow \langle ex'/exs', m' \rangle$

using *assms*

by(*cases*)(*auto dest: Red1.intros red1-False-into-red1-True*)

lemma *red1-Suspend-is-call*:

$\llbracket uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; Suspend\ w \in set\ \{ta\}_w \rrbracket \implies call1\ e' \neq None$

and *reds-Suspend-is-calls*:

$\llbracket uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; Suspend\ w \in set\ \{ta\}_w \rrbracket \implies calls1\ es' \neq None$

by(*induct rule: red1-reds1.inducts*)(*auto dest: red-external-Suspend-StaySame*)

lemma *Red1-Suspend-is-call*:

$\llbracket uf, P, t \vdash 1 \langle (e, xs)/exs, h \rangle -ta \rightarrow \langle (e', xs')/exs', h' \rangle; Suspend\ w \in set\ \{ta\}_w \rrbracket \implies call1\ e' \neq None$

by(*auto elim!: Red1.cases dest: red1-Suspend-is-call*)

lemma *Red1-mthr: multithreaded final-expr1 (mred1g uf P)*

by(*unfold-locales*)(*fastforce elim!: Red1.cases dest: red1-new-thread-heap*)**+**

lemma *red1- τ move1-heap-unchanged*: $\llbracket uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \tau move1\ P\ (hp\ s)\ e \rrbracket \implies hp\ s' = hp\ s$

and *red1- τ moves1-heap-unchanged*: $\llbracket uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \tau moves1\ P\ (hp\ s)\ es \rrbracket \implies hp\ s' = hp\ s$

apply(*induct rule: red1-reds1.inducts*)

apply(*fastforce simp add: map-eq-append-conv $\tau external'$ -def $\tau external$ -def dest: $\tau external'$ -red-external-heap-unch*)
done

lemma *Red1- τ mthr-wf: τ multithreaded-wf final-expr1 (mred1g uf P) ($\tau MOVE1\ P$)*

proof –

interpret *multithreaded final-expr1 mred1g uf P convert-RA*

by(*rule Red1-mthr*)

show *?thesis*

proof

fix $x1\ m1\ t\ ta1\ x1'\ m1'$

assume $mred1g\ uf\ P\ t\ (x1,\ m1)\ ta1\ (x1',\ m1')\ \tau MOVE1\ P\ (x1,\ m1)\ ta1\ (x1',\ m1')$

thus $m1 = m1'$ **by**(*cases x1*)(*fastforce elim!: Red1.cases dest: red1- τ move1-heap-unchanged*)

next

fix $s\ ta\ s'$

assume $\tau MOVE1\ P\ s\ ta\ s'$

thus $ta = \varepsilon$ **by**(*simp add: split-beta*)

qed
qed

end

sublocale *J1-heap-base* < *Red1-mthr*:

τ multithreaded-wf
final-expr1
mred1g uf *P*
convert-RA
 τ MOVE1 *P*
for uf *P*
by(rule *Red1- τ mthr-wf*)

context *J1-heap-base* **begin**

lemma τ Red1't-into-Red1'- τ mthr-silent-movet:

τ Red1gt uf *P* t h (*ex2*, *exs2*) (*ex2''*, *exs2''*)
 \implies *Red1-mthr.silent-movet* uf *P* t ((*ex2*, *exs2*), *h*) ((*ex2''*, *exs2''*), *h*)

apply(induct rule: *tranclp-induct2*)

apply *clarsimp*

apply(rule *tranclp.r-into-trancl*)

apply(*simp* add: *Red1-mthr.silent-move-iff*)

apply(*erule* *tranclp.trancl-into-trancl*)

apply(*simp* add: *Red1-mthr.silent-move-iff*)

done

lemma τ Red1t-into-Red1'- τ mthr-silent-moves:

τ Red1gt uf *P* t h (*ex2*, *exs2*) (*ex2''*, *exs2''*)
 \implies *Red1-mthr.silent-moves* uf *P* t ((*ex2*, *exs2*), *h*) ((*ex2''*, *exs2''*), *h*)

by(rule *tranclp-into-rtranclp*)(rule τ Red1't-into-Red1'- τ mthr-silent-movet)

lemma τ Red1'r-into-Red1'- τ mthr-silent-moves:

τ Red1gr uf *P* t h (*ex*, *exs*) (*ex'*, *exs'*) \implies *Red1-mthr.silent-moves* uf *P* t ((*ex*, *exs*), *h*) ((*ex'*, *exs'*), *h*)

apply(induct rule: *rtranclp-induct2*)

apply *blast*

apply(*erule* *rtranclp.rtrancl-into-rtrancl*)

apply(*simp* add: *Red1-mthr.silent-move-iff*)

done

lemma τ Red1r-rtranclpD:

τ Red1gr uf *P* t h *s s'* \implies τ trsys.silent-moves (*mred1g* uf *P* t) (τ MOVE1 *P*) (*s*, *h*) (*s'*, *h*)

apply(induct rule: *rtranclp-induct*)

apply(*auto elim!*: *rtranclp.rtrancl-into-rtrancl* *intro*: τ trsys.silent-move.intros)

done

lemma τ Red1t-tranclpD:

τ Red1gt uf *P* t h *s s'* \implies τ trsys.silent-movet (*mred1g* uf *P* t) (τ MOVE1 *P*) (*s*, *h*) (*s'*, *h*)

apply(induct rule: *tranclp-induct*)

apply(rule *tranclp.r-into-trancl*)

apply(*auto elim!*: *tranclp.trancl-into-trancl* *intro!*: τ trsys.silent-move.intros *simp*: τ Red1g-def *split-def*)

done

lemma $\tau mreds1\text{-Val-Nil}$: $\tau trsys.silent\text{-moves} (mred1g\ uf\ P\ t) (\tau MOVE1\ P) (((Val\ v,\ xs),\ []),\ h) s \longleftrightarrow s = (((Val\ v,\ xs),\ []),\ h)$

proof

assume $\tau trsys.silent\text{-moves} (mred1g\ uf\ P\ t) (\tau MOVE1\ P) (((Val\ v,\ xs),\ []),\ h) s$

thus $s = (((Val\ v,\ xs),\ []),\ h)$

by $induct(auto\ elim!: Red1\text{-mthr.silent-move.cases}\ Red1.cases)$

qed $auto$

lemma $\tau mreds1\text{-Throw-Nil}$:

$\tau trsys.silent\text{-moves} (mred1g\ uf\ P\ t) (\tau MOVE1\ P) (((Throw\ a,\ xs),\ []),\ h) s \longleftrightarrow s = (((Throw\ a,\ xs),\ []),\ h)$

proof

assume $\tau trsys.silent\text{-moves} (mred1g\ uf\ P\ t) (\tau MOVE1\ P) (((Throw\ a,\ xs),\ []),\ h) s$

thus $s = (((Throw\ a,\ xs),\ []),\ h)$

by $induct(auto\ elim!: Red1\text{-mthr.silent-move.cases}\ Red1.cases)$

qed $auto$

end

end

7.7 Deadlock perservation for the intermediate language

theory $J1Deadlock$ **imports**

$J1$

$../Framework/FWDeadlock$

$../Common/ExternalCallWF$

begin

context $J1\text{-heap-base}$ **begin**

lemma $IUF\text{-red-taD}$:

$True, P, t \vdash 1 \langle e, s \rangle \text{-ta} \rightarrow \langle e', s' \rangle$

$\implies \exists e' ta' s'. False, P, t \vdash 1 \langle e, s \rangle \text{-ta}' \rightarrow \langle e', s' \rangle \wedge$

$collect\text{-locks} \llbracket ta' \rrbracket_l \subseteq collect\text{-locks} \llbracket ta \rrbracket_l \wedge set \llbracket ta' \rrbracket_c \subseteq set \llbracket ta \rrbracket_c \wedge collect\text{-interrupts} \llbracket ta' \rrbracket_i \subseteq collect\text{-interrupts} \llbracket ta \rrbracket_i \wedge$

$(\exists s. Red1\text{-mthr.actions-ok}\ s\ t\ ta')$

and $IUFs\text{-reds-taD}$:

$True, P, t \vdash 1 \langle es, s \rangle \text{-ta} \rightarrow \langle es', s' \rangle$

$\implies \exists es' ta' s'. False, P, t \vdash 1 \langle es, s \rangle \text{-ta}' \rightarrow \langle es', s' \rangle \wedge$

$collect\text{-locks} \llbracket ta' \rrbracket_l \subseteq collect\text{-locks} \llbracket ta \rrbracket_l \wedge set \llbracket ta' \rrbracket_c \subseteq set \llbracket ta \rrbracket_c \wedge collect\text{-interrupts} \llbracket ta' \rrbracket_i \subseteq collect\text{-interrupts} \llbracket ta \rrbracket_i \wedge$

$(\exists s. Red1\text{-mthr.actions-ok}\ s\ t\ ta')$

proof ($induct\ rule: red1\text{-reds1.inducts}$)

case $Red1InstanceOf\ \mathbf{thus}\ ?case$

using $[[hyps\text{-subst-thin} = true]]$

by ($auto\ intro!: exI\ red1\text{-reds1.Red1InstanceOf}\ simp\ del: split\text{-paired-Ex}$) ($(subst\ fst\text{-conv}\ snd\text{-conv}\ wset\text{-def}) +, simp$)

next

case $Red1CallExternal\ \mathbf{thus}\ ?case$

by ($fastforce\ simp\ del: split\text{-paired-Ex}\ dest: red\text{-external-ta-satisfiable}$ **where** $final = final\text{-expr1} :: ('addr\ expr1 \times 'addr\ val\ list) \times ('addr\ expr1 \times 'addr\ val\ list)\ list \implies bool$) $intro: red1\text{-reds1.Red1CallExternal}$)

```

next
  case Lock1Synchronized thus ?case
    by(auto intro!: exI exI[where x=(K$ None, (Map.empty, undefined), Map.empty, {})] red1-reds1.Lock1Synchronized
simp del: split-paired-Ex simp add: lock-ok-las-def finfun-upd-apply may-lock.intros(1))
next
  case (Synchronized1Red2 e s ta e' s' V a)
  then obtain e' ta' s'
    where False,P,t ⊢1 ⟨e,s⟩ -ta'→ ⟨e',s'⟩
    and L: collect-locks {ta'}l ⊆ collect-locks {ta}l ∧ set {ta'}c ⊆ set {ta}c ∧ collect-interrupts {ta'}i
    ⊆ collect-interrupts {ta}i
    and aok: ∃ s. Red1-mthr.actions-ok s t ta'
    by blast
  from ⟨False,P,t ⊢1 ⟨e,s⟩ -ta'→ ⟨e',s'⟩⟩ have False,P,t ⊢1 ⟨insyncV (a) e, s⟩ -ta'→ ⟨insyncV (a)
e', s'⟩
    by(rule red1-reds1.Synchronized1Red2)
  thus ?case using L aok by blast
next
  case Unlock1Synchronized thus ?case
    by(auto simp del: split-paired-Ex intro!: exI exI[where x=(K$ [(t, 0)], (Map.empty, undefined),
Map.empty, {})] red1-reds1.Unlock1Synchronized simp add: lock-ok-las-def finfun-upd-apply)
next
  case Unlock1SynchronizedFail thus ?case
    by(auto simp del: split-paired-Ex intro!: exI exI[where x=(K$ [(t, 0)], (Map.empty, unde-
fined), Map.empty, {})] red1-reds1.Unlock1Synchronized simp add: lock-ok-las-def finfun-upd-apply col-
lect-locks-def split: if-split-asm)
next
  case Synchronized1Throw2 thus ?case
    by(auto simp del: split-paired-Ex intro!: exI exI[where x=(K$ [(t, 0)], (Map.empty, unde-
fined), Map.empty, {})] red1-reds1.Synchronized1Throw2 simp add: lock-ok-las-def finfun-upd-apply)
next
  case Synchronized1Throw2Fail thus ?case
    by(auto simp del: split-paired-Ex intro!: exI exI[where x=(K$ [(t, 0)], (Map.empty, unde-
fined), Map.empty, {})] red1-reds1.Synchronized1Throw2 simp add: lock-ok-las-def finfun-upd-apply
collect-locks-def split: if-split-asm)
qed(fastforce intro: red1-reds1.intros)+

lemma IUF-Red1-taD:
  assumes True,P,t ⊢1 ⟨ex/exs, h⟩ -ta→ ⟨ex'/exs', h'⟩
  shows ∃ ex' exs' h' ta'. False,P,t ⊢1 ⟨ex/exs, h⟩ -ta'→ ⟨ex'/exs', h'⟩ ∧
    collect-locks {ta'}l ⊆ collect-locks {ta}l ∧ set {ta'}c ⊆ set {ta}c ∧ collect-interrupts {ta'}i ⊆
collect-interrupts {ta}i ∧
    (∃ s. Red1-mthr.actions-ok s t ta')
using assms
apply(cases)
apply(safe dest!: IUF-red-taD)
  apply(simp del: split-paired-Ex)
  apply(rule exI conjI)+
  apply(erule red1Red)
  apply simp
  apply blast
  apply(rule exI conjI red1Call)+
  apply(auto simp add: lock-ok-las-def)
  apply(rule exI conjI red1Return)+
  apply auto

```

done

lemma *mred1'-mred1-must-sync-eq*:

Red1-mthr.must-sync False P t x (shr s) = Red1-mthr.must-sync True P t x (shr s)

proof

assume *Red1-mthr.must-sync False P t x (shr s)*

thus *Red1-mthr.must-sync True P t x (shr s)*

by(rule *Red1-mthr.must-syncE*)(rule *Red1-mthr.must-syncI*, auto simp add: *split-def simp del: split-paired-Ex* intro: *Red1-False-into-Red1-True*)

next

assume *Red1-mthr.must-sync True P t x (shr s)*

thus *Red1-mthr.must-sync False P t x (shr s)*

apply(rule *Red1-mthr.must-syncE*)

apply(rule *Red1-mthr.must-syncI*)

apply(cases *x*)

apply(auto simp add: *split-beta split-paired-Ex*)

apply(drule *IUF-Red1-taD*)

apply simp

apply blast

done

qed

lemma *Red1-Red1'-deadlock-inv*:

Red1-mthr.deadlock True P s = Red1-mthr.deadlock False P s

proof(rule *iffI*)

assume *dead: Red1-mthr.deadlock True P s*

show *Red1-mthr.deadlock False P s*

proof(rule *multithreaded-base.deadlockI*)

fix *t x*

assume *tst: thr s t = [(x, no-wait-locks)]*

and *nfin: ¬ final-expr1 x*

and *wst: wset s t = None*

with *dead* obtain *ms: Red1-mthr.must-sync True P t x (shr s)*

and *cs [rule-format]: ∀ LT. Red1-mthr.can-sync True P t x (shr s) LT →*
(∃ lt ∈ LT. final-thread.must-wait final-expr1 s t lt (dom (thr s)))

by(rule *Red1-mthr.deadlockD1*)

from *ms*[folded *mred1'-mred1-must-sync-eq*]

show *Red1-mthr.must-sync False P t x (shr s) ∧*

(∀ LT. Red1-mthr.can-sync False P t x (shr s) LT →

(∃ lt ∈ LT. final-thread.must-wait final-expr1 s t lt (dom (thr s))))

proof

show *∀ LT. Red1-mthr.can-sync False P t x (shr s) LT →*

(∃ lt ∈ LT. final-thread.must-wait final-expr1 s t lt (dom (thr s)))

proof(intro *strip*)

fix *LT*

assume *Red1-mthr.can-sync False P t x (shr s) LT*

then obtain *ta x' m' where mred1' P t (x, shr s) ta (x', m')*

and [*simp*]: *LT = collect-locks {ta}_l <+> collect-cond-actions {ta}_c <+> collect-interrupts {ta}_i*

by(rule *Red1-mthr.can-syncE*)

hence *mred1 P t (x, shr s) ta (x', m')* by(auto simp add: *split-beta* intro: *Red1-False-into-Red1-True*)

hence *Red1-mthr.can-sync True P t x (shr s) LT* by(rule *Red1-mthr.can-syncI*) *simp*

thus *∃ lt ∈ LT. final-thread.must-wait final-expr1 s t lt (dom (thr s))* by(rule *cs*)

qed


```

qed
next
fix t x ln l
assume thr s t = [(x, ln)] 0 < ln $ l  $\neg$  waiting (wset s t)
thus  $\exists l t'. 0 < ln $ l \wedge t \neq t' \wedge thr s t' \neq None \wedge has-lock ((locks s) $ l) t'$ 
  by(rule Red1-mthr.deadlockD2[OF dead]) blast
next
fix t x w
assume thr s t = [(x, no-wait-locks)]
thus wset s t  $\neq$  [PostWS w]
  by(rule Red1-mthr.deadlockD3[OF dead, rule-format])
qed
next
assume dead: Red1-mthr.deadlock False P s
show Red1-mthr.deadlock True P s
proof(rule Red1-mthr.deadlockI)
  fix t x
  assume tst: thr s t = [(x, no-wait-locks)]
  and nfin:  $\neg$  final-expr1 x
  and wst: wset s t = None
  with dead obtain ms: Red1-mthr.must-sync False P t x (shr s)
  and cs [rule-format]:  $\forall LT. Red1-mthr.can-sync False P t x (shr s) LT \longrightarrow$ 
    ( $\exists lt \in LT. final-thread.must-wait final-expr1 s t lt (dom (thr s))$ )
  by(rule Red1-mthr.deadlockD1)
  from ms[unfolded mred1'-mred1-must-sync-eq]
  show Red1-mthr.must-sync True P t x (shr s)  $\wedge$ 
    ( $\forall LT. Red1-mthr.can-sync True P t x (shr s) LT \longrightarrow$ 
      ( $\exists lt \in LT. final-thread.must-wait final-expr1 s t lt (dom (thr s))$ ))
  proof
  show  $\forall LT. Red1-mthr.can-sync True P t x (shr s) LT \longrightarrow$ 
    ( $\exists lt \in LT. final-thread.must-wait final-expr1 s t lt (dom (thr s))$ )
  proof(intro strip)
  fix LT
  assume Red1-mthr.can-sync True P t x (shr s) LT
  then obtain ta x' m' where mred1 P t (x, shr s) ta (x', m')
  and [simp]:  $LT = collect-locks \{ta\}_l <+> collect-cond-actions \{ta\}_c <+> collect-interrupts$ 
 $\{ta\}_i$ 
  by(rule Red1-mthr.can-syncE)
  then obtain e xs exs e' xs' exs' where x [simp]:  $x = ((e, xs), exs) x' = ((e', xs'), exs')$ 
  and red:  $True, P, t \vdash 1 \langle (e, xs)/exs, shr s \rangle -ta \rightarrow \langle (e', xs')/exs', m' \rangle$  by(cases x, cases x')
fastforce
  from IUF-Red1-taD[OF red] obtain ex'' exs'' h'' ta'
  where red':  $False, P, t \vdash 1 \langle (e, xs)/exs, shr s \rangle -ta' \rightarrow \langle ex''/exs'', h'' \rangle$ 
  and  $collect-locks \{ta'\}_l <+> collect-cond-actions \{ta'\}_c <+> collect-interrupts \{ta'\}_i \subseteq$ 
 $collect-locks \{ta\}_l <+> collect-cond-actions \{ta\}_c <+> collect-interrupts \{ta\}_i$ 
  by auto blast
  then obtain LT' where cs': Red1-mthr.can-sync False P t x (shr s) LT'
  and LT':  $LT' \subseteq LT$  by(cases ex'')(fastforce intro!: Red1-mthr.can-syncI)
  with cs[of LT'] show  $\exists lt \in LT. final-thread.must-wait final-expr1 s t lt (dom (thr s))$  by auto
qed
qed
next
fix t x ln l
assume thr s t = [(x, ln)] 0 < ln $ l  $\neg$  waiting (wset s t)

```

```

thus  $\exists l t'. 0 < ln \$ l \wedge t \neq t' \wedge thr s t' \neq None \wedge has-lock ((locks s) \$ l) t'$ 
  by(rule Red1-mthr.deadlockD2[OF dead]) blast
next
  fix  $t x w$ 
  assume  $thr s t = [(x, no-wait-locks)]$ 
  thus  $wset s t \neq [PostWS w]$ 
  by(rule Red1-mthr.deadlockD3[OF dead, rule-format])
qed
qed
end
end

```

7.8 Program Compilation

theory *PCompiler*

imports

../Common/WellForm

../Common/BinOp

../Common/Conform

begin

definition *compM* :: $(mname \Rightarrow ty\ list \Rightarrow ty \Rightarrow 'a \Rightarrow 'b) \Rightarrow 'a\ mdecl' \Rightarrow 'b\ mdecl'$

where $compM\ f \equiv \lambda(M, Ts, T, m). (M, Ts, T, map-option\ (f\ M\ Ts\ T)\ m)$

definition *compC* :: $(cname \Rightarrow mname \Rightarrow ty\ list \Rightarrow ty \Rightarrow 'a \Rightarrow 'b) \Rightarrow 'a\ cdecl \Rightarrow 'b\ cdecl$

where $compC\ f \equiv \lambda(C, D, Fdecls, Mdecls). (C, D, Fdecls, map\ (compM\ (f\ C))\ Mdecls)$

primrec *compP* :: $(cname \Rightarrow mname \Rightarrow ty\ list \Rightarrow ty \Rightarrow 'a \Rightarrow 'b) \Rightarrow 'a\ prog \Rightarrow 'b\ prog$

where $compP\ f\ (Program\ P) = Program\ (map\ (compC\ f)\ P)$

Compilation preserves the program structure. Therefore lookup functions either commute with compilation (like method lookup) or are preserved by it (like the subclass relation).

lemma *map-of-map4*:

$map-of\ (map\ (\lambda(x, a, b, c). (x, a, b, f\ x\ a\ b\ c))\ ts) =$

$(\lambda x. map-option\ (\lambda(a, b, c). (a, b, f\ x\ a\ b\ c))\ (map-of\ ts\ x))$

apply(*induct* *ts*)

apply *simp*

apply(*rule* *ext*)

apply *fastforce*

done

lemma *class-compP*:

$class\ P\ C = Some\ (D, fs, ms)$

$\implies class\ (compP\ f\ P)\ C = Some\ (D, fs, map\ (compM\ (f\ C))\ ms)$

by(*cases* *P*)(*simp* *add: class-def compP-def compC-def map-of-map4*)

lemma *class-compPD*:

$class\ (compP\ f\ P)\ C = Some\ (D, fs, cms)$

$\implies \exists ms. class\ P\ C = Some(D, fs, ms) \wedge cms = map\ (compM\ (f\ C))\ ms$

by(*cases* *P*)(*clarsimp* *simp* *add: class-def compP-def compC-def map-of-map4*)

lemma [simp]: $is-class (compP f P) C = is-class P C$

lemma [simp]: $class (compP f P) C = map-option (\lambda c. snd(compC f (C,c))) (class P C)$

lemma sees-methods-compP:

$P \vdash C \text{ sees-methods } Mm \implies$
 $compP f P \vdash C \text{ sees-methods } (\lambda M. map-option (\lambda((Ts,T,m),D). ((Ts,T,map-option (f D M Ts T) m),D)) (Mm M))$

lemma sees-method-compP:

$P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D \implies$
 $compP f P \vdash C \text{ sees } M: Ts \rightarrow T = map-option (f D M Ts T) m \text{ in } D$

lemma [simp]:

$P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D \implies$
 $method (compP f P) C M = (D, Ts, T, map-option (f D M Ts T) m)$

lemma sees-methods-compPD:

$\llbracket cP \vdash C \text{ sees-methods } Mm'; cP = compP f P \rrbracket \implies$
 $\exists Mm. P \vdash C \text{ sees-methods } Mm \wedge$
 $Mm' = (\lambda M. map-option (\lambda((Ts,T,m),D). ((Ts,T,map-option (f D M Ts T) m),D)) (Mm M))$

lemma sees-method-compPD:

$compP f P \vdash C \text{ sees } M: Ts \rightarrow T = fm \text{ in } D \implies$
 $\exists m. P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D \wedge map-option (f D M Ts T) m = fm$

lemma sees-method-native-compP [simp]:

$compP f P \vdash C \text{ sees } M: Ts \rightarrow T = Native \text{ in } D \iff P \vdash C \text{ sees } M: Ts \rightarrow T = Native \text{ in } D$
by(auto dest: sees-method-compPD sees-method-compP)

lemma [simp]: $subcls1 (compP f P) = subcls1 P$

by(fastforce simp add: is-class-def compC-def intro:subcls1I order-antisym dest:subcls1D)

lemma [simp]: $is-type (compP f P) T = is-type P T$

by(induct T)(auto cong: ty.case-cong)

lemma is-type-compP [simp]: $is-type (compP f P) = is-type P$

by auto

lemma compP-widen[simp]:

$(compP f P \vdash T \leq T') = (P \vdash T \leq T')$
by(induct T' arbitrary: T)(simp-all add: widen-Class widen-Array)

lemma [simp]: $(compP f P \vdash Ts [\leq] Ts') = (P \vdash Ts [\leq] Ts')$

lemma is-lub-compP [simp]:

$is-lub (compP f P) = is-lub P$
by(auto intro!: ext elim!: is-lub.cases intro: is-lub.intros)

lemma [simp]:

fixes $f :: cname \Rightarrow mname \Rightarrow ty \text{ list} \Rightarrow ty \Rightarrow 'a \Rightarrow 'b$
shows $(compP f P \vdash C \text{ has-fields } FDTs) = (P \vdash C \text{ has-fields } FDTs)$

lemma [simp]: $fields (compP f P) C = fields P C$

lemma [simp]: $(compP f P \vdash C \text{ sees } F:T (fm) \text{ in } D) = (P \vdash C \text{ sees } F:T (fm) \text{ in } D)$

lemma [simp]: $\text{field } (\text{compP } f P) F D = \text{field } P F D$

7.8.1 Invariance of *wf-prog* under compilation

lemma [iff]: $\text{distinct-fst } (\text{classes } (\text{compP } f P)) = \text{distinct-fst } (\text{classes } P)$

lemma [iff]: $\text{distinct-fst } (\text{map } (\text{compM } f) ms) = \text{distinct-fst } ms$

lemma [iff]: $\text{wf-syscls } (\text{compP } f P) = \text{wf-syscls } P$
unfolding *wf-syscls-def* **by** *auto*

lemma [iff]: $\text{wf-fdecl } (\text{compP } f P) = \text{wf-fdecl } P$

lemma *set-compP*:
 $(\text{class } (\text{compP } f P) C = \lfloor (D, fs, ms') \rfloor) \longleftrightarrow$
 $(\exists ms. \text{class } P C = \lfloor (D, fs, ms) \rfloor \wedge ms' = \text{map } (\text{compM } (f C)) ms)$
by(*cases P*)(*auto simp add: compC-def image-iff map-of-map4*)

lemma *compP-has-method*: $\text{compP } f P \vdash C \text{ has } M \longleftrightarrow P \vdash C \text{ has } M$
unfolding *has-method-def*
by(*fastforce dest: sees-method-compPD intro: sees-method-compP*)

lemma *is-native-compP* [simp]: $\text{is-native } (\text{compP } f P) = \text{is-native } P$
by(*auto simp add: fun-eq-iff is-native.simps*)

lemma $\tau\text{external-compP}$ [simp]:
 $\tau\text{external } (\text{compP } f P) = \tau\text{external } P$
by(*auto intro!: ext simp add: $\tau\text{external-def}$*)

context *heap-base* **begin**

lemma *heap-clone-compP* [simp]:
 $\text{heap-clone } (\text{compP } f P) = \text{heap-clone } P$
by(*intro ext*)(*auto elim!: heap-clone.cases intro: heap-clone.intros*)

lemma *red-external-compP* [simp]:
 $\text{compP } f P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow \text{ext } \langle va, h' \rangle \longleftrightarrow P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow \text{ext } \langle va, h' \rangle$
by(*auto elim!: red-external.cases intro: red-external.intros*)

lemma $\tau\text{external}'\text{-compP}$ [simp]:
 $\tau\text{external}' (\text{compP } f P) = \tau\text{external}' P$
by(*simp add: $\tau\text{external}'\text{-def}$ [abs-def]*)

end

lemma *wf-overriding-compP* [simp]: $\text{wf-overriding } (\text{compP } f P) D (\text{compM } (f C) m) = \text{wf-overriding } P D m$
by(*cases m*)(*fastforce intro: sees-method-compP[where f=f] dest: sees-method-compPD[where f=f] simp add: compM-def*)

lemma *wf-cdecl-compPI*:
assumes *wf1-imp-wf2*:
 $\bigwedge C M Ts T m. \llbracket \text{wf-mdecl } wf_1 P C (M, Ts, T, [m]); P \vdash C \text{ sees } M:Ts \rightarrow T = [m] \text{ in } C \rrbracket$

```

    ⇒ wf-mdecl wf2 (compP f P) C (M, Ts, T, [f C M Ts T m])
  and wfcP1: ∀ C rest. class P C = [rest] → wf-cdecl wf1 P (C, rest)
  and xcomp: class (compP f P) C = [rest']
  and wf: wf-prog p P
  shows wf-cdecl wf2 (compP f P) (C, rest')
proof -
  obtain D fs ms' where x: rest' = (D, fs, ms') by(cases rest')
  with xcomp obtain ms where xsrc: class P C = [(D,fs,ms)]
    and ms': ms' = map (compM (f C)) ms
    by(auto simp add: set-compP compC-def)
  from xsrc wfcP1 have wf1: wf-cdecl wf1 P (C,D,fs,ms) by blast
  { fix field
    assume field ∈ set fs
    with wf1 have wf-fdecl (compP f P) field by(simp add: wf-cdecl-def)
  }
  moreover from wf1 have distinct-fst fs by(simp add: wf-cdecl-def)
  moreover
  { fix m
    assume mset': m ∈ set ms'
    obtain M Ts' T' body' where m: m = (M, Ts', T', body') by(cases m)
    with ms' obtain body where mf: body' = map-option (f C M Ts' T') body
      and mset: (M, Ts', T', body) ∈ set ms using mset'
      by(clarsimp simp add: image-iff compM-def)
    moreover from mset xsrc wfcP1 have wf-mdecl wf1 P C (M,Ts',T',body)
      by(fastforce simp add: wf-cdecl-def)
    moreover from wf xsrc mset x have P ⊢ C sees M:Ts'→T' = body in C
      by(auto intro: mdecl-visible)
    ultimately have wf-mdecl wf2 (compP f P) C m using m
      by(cases body)(simp add: wf-mdecl-def, auto intro: wf1-imp-wf2) }
  moreover from wf1 have distinct-fst ms by(simp add: wf-cdecl-def)
  with ms' have distinct-fst ms' by(auto)
  moreover
  { assume CObj: C ≠ Object
    with xsrc wfcP1
    have part1: is-class (compP f P) D ⊢ compP f P ⊢ D ≼* C
      by(auto simp add: wf-cdecl-def)
    { fix m
      assume mset': m ∈ set ms'
      obtain M Ts T body' where m: m = (M, Ts, T, body') by(cases m)
      with mset' ms' obtain body where mf: body' = map-option (f C M Ts T) body
        and mset: (M, Ts, T, body) ∈ set ms
        by(clarsimp simp add: image-iff compM-def)
      from wf1 CObj mset
      have wf-overriding P D (M, Ts, T, body) by(auto simp add: wf-cdecl-def simp del: wf-overriding.simps)
      hence wf-overriding (compP f P) D m unfolding m mf
        by(subst (asm) wf-overriding-compP[symmetric, where f=f and C=C])(simp del: wf-overriding.simps
wf-overriding-compP add: compM-def) }
      note this part1 }
    moreover
    { assume C = Thread
      with wf1 ms' have ∃ m. (run, [], Void, m) ∈ set ms'
        by(fastforce simp add: wf-cdecl-def image-iff compM-def)+ }
    ultimately show ?thesis unfolding x wf-cdecl-def by blast
  }
qed

```

lemma *wf-prog-compPI*:

assumes *lift*:

$\bigwedge C M Ts T m.$

$\llbracket P \vdash C \text{ sees } M:Ts \rightarrow T = \lfloor m \rfloor \text{ in } C; \text{ wf-mdecl } wf_1 P C (M, Ts, T, \lfloor m \rfloor) \rrbracket$

$\implies \text{ wf-mdecl } wf_2 (\text{comp}P f P) C (M, Ts, T, \lfloor f C M Ts T m \rfloor)$

and *wf*: *wf-prog* $wf_1 P$

shows *wf-prog* $wf_2 (\text{comp}P f P)$

using *wf*

apply (*clarsimp simp add:wf-prog-def2*)

apply(*rule wf-cdecl-compPI[OF lift], assumption+*)

apply(*auto intro: wf*)

done

lemma *wf-cdecl-compPD*:

assumes *wf1-imp-wf2*:

$\bigwedge C M Ts T m. \llbracket \text{ wf-mdecl } wf_1 (\text{comp}P f P) C (M, Ts, T, \lfloor f C M Ts T m \rfloor); \text{comp}P f P \vdash C \text{ sees } M:Ts \rightarrow T = \lfloor f C M Ts T m \rfloor \text{ in } C \rrbracket$

$\implies \text{ wf-mdecl } wf_2 P C (M, Ts, T, \lfloor m \rfloor)$

and *wfcP1*: $\forall C \text{ rest. class } (\text{comp}P f P) C = \lfloor \text{rest} \rfloor \longrightarrow \text{ wf-cdecl } wf_1 (\text{comp}P f P) (C, \text{rest})$

and *xcomp*: $\text{class } P C = \lfloor \text{rest} \rfloor$

and *wf*: *wf-prog* $wf\text{-md } (\text{comp}P f P)$

shows *wf-cdecl* $wf_2 P (C, \text{rest})$

proof –

obtain *D fs ms'* **where** $x: \text{rest} = (D, fs, ms')$ **by**(*cases rest*)

with *xcomp* **have** *xsrc*: $\text{class } (\text{comp}P f P) C = \lfloor (D, fs, \text{map } (\text{comp}M (f C)) ms') \rfloor$

by(*auto simp add: set-compP compC-def*)

from *xsrc wfcP1* **have** *wf1*: $\text{ wf-cdecl } wf_1 (\text{comp}P f P) (C, D, fs, \text{map } (\text{comp}M (f C)) ms')$ **by** *blast*

{ **fix** *field*

assume $\text{field} \in \text{set } fs$

with *wf1* **have** *wf-fdecl* $P \text{ field}$ **by**(*simp add: wf-cdecl-def*)

}

moreover from *wf1* **have** *distinct-fst fs* **by**(*simp add: wf-cdecl-def*)

moreover

{ **fix** *m*

assume $mset': m \in \text{set } ms'$

obtain $M Ts' T' \text{ body}'$ **where** $m: m = (M, Ts', T', \text{body}')$ **by**(*cases m*)

hence *mset*: $(M, Ts', T', \text{map-option } (f C M Ts' T') \text{ body}') \in \text{set } (\text{map } (\text{comp}M (f C)) ms')$ **using**

mset'

by(*auto simp add: image-iff compM-def intro: rev-bexI*)

moreover from *wf xsrc mset x* **have** $\text{comp}P f P \vdash C \text{ sees } M:Ts' \rightarrow T' = \text{map-option } (f C M Ts' T') \text{ body}' \text{ in } C$

by(*auto intro: mdecl-visible*)

moreover from *mset wfcP1*[*rule-format, OF xsrc*]

have $\text{ wf-mdecl } wf_1 (\text{comp}P f P) C (M, Ts', T', \text{map-option } (f C M Ts' T') \text{ body}')$

by(*auto simp add: wf-cdecl-def*)

ultimately have $\text{ wf-mdecl } wf_2 P C m$ **using** *m*

by(*cases body'*)(*simp add: wf-mdecl-def, auto intro: wf1-imp-wf2*) }

moreover from *wf1* **have** *distinct-fst ms'* **by**(*simp add: wf-cdecl-def*)

moreover

{ **assume** *CObj*: $C \neq \text{Object}$

with *xsrc wfcP1*

have *part1*: $\text{is-class } P D \neg P \vdash D \preceq^* C$

by(*auto simp add: wf-cdecl-def*)

```

{ fix m
  assume mset': m ∈ set ms'
  with wf1 CObj have wf-overriding (compP f P) D (compM (f C) m)
    by(simp add: wf-cdecl-def del: wf-overriding-compP)
  hence wf-overriding P D m by simp }
note this part1 }
moreover
{ assume C = Thread
  with wf1 have ∃ m. (run, [], Void, m) ∈ set ms'
  by(fastforce simp add: wf-cdecl-def image-iff compM-def)+ }
ultimately show ?thesis unfolding x wf-cdecl-def by blast
qed

```

lemma *wf-prog-compPD*:

assumes *wf*: *wf-prog wf1 (compP f P)*

and *lift*:

```

∧ C M Ts T m.
  [| compP f P ⊢ C sees M:Ts→T = [f C M Ts T m] in C; wf-mdecl wf1 (compP f P) C (M,Ts,T,
  [f C M Ts T m]) |]
  ⇒ wf-mdecl wf2 P C (M,Ts,T,[m])

```

shows *wf-prog wf2 P*

using *wf*

apply(*clarsimp simp add:wf-prog-def2*)

apply(*rule wf-cdecl-compPD[OF lift], assumption+*)

apply(*auto intro: wf*)

done

lemma *WT-binop-compP [simp]*: *compP f P ⊢ T1 «bop» T2 :: T ⟷ P ⊢ T1 «bop» T2 :: T*

by(*cases bop*)(*fastforce*)**+**

lemma *WTrt-binop-compP [simp]*: *compP f P ⊢ T1 «bop» T2 : T ⟷ P ⊢ T1 «bop» T2 : T*

by(*cases bop*)(*fastforce*)**+**

lemma *binop-relevant-class-compP [simp]*: *binop-relevant-class bop (compP f P) = binop-relevant-class bop P*

by(*cases bop*) *simp-all*

lemma *is-class-compP [simp]*:

is-class (compP f P) = is-class P

by(*simp add: is-class-def fun-eq-iff*)

lemma *has-field-compP [simp]*:

compP f P ⊢ C has F:T (fm) in D ⟷ P ⊢ C has F:T (fm) in D

by(*auto simp add: has-field-def*)

context *heap-base* **begin**

lemma *compP-addr-loc-type [simp]*:

addr-loc-type (compP f P) = addr-loc-type P

by(*auto elim!: addr-loc-type.cases intro: addr-loc-type.intros intro!: ext*)

lemma *conf-compP [simp]*:

compP f P, h ⊢ v :≤ T ⟷ P, h ⊢ v :≤ T

by(*simp add: conf-def*)

lemma *compP-conf*: $\text{conf } (\text{compP } f \ P) = \text{conf } P$
by(*auto simp add: conf-def intro!: ext*)

lemma *compP-confs*: $\text{compP } f \ P, h \vdash \text{vs } [:\leq] \ Ts \longleftrightarrow P, h \vdash \text{vs } [:\leq] \ Ts$
by(*simp add: compP-conf*)

lemma *tconf-compP* [*simp*]: $\text{compP } f \ P, h \vdash t \ \sqrt{t} \longleftrightarrow P, h \vdash t \ \sqrt{t}$
by(*auto simp add: tconf-def*)

lemma *wf-start-state-compP* [*simp*]:
 $\text{wf-start-state } (\text{compP } f \ P) = \text{wf-start-state } P$
by(*auto 4 6 simp add: fun-eq-iff wf-start-state.simps compP-conf dest: sees-method-compP[where f=f] sees-method-compPD[where f=f]*)

end

lemma *compP-addr-conv*:
 $\text{addr-conv } \text{addr2thead-id } \text{thread-id2addr } \text{typeof-addr } (\text{compP } f \ P) = \text{addr-conv } \text{addr2thead-id } \text{thread-id2addr } \text{typeof-addr } P$
unfolding *addr-conv-def*
by *simp*

lemma *compP-heap*:
 $\text{heap } \text{addr2thead-id } \text{thread-id2addr } \text{allocate } \text{typeof-addr } \text{heap-write } (\text{compP } f \ P) =$
 $\text{heap } \text{addr2thead-id } \text{thread-id2addr } \text{allocate } \text{typeof-addr } \text{heap-write } P$
unfolding *heap-def compP-addr-conv heap-axioms-def*
by *auto*

lemma *compP-heap-conf*:
 $\text{heap-conf } \text{addr2thead-id } \text{thread-id2addr } \text{empty-heap } \text{allocate } \text{typeof-addr } \text{heap-write } \text{hconf } (\text{compP } f \ P) =$
 $\text{heap-conf } \text{addr2thead-id } \text{thread-id2addr } \text{empty-heap } \text{allocate } \text{typeof-addr } \text{heap-write } \text{hconf } P$
unfolding *heap-conf-def heap-conf-axioms-def compP-heap*
unfolding *heap-base.compP-conf heap-base.compP-addr-loc-type is-type-compP is-class-compP*
by(*rule refl*)

lemma *compP-heap-conf-read*:
 $\text{heap-conf-read } \text{addr2thead-id } \text{thread-id2addr } \text{empty-heap } \text{allocate } \text{typeof-addr } \text{heap-read } \text{heap-write } \text{hconf } (\text{compP } f \ P) =$
 $\text{heap-conf-read } \text{addr2thead-id } \text{thread-id2addr } \text{empty-heap } \text{allocate } \text{typeof-addr } \text{heap-read } \text{heap-write } \text{hconf } P$
unfolding *heap-conf-read-def heap-conf-read-axioms-def*
unfolding *compP-heap-conf heap-base.compP-conf heap-base.compP-addr-loc-type*
by(*rule refl*)

compiler composition

lemma *compM-compM*:
 $\text{compM } f \ (\text{compM } g \ md) = \text{compM } (\lambda M \ Ts \ T. f \ M \ Ts \ T \circ g \ M \ Ts \ T) \ md$
by(*cases md*)(*simp add: compM-def option.map-comp o-def*)

lemma *compC-compC*:
 $\text{compC } f \ (\text{compC } g \ cd) = \text{compC } (\lambda C \ M \ Ts \ T. f \ C \ M \ Ts \ T \circ g \ C \ M \ Ts \ T) \ cd$
by(*simp add: compC-def split-beta compM-compM*)

lemma *compP-compP*:

$compP f (compP g P) = compP (\lambda C M Ts T. f C M Ts T \circ g C M Ts T) P$
by(cases P)(simp add: compC-compC)

end

7.9 Compilation Stage 2

theory *Compiler2*

imports *PCompiler J1State ../JVM/JVMInstructions*

begin

primrec *compE2* :: 'addr expr1 \Rightarrow 'addr instr list

and *compEs2* :: 'addr expr1 list \Rightarrow 'addr instr list

where

$compE2 (new C) = [New C]$
 $compE2 (newA T[e]) = compE2 e @ [NewArray T]$
 $compE2 (Cast T e) = compE2 e @ [Checkcast T]$
 $compE2 (e instanceof T) = compE2 e @ [Instanceof T]$
 $compE2 (Val v) = [Push v]$
 $compE2 (e1 \llcorner bop \llcorner e2) = compE2 e1 @ compE2 e2 @ [BinOpInstr bop]$
 $compE2 (Var i) = [Load i]$
 $compE2 (i:=e) = compE2 e @ [Store i, Push Unit]$
 $compE2 (a[i]) = compE2 a @ compE2 i @ [ALoad]$
 $compE2 (a[i] := e) = compE2 a @ compE2 i @ compE2 e @ [AStore, Push Unit]$
 $compE2 (a.length) = compE2 a @ [ALength]$
 $compE2 (e.F\{D\}) = compE2 e @ [Getfield F D]$
 $compE2 (e1.F\{D\} := e2) = compE2 e1 @ compE2 e2 @ [Putfield F D, Push Unit]$
 $compE2 (e.compareAndSwap(D.F, e', e'')) = compE2 e @ compE2 e' @ compE2 e'' @ [CAS F D]$
 $compE2 (e.M(es)) = compE2 e @ compEs2 es @ [Invoke M (size es)]$
 $compE2 (\{i:T=vo; e\}) = (case vo of None \Rightarrow [] \mid [v] \Rightarrow [Push v, Store i]) @ compE2 e$
 $compE2 (sync_V(o') e) = compE2 o' @ [Dup, Store V, MEnter] @$
 $compE2 e @ [Load V, MExit, Goto 4, Load V, MExit, ThrowExc]$
 $compE2 (insync_V(a) e) = [Goto 1] \text{ --- Define insync sensibly}$
 $compE2 (e1;;e2) = compE2 e1 @ [Pop] @ compE2 e2$
 $compE2 (if (e) e1 else e2) =$
 $(let cnd = compE2 e;$
 $thn = compE2 e1;$
 $els = compE2 e2;$
 $test = IfFalse (int(size thn + 2));$
 $thnex = Goto (int(size els + 1))$
 $in cnd @ [test] @ thn @ [thnex] @ els)$
 $compE2 (while (e) c) =$
 $(let cnd = compE2 e;$
 $bdy = compE2 c;$
 $test = IfFalse (int(size bdy + 3));$
 $loop = Goto (-int(size bdy + size cnd + 2))$
 $in cnd @ [test] @ bdy @ [Pop] @ [loop] @ [Push Unit])$
 $compE2 (throw e) = compE2 e @ [ThrowExc]$
 $compE2 (try e1 catch(C i) e2) =$
 $(let catch = compE2 e2$
 $in compE2 e1 @ [Goto (int(size catch)+2), Store i] @ catch)$

```
| compEs2 [] = []
| compEs2 (e#es) = compE2 e @ compEs2 es
```

Compilation of exception table. Is given start address of code to compute absolute addresses necessary in exception table.

```
fun compxE2 :: 'addr expr1 ⇒ pc ⇒ nat ⇒ ex-table
and compEs2 :: 'addr expr1 list ⇒ pc ⇒ nat ⇒ ex-table
where
  compxE2 (new C) pc d = []
| compxE2 (newA T[e]) pc d = compxE2 e pc d
| compxE2 (Cast T e) pc d = compxE2 e pc d
| compxE2 (e instanceof T) pc d = compxE2 e pc d
| compxE2 (Val v) pc d = []
| compxE2 (e1 «bop» e2) pc d =
  compxE2 e1 pc d @ compxE2 e2 (pc + size(compE2 e1)) (d+1)
| compxE2 (Var i) pc d = []
| compxE2 (i:=e) pc d = compxE2 e pc d
| compxE2 (a[i]) pc d = compxE2 a pc d @ compxE2 i (pc + size (compE2 a)) (d + 1)
| compxE2 (a[i] := e) pc d =
  (let pc1 = pc + size (compE2 a);
   pc2 = pc1 + size (compE2 i)
   in compxE2 a pc d @ compxE2 i pc1 (d + 1) @ compxE2 e pc2 (d + 2))
| compxE2 (a.length) pc d = compxE2 a pc d
| compxE2 (e.F{D}) pc d = compxE2 e pc d
| compxE2 (e1.F{D} := e2) pc d = compxE2 e1 pc d @ compxE2 e2 (pc + size (compE2 e1)) (d + 1)
| compxE2 (e.compareAndSwap(D.F, e', e'')) pc d =
  (let pc1 = pc + size (compE2 e);
   pc2 = pc1 + size (compE2 e')
   in compxE2 e pc d @ compxE2 e' pc1 (d + 1) @ compxE2 e'' pc2 (d + 2))
| compxE2 (e.M(es)) pc d = compxE2 e pc d @ compEs2 es (pc + size(compE2 e)) (d+1)
| compxE2 ({i:T=vo; e}) pc d = compxE2 e (case vo of None ⇒ pc | [v] ⇒ Suc (Suc pc)) d
| compxE2 (sync_V (o') e) pc d =
  (let pc1 = pc + size (compE2 o') + 3;
   pc2 = pc1 + size(compE2 e)
   in compxE2 o' pc d @ compxE2 e pc1 d @ [(pc1, pc2, None, Suc (Suc (Suc pc2)), d)])
| compxE2 (insync_V (a) e) pc d = []
| compxE2 (e1;;e2) pc d =
  compxE2 e1 pc d @ compxE2 e2 (pc+size(compE2 e1)+1) d
| compxE2 (if (e) e1 else e2) pc d =
  (let pc1 = pc + size(compE2 e) + 1;
   pc2 = pc1 + size(compE2 e1) + 1
   in compxE2 e pc d @ compxE2 e1 pc1 d @ compxE2 e2 pc2 d)
| compxE2 (while (b) e) pc d =
  compxE2 b pc d @ compxE2 e (pc+size(compE2 b)+1) d
| compxE2 (throw e) pc d = compxE2 e pc d
| compxE2 (try e1 catch (C i) e2) pc d =
  (let pc1 = pc + size(compE2 e1)
   in compxE2 e1 pc d @ compxE2 e2 (pc1+2) d @ [(pc,pc1,Some C,pc1+1,d)])

| compEs2 [] pc d = []
| compEs2 (e#es) pc d = compxE2 e pc d @ compEs2 es (pc+size(compE2 e)) (d+1)
```

lemmas *compxE2-compEs2-induct* =
compxE2-compEs2.induct[
unfolded meta-all5-eq-conv meta-all4-eq-conv meta-all3-eq-conv meta-all2-eq-conv meta-all-eq-conv,
case-names
new NewArray Cast InstanceOf Val BinOp Var LAss AAcc AAss ALen FAcc FAss Call Block
Synchronized InSynchronized Seq Cond While throw TryCatch
Nil Cons]

lemma *compE2-neq-Nil* [*simp*]: *compE2 e* ≠ []
by(*induct e*) *auto*

declare *compE2-neq-Nil*[*symmetric, simp*]

lemma *compEs2-append* [*simp*]: *compEs2 (es @ es')* = *compEs2 es @ compEs2 es'*
by(*induct es*) *auto*

lemma *compEs2-eq-Nil-conv* [*simp*]: *compEs2 es* = [] \longleftrightarrow *es* = []
by(*cases es*) *auto*

lemma *compEs2-map-Val*: *compEs2 (map Val vs)* = *map Push vs*
by(*induct vs*) *auto*

lemma *compE2-0th-neq-Invoke* [*simp*]:
compE2 e ! 0 ≠ *Invoke M n*
by(*induct e*)(*auto simp add: nth-append*)

declare *compE2-0th-neq-Invoke*[*symmetric, simp*]

lemma *compEs2-append* [*simp*]:
compEs2 (es @ es') pc d = *compEs2 es pc d @ compEs2 es' (length (compEs2 es) + pc) (length es + d)*
by(*induct es arbitrary: pc d*)(*auto simp add: ac-simps*)

lemma *compEs2-map-Val* [*simp*]: *compEs2 (map Val vs) pc d* = []
by(*induct vs arbitrary: d pc*) *auto*

lemma *compE2-blocks1* [*simp*]:
compE2 (blocks1 n Ts body) = *compE2 body*
by(*induct n Ts body rule: blocks1.induct*)(*auto*)

lemma *compxE2-blocks1* [*simp*]:
compxE2 (blocks1 n Ts body) = *compxE2 body*
by(*induct n Ts body rule: blocks1.induct*)(*auto intro!: ext*)

lemma *fixes e :: 'addr expr1 and es :: 'addr expr1 list*
shows *compE2-not-Return: Return* ∉ *set (compE2 e)*
and *compEs2-not-Return: Return* ∉ *set (compEs2 es)*
by(*induct e and es rule: compE2.induct compEs2.induct*)(*auto*)

primrec *max-stack* :: '*addr expr1* ⇒ *nat*
and *max-stacks* :: '*addr expr1 list* ⇒ *nat*
where
max-stack (new C) = 1
| *max-stack (newA T[e])* = *max-stack e*

$\text{max-stack } (\text{Cast } C \ e) = \text{max-stack } e$
 $\text{max-stack } (e \ \text{instanceof } T) = \text{max-stack } e$
 $\text{max-stack } (\text{Val } v) = 1$
 $\text{max-stack } (e1 \ \ll\text{bop}\ \gg \ e2) = \max (\text{max-stack } e1) (\text{max-stack } e2) + 1$
 $\text{max-stack } (\text{Var } i) = 1$
 $\text{max-stack } (i := e) = \text{max-stack } e$
 $\text{max-stack } (a[i]) = \max (\text{max-stack } a) (\text{max-stack } i + 1)$
 $\text{max-stack } (a[i] := e) = \max (\max (\text{max-stack } a) (\text{max-stack } i + 1)) (\text{max-stack } e + 2)$
 $\text{max-stack } (a.\text{length}) = \text{max-stack } a$
 $\text{max-stack } (e.F\{D\}) = \text{max-stack } e$
 $\text{max-stack } (e1.F\{D\} := e2) = \max (\text{max-stack } e1) (\text{max-stack } e2) + 1$
 $\text{max-stack } (e.\text{compareAndSwap}(D.F, e', e'')) = \max (\max (\text{max-stack } e) (\text{max-stack } e' + 1)) (\text{max-stack } e'' + 2)$
 $\text{max-stack } (e.M(es)) = \max (\text{max-stack } e) (\text{max-stacks } es) + 1$
 $\text{max-stack } (\{i:T=v0; e\}) = \text{max-stack } e$
 $\text{max-stack } (\text{sync}_V(o') \ e) = \max (\text{max-stack } o') (\max (\text{max-stack } e) \ 2)$
 $\text{max-stack } (\text{insync}_V(a) \ e) = 1$
 $\text{max-stack } (e1;;e2) = \max (\text{max-stack } e1) (\text{max-stack } e2)$
 $\text{max-stack } (\text{if } (e) \ e_1 \ \text{else } e_2) =$
 $\quad \max (\text{max-stack } e) (\max (\text{max-stack } e_1) (\text{max-stack } e_2))$
 $\text{max-stack } (\text{while } (e) \ c) = \max (\text{max-stack } e) (\text{max-stack } c)$
 $\text{max-stack } (\text{throw } e) = \text{max-stack } e$
 $\text{max-stack } (\text{try } e1 \ \text{catch}(C \ i) \ e2) = \max (\text{max-stack } e1) (\text{max-stack } e2)$

$\text{max-stacks } [] = 0$
 $\text{max-stacks } (e\#es) = \max (\text{max-stack } e) (1 + \text{max-stacks } es)$

lemma *max-stack1*: $1 \leq \text{max-stack } e$

lemma *max-stacks-ge-length*: $\text{max-stacks } es \geq \text{length } es$

by(*induct es, auto*)

lemma *max-stack-blocks1* [*simp*]:

$\text{max-stack } (\text{blocks1 } n \ Ts \ \text{body}) = \text{max-stack } \text{body}$

by(*induct n Ts body rule: blocks1.induct*) *auto*

definition *compMb2* :: '*addr expr1* \Rightarrow '*addr jvm-method*

where

$\text{compMb2} \equiv \lambda \text{body}.$

$\text{let } \text{ins} = \text{compE2 } \text{body} \ @ \ [\text{Return}];$

$\text{xt} = \text{compxE2 } \text{body} \ 0 \ 0$

in ($\text{max-stack } \text{body}, \text{max-vars } \text{body}, \text{ins}, \text{xt}$)

definition *compP2* :: '*addr J1-prog* \Rightarrow '*addr jvm-prog*

where $\text{compP2} \equiv \text{compP } (\lambda C \ M \ Ts \ T. \text{compMb2})$

lemma *compMb2*:

$\text{compMb2 } e = (\text{max-stack } e, \text{max-vars } e, (\text{compE2 } e \ @ \ [\text{Return}]), \text{compxE2 } e \ 0 \ 0)$

by (*simp add: compMb2-def*)

end

7.10 Various Operations for Exception Tables

theory *Exception-Tables* **imports**

Compiler2

../Common/ExternalCallWF

../JVM/JVMExceptions

begin

definition $pcs :: ex\text{-}table \Rightarrow nat\ set$

where $pcs\ xt \equiv \bigcup (f,t,C,h,d) \in set\ xt. \{f ..< t\}$

lemma *pcs-subset*:

fixes $e :: 'addr\ expr1$ **and** $es :: 'addr\ expr1\ list$

shows $pcs(compxE2\ e\ pc\ d) \subseteq \{pc..<pc+size(compE2\ e)\}$

and $pcs(compxEs2\ es\ pc\ d) \subseteq \{pc..<pc+size(compEs2\ es)\}$

apply(*induct e pc d and es pc d rule: compxE2-compxEs2-induct*)

apply (*simp-all add:pcs-def*)

apply (*fastforce*)+

done

lemma *pcs-Nil* [*simp*]: $pcs\ [] = \{\}$

by(*simp add:pcs-def*)

lemma *pcs-Cons* [*simp*]: $pcs\ (x\#\!xt) = \{fst\ x\ ..< fst(snd\ x)\} \cup pcs\ xt$

by(*auto simp add: pcs-def*)

lemma *pcs-append* [*simp*]: $pcs(xt_1\ @\ xt_2) = pcs\ xt_1 \cup pcs\ xt_2$

by(*simp add:pcs-def*)

lemma [*simp*]: $pc < pc_0 \vee pc_0 + size(compE2\ e) \leq pc \Longrightarrow pc \notin pcs(compxE2\ e\ pc_0\ d)$

using *pcs-subset* **by** *fastforce*

lemma [*simp*]: $pc < pc_0 \vee pc_0 + size(compEs2\ es) \leq pc \Longrightarrow pc \notin pcs(compxEs2\ es\ pc_0\ d)$

using *pcs-subset* **by** *fastforce*

lemma [*simp*]: $pc_1 + size(compE2\ e_1) \leq pc_2 \Longrightarrow pcs(compxE2\ e_1\ pc_1\ d_1) \cap pcs(compxE2\ e_2\ pc_2\ d_2) = \{\}$

using *pcs-subset* **by** *fastforce*

lemma [*simp*]: $pc_1 + size(compE2\ e) \leq pc_2 \Longrightarrow pcs(compxE2\ e\ pc_1\ d_1) \cap pcs(compxEs2\ es\ pc_2\ d_2) = \{\}$

using *pcs-subset* **by** *fastforce*

lemma *match-ex-table-append-not-pcs* [*simp*]:

$pc \notin pcs\ xt_0 \Longrightarrow match\text{-}ex\text{-}table\ P\ C\ pc\ (xt_0\ @\ xt_1) = match\text{-}ex\text{-}table\ P\ C\ pc\ xt_1$

by (*induct xt0*) (*auto simp: matches-ex-entry-def*)

lemma *outside-pcs-not-matches-entry* [*simp*]:

$\llbracket x \in set\ xt; pc \notin pcs\ xt \rrbracket \Longrightarrow \neg matches\text{-}ex\text{-}entry\ P\ D\ pc\ x$

by(*auto simp:matches-ex-entry-def pcs-def*)

lemma *outside-pcs-compxE2-not-matches-entry* [*simp*]:

assumes $xe: xe \in set(compxE2\ e\ pc\ d)$

and *outside*: $pc' < pc \vee pc + size(compE2\ e) \leq pc'$

shows $\neg \text{matches-ex-entry } P \ C \ pc' \ xe$
proof
assume $\text{matches-ex-entry } P \ C \ pc' \ xe$
with xe **have** $pc' \in \text{pcs}(\text{comp}xE2 \ e \ pc \ d)$
by($\text{force simp add:matches-ex-entry-def pcs-def}$)
with $outside$ **show** $False$ **by** simp
qed

lemma $\text{outside-pcs-comp}xEs2\text{-not-matches-entry}$ [simp]:
assumes $xe: xe \in \text{set}(\text{comp}xEs2 \ es \ pc \ d)$
and $outside: pc' < pc \vee pc + \text{size}(\text{comp}Es2 \ es) \leq pc'$
shows $\neg \text{matches-ex-entry } P \ C \ pc' \ xe$
proof
assume $\text{matches-ex-entry } P \ C \ pc' \ xe$
with xe **have** $pc' \in \text{pcs}(\text{comp}xEs2 \ es \ pc \ d)$
by($\text{force simp add:matches-ex-entry-def pcs-def}$)
with $outside$ **show** $False$ **by** simp
qed

lemma $\text{match-ex-table-app}$ [simp]:
 $\forall xte \in \text{set } xt_1. \neg \text{matches-ex-entry } P \ D \ pc \ xte \implies$
 $\text{match-ex-table } P \ D \ pc \ (xt_1 \ @ \ xt) = \text{match-ex-table } P \ D \ pc \ xt$
by($\text{induct } xt_1$) simp-all

lemma $\text{match-ex-table-eq-NoneI}$ [simp]:
 $\forall x \in \text{set } xtab. \neg \text{matches-ex-entry } P \ C \ pc \ x \implies$
 $\text{match-ex-table } P \ C \ pc \ xtab = None$
using $\text{match-ex-table-app}$ [**where** $?xt = []$] **by** fastforce

lemma $\text{match-ex-table-not-pcs-None}$:
 $pc \notin \text{pcs } xt \implies \text{match-ex-table } P \ C \ pc \ xt = None$
by($\text{auto intro: match-ex-table-eq-NoneI}$)

lemma match-ex-entry :
fixes $start$ **shows**
 $\text{matches-ex-entry } P \ C \ pc \ (start, end, \text{catch-type}, \text{handler}) =$
 $(start \leq pc \wedge pc < end \wedge (\text{case } \text{catch-type} \text{ of } None \Rightarrow True \mid [C'] \Rightarrow P \vdash C \preceq^* C'))$
by($\text{simp add:matches-ex-entry-def}$)

lemma $\text{pcs-comp}xE2D$ [dest]:
 $pc \in \text{pcs}(\text{comp}xE2 \ e \ pc' \ d) \implies pc' \leq pc \wedge pc < pc' + \text{length}(\text{comp}E2 \ e)$
using pcs-subset **by**(fastforce)

lemma $\text{pcs-comp}xEs2D$ [dest]:
 $pc \in \text{pcs}(\text{comp}xEs2 \ es \ pc' \ d) \implies pc' \leq pc \wedge pc < pc' + \text{length}(\text{comp}Es2 \ es)$
using pcs-subset **by**(fastforce)

definition $\text{shift} :: \text{nat} \Rightarrow \text{ex-table} \Rightarrow \text{ex-table}$
where
 $\text{shift } n \ xt \equiv \text{map } (\lambda(\text{from}, \text{to}, C, \text{handler}, \text{depth}). (n + \text{from}, n + \text{to}, C, n + \text{handler}, \text{depth})) \ xt$

lemma shift-0 [simp]: $\text{shift } 0 \ xt = xt$
by($\text{induct } xt$)($\text{auto simp:shift-def}$)

lemma *shift-Nil* [*simp*]: $\text{shift } n \ [] = []$
by(*simp add:shift-def*)

lemma *shift-Cons-tuple* [*simp*]:
 $\text{shift } n \ ((\text{from}, \text{to}, C, \text{handler}, \text{depth}) \# xt) = (\text{from} + n, \text{to} + n, C, \text{handler} + n, \text{depth}) \# \text{shift } n \ xt$
by(*simp add:shift-def*)

lemma *shift-append* [*simp*]: $\text{shift } n \ (xt_1 @ xt_2) = \text{shift } n \ xt_1 @ \text{shift } n \ xt_2$
by(*simp add:shift-def*)

lemma *shift-shift* [*simp*]: $\text{shift } m \ (\text{shift } n \ xt) = \text{shift } (m+n) \ xt$
by(*simp add:shift-def split-def*)

lemma *fixes e :: 'addr expr1 and es :: 'addr expr1 list*
shows *shift-compxE2*: $\text{shift } pc \ (\text{compxE2 } e \ pc' \ d) = \text{compxE2 } e \ (pc' + pc) \ d$
and *shift-compxEs2*: $\text{shift } pc \ (\text{compxEs2 } es \ pc' \ d) = \text{compxEs2 } es \ (pc' + pc) \ d$
by(*induct e and es arbitrary: pc pc' d and pc pc' d rule: compE2.induct compEs2.induct*)
(auto simp:shift-def ac-simps)

lemma *compxE2-size-convs* [*simp*]: $n \neq 0 \implies \text{compxE2 } e \ n \ d = \text{shift } n \ (\text{compxE2 } e \ 0 \ d)$
and *compxEs2-size-convs*: $n \neq 0 \implies \text{compxEs2 } es \ n \ d = \text{shift } n \ (\text{compxEs2 } es \ 0 \ d)$
by(*simp-all add:shift-compxE2 shift-compxEs2*)

lemma *pcs-shift-conv* [*simp*]: $\text{pcs} \ (\text{shift } n \ xt) = (+) \ n \ ' \ \text{pcs } xt$
apply(*auto simp add:shift-def pcs-def*)
apply(*rule-tac x=x-n in image-eqI*)
apply(*auto*)
apply(*rule bexI*)
prefer 2
apply(*assumption*)
apply(*auto*)
done

lemma *image-plus-const-conv* [*simp*]:
fixes $m :: nat$
shows $m \in (+) \ n \ ' \ A \longleftrightarrow m \geq n \wedge m - n \in A$
by(*force*)

lemma *match-ex-table-shift-eq-None-conv* [*simp*]:
 $\text{match-ex-table } P \ C \ pc \ (\text{shift } n \ xt) = \text{None} \longleftrightarrow pc < n \vee \text{match-ex-table } P \ C \ (pc - n) \ xt = \text{None}$
by(*induct xt*)(*auto simp add: match-ex-entry split: if-split-asm*)

lemma *match-ex-table-shift-pc-None*:
 $pc \geq n \implies \text{match-ex-table } P \ C \ pc \ (\text{shift } n \ xt) = \text{None} \longleftrightarrow \text{match-ex-table } P \ C \ (pc - n) \ xt = \text{None}$
by(*simp add: match-ex-table-shift-eq-None-conv*)

lemma *match-ex-table-shift-eq-Some-conv* [*simp*]:
 $\text{match-ex-table } P \ C \ pc \ (\text{shift } n \ xt) = \lfloor (pc', d) \rfloor \longleftrightarrow$
 $pc \geq n \wedge pc' \geq n \wedge \text{match-ex-table } P \ C \ (pc - n) \ xt = \lfloor (pc' - n, d) \rfloor$
by(*induct xt*)(*auto simp add: match-ex-entry split: if-split-asm*)

lemma *match-ex-table-shift*:
 $\text{match-ex-table } P \ C \ pc \ xt = \lfloor (pc', d) \rfloor \implies \text{match-ex-table } P \ C \ (n + pc) \ (\text{shift } n \ xt) = \lfloor (n + pc', d) \rfloor$

by(*simp add: match-ex-table-shift-eq-Some-conv*)

lemma *match-ex-table-shift-pcD*:

match-ex-table P C pc (shift n xt) = [(pc', d)] \implies pc \geq n \wedge pc' \geq n \wedge match-ex-table P C (pc - n) xt = [(pc' - n, d)]

by(*simp add: match-ex-table-shift-eq-Some-conv*)

lemma *match-ex-table-pcsD*: *match-ex-table P C pc xt = [(pc', D)] \implies pc \in pcs xt*

by(*induct xt*)(*auto split: if-split-asm simp add: match-ex-entry*)

definition *stack-xlift* :: *nat \Rightarrow ex-table \Rightarrow ex-table*

where *stack-xlift n xt \equiv map (λ (from,to,C,handler,depth). (from, to, C, handler, n + depth)) xt*

lemma *stack-xlift-0* [*simp*]: *stack-xlift 0 xt = xt*

by(*induct xt, auto simp add: stack-xlift-def*)

lemma *stack-xlift-Nil* [*simp*]: *stack-xlift n [] = []*

by(*simp add: stack-xlift-def*)

lemma *stack-xlift-Cons-tuple* [*simp*]:

stack-xlift n ((from, to, C, handler, depth) # xt) = (from, to, C, handler, depth + n) # stack-xlift n xt

by(*simp add: stack-xlift-def*)

lemma *stack-xlift-append* [*simp*]: *stack-xlift n (xt @ xt') = stack-xlift n xt @ stack-xlift n xt'*

by(*simp add: stack-xlift-def*)

lemma *stack-xlift-stack-xlift* [*simp*]: *stack-xlift n (stack-xlift m xt) = stack-xlift (n + m) xt*

by(*simp add: stack-xlift-def split-def*)

lemma *fixes e* :: '*addr expr1 and es* :: '*addr expr1 list*

shows *stack-xlift-compxE2*: *stack-xlift n (compxE2 e pc d) = compxE2 e pc (n + d)*

and *stack-xlift-compEs2*: *stack-xlift n (compEs2 es pc d) = compEs2 es pc (n + d)*

by(*induct e and es arbitrary: d pc and d pc rule: compE2.induct compEs2.induct*)

(*auto simp add: shift-compxE2 simp del: compxE2-size-conv*)

lemma *compxE2-stack-xlift-conv* [*simp*]: *d > 0 \implies compxE2 e pc d = stack-xlift d (compxE2 e pc 0)*

and *compEs2-stack-xlift-conv* [*simp*]: *d > 0 \implies compEs2 es pc d = stack-xlift d (compEs2 es pc 0)*

by(*simp-all add: stack-xlift-compxE2 stack-xlift-compEs2*)

lemma *stack-xlift-shift* [*simp*]: *stack-xlift d (shift n xt) = shift n (stack-xlift d xt)*

by(*induct xt*)(*auto*)

lemma *pcs-stack-xlift-conv* [*simp*]: *pcs (stack-xlift n xt) = pcs xt*

by(*auto simp add: pcs-def stack-xlift-def*)

lemma *match-ex-table-stack-xlift-eq-None-conv* [*simp*]:

match-ex-table P C pc (stack-xlift d xt) = None \iff match-ex-table P C pc xt = None

by(*induct xt*)(*auto simp add: match-ex-entry*)

lemma *match-ex-table-stack-xlift-eq-Some-conv* [*simp*]:

$match\text{-}ex\text{-}table\ P\ C\ pc\ (stack\text{-}xlift\ n\ xt) = \lfloor (pc', d) \rfloor \iff d \geq n \wedge match\text{-}ex\text{-}table\ P\ C\ pc\ xt = \lfloor (pc', d - n) \rfloor$

by(*induct xt*)(*auto simp add: match-ex-entry*)

lemma *match-ex-table-stack-xliftD*:

$match\text{-}ex\text{-}table\ P\ C\ pc\ (stack\text{-}xlift\ n\ xt) = \lfloor (pc', d) \rfloor \implies d \geq n \wedge match\text{-}ex\text{-}table\ P\ C\ pc\ xt = \lfloor (pc', d - n) \rfloor$

by(*simp*)

lemma *match-ex-table-stack-xlift*:

$match\text{-}ex\text{-}table\ P\ C\ pc\ xt = \lfloor (pc', d) \rfloor \implies match\text{-}ex\text{-}table\ P\ C\ pc\ (stack\text{-}xlift\ n\ xt) = \lfloor (pc', n + d) \rfloor$

by *simp*

lemma *pcs-stack-xlift: pcs (stack-xlift n xt) = pcs xt*

by(*auto simp add: stack-xlift-def pcs-def*)

lemma *match-ex-table-None-append [simp]*:

$match\text{-}ex\text{-}table\ P\ C\ pc\ xt = None$

$\implies match\text{-}ex\text{-}table\ P\ C\ pc\ (xt @ xt') = match\text{-}ex\text{-}table\ P\ C\ pc\ xt'$

by(*induct xt, auto*)

lemma *match-ex-table-Some-append [simp]*:

$match\text{-}ex\text{-}table\ P\ C\ pc\ xt = \lfloor (pc', d) \rfloor \implies match\text{-}ex\text{-}table\ P\ C\ pc\ (xt @ xt') = \lfloor (pc', d) \rfloor$

by(*induct xt*)(*auto*)

lemma *match-ex-table-append*:

$match\text{-}ex\text{-}table\ P\ C\ pc\ (xt @ xt') = (case\ match\text{-}ex\text{-}table\ P\ C\ pc\ xt\ of\ None \Rightarrow match\text{-}ex\text{-}table\ P\ C\ pc\ xt'$

$\mid Some\ pcd \Rightarrow Some\ pcd)$

by(*auto*)

lemma *match-ex-table-pc-length-compE2*:

$match\text{-}ex\text{-}table\ P\ a\ pc\ (compxE2\ e\ pc'\ d) = \lfloor pcd \rfloor \implies pc' \leq pc \wedge pc < length\ (compE2\ e) + pc'$

and *match-ex-table-pc-length-compEs2*:

$match\text{-}ex\text{-}table\ P\ a\ pc\ (compEs2\ es\ pc'\ d) = \lfloor pcd \rfloor \implies pc' \leq pc \wedge pc < length\ (compEs2\ es) + pc'$

using *pcs-subset* **by**(*cases pcd, fastforce dest!: match-ex-table-pcsD*)**+**

lemma *match-ex-table-compxE2-shift-conv*:

$f > 0 \implies match\text{-}ex\text{-}table\ P\ C\ pc\ (compxE2\ e\ f\ d) = \lfloor (pc', d') \rfloor \iff pc \geq f \wedge pc' \geq f \wedge match\text{-}ex\text{-}table\ P\ C\ (pc - f)\ (compxE2\ e\ 0\ d) = \lfloor (pc' - f, d') \rfloor$

by *simp*

lemma *match-ex-table-compEs2-shift-conv*:

$f > 0 \implies match\text{-}ex\text{-}table\ P\ C\ pc\ (compEs2\ es\ f\ d) = \lfloor (pc', d') \rfloor \iff pc \geq f \wedge pc' \geq f \wedge match\text{-}ex\text{-}table\ P\ C\ (pc - f)\ (compEs2\ es\ 0\ d) = \lfloor (pc' - f, d') \rfloor$

by(*simp add: compEs2-size-convs*)

lemma *match-ex-table-compxE2-stack-conv*:

$d > 0 \implies match\text{-}ex\text{-}table\ P\ C\ pc\ (compxE2\ e\ 0\ d) = \lfloor (pc', d') \rfloor \iff d' \geq d \wedge match\text{-}ex\text{-}table\ P\ C\ pc\ (compxE2\ e\ 0\ 0) = \lfloor (pc', d' - d) \rfloor$

by *simp*

lemma *match-ex-table-compEs2-stack-conv*:

$d > 0 \implies \text{match-ex-table } P \ C \ pc \ (\text{compxEs2 } es \ 0 \ d) = \lfloor (pc', d') \rfloor \longleftrightarrow d' \geq d \wedge \text{match-ex-table } P \ C \ pc \ (\text{compxEs2 } es \ 0 \ 0) = \lfloor (pc', d' - d) \rfloor$
by(*simp add: compxEs2-stack-xlift-convs*)

lemma fixes $e :: 'addr \ expr1$ **and** $es :: 'addr \ expr1 \ list$

shows *match-ex-table-compxE2-not-same: match-ex-table* $P \ C \ pc \ (\text{compxE2 } e \ n \ d) = \lfloor (pc', d') \rfloor \implies pc \neq pc'$

and *match-ex-table-compxEs2-not-same: match-ex-table* $P \ C \ pc \ (\text{compxEs2 } es \ n \ d) = \lfloor (pc', d') \rfloor \implies pc \neq pc'$

apply(*induct e n d and es n d rule: compxE2-compxEs2-induct*)

apply(*auto simp add: match-ex-table-append match-ex-entry simp del: compxE2-size-convs compxEs2-size-convs compxE2-stack-xlift-convs compxEs2-stack-xlift-convs split: if-split-asm*)

done

end

7.11 Type rules for the intermediate language

theory *J1WellType imports*

J1State

../Common/ExternalCallWF

../Common/SemiType

begin

declare *Listn.lesub-list-impl-same-size*[*simp del*] *listE-length* [*simp del*]

7.11.1 Well-Typedness

type-synonym

env1 = ty list — type environment indexed by variable number

inductive *WT1* :: *'addr J1-prog* \Rightarrow *env1* \Rightarrow *'addr expr1* \Rightarrow *ty* \Rightarrow *bool* ($\langle -, \vdash 1 - :: \rightarrow [51, 0, 0, 51] 50 \rangle$)

and *WTs1* :: *'addr J1-prog* \Rightarrow *env1* \Rightarrow *'addr expr1 list* \Rightarrow *ty list* \Rightarrow *bool* ($\langle -, \vdash 1 - [::] \rightarrow [51, 0, 0, 51] 50 \rangle$)

for $P :: 'addr \ J1\text{-prog}$

where

WT1New:

is-class $P \ C \implies$

$P, E \vdash 1 \ \text{new } C :: \text{Class } C$

| *WT1NewArray:*

$\llbracket P, E \vdash 1 \ e :: \text{Integer}; \text{is-type } P \ (T[]) \rrbracket \implies$

$P, E \vdash 1 \ \text{newA } T[e] :: T[]$

| *WT1Cast:*

$\llbracket P, E \vdash 1 \ e :: T; P \vdash U \leq T \vee P \vdash T \leq U; \text{is-type } P \ U \rrbracket$

$\implies P, E \vdash 1 \ \text{Cast } U \ e :: U$

| *WT1InstanceOf:*

$\llbracket P, E \vdash 1 \ e :: T; P \vdash U \leq T \vee P \vdash T \leq U; \text{is-type } P \ U; \text{is-refT } U \rrbracket$

$\implies P, E \vdash 1 \ e \ \text{instanceof } U :: \text{Boolean}$

| *WT1Val:*

typeof $v = \text{Some } T \implies$

$P, E \vdash 1 \text{ Val } v :: T$

| *WT1Var*:

$\llbracket E!V = T; V < \text{size } E \rrbracket \Longrightarrow$
 $P, E \vdash 1 \text{ Var } V :: T$

| *WT1BinOp*:

$\llbracket P, E \vdash 1 e1 :: T1; P, E \vdash 1 e2 :: T2; P \vdash T1 \llbracket \text{bop} \rrbracket T2 :: T \rrbracket$
 $\Longrightarrow P, E \vdash 1 e1 \llbracket \text{bop} \rrbracket e2 :: T$

| *WT1LAss*:

$\llbracket E!i = T; i < \text{size } E; P, E \vdash 1 e :: T'; P \vdash T' \leq T \rrbracket$
 $\Longrightarrow P, E \vdash 1 i := e :: \text{Void}$

| *WT1AAcc*:

$\llbracket P, E \vdash 1 a :: T[]; P, E \vdash 1 i :: \text{Integer} \rrbracket$
 $\Longrightarrow P, E \vdash 1 a[i] :: T$

| *WT1AAss*:

$\llbracket P, E \vdash 1 a :: T[]; P, E \vdash 1 i :: \text{Integer}; P, E \vdash 1 e :: T'; P \vdash T' \leq T \rrbracket$
 $\Longrightarrow P, E \vdash 1 a[i] := e :: \text{Void}$

| *WT1ALength*:

$P, E \vdash 1 a :: T[] \Longrightarrow P, E \vdash 1 a \cdot \text{length} :: \text{Integer}$

| *WTFAcc1*:

$\llbracket P, E \vdash 1 e :: U; \text{class-type-of}' U = [C]; P \vdash C \text{ sees } F:T (fm) \text{ in } D \rrbracket$
 $\Longrightarrow P, E \vdash 1 e \cdot F\{D\} :: T$

| *WTFAss1*:

$\llbracket P, E \vdash 1 e1 :: U; \text{class-type-of}' U = [C]; P \vdash C \text{ sees } F:T (fm) \text{ in } D; P, E \vdash 1 e2 :: T'; P \vdash T' \leq T \rrbracket$
 $\Longrightarrow P, E \vdash 1 e1 \cdot F\{D\} := e2 :: \text{Void}$

| *WTCAS1*:

$\llbracket P, E \vdash 1 e1 :: U; \text{class-type-of}' U = [C]; P \vdash C \text{ sees } F:T (fm) \text{ in } D; \text{volatile } fm;$
 $P, E \vdash 1 e2 :: T'; P \vdash T' \leq T; P, E \vdash 1 e3 :: T''; P \vdash T'' \leq T \rrbracket$
 $\Longrightarrow P, E \vdash 1 e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3) :: \text{Boolean}$

| *WT1Call*:

$\llbracket P, E \vdash 1 e :: U; \text{class-type-of}' U = [C]; P \vdash C \text{ sees } M:Ts \rightarrow T = m \text{ in } D;$
 $P, E \vdash 1 es [::] Ts'; P \vdash Ts' [\leq] Ts \rrbracket$
 $\Longrightarrow P, E \vdash 1 e \cdot M(es) :: T$

| *WT1Block*:

$\llbracket \text{is-type } P T; P, E@[T] \vdash 1 e :: T'; \text{case } vo \text{ of } None \Rightarrow \text{True} \mid [v] \Rightarrow \exists T'. \text{typeof } v = [T'] \wedge P \vdash T' \leq T \rrbracket$
 $\Longrightarrow P, E \vdash 1 \{V:T=vo; e\} :: T'$

| *WT1Synchronized*:

$\llbracket P, E \vdash 1 o' :: T; \text{is-refT } T; T \neq NT; P, E@[Class \text{ Object}] \vdash 1 e :: T' \rrbracket$
 $\Longrightarrow P, E \vdash 1 \text{sync}_V(o') e :: T'$

| *WT1Seq*:

$$\begin{aligned} & \llbracket P, E \vdash 1 e_1 :: T_1; P, E \vdash 1 e_2 :: T_2 \rrbracket \\ & \implies P, E \vdash 1 e_1 ;; e_2 :: T_2 \end{aligned}$$

| *WT1Cond*:

$$\begin{aligned} & \llbracket P, E \vdash 1 e :: \text{Boolean}; P, E \vdash 1 e_1 :: T_1; P, E \vdash 1 e_2 :: T_2; P \vdash \text{lub}(T_1, T_2) = T \rrbracket \\ & \implies P, E \vdash 1 \text{if } (e) e_1 \text{ else } e_2 :: T \end{aligned}$$

| *WT1While*:

$$\begin{aligned} & \llbracket P, E \vdash 1 e :: \text{Boolean}; P, E \vdash 1 c :: T \rrbracket \\ & \implies P, E \vdash 1 \text{while } (e) c :: \text{Void} \end{aligned}$$

| *WT1Throw*:

$$\begin{aligned} & \llbracket P, E \vdash 1 e :: \text{Class } C; P \vdash C \preceq^* \text{Throwable} \rrbracket \implies \\ & P, E \vdash 1 \text{throw } e :: \text{Void} \end{aligned}$$

| *WT1Try*:

$$\begin{aligned} & \llbracket P, E \vdash 1 e_1 :: T; P, E@[\text{Class } C] \vdash 1 e_2 :: T; \text{is-class } P C \rrbracket \\ & \implies P, E \vdash 1 \text{try } e_1 \text{ catch}(C V) e_2 :: T \end{aligned}$$

| *WT1Nil*: $P, E \vdash 1 [] [::] []$

| *WT1Cons*: $\llbracket P, E \vdash 1 e :: T; P, E \vdash 1 es [::] Ts \rrbracket \implies P, E \vdash 1 e \# es [::] T \# Ts$

declare *WT1-WTs1.intros*[*intro!*]

declare *WT1Nil*[*iff*]

inductive-cases *WT1-WTs1-cases*[*elim!*]:

$$\begin{aligned} & P, E \vdash 1 \text{Val } v :: T \\ & P, E \vdash 1 \text{Var } i :: T \\ & P, E \vdash 1 \text{Cast } D e :: T \\ & P, E \vdash 1 e \text{instanceof } U :: T \\ & P, E \vdash 1 i := e :: T \\ & P, E \vdash 1 \{i:U=vo; e\} :: T \\ & P, E \vdash 1 e_1 ;; e_2 :: T \\ & P, E \vdash 1 \text{if } (e) e_1 \text{ else } e_2 :: T \\ & P, E \vdash 1 \text{while } (e) c :: T \\ & P, E \vdash 1 \text{throw } e :: T \\ & P, E \vdash 1 \text{try } e_1 \text{ catch}(C i) e_2 :: T \\ & P, E \vdash 1 e \cdot F\{D\} :: T \\ & P, E \vdash 1 e_1 \cdot F\{D\} := e_2 :: T \\ & P, E \vdash 1 e \cdot \text{compareAndSwap}(D \cdot F, e', e'') :: T \\ & P, E \vdash 1 e_1 \ll \text{bop} \gg e_2 :: T \\ & P, E \vdash 1 \text{new } C :: T \\ & P, E \vdash 1 \text{newA } T'[e] :: T \\ & P, E \vdash 1 a[i] := e :: T \\ & P, E \vdash 1 a[i] :: T \\ & P, E \vdash 1 a \cdot \text{length} :: T \\ & P, E \vdash 1 e \cdot M(es) :: T \\ & P, E \vdash 1 \text{sync}_V(o') e :: T \\ & P, E \vdash 1 \text{insync}_V(a) e :: T \\ & P, E \vdash 1 [] [::] Ts \\ & P, E \vdash 1 e \# es [::] Ts \end{aligned}$$

lemma *WTs1-same-size*: $P, E \vdash 1 es [::] Ts \implies \text{size } es = \text{size } Ts$

by (induct es arbitrary: Ts) auto

lemma *WTs1-snoc-cases*:

assumes *wt*: $P, E \vdash 1 \text{ es} @ [e] [::] Ts$

obtains $T Ts'$ where $P, E \vdash 1 \text{ es} [::] Ts' P, E \vdash 1 e :: T$

proof –

from *wt* have $\exists T Ts'. P, E \vdash 1 \text{ es} [::] Ts' \wedge P, E \vdash 1 e :: T$

by (induct es arbitrary: Ts) auto

thus *thesis* by (auto intro: that)

qed

lemma *WTs1-append*:

assumes *wt*: $P, Env \vdash 1 \text{ es} @ \text{es}' [::] Ts$

obtains $Ts' Ts''$ where $P, Env \vdash 1 \text{ es} [::] Ts' P, Env \vdash 1 \text{ es}' [::] Ts''$

proof –

from *wt* have $\exists Ts' Ts''. P, Env \vdash 1 \text{ es} [::] Ts' \wedge P, Env \vdash 1 \text{ es}' [::] Ts''$

by (induct es arbitrary: Ts) auto

thus *?thesis* by (auto intro: that)

qed

lemma *WT1-not-contains-insync*: $P, E \vdash 1 e :: T \implies \neg \text{contains-insync } e$

and *WTs1-not-contains-insyncs*: $P, E \vdash 1 \text{ es} [::] Ts \implies \neg \text{contains-insyncs } \text{es}$

by (induct rule: *WT1-WTs1.inducts*) auto

lemma *WT1-expr-locks*: $P, E \vdash 1 e :: T \implies \text{expr-locks } e = (\lambda a. 0)$

and *WTs1-expr-lockss*: $P, E \vdash 1 \text{ es} [::] Ts \implies \text{expr-lockss } \text{es} = (\lambda a. 0)$

by (induct rule: *WT1-WTs1.inducts*) (auto)

lemma assumes *wf*: *wf-prog wfmd P*

shows *WT1-unique*: $P, E \vdash 1 e :: T1 \implies P, E \vdash 1 e :: T2 \implies T1 = T2$

and *WTs1-unique*: $P, E \vdash 1 \text{ es} [::] Ts1 \implies P, E \vdash 1 \text{ es} [::] Ts2 \implies Ts1 = Ts2$

apply (induct arbitrary: *T2* and *Ts2* rule: *WT1-WTs1.inducts*)

apply blast

apply blast

apply blast

apply blast

apply *clarsimp*

apply blast

apply (blast dest: *WT-binop-fun*)

apply blast

apply blast

apply blast

apply blast

apply (blast dest: *sees-field-idemp sees-field-fun*)

apply (blast dest: *sees-field-fun*)

apply blast

apply (erule *WT1-WTs1-cases*)

apply (*simp*)

apply (blast dest: *sees-method-idemp sees-method-fun*)

apply blast

apply blast

apply blast

```

apply(blast dest: is-lub-unique[OF wf])
apply blast
apply blast
apply blast
apply blast
apply blast
done

```

```

lemma assumes wf: wf-prog p P
  shows WT1-is-type: P,E ⊢1 e :: T ⇒ set E ⊆ types P ⇒ is-type P T
  and WTs1-is-type: P,E ⊢1 es [::] Ts ⇒ set E ⊆ types P ⇒ set Ts ⊆ types P
apply(induct rule: WT1-WTs1.inducts)
apply simp
apply simp
apply simp
apply simp
apply (simp add: typeof-lit-is-type)
apply (fastforce intro: nth-mem)
apply(simp add: WT-binop-is-type)
apply(simp)
apply(simp del: is-type-array add: is-type-ArrayD)
apply(simp)
apply(simp)
apply (simp add: sees-field-is-type[OF - wf])
apply simp
apply simp
apply(fastforce dest!: sees-wf-mdecl[OF wf] simp: wf-mdecl-def)
apply(simp)
apply(simp add: is-class-Object[OF wf])
apply simp
apply(blast dest: is-lub-is-type[OF wf])
apply simp
apply simp
apply simp
apply simp
apply(simp)
done

```

lemma blocks1-WT:

```

  [ P,Env @ Ts ⊢1 body :: T; set Ts ⊆ types P ] ⇒ P,Env ⊢1 blocks1 (length Env) Ts body :: T
proof(induct n≡length Env Ts body arbitrary: Env rule: blocks1.induct)
  case 1 thus ?case by simp
next
  case (2 T' Ts e)
  note IH = ⟨∧ Env'. [Suc (length Env) = length Env'; P,Env' @ Ts ⊢1 e :: T; set Ts ⊆ types P ]
    ⇒ P,Env' ⊢1 blocks1 (length Env') Ts e :: T⟩
  from ⟨set (T' # Ts) ⊆ types P⟩ have set Ts ⊆ types P is-type P T' by(auto)
  moreover from ⟨P,Env @ T' # Ts ⊢1 e :: T⟩ have P,(Env @ [T']) @ Ts ⊢1 e :: T by simp
  note IH[OF - this]
  ultimately show ?case by auto
qed

```

```

lemma WT1-fv: [ P,E ⊢1 e :: T; B e (length E); syncvars e ] ⇒ fv e ⊆ {0..and WTs1-fvs: [ P,E ⊢1 es [::] Ts; Bs es (length E); syncvarss es ] ⇒ fvs es ⊆ {0..

```

```

proof(induct rule: WT1-WTs1.inducts)
  case (WT1Synchronized E e1 T e2 T' V)
    note IH1 =  $\langle \llbracket \mathcal{B} \ e1 \ (length \ E); \ syncvars \ e1 \rrbracket \Longrightarrow \ fv \ e1 \subseteq \{0..\langle length \ E \rangle\} \rangle$ 
    note IH2 =  $\langle \llbracket \mathcal{B} \ e2 \ (length \ (E \ @ \ [Class \ Object])); \ syncvars \ e2 \rrbracket \Longrightarrow \ fv \ e2 \subseteq \{0..\langle length \ (E \ @ \ [Class \ Object]) \rangle\} \rangle$ 
    from  $\langle \mathcal{B} \ (sync_V \ (e1) \ e2) \ (length \ E) \rangle$  have [simp]:  $V = length \ E$ 
      and B1:  $\mathcal{B} \ e1 \ (length \ E)$  and B2:  $\mathcal{B} \ e2 \ (Suc \ (length \ E))$  by auto
    from  $\langle syncvars \ (sync_V \ (e1) \ e2) \rangle$  have sync1: syncvars e1 and sync2: syncvars e2 and V:  $V \notin fv \ e2$  by auto
    have  $fv \ e2 \subseteq \{0..\langle length \ E \rangle\}$ 
    proof
      fix x
      assume x:  $x \in fv \ e2$ 
      with V have  $x \neq length \ E$  by auto
      moreover from IH2 B2 sync2 have  $fv \ e2 \subseteq \{0..\langle Suc \ (length \ E) \rangle\}$  by auto
      with x have  $x < Suc \ (length \ E)$  by auto
      ultimately show  $x \in \{0..\langle length \ E \rangle\}$  by auto
    qed
    with IH1[OF B1 sync1] show ?case by(auto)
  next
    case (WT1Cond E e e1 T1 e2 T2 T)
    thus ?case by(auto del: subsetI)
qed fastforce+

end

```

7.12 Well-Formedness of Intermediate Language

theory *J1WellForm* **imports**

../J/DefAss

J1WellType

begin

7.12.1 Well-formedness

definition *wf-J1-mdecl* :: $'addr \ J1\text{-prog} \Rightarrow \ cname \Rightarrow 'addr \ expr1 \ mdecl \Rightarrow \ bool$

where

$wf\text{-}J1\text{-mdecl} \ P \ C \equiv \lambda(M, Ts, T, body). \\ (\exists T'. P, Class \ C \# Ts \vdash 1 \ body :: T' \wedge P \vdash T' \leq T) \wedge \\ \mathcal{D} \ body \ [\{ ..size \ Ts \}] \wedge \mathcal{B} \ body \ (size \ Ts + 1) \wedge syncvars \ body$

lemma *wf-J1-mdecl*[*simp*]:

$wf\text{-}J1\text{-mdecl} \ P \ C \ (M, Ts, T, body) \equiv \\ ((\exists T'. P, Class \ C \# Ts \vdash 1 \ body :: T' \wedge P \vdash T' \leq T) \wedge \\ \mathcal{D} \ body \ [\{ ..size \ Ts \}] \wedge \mathcal{B} \ body \ (size \ Ts + 1)) \wedge syncvars \ body$

by (*simp add:wf-J1-mdecl-def*)

abbreviation *wf-J1-prog* :: $'addr \ J1\text{-prog} \Rightarrow \ bool$

where $wf\text{-}J1\text{-prog} == wf\text{-prog} \ wf\text{-}J1\text{-mdecl}$

end

7.13 Preservation of Well-Typedness in Stage 2

```

theory TypeComp
imports
  Exception-Tables
  J1WellForm
  ../BV/BVSpec
  HOL-Library.Prefix-Order
  HOL-Library.Sublist
begin

locale TC0 =
  fixes P :: 'addr J1-prog and mxl :: nat
begin

definition ty :: ty list  $\Rightarrow$  'addr expr1  $\Rightarrow$  ty
where ty E e  $\equiv$  THE T. P, E  $\vdash$  1 e :: T

definition tyl :: ty list  $\Rightarrow$  nat set  $\Rightarrow$  tyl
where tyl E A'  $\equiv$  map ( $\lambda$ i. if i  $\in$  A'  $\wedge$  i < size E then OK(E!i) else Err) [0.. $mxl$ ]

definition tyi' :: ty list  $\Rightarrow$  ty list  $\Rightarrow$  nat set option  $\Rightarrow$  tyi'
where tyi' ST E A  $\equiv$  case A of None  $\Rightarrow$  None | [A']  $\Rightarrow$  Some(ST, tyl E A')

definition after :: ty list  $\Rightarrow$  nat set option  $\Rightarrow$  ty list  $\Rightarrow$  'addr expr1  $\Rightarrow$  tyi'
  where after E A ST e  $\equiv$  tyi' (ty E e # ST) E (A  $\sqcup$  A e)

end

locale TC1 = TC0 +
  fixes wfmd
  assumes wf-prog: wf-prog wfmd P
begin

lemma ty-def2 [simp]: P, E  $\vdash$  1 e :: T  $\implies$  ty E e = T
apply(unfold ty-def ty-def)
apply(blast intro: the-equality WT1-unique[OF wf-prog])
done

end

context TC0 begin

lemma tyi'-None [simp]: tyi' ST E None = None
by(simp add:tyi'-def)

lemma tyl-app-diff[simp]:
  tyl (E@[T]) (A - {size E}) = tyl E A
by(auto simp add:tyl-def hyperset-defs)

lemma tyi'-app-diff[simp]:
  tyi' ST (E @ [T]) (A  $\ominus$  size E) = tyi' ST E A
by(auto simp add:tyi'-def hyperset-defs)

```


lemma *ty_l-antimono*:

$A \subseteq A' \implies P \vdash ty_l E A' [\leq_{\top}] ty_l E A$
by(*auto simp:ty_l-def list-all2-conv-all-nth*)

lemma *ty_i'-antimono*:

$A \subseteq A' \implies P \vdash ty_i' ST E [A'] \leq' ty_i' ST E [A]$
by(*auto simp:ty_i'-def ty_l-def list-all2-conv-all-nth*)

lemma *ty_l-env-antimono*:

$P \vdash ty_l (E@[T]) A [\leq_{\top}] ty_l E A$
by(*auto simp:ty_l-def list-all2-conv-all-nth*)

lemma *ty_i'-env-antimono*:

$P \vdash ty_i' ST (E@[T]) A \leq' ty_i' ST E A$
by(*auto simp:ty_i'-def ty_l-def list-all2-conv-all-nth*)

lemma *ty_i'-incr*:

$P \vdash ty_i' ST (E @ [T]) [insert (size E) A] \leq' ty_i' ST E [A]$
by(*auto simp:ty_i'-def ty_l-def list-all2-conv-all-nth*)

lemma *ty_l-incr*:

$P \vdash ty_l (E @ [T]) (insert (size E) A) [\leq_{\top}] ty_l E A$
by(*auto simp: hyperset-defs ty_l-def list-all2-conv-all-nth*)

lemma *ty_l-in-types*:

$set E \subseteq types P \implies ty_l E A \in list\ maxl (err (types P))$
by(*auto simp add:ty_l-def intro!:listI dest!: nth-mem*)

function *compT* :: *ty list* \Rightarrow *nat hyperset* \Rightarrow *ty list* \Rightarrow '*addr expr1* \Rightarrow *ty_i' list*

and *compTs* :: *ty list* \Rightarrow *nat hyperset* \Rightarrow *ty list* \Rightarrow '*addr expr1 list* \Rightarrow *ty_i' list*

where

compT *E A ST* (*new C*) = []
| *compT* *E A ST* (*newA T[e]*) = *compT* *E A ST e* @ [*after E A ST e*]
| *compT* *E A ST* (*Cast C e*) = *compT* *E A ST e* @ [*after E A ST e*]
| *compT* *E A ST* (*e instanceof T*) = *compT* *E A ST e* @ [*after E A ST e*]
| *compT* *E A ST* (*Val v*) = []
| *compT* *E A ST* (*e1 «bop» e2*) =
(*let* *ST1* = *ty E e1 # ST*; *A1* = *A* \sqcup \mathcal{A} *e1* *in*
compT *E A ST e1* @ [*after E A ST e1*] @
compT *E A1 ST1 e2* @ [*after E A1 ST1 e2*])
| *compT* *E A ST* (*Var i*) = []
| *compT* *E A ST* (*i := e*) = *compT* *E A ST e* @ [*after E A ST e*, *ty_i' ST E* (*A* \sqcup \mathcal{A} *e* \sqcup [*i*])]
| *compT* *E A ST* (*a[i]*) =
(*let* *ST1* = *ty E a # ST*; *A1* = *A* \sqcup \mathcal{A} *a*
in compT *E A ST a* @ [*after E A ST a*] @ *compT* *E A1 ST1 i* @ [*after E A1 ST1 i*])
| *compT* *E A ST* (*a[i] := e*) =
(*let* *ST1* = *ty E a # ST*; *A1* = *A* \sqcup \mathcal{A} *a*;
ST2 = *ty E i # ST1*; *A2* = *A1* \sqcup \mathcal{A} *i*; *A3* = *A2* \sqcup \mathcal{A} *e*

$$\begin{aligned}
& \text{in } \text{compT } E \ A \ ST \ a \ @ \ [\text{after } E \ A \ ST \ a] \ @ \ \text{compT } E \ A1 \ ST1 \ i \ @ \ [\text{after } E \ A1 \ ST1 \ i] \ @ \ \text{compT } E \\
& A2 \ ST2 \ e \ @ \ [\text{after } E \ A2 \ ST2 \ e, \ ty_i' \ ST \ E \ A3]) \\
& | \ \text{compT } E \ A \ ST \ (a \cdot \text{length}) = \text{compT } E \ A \ ST \ a \ @ \ [\text{after } E \ A \ ST \ a] \\
& | \ \text{compT } E \ A \ ST \ (e \cdot F\{D\}) = \text{compT } E \ A \ ST \ e \ @ \ [\text{after } E \ A \ ST \ e] \\
& | \ \text{compT } E \ A \ ST \ (e1 \cdot F\{D\} := e2) = \\
& \quad (\text{let } ST1 = ty \ E \ e1 \ #ST; A1 = A \sqcup \mathcal{A} \ e1; A2 = A1 \sqcup \mathcal{A} \ e2 \\
& \quad \text{in } \text{compT } E \ A \ ST \ e1 \ @ \ [\text{after } E \ A \ ST \ e1] \ @ \ \text{compT } E \ A1 \ ST1 \ e2 \ @ \ [\text{after } E \ A1 \ ST1 \ e2] \ @ \ [ty_i' \\
& ST \ E \ A2]) \\
& | \ \text{compT } E \ A \ ST \ (e1 \cdot \text{compareAndSwap}(D \cdot F, \ e2, \ e3)) = \\
& \quad (\text{let } ST1 = ty \ E \ e1 \ # \ ST; A1 = A \sqcup \mathcal{A} \ e1; ST2 = ty \ E \ e2 \ # \ ST1; A2 = A1 \sqcup \mathcal{A} \ e2; A3 = A2 \\
& \sqcup \mathcal{A} \ e3 \\
& \quad \text{in } \text{compT } E \ A \ ST \ e1 \ @ \ [\text{after } E \ A \ ST \ e1] \ @ \ \text{compT } E \ A1 \ ST1 \ e2 \ @ \ [\text{after } E \ A1 \ ST1 \ e2] \ @ \ \text{compT } \\
& E \ A2 \ ST2 \ e3 \ @ \ [\text{after } E \ A2 \ ST2 \ e3]) \\
& | \ \text{compT } E \ A \ ST \ (e \cdot M(es)) = \\
& \quad \text{compT } E \ A \ ST \ e \ @ \ [\text{after } E \ A \ ST \ e] \ @ \\
& \quad \text{compTs } E \ (A \sqcup \mathcal{A} \ e) \ (ty \ E \ e \ # \ ST) \ es \\
& | \ \text{compT } E \ A \ ST \ \{i:T=None; e\} = \text{compT } (E@[T]) \ (A \ominus i) \ ST \ e \\
& | \ \text{compT } E \ A \ ST \ \{i:T=[v]; e\} = \\
& \quad [\text{after } E \ A \ ST \ (\text{Val } v), \ ty_i' \ ST \ (E@[T]) \ (A \sqcup \{i\})] \ @ \ \text{compT } (E@[T]) \ (A \sqcup \{i\}) \ ST \ e \\
& | \ \text{compT } E \ A \ ST \ (\text{sync}_i \ (e1) \ e2) = \\
& \quad (\text{let } A1 = A \sqcup \mathcal{A} \ e1 \sqcup \{i\}; E1 = E \ @ \ [\text{Class } \text{Object}]; ST2 = ty \ E1 \ e2 \ # \ ST; A2 = A1 \sqcup \mathcal{A} \ e2 \\
& \quad \text{in } \text{compT } E \ A \ ST \ e1 \ @ \\
& \quad \quad [\text{after } E \ A \ ST \ e1, \\
& \quad \quad \quad ty_i' \ (\text{Class } \text{Object} \ # \ \text{Class } \text{Object} \ # \ ST) \ E \ (A \sqcup \mathcal{A} \ e1), \\
& \quad \quad \quad ty_i' \ (\text{Class } \text{Object} \ # \ ST) \ E1 \ A1, \\
& \quad \quad \quad ty_i' \ ST \ E1 \ A1] \ @ \\
& \quad \quad \text{compT } E1 \ A1 \ ST \ e2 \ @ \\
& \quad \quad [ty_i' \ ST2 \ E1 \ A2, \ ty_i' \ (\text{Class } \text{Object} \ # \ ST2) \ E1 \ A2, \ ty_i' \ ST2 \ E1 \ A2, \\
& \quad \quad \quad ty_i' \ (\text{Class } \text{Throwable} \ # \ ST) \ E1 \ A1, \\
& \quad \quad \quad ty_i' \ (\text{Class } \text{Object} \ # \ \text{Class } \text{Throwable} \ # \ ST) \ E1 \ A1, \\
& \quad \quad \quad ty_i' \ (\text{Class } \text{Throwable} \ # \ ST) \ E1 \ A1]) \\
& | \ \text{compT } E \ A \ ST \ (\text{insync}_i \ (a) \ e) = [] \\
& | \ \text{compT } E \ A \ ST \ (e1;;e2) = \\
& \quad (\text{let } A1 = A \sqcup \mathcal{A} \ e1 \ \text{in} \\
& \quad \quad \text{compT } E \ A \ ST \ e1 \ @ \ [\text{after } E \ A \ ST \ e1, \ ty_i' \ ST \ E \ A1] \ @ \\
& \quad \quad \text{compT } E \ A1 \ ST \ e2) \\
& | \ \text{compT } E \ A \ ST \ (\text{if } (e) \ e1 \ \text{else } e2) = \\
& \quad (\text{let } A0 = A \sqcup \mathcal{A} \ e; \tau = ty_i' \ ST \ E \ A0 \ \text{in} \\
& \quad \quad \text{compT } E \ A \ ST \ e \ @ \ [\text{after } E \ A \ ST \ e, \ \tau] \ @ \\
& \quad \quad \text{compT } E \ A0 \ ST \ e1 \ @ \ [\text{after } E \ A0 \ ST \ e1, \ \tau] \ @ \\
& \quad \quad \text{compT } E \ A0 \ ST \ e2) \\
& | \ \text{compT } E \ A \ ST \ (\text{while } (e) \ c) = \\
& \quad (\text{let } A0 = A \sqcup \mathcal{A} \ e; A1 = A0 \sqcup \mathcal{A} \ c; \tau = ty_i' \ ST \ E \ A0 \ \text{in} \\
& \quad \quad \text{compT } E \ A \ ST \ e \ @ \ [\text{after } E \ A \ ST \ e, \ \tau] \ @ \\
& \quad \quad \text{compT } E \ A0 \ ST \ c \ @ \ [\text{after } E \ A0 \ ST \ c, \ ty_i' \ ST \ E \ A1, \ ty_i' \ ST \ E \ A0]) \\
& | \ \text{compT } E \ A \ ST \ (\text{throw } e) = \text{compT } E \ A \ ST \ e \ @ \ [\text{after } E \ A \ ST \ e] \\
& | \ \text{compT } E \ A \ ST \ (\text{try } e1 \ \text{catch}(C \ i) \ e2) = \\
& \quad \text{compT } E \ A \ ST \ e1 \ @ \ [\text{after } E \ A \ ST \ e1] \ @ \\
& \quad [ty_i' \ (\text{Class } C \ #ST) \ E \ A, \ ty_i' \ ST \ (E@[Class \ C]) \ (A \sqcup \{i\})] \ @ \\
& \quad \text{compT } (E@[Class \ C]) \ (A \sqcup \{i\}) \ ST \ e2 \\
& | \ \text{compTs } E \ A \ ST \ [] = []
\end{aligned}$$

| $compTs\ E\ A\ ST\ (e\#es) = compT\ E\ A\ ST\ e\ @\ [after\ E\ A\ ST\ e]\ @\ compTs\ E\ (A\ \sqcup\ (A\ e))\ (ty\ E\ e\ \#\ ST)\ es$

by *pat-completeness simp-all*

termination

apply(*relation case-sum* ($\lambda p. size\ (snd\ (snd\ (snd\ p)))$) ($\lambda p. size\ list\ size\ (snd\ (snd\ (snd\ p)))$) *<*mlex*>* {})

apply(*rule wf-mlex[OF wf-empty]*)

apply(*rule mlex-less, simp*)⁺

done

lemmas *compT-compTs-induct* =

compT-compTs.induct[
unfolded meta-all5-eq-conv meta-all4-eq-conv meta-all3-eq-conv meta-all2-eq-conv meta-all-eq-conv,
case-names
new NewArray Cast InstanceOf Val BinOp Var LAss AAcc AAss ALen FAcc FAss CompareAndSwap
Call BlockNone BlockSome
Synchronized InSynchronized Seq Cond While throw TryCatch
Nil Cons]

definition *compTa* :: *ty list* \Rightarrow *nat hyperset* \Rightarrow *ty list* \Rightarrow '*addr expr1*' \Rightarrow *ty_i' list*

where *compTa* *E A ST e* \equiv *compT E A ST e* @ [*after E A ST e*]

lemmas *compE2-not-Nil* = *compE2-neq-Nil*

declare *compE2-not-Nil*[*simp*]

lemma *compT-sizes*[*simp*]:

shows $size(compT\ E\ A\ ST\ e) = size(compE2\ e) - 1$

and $size(compTs\ E\ A\ ST\ es) = size(compEs2\ es)$

apply(*induct E A ST e and E A ST es rule: compT-compTs-induct*)

apply(*auto split:nat-diff-split*)

done

lemma *compT-None-not-Some* [*simp*]: $[\tau] \notin set\ (compT\ E\ None\ ST\ e)$

and $compTs\ None\ not\ Some$ [*simp*]: $[\tau] \notin set\ (compTs\ E\ None\ ST\ es)$

by(*induct E A \equiv None :: nat hyperset ST e and E A \equiv None :: nat hyperset ST es rule: compT-compTs-induct*)
(*simp-all add:after-def*)

lemma *pair-eq-ty_i'-conv*:

$([(ST, LT)] = ty_i'\ ST_0\ E\ A) = (case\ A\ of\ None\ \Rightarrow\ False\ |\ Some\ A\ \Rightarrow\ (ST = ST_0 \wedge LT = ty_l\ E\ A))$

by(*simp add:ty_i'-def*)

lemma *pair-conv-ty_i'*: $[(ST, ty_l\ E\ A)] = ty_i'\ ST\ E\ [A]$

by(*simp add:ty_i'-def*)

lemma *ty_i'-antimono2*:

$\llbracket E \leq E'; A \subseteq A' \rrbracket \Longrightarrow P \vdash ty_i'\ ST\ E'\ [A'] \leq' ty_i'\ ST\ E\ [A]$

by(*auto simp:ty_i'-def ty_l-def list-all2-conv-all-nth less-eq-list-def prefix-def*)

declare *ty_i'-antimono* [*intro!*] *after-def*[*simp*] *pair-conv-ty_i'*[*simp*] *pair-eq-ty_i'-conv*[*simp*]

lemma *compT-LT-prefix*:

$\llbracket [(ST,LT)] \in set(compT\ E\ A\ ST_0\ e); B\ e\ (size\ E) \rrbracket \Longrightarrow P \vdash [(ST,LT)] \leq' ty_i'\ ST\ E\ A$

and *compTs-LT-prefix*:

```

[[ [(ST,LT)] ∈ set(compTs E A ST0 es); Bs es (size E) ]] ⇒ P ⊢ [(ST,LT)] ≤' ty_i' ST E A
proof(induct E A ST0 e and E A ST0 es rule: compT-compTs-induct)
  case FAss thus ?case by(fastforce simp:hyperset-defs elim!:sup-state-opt-trans)
next
  case BinOp thus ?case
    by(fastforce simp:hyperset-defs elim!:sup-state-opt-trans split:bop.splits)
next
  case Seq thus ?case by(fastforce simp:hyperset-defs elim!:sup-state-opt-trans)
next
  case While thus ?case by(fastforce simp:hyperset-defs elim!:sup-state-opt-trans)
next
  case Cond thus ?case by(fastforce simp:hyperset-defs elim!:sup-state-opt-trans)
next
  case BlockNone thus ?case by(auto)
next
  case BlockSome thus ?case
    by(clarsimp simp only: ty_i'-def)(fastforce intro: ty_i'-incr simp add: hyperset-defs elim: sup-state-opt-trans)
next
  case Call thus ?case by(fastforce simp:hyperset-defs elim!:sup-state-opt-trans)
next
  case Cons thus ?case
    by(fastforce simp:hyperset-defs elim!:sup-state-opt-trans)
next
  case TryCatch thus ?case
    by(fastforce simp:hyperset-defs intro!: ty_i'-incr elim!:sup-state-opt-trans)
next
  case NewArray thus ?case by(auto simp add: hyperset-defs)
next
  case AAcc thus ?case by(fastforce simp:hyperset-defs elim!:sup-state-opt-trans)
next
  case AAss thus ?case by(auto simp:hyperset-defs Un-ac elim!:sup-state-opt-trans)
next
  case ALen thus ?case by(auto simp add: hyperset-defs)
next
  case CompareAndSwap thus ?case by(auto simp: hyperset-defs Un-ac elim!:sup-state-opt-trans)
next
  case Synchronized thus ?case
    by(fastforce simp add: hyperset-defs elim: sup-state-opt-trans intro: sup-state-opt-trans[OF ty_i'-incr
ty_i'-antimono2])
qed (auto simp:hyperset-defs)

declare ty_i'-antimono [rule del] after-def[simp del] pair-conv-ty_i'[simp del] pair-eq-ty_i'-conv[simp del]

lemma OK-None-states [iff]: OK None ∈ states P mxs mxl
by(simp add: JVM-states-unfold)

end

context TC1 begin

lemma after-in-states:
[[ P,E ⊢1 e :: T; set E ⊆ types P; set ST ⊆ types P; size ST + max-stack e ≤ mxs ]]
⇒ OK (after E A ST e) ∈ states P mxs mxl
apply(subgoal-tac size ST + 1 ≤ mxs)

```

```

apply(simp add:after-def tyi'-def JVM-states-unfold ty1-in-types)
apply(clarify intro!: exI)
apply(rule conjI)
  apply(rule exI[where x=length ST + 1], fastforce)
apply(clarsimp)
apply(rule conjI[OF WT1-is-type[OF wf-prog]], auto intro: listI)
using max-stack1[of e] by simp

```

end

context TC0 **begin**

```

lemma OK-tyi'-in-statesI [simp]:
  [[ set E ⊆ types P; set ST ⊆ types P; size ST ≤ mxs ]]
  ⇒ OK (tyi' ST E A) ∈ states P mxs mxl
apply(simp add:tyi'-def JVM-states-unfold ty1-in-types)
apply(blast intro!:listI)
done

```

end

```

lemma is-class-type-aux: is-class P C ⇒ is-type P (Class C)
by(simp)

```

context TC1 **begin**

```

declare is-type.simps[simp del] subsetI[rule del]

```

theorem

shows compT-states:

```

[[ P,E ⊢1 e :: T; set E ⊆ types P; set ST ⊆ types P;
  size ST + max-stack e ≤ mxs; size E + max-vars e ≤ mxl ]]
⇒ OK ' set(compT E A ST e) ⊆ states P mxs mxl
(is PROP ?P e E T A ST)

```

and compTs-states:

```

[[ P,E ⊢1 es[::]Ts; set E ⊆ types P; set ST ⊆ types P;
  size ST + max-stacks es ≤ mxs; size E + max-varss es ≤ mxl ]]
⇒ OK ' set(compTs E A ST es) ⊆ states P mxs mxl
(is PROP ?Ps es E Ts A ST)

```

proof(induct E A ST e **and** E A ST es arbitrary: T **and** Ts rule: compT-compTs-induct)

```

  case new thus ?case by(simp)
next
  case (Cast C e) thus ?case by (auto simp:after-in-states)
next
  case InstanceOf thus ?case by (auto simp:after-in-states)
next
  case Val thus ?case by(simp)
next
  case Var thus ?case by(simp)
next
  case LAss thus ?case by(auto simp:after-in-states)
next
  case FAcc thus ?case by(auto simp:after-in-states)

```

```

next
  case FAss thus ?case
    by(auto simp:image-Un WT1-is-type[OF wf-prog] after-in-states)
next
  case CompareAndSwap thus ?case by(auto simp:image-Un WT1-is-type[OF wf-prog] after-in-states)
next
  case Seq thus ?case
    by(auto simp:image-Un after-in-states)
next
  case BinOp thus ?case
    by(auto simp:image-Un WT1-is-type[OF wf-prog] after-in-states)
next
  case Cond thus ?case
    by(force simp:image-Un WT1-is-type[OF wf-prog] after-in-states)
next
  case While thus ?case
    by(auto simp:image-Un WT1-is-type[OF wf-prog] after-in-states)
next
  case BlockNone thus ?case by auto
next
  case (BlockSome E A ST i ty v exp)
  with max-stack1[of exp] show ?case by(auto intro: after-in-states)
next
  case (TryCatch E A ST e1 C i e2)
  moreover have size ST + 1 ≤ mxs using TryCatch.prems max-stack1[of e1] by auto
  ultimately show ?case
    by(auto simp:image-Un WT1-is-type[OF wf-prog] after-in-states
       is-class-type-aux)
next
  case Nil thus ?case by simp
next
  case Cons thus ?case
    by(auto simp:image-Un WT1-is-type[OF wf-prog] after-in-states)
next
  case throw thus ?case
    by(auto simp: WT1-is-type[OF wf-prog] after-in-states)
next
  case Call thus ?case
    by(auto simp:image-Un WT1-is-type[OF wf-prog] after-in-states)
next
  case NewArray thus ?case
    by(auto simp:image-Un WT1-is-type[OF wf-prog] after-in-states)
next
  case AAcc thus ?case by(auto simp:image-Un WT1-is-type[OF wf-prog] after-in-states)
next
  case AAss thus ?case by(auto simp:image-Un WT1-is-type[OF wf-prog] after-in-states)
next
  case ALen thus ?case by(auto simp:image-Un WT1-is-type[OF wf-prog] after-in-states)
next
  case InSynchronized thus ?case by auto
next
  case (Synchronized E A ST i exp1 exp2)
  from  $\langle P, E \vdash 1 \text{ sync}_i (exp1) exp2 :: T \rangle$  obtain T1
  where wt1:  $P, E \vdash 1 \text{ exp1} :: T1$  and T1: is-refT T1 T1 ≠ NT

```

and $wt2: P, E@[Class\ Object] \vdash 1\ exp2 :: T$ **by** *auto*
moreover **note** $E = \langle set\ E \subseteq types\ P \rangle$ **with** *wf-prog*
have $E': set\ (E@[Class\ Object]) \subseteq types\ P$ **by**(*auto simp add: is-type.simps*)
moreover **from** *wf-prog wt2 E'* **have** $T: is-type\ P\ T$ **by**(*rule WT1-is-type*)
note $ST = \langle set\ ST \subseteq types\ P \rangle$ **with** *wf-prog*
have $ST': set\ (Class\ Object\ \#\ ST) \subseteq types\ P$ **by**(*auto simp add: is-type.simps*)
moreover **from** *wf-prog* **have** *throwable: is-type P (Class Throwable)*
unfolding *is-type.simps* **by**(*rule is-class-Throwable*)
ultimately **show** *?case using Synchronized max-stack1[of exp2] T*
by(*auto simp add: image-Un after-in-states*)
qed

declare *is-type.simps[simp] subsetI[intro!]*

end

locale $TC2 = TC0 +$
fixes $T_r :: ty$ **and** $mxs :: pc$
begin

definition

$wt-instrs :: 'addr\ instr\ list \Rightarrow ex-table \Rightarrow ty_i'\ list \Rightarrow bool$ ($\langle \vdash -, - /[::] / - \rangle [0,0,51] 50$)

where

$\vdash is,xt [::] \tau s \equiv size\ is < size\ \tau s \wedge pcs\ xt \subseteq \{0..<size\ is\} \wedge (\forall pc < size\ is.\ P, T_r, mxs, size\ \tau s, xt \vdash is!pc, pc :: \tau s)$

lemmas $wt-defs = wt-instrs-def\ wt-instr-def\ app-def\ eff-def\ norm-eff-def$

lemma $wt-instrs-Nil$ [*simp*]: $\tau s \neq [] \Longrightarrow \vdash [], [] [::] \tau s$
by(*simp add: wt-defs*)

end

locale $TC3 = TC1 + TC2$

lemma $eff-None$ [*simp*]: $eff\ i\ P\ pc\ et\ None = []$
by (*simp add: Effect.eff-def*)

declare $split-comp-eq$ [*simp del*]

lemma $wt-instr-appR$:

$\llbracket P, T, m, mpc, xt \vdash is!pc, pc :: \tau s;$
 $pc < size\ is; size\ is < size\ \tau s; mpc \leq size\ \tau s; mpc \leq mpc' \rrbracket$

$\Longrightarrow P, T, m, mpc', xt \vdash is!pc, pc :: \tau s @ \tau s'$

by (*fastforce simp: wt-instr-def app-def*)

lemma $relevant-entries-shift$ [*simp*]:

$relevant-entries\ P\ i\ (pc+n)\ (shift\ n\ xt) = shift\ n\ (relevant-entries\ P\ i\ pc\ xt)$

apply (*induct xt*)

apply (*unfold relevant-entries-def shift-def*)

apply *simp*

apply (*auto simp add: is-relevant-entry-def*)

done

lemma *xcpt-eff-shift* [*simp*]:
 $xcpt\text{-}eff\ i\ P\ (pc+n)\ \tau\ (shift\ n\ xt) =$
 $map\ (\lambda(pc,\tau).\ (pc + n,\ \tau))\ (xcpt\text{-}eff\ i\ P\ pc\ \tau\ xt)$
apply(*simp add: xcpt-eff-def*)
apply(*cases* τ)
apply(*auto simp add: shift-def*)
done

lemma *eff-shift* [*simp*]:
 $app_i\ (i,\ P,\ pc,\ m,\ T,\ \tau) \implies$
 $eff\ i\ P\ (pc+n)\ (shift\ n\ xt)\ (Some\ \tau) =$
 $map\ (\lambda(pc,\tau).\ (pc+n,\tau))\ (eff\ i\ P\ pc\ xt\ (Some\ \tau))$
apply(*simp add: eff-def norm-eff-def*)
apply(*cases* i , *auto*)
done

lemma *xcpt-app-shift* [*simp*]:
 $xcpt\text{-}app\ i\ P\ (pc+n)\ m\ (shift\ n\ xt)\ \tau = xcpt\text{-}app\ i\ P\ pc\ m\ xt\ \tau$
by (*simp add: xcpt-app-def*) (*auto simp add: shift-def*)

lemma *wt-instr-appL*:
 $\llbracket P, T, m, mpc, xt \vdash i, pc :: \tau s; pc < size\ \tau s; mpc \leq size\ \tau s \rrbracket$
 $\implies P, T, m, mpc + size\ \tau s', shift\ (size\ \tau s')\ xt \vdash i, pc + size\ \tau s' :: \tau s' @ \tau s$
apply(*clarsimp simp add: wt-instr-def app-def*)
apply(*auto*)
apply(*cases* i , *auto*)
done

lemma *wt-instr-Cons*:
 $\llbracket P, T, m, mpc - 1, \rrbracket \vdash i, pc - 1 :: \tau s;$
 $0 < pc; 0 < mpc; pc < size\ \tau s + 1; mpc \leq size\ \tau s + 1 \rrbracket$
 $\implies P, T, m, mpc, \rrbracket \vdash i, pc :: \tau \# \tau s$
apply(*drule wt-instr-appL[where $\tau s' = [\tau]$]*)
apply *arith*
apply *arith*
apply (*simp split: nat-diff-split-asm*)
done

lemma *wt-instr-append*:
 $\llbracket P, T, m, mpc - size\ \tau s', \rrbracket \vdash i, pc - size\ \tau s' :: \tau s;$
 $size\ \tau s' \leq pc; size\ \tau s' \leq mpc; pc < size\ \tau s + size\ \tau s'; mpc \leq size\ \tau s + size\ \tau s' \rrbracket$
 $\implies P, T, m, mpc, \rrbracket \vdash i, pc :: \tau s' @ \tau s$
apply(*drule wt-instr-appL[where $\tau s' = \tau s'$]*)
apply *arith*
apply *arith*

apply (*simp split:nat-diff-split-asm*)
done

lemma *xcpt-app-pcs*:

$pc \notin pcs \ xt \implies xcpt\text{-app } i \ P \ pc \ m\ x \ s \ xt \ \tau$
by (*auto simp add: xcpt-app-def relevant-entries-def is-relevant-entry-def pcs-def*)

lemma *xcpt-eff-pcs*:

$pc \notin pcs \ x \implies xcpt\text{-eff } i \ P \ pc \ \tau \ x \ t = []$
by (*cases* τ)
(*auto simp add: is-relevant-entry-def xcpt-eff-def relevant-entries-def pcs-def*
intro!: filter-False)

lemma *pcs-shift*:

$pc < n \implies pc \notin pcs \ (\text{shift } n \ x \ t)$
by (*auto simp add: shift-def pcs-def*)

lemma *xcpt-eff-shift-pc-ge-n*: **assumes** $x \in \text{set } (xcpt\text{-eff } i \ P \ pc \ \tau \ (\text{shift } n \ x \ t))$

shows $n \leq pc$

proof –

{ **assume** $pc < n$
hence $pc \notin pcs \ (\text{shift } n \ x \ t)$ **by**(*rule pcs-shift*)
with *assms* **have** *False*
by(*auto simp add: pcs-def xcpt-eff-def is-relevant-entry-def relevant-entries-def split-beta cong: filter-cong*) }
thus *?thesis* **by**(*cases* $n \leq pc$)(*auto*)
qed

lemma *wt-instr-appRx*:

$\llbracket P, T, m, mpc, xt \vdash is!pc, pc :: \tau s; pc < \text{size } is; \text{size } is < \text{size } \tau s; mpc \leq \text{size } \tau s \rrbracket$
 $\implies P, T, m, mpc, xt @ \text{shift } (\text{size } is) \ xt' \vdash is!pc, pc :: \tau s$
apply(*clarsimp simp: wt-instr-def eff-def app-def*)
apply(*fastforce dest: xcpt-eff-shift-pc-ge-n intro!: xcpt-app-pcs[OF pcs-shift]*)
done

lemma *wt-instr-appLx*:

$\llbracket P, T, m, mpc, xt \vdash i, pc :: \tau s; pc \notin pcs \ xt' \rrbracket$
 $\implies P, T, m, mpc, xt' @ xt \vdash i, pc :: \tau s$
by (*auto simp: wt-instr-def app-def eff-def xcpt-app-pcs xcpt-eff-pcs*)

context *TC2* **begin**

lemma *wt-instrs-extR*:

$\vdash is, xt [::] \tau s \implies \vdash is, xt [::] \tau s @ \tau s'$
by(*auto simp add: wt-instrs-def wt-instr-appR*)

lemma *wt-instrs-ext*:

$\llbracket \vdash is_1, xt_1 [::] \tau s_1 @ \tau s_2; \vdash is_2, xt_2 [::] \tau s_2; \text{size } \tau s_1 = \text{size } is_1 \rrbracket$
 $\implies \vdash is_1 @ is_2, xt_1 @ \text{shift } (\text{size } is_1) \ xt_2 [::] \tau s_1 @ \tau s_2$

```

apply(clarsimp simp:wt-instrs-def)
apply(rule conjI, fastforce)
apply(rule conjI, fastforce simp add: pcs-shift-conv)
apply clarsimp
apply(rule conjI, fastforce simp:wt-instr-appRx)
apply clarsimp
apply(erule-tac x = pc - size is1 in allE)+
apply(thin-tac P  $\longrightarrow$  Q for P Q)
apply(erule impE, arith)
apply(drule-tac  $\tau s' = \tau s_1$  in wt-instr-appL)
  apply arith
  apply simp
apply(fastforce simp add:add.commute intro!: wt-instr-appLx)
done

```

corollary *wt-instrs-ext2*:

```

[[  $\vdash is_2, xt_2 [::] \tau s_2; \vdash is_1, xt_1 [::] \tau s_1 @ \tau s_2; size \tau s_1 = size is_1$  ]]
 $\implies \vdash is_1 @ is_2, xt_1 @ shift (size is_1) xt_2 [::] \tau s_1 @ \tau s_2$ 
by(rule wt-instrs-ext)

```

corollary *wt-instrs-ext-prefix* [*trans*]:

```

[[  $\vdash is_1, xt_1 [::] \tau s_1 @ \tau s_2; \vdash is_2, xt_2 [::] \tau s_3;$ 
   $size \tau s_1 = size is_1; \tau s_3 \leq \tau s_2$  ]]
 $\implies \vdash is_1 @ is_2, xt_1 @ shift (size is_1) xt_2 [::] \tau s_1 @ \tau s_2$ 
by(bestsimp simp:less-eq-list-def prefix-def elim: wt-instrs-ext dest:wt-instrs-extR)

```

corollary *wt-instrs-app*:

```

assumes is1:  $\vdash is_1, xt_1 [::] \tau s_1 @ [\tau]$ 
assumes is2:  $\vdash is_2, xt_2 [::] \tau \# \tau s_2$ 
assumes s:  $size \tau s_1 = size is_1$ 
shows  $\vdash is_1 @ is_2, xt_1 @ shift (size is_1) xt_2 [::] \tau s_1 @ \tau \# \tau s_2$ 
proof –
  from is1 have  $\vdash is_1, xt_1 [::] (\tau s_1 @ [\tau]) @ \tau s_2$ 
  by (rule wt-instrs-extR)
  hence  $\vdash is_1, xt_1 [::] \tau s_1 @ \tau \# \tau s_2$  by simp
  from this is2 s show ?thesis by (rule wt-instrs-ext)
qed

```

corollary *wt-instrs-app-last* [*trans*]:

```

[[  $\vdash is_2, xt_2 [::] \tau \# \tau s_2; \vdash is_1, xt_1 [::] \tau s_1;$ 
   $last \tau s_1 = \tau; size \tau s_1 = size is_1 + 1$  ]]
 $\implies \vdash is_1 @ is_2, xt_1 @ shift (size is_1) xt_2 [::] \tau s_1 @ \tau s_2$ 
apply(cases  $\tau s_1$  rule:rev-cases)
apply simp
apply(simp add:wt-instrs-app)
done

```

corollary *wt-instrs-append-last* [*trans*]:

```

[[  $\vdash is, xt [::] \tau s; P, T_r, m\bar{x}s, m\bar{p}c, [] \vdash i, pc :: \tau s;$ 

```

```

    pc = size is; mpc = size  $\tau$ s; size is + 1 < size  $\tau$ s ]
  =>  $\vdash is@[i],xt [::] \tau s$ 
apply(clarsimp simp add:wt-instrs-def)
apply(rule conjI, fastforce)
apply(fastforce intro!:wt-instr-appLx[where xt = [],simplified]
        dest!:less-antisym)
done

```

corollary wt-instrs-app2:

```

  [  $\vdash (is_2 :: 'b$  instr list),xt2 [::]  $\tau' \# \tau s_2$ ;  $\vdash is_1,xt_1 [::] \tau \# \tau s_1 @[\tau]$ ;
    xt' = xt1 @ shift (size is1) xt2; size  $\tau s_1 + 1 = size is_1$  ]
  =>  $\vdash is_1 @ is_2,xt' [::] \tau \# \tau s_1 @ \tau' \# \tau s_2$ 
using wt-instrs-app[where ? $\tau s_1.0 = \tau \# \tau s_1$  and ?'b = 'b] by simp

```

corollary wt-instrs-app2-simp[trans,simp]:

```

  [  $\vdash (is_2 :: 'b$  instr list),xt2 [::]  $\tau' \# \tau s_2$ ;  $\vdash is_1,xt_1 [::] \tau \# \tau s_1 @[\tau]$ ; size  $\tau s_1 + 1 = size is_1$  ]
  =>  $\vdash is_1 @ is_2, xt_1 @ shift (size is_1) xt_2 [::] \tau \# \tau s_1 @ \tau' \# \tau s_2$ 
using wt-instrs-app[where ? $\tau s_1.0 = \tau \# \tau s_1$  and ?'b = 'b] by simp

```

corollary wt-instrs-Cons[simp]:

```

  [  $\tau s \neq []$ ;  $\vdash [i],[] [::] [\tau,\tau']$ ;  $\vdash is,xt [::] \tau' \# \tau s$  ]
  =>  $\vdash i \# is, shift 1 xt [::] \tau \# \tau' \# \tau s$ 
using wt-instrs-app2[where ?is1.0 = [i] and ? $\tau s_1.0 = []$  and ?is2.0 = is
  and ?xt1.0 = []]
by simp

```

corollary wt-instrs-Cons2[trans]:

```

  assumes  $\tau s$ :  $\vdash is,xt [::] \tau s$ 
  assumes  $i$ :  $P, T_r, mxs, mpc, [] \vdash i, 0 :: \tau \# \tau s$ 
  assumes  $mpc$ :  $mpc = size \tau s + 1$ 
  shows  $\vdash i \# is, shift 1 xt [::] \tau \# \tau s$ 
proof -
  from  $\tau s$  have  $\tau s \neq []$  by (auto simp: wt-instrs-def)
  with  $mpc$   $i$  have  $\vdash [i], [] [::] [\tau] @ \tau s$  by (simp add: wt-instrs-def)
  with  $\tau s$  show ?thesis by (fastforce dest: wt-instrs-ext)
qed

```

lemma wt-instrs-last-incr[trans]:

```

  [  $\vdash is,xt [::] \tau s @[\tau]$ ;  $P \vdash \tau \leq' \tau'$  ] =>  $\vdash is,xt [::] \tau s @[\tau']$ 
apply(clarsimp simp add:wt-instrs-def wt-instr-def)
apply(rule conjI)
apply(fastforce)
apply(clarsimp)
apply(rename-tac pc' tau')
apply(erule allE, erule (1) impE)
apply(clarsimp)
apply(drule (1) bspec)
apply(clarsimp)
apply(subgoal-tac pc' = size  $\tau s$ )

```

```

prefer 2
apply(clarsimp simp:app-def)
apply(drule (1) bspec)
apply(clarsimp)
apply(auto elim!:sup-state-opt-trans)
done

```

end

lemma [iff]: $xcpt\text{-app } i P pc mxs [] \tau$
by (simp add: xcpt-app-def relevant-entries-def)

lemma [simp]: $xcpt\text{-eff } i P pc \tau [] = []$
by (simp add: xcpt-eff-def relevant-entries-def)

context TC2 **begin**

lemma wt-New:
 $\llbracket is\text{-class } P C; size ST < mxs \rrbracket \implies$
 $\vdash [New C], [] [::] [ty_i' ST E A, ty_i' (Class C \# ST) E A]$
by(simp add:wt-defs ty_i'-def)

lemma wt-Cast:
 $is\text{-type } P T \implies$
 $\vdash [Checkcast T], [] [::] [ty_i' (U \# ST) E A, ty_i' (T \# ST) E A]$
by(simp add: ty_i'-def wt-defs)

lemma wt-Instanceof:
 $\llbracket is\text{-type } P T; is\text{-ref } T U \rrbracket \implies$
 $\vdash [Instanceof T], [] [::] [ty_i' (U \# ST) E A, ty_i' (Boolean \# ST) E A]$
by(simp add: ty_i'-def wt-defs)

lemma wt-Push:
 $\llbracket size ST < mxs; typeof v = Some T \rrbracket$
 $\implies \vdash [Push v], [] [::] [ty_i' ST E A, ty_i' (T \# ST) E A]$
by(simp add: ty_i'-def wt-defs)

lemma wt-Pop:
 $\vdash [Pop], [] [::] (ty_i' (T \# ST) E A \# ty_i' ST E A \# \tau s)$
by(simp add: ty_i'-def wt-defs)

lemma wt-BinOpInstr:
 $P \vdash T1 \llbracket bop \rrbracket T2 :: T \implies \vdash [BinOpInstr bop], [] [::] [ty_i' (T2 \# T1 \# ST) E A, ty_i' (T \# ST) E A]$
by(auto simp:ty_i'-def wt-defs dest: WT-binop-WTrt-binop intro: list-all2-refl)

lemma wt-Load:
 $\llbracket size ST < mxs; size E \leq mxl; i \in E; i < size E \rrbracket$
 $\implies \vdash [Load i], [] [::] [ty_i' ST E A, ty_i' (E!i \# ST) E A]$
by(auto simp add:ty_i'-def wt-defs ty_l-def hyperset-defs intro: widens-refl)

lemma *wt-Store*:

$\llbracket P \vdash T \leq E!i; i < \text{size } E; \text{size } E \leq \text{m}xl \rrbracket \implies$
 $\vdash [\text{Store } i], \llbracket [::] [ty_i' (T \# ST) E A, ty_i' ST E (\{\{i\}\} \sqcup A)]$
by(*auto simp: hyperset-defs nth-list-update ty_i'-def wt-defs ty_i-def*
intro: list-all2-all-nthI)

lemma *wt-Get*:

$\llbracket P \vdash C \text{ sees } F:T (fm) \text{ in } D; \text{class-type-of}' U = \lfloor C \rfloor \rrbracket \implies$
 $\vdash [\text{Getfield } F D], \llbracket [::] [ty_i' (U \# ST) E A, ty_i' (T \# ST) E A]$
by(*cases U*)(*auto simp: ty_i'-def wt-defs dest: sees-field-idemp sees-field-decl-above intro: widens-refl*
widen-trans widen-array-object)

lemma *wt-Put*:

$\llbracket P \vdash C \text{ sees } F:T (fm) \text{ in } D; \text{class-type-of}' U = \lfloor C \rfloor; P \vdash T' \leq T \rrbracket \implies$
 $\vdash [\text{Putfield } F D], \llbracket [::] [ty_i' (T' \# U \# ST) E A, ty_i' ST E A]$
by(*cases U*)(*auto 4 3 intro: sees-field-idemp widen-trans widen-array-object dest: sees-field-decl-above*
simp: ty_i'-def wt-defs)

lemma *wt-CAS*:

$\llbracket P \vdash C \text{ sees } F:T (fm) \text{ in } D; \text{class-type-of}' U' = \lfloor C \rfloor; \text{volatile } fm; P \vdash T2 \leq T; P \vdash T3 \leq T \rrbracket \implies$
 $\vdash [\text{CAS } F D], \llbracket [::] [ty_i' (T3 \# T2 \# U' \# ST) E A, ty_i' (\text{Boolean} \# ST) E A]$
by(*cases U'*)(*auto 4 4 simp add: ty_i'-def wt-defs intro: sees-field-idemp widen-trans widen-array-object*
dest: sees-field-decl-above)

lemma *wt-Throw*:

$P \vdash C \preceq^* \text{Throwable} \implies \vdash [\text{ThrowExc}], \llbracket [::] [ty_i' (\text{Class } C \# ST) E A, \tau']$
by(*simp add: ty_i'-def wt-defs*)

lemma *wt-IfFalse*:

$\llbracket 2 \leq i; \text{nat } i < \text{size } \tau s + 2; P \vdash ty_i' ST E A \leq' \tau s ! \text{nat}(i - 2) \rrbracket$
 $\implies \vdash [\text{IfFalse } i], \llbracket [::] [ty_i' (\text{Boolean} \# ST) E A \# ty_i' ST E A \# \tau s]$
by(*auto simp add: ty_i'-def wt-defs eval-nat-numeral nat-diff-distrib*)

lemma *wt-Goto*:

$\llbracket 0 \leq \text{int } pc + i; \text{nat } (\text{int } pc + i) < \text{size } \tau s; \text{size } \tau s \leq \text{mpc};$
 $P \vdash \tau s!pc \leq' \tau s ! \text{nat } (\text{int } pc + i) \rrbracket$
 $\implies P, T, \text{m}xs, \text{mpc}, \llbracket \vdash \text{Goto } i, pc :: \tau s$
by(*clarsimp simp add: wt-defs*)

end

context *TC3* **begin**

lemma *wt-Invoke*:

$\llbracket \text{size } es = \text{size } Ts'; \text{class-type-of}' U = \lfloor C \rfloor; P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D; P \vdash Ts' [\leq] Ts \rrbracket$
 $\implies \vdash [\text{Invoke } M (\text{size } es)], \llbracket [::] [ty_i' (\text{rev } Ts' @ U \# ST) E A, ty_i' (T \# ST) E A]$
apply(*clarsimp simp add: ty_i'-def wt-defs*)
apply *safe*
apply(*simp-all (no-asm-use)*)
apply(*auto simp add: intro: widens-refl*)
done

end

declare *nth-append*[*simp del*]
 declare [[*simproc del: list-to-set-comprehension*]]

context *TC2* begin

corollary *wt-instrs-app3*[*simp*]:

[[$\vdash (is_2 :: 'b \text{ instr list}), [] :: (\tau' \# \tau s_2); \vdash is_1, xt_1 :: \tau \# \tau s_1 @ [\tau']$; $size \tau s_1 + 1 = size is_1$]
 $\implies \vdash (is_1 @ is_2), xt_1 :: \tau \# \tau s_1 @ \tau' \# \tau s_2$

using *wt-instrs-app2*[**where** *?xt₂.0* = [] **and** *?b* = 'b] **by** (*simp add:shift-def*)

corollary *wt-instrs-Cons3*[*simp*]:

[[$\tau s \neq []$; $\vdash [i], [] :: [\tau, \tau']$; $\vdash is, [] :: \tau' \# \tau s$]
 $\implies \vdash (i \# is), [] :: \tau \# \tau' \# \tau s$

using *wt-instrs-Cons*[**where** *?xt* = []]

by (*simp add:shift-def*)

lemma *wt-instrs-xapp*:

[[$\vdash is_1 @ is_2, xt :: \tau s_1 @ ty_i' (Class D \# ST) E A \# \tau s_2$;
 $\forall \tau \in set \tau s_1. \forall ST' LT'. \tau = Some(ST', LT') \longrightarrow$
 $size ST \leq size ST' \wedge P \vdash Some(drop(size ST' - size ST) ST', LT') \leq' ty_i' ST E A$;
 $size is_1 = size \tau s_1$; $size ST < mxs$; $case Co \text{ of } None \Rightarrow D = Throwable \mid Some C \Rightarrow D = C \wedge$
is-class P C] \implies

$\vdash is_1 @ is_2, xt @ [(0, size is_1 - Suc n, Co, size is_1, size ST)] :: \tau s_1 @ ty_i' (Class D \# ST) E A \# \tau s_2$

apply(*simp add:wt-instrs-def split del: option.split-asm*)

apply(*rule conjI*)

apply(*clarsimp split del: option.split-asm*)

apply *arith*

apply(*clarsimp split del: option.split-asm*)

apply(*erule allE, erule (1) impE*)

apply(*clarsimp simp add: wt-instr-def app-def eff-def split del: option.split-asm*)

apply(*rule conjI*)

apply (*thin-tac* $\forall x \in A \cup B. P x$ **for** *A B P*)

apply (*thin-tac* $\forall x \in A \cup B. P x$ **for** *A B P*)

apply (*clarsimp simp add: xcpt-app-def relevant-entries-def split del: option.split-asm*)

apply (*simp add: nth-append is-relevant-entry-def split: if-split-asm split del: option.split-asm*)

apply (*drule-tac* $x = \tau s_1 ! pc$ **in** *bspec*)

apply (*blast intro: nth-mem*)

apply *fastforce*

apply *fastforce*

apply (*rule conjI*)

apply(*clarsimp split del: option.split-asm*)

apply (*erule disjE, blast*)

apply (*erule disjE, blast*)

apply (*clarsimp simp add: xcpt-eff-def relevant-entries-def split: if-split-asm*)

apply(*clarsimp split del: option.split-asm*)

apply (*erule disjE, blast*)

apply (*erule disjE, blast*)

apply (*clarsimp simp add: xcpt-eff-def relevant-entries-def split: if-split-asm split del: option.split-asm*)

apply (*simp add: nth-append is-relevant-entry-def split: if-split-asm split del: option.split-asm*)

```

apply (drule-tac  $x = \tau s_1!pc$  in  $bspec$ )
apply (blast intro: nth-mem)
apply (fastforce simp add: tyi'-def)
done

```

lemma *wt-instrs-xapp-Some[trans]*:

```

[[ $\vdash is_1 @ is_2, xt [::] \tau s_1 @ ty_i'$  (Class  $C \# ST$ )  $E A \# \tau s_2$ ;
 $\forall \tau \in set \tau s_1. \forall ST' LT'. \tau = Some(ST',LT') \longrightarrow$ 
 $size ST \leq size ST' \wedge P \vdash Some(drop(size ST' - size ST) ST',LT') \leq' ty_i' ST E A$ ;
 $size is_1 = size \tau s_1$ ;  $is-class P C$ ;  $size ST < mxs$  ]]  $\implies$ 
 $\vdash is_1 @ is_2, xt @ [(0, size is_1 - Suc n, Some C, size is_1, size ST)] [::] \tau s_1 @ ty_i'$  (Class  $C \# ST$ )  $E A$ 
 $\# \tau s_2$ 
by(erule ( $\exists$ ) wt-instrs-xapp) simp

```

lemma *wt-instrs-xapp-Any*:

```

[[ $\vdash is_1 @ is_2, xt [::] \tau s_1 @ ty_i'$  (Class Throwable  $\# ST$ )  $E A \# \tau s_2$ ;
 $\forall \tau \in set \tau s_1. \forall ST' LT'. \tau = Some(ST',LT') \longrightarrow$ 
 $size ST \leq size ST' \wedge P \vdash Some(drop(size ST' - size ST) ST',LT') \leq' ty_i' ST E A$ ;
 $size is_1 = size \tau s_1$ ;  $size ST < mxs$  ]]  $\implies$ 
 $\vdash is_1 @ is_2, xt @ [(0, size is_1 - Suc n, None, size is_1, size ST)] [::] \tau s_1 @ ty_i'$  (Class Throwable  $\# ST$ )
 $E A \# \tau s_2$ 
by(erule ( $\exists$ ) wt-instrs-xapp) simp

```

end

```

declare [[simproc add: list-to-set-comprehension]]
declare nth-append[simp]

```

lemma *drop-Cons-Suc*:

$\bigwedge xs. drop\ n\ xs = y\#ys \implies drop\ (Suc\ n)\ xs = ys$

```

apply (induct  $n$ )
apply simp
apply (simp add: drop-Suc)
done

```

lemma *drop-mess*:

$[[Suc\ (length\ xs_0) \leq length\ xs; drop\ (length\ xs - Suc\ (length\ xs_0))\ xs = x\ \# xs_0]$
 $\implies drop\ (length\ xs - length\ xs_0)\ xs = xs_0$

```

apply (cases  $xs$ )
apply simp
apply (simp add: Suc-diff-le)
apply (case-tac length list - length xs_0)
apply simp
apply (simp add: drop-Cons-Suc)
done

```

lemma *drop-mess2*:

assumes $len: Suc\ (Suc\ (length\ xs_0)) \leq length\ xs$
and $drop: drop\ (length\ xs - Suc\ (Suc\ (length\ xs_0)))\ xs = x1\ \# x2\ \# xs_0$
shows $drop\ (length\ xs - length\ xs_0)\ xs = xs_0$

```

proof(cases  $xs$ )
case Nil with assms show ?thesis by simp
next
case (Cons  $x\ xs'$ )

```

```

note Cons[simp]
show ?thesis
proof(cases xs')
  case Nil with assms show ?thesis by(simp)
next
  case (Cons x' xs'')
  note Cons[simp]
  show ?thesis
  proof(rule drop-mess)
    from len show Suc (length xs0) ≤ length xs by simp
  next
  have drop (length xs - length (x2 # xs0)) xs = x2 # xs0
  proof(rule drop-mess)
    from len show Suc (length (x2 # xs0)) ≤ length xs by(simp)
  next
  from drop show drop (length xs - Suc (length (x2 # xs0))) xs = x1 # x2 # xs0 by simp
  qed
  thus drop (length xs - Suc (length xs0)) xs = x2 # xs0 by(simp)
  qed
qed
qed

```

abbreviation postfix :: 'a list ⇒ 'a list ⇒ bool (‹(-/ ≧≧ -)› [51, 50] 50) **where**
 postfix xs ys ≡ suffix ys xs

lemma postfix-conv-eq-length-drop:

$ST' \gg ST \longleftrightarrow \text{length } ST \leq \text{length } ST' \wedge \text{drop } (\text{length } ST' - \text{length } ST) ST' = ST$

apply(auto)

apply (metis append-eq-conv-conj append-take-drop-id diff-is-0-eq drop-0 linorder-not-less nat-le-linear suffix-take)

apply (metis append-take-drop-id length-drop suffix-take same-append-eq size-list-def)

by (metis suffix-drop)

declare suffix-ConsI[simp]

context TC0 **begin**

declare after-def[simp] pair-eq-ty_i'-conv[simp]

lemma

assumes ST0 ≧≧ ST'

shows compT-ST-prefix:

$[(ST,LT)] \in \text{set}(\text{compT } E \ A \ ST0 \ e) \implies ST \gg ST'$

and compTs-ST-prefix:

$[(ST,LT)] \in \text{set}(\text{compTs } E \ A \ ST0 \ es) \implies ST \gg ST'$

using assms

by(induct E A ST0 e **and** E A ST0 es rule: compT-compTs-induct) auto

declare after-def[simp del] pair-eq-ty_i'-conv[simp del]

end

declare suffix-ConsI[simp del]

lemma *fun-of-simp* [*simp*]: *fun-of* S x $y = ((x,y) \in S)$
by (*simp add: fun-of-def*)

declare *widens-refl* [*iff*]

context *TC3* **begin**

theorem *compT-wt-instrs*:

$\llbracket P, E \vdash 1 e :: T; \mathcal{D} e A; \mathcal{B} e (\text{size } E); \text{size } ST + \text{max-stack } e \leq \text{maxs}; \text{size } E + \text{max-vars } e \leq \text{maxl}; \text{set } E \subseteq \text{types } P \rrbracket$

$\implies \vdash \text{compE2 } e, \text{compxE2 } e \ 0 (\text{size } ST) [\::] \text{ty}_i' ST E A \# \text{compT } E A ST e @ [\text{after } E A ST e]$
(is *PROP* $?P e E T A ST$ **)**

and *compTs-wt-instrs*:

$\llbracket P, E \vdash 1 es [\::] Ts; \mathcal{D} s es A; \mathcal{B} s es (\text{size } E); \text{size } ST + \text{max-stacks } es \leq \text{maxs}; \text{size } E + \text{max-varss } es \leq \text{maxl}; \text{set } E \subseteq \text{types } P \rrbracket$

$\implies \text{let } \tau s = \text{ty}_i' ST E A \# \text{compTs } E A ST es$

$\text{in } \vdash \text{compEs2 } es, \text{compxEs2 } es \ 0 (\text{size } ST) [\::] \tau s \wedge \text{last } \tau s = \text{ty}_i' (\text{rev } Ts @ ST) E (A \sqcup \mathcal{A} s es)$

(is *PROP* $?Ps es E Ts A ST$ **)**

proof(*induct* $E A ST e$ **and** $E A ST es$ *arbitrary: T and Ts rule: compT-compTs-induct*)

case (*TryCatch* $E A ST e_1 C i e_2$)

hence [*simp*]: $i = \text{size } E$ **by** *simp*

have $wt_1: P, E \vdash 1 e_1 :: T$ **and** $wt_2: P, E @ [\text{Class } C] \vdash 1 e_2 :: T$

and *class: is-class* $P C$ **using** *TryCatch* **by** *auto*

let $?A_1 = A \sqcup \mathcal{A} e_1$ **let** $?A_i = A \sqcup [\{i\}]$ **let** $?E_i = E @ [\text{Class } C]$

let $? \tau = \text{ty}_i' ST E A$ **let** $? \tau_{s_1} = \text{compT } E A ST e_1$

let $? \tau_1 = \text{ty}_i' (T \# ST) E ?A_1$ **let** $? \tau_2 = \text{ty}_i' (\text{Class } C \# ST) E A$

let $? \tau_3 = \text{ty}_i' ST ?E_i ?A_i$ **let** $? \tau_{s_2} = \text{compT } ?E_i ?A_i ST e_2$

let $? \tau_2' = \text{ty}_i' (T \# ST) ?E_i (?A_i \sqcup \mathcal{A} e_2)$

let $? \tau' = \text{ty}_i' (T \# ST) E (A \sqcup \mathcal{A} e_1 \sqcap (\mathcal{A} e_2 \ominus i))$

let $?go = \text{Goto} (\text{int}(\text{size}(\text{compE2 } e_2)) + 2)$

have *PROP* $?P e_2 ?E_i T ?A_i ST$ **by** *fact*

hence $\vdash \text{compE2 } e_2, \text{compxE2 } e_2 \ 0 (\text{size } ST) [\::] (? \tau_3 \# ? \tau_{s_2}) @ [? \tau_2 \uparrow]$

using *TryCatch.prem*s *class* **by**(*auto simp:after-def*)

also have $?A_i \sqcup \mathcal{A} e_2 = (A \sqcup \mathcal{A} e_2) \sqcup [\{\text{size } E\}]$

by(*fastforce simp:hyperset-defs*)

also have $P \vdash \text{ty}_i' (T \# ST) ?E_i \dots \leq' \text{ty}_i' (T \# ST) E (A \sqcup \mathcal{A} e_2)$

by(*simp add:hyperset-defs ty_l-incr ty_i'-def*)

also have $P \vdash \dots \leq' \text{ty}_i' (T \# ST) E (A \sqcup \mathcal{A} e_1 \sqcap (\mathcal{A} e_2 \ominus i))$

by(*auto intro!: ty_l-antimono simp:hyperset-defs ty_i'-def*)

also have $(? \tau_3 \# ? \tau_{s_2}) @ [? \tau \uparrow] = ? \tau_3 \# ? \tau_{s_2} @ [? \tau \uparrow]$ **by** *simp*

also have $\vdash [\text{Store } i], [\::] ? \tau_2 \# [] @ [? \tau_3]$

using *TryCatch.prem*s

by(*auto simp:nth-list-update wt-defs ty_i'-def ty_l-def*)

list-all2-conv-all-nth hyperset-defs)

also have $[] @ (? \tau_3 \# ? \tau_{s_2} @ [? \tau \uparrow]) = (? \tau_3 \# ? \tau_{s_2} @ [? \tau \uparrow])$ **by** *simp*

also have $P, T_r, \text{maxs}, \text{size}(\text{compE2 } e_2) + 3, [] \vdash ?go, 0 :: ? \tau_1 \# ? \tau_2 \# ? \tau_3 \# ? \tau_{s_2} @ [? \tau \uparrow]$

by(*auto simp: hyperset-defs ty_i'-def wt-defs nth-Cons nat-add-distrib*)

fun-of-def intro: ty_l-antimono list-all2-refl split:nat.split)

also have $\vdash \text{compE2 } e_1, \text{compxE2 } e_1 \ 0 (\text{size } ST) [\::] ? \tau \# ? \tau_{s_1} @ [? \tau_1]$

using *TryCatch* **by**(*auto simp:after-def*)

also have $? \tau \# ? \tau_{s_1} @ [? \tau_1] \# ? \tau_2 \# ? \tau_3 \# ? \tau_{s_2} @ [? \tau \uparrow] =$

$(? \tau \# ? \tau_{s_1} @ [? \tau_1]) @ ? \tau_2 \# ? \tau_3 \# ? \tau_{s_2} @ [? \tau \uparrow]$ **by** *simp*

```

also have  $\text{compE2 } e_1 @ ?go \# [Store \ i] @ \text{compE2 } e_2 =$ 
   $(\text{compE2 } e_1 @ [?go]) @ (Store \ i \# \text{compE2 } e_2) \text{ by simp}$ 
also
let  $?Q \ \tau = \forall ST' \ LT'. \ \tau = [(ST', \ LT')] \longrightarrow$ 
   $size \ ST \leq size \ ST' \wedge P \vdash Some \ (drop \ (size \ ST' - size \ ST) \ ST',LT') \leq' \ ty_i' \ ST \ E \ A$ 
  {
    have  $?Q \ (ty_i' \ ST \ E \ A) \text{ by} (clarsimp \ simp \ add: \ ty_i'\text{-def})$ 
    moreover have  $?Q \ (ty_i' \ (T \# \ ST) \ E \ ?A_1)$ 
    by  $(fastforce \ simp \ add: \ ty_i'\text{-def} \ hyperset\text{-defs} \ intro!: \ ty_1\text{-antimono})$ 
    moreover { fix  $\tau$ 
      assume  $\tau: \tau \in set \ (compT \ E \ A \ ST \ e_1)$ 
      hence  $\forall ST' \ LT'. \ \tau = [(ST', \ LT')] \longrightarrow ST' \ggg ST \text{ by} (auto \ intro: \ compT\text{-}ST\text{-prefix}[OF$ 
         $suffix\text{-order.order-refl])$ 
      with  $\tau \text{ have } ?Q \ \tau \text{ unfolding postfix-conv-eq-length-drop using } \langle \mathcal{B} \ (try \ e_1 \ catch(C \ i) \ e_2) \ (length \ E) \rangle$ 
      by} (fastforce \ dest!: \ compT\text{-}LT\text{-prefix} \ simp \ add: \ ty_i'\text{-def}) \}
    ultimately
    have  $\forall \tau \in set \ (ty_i' \ ST \ E \ A \# \ compT \ E \ A \ ST \ e_1 @ [ty_i' \ (T \# \ ST) \ E \ ?A_1]). \ ?Q \ \tau \text{ by auto}$ 
  }
also from  $TryCatch.prem\ max\text{-}stack1[of \ e_1] \text{ have } size \ ST + 1 \leq mxs \text{ by auto}$ 
ultimately show  $?case \text{ using } wt_1 \ wt_2 \ TryCatch.prem\ class$ 
by  $(simp \ add: \ after\text{-}def)(erule\text{-}tac \ x=0 \text{ in } meta\text{-}allE, \ simp)$ 
next
case  $(Synchronized \ E \ A \ ST \ i \ e_1 \ e_2)$ 
note  $wt = \langle P, E \vdash 1 \ sync_i \ (e_1) \ e_2 :: T \rangle$ 
then obtain  $U \text{ where } wt_1: P, E \vdash 1 \ e_1 :: U$ 
and  $U: is\text{-}refT \ U \ U \neq NT$ 
and  $wt_2: P, E @ [Class \ Object] \vdash 1 \ e_2 :: T \text{ by auto}$ 
from  $\langle \mathcal{B} \ (sync_i \ (e_1) \ e_2) \ (length \ E) \rangle \text{ have } [simp]: i = length \ E$ 
and  $B_1: \mathcal{B} \ e_1 \ (length \ E) \text{ and } B_2: \mathcal{B} \ e_2 \ (length \ (E @ [Class \ Object])) \text{ by auto}$ 

note  $lenST = \langle length \ ST + \ max\text{-}stack \ (sync_i \ (e_1) \ e_2) \leq mxs \rangle$ 
note  $lenE = \langle length \ E + \ max\text{-}vars \ (sync_i \ (e_1) \ e_2) \leq mxl \rangle$ 

let  $?A1 = A \sqcup \mathcal{A} \ e_1 \text{ let } ?A2 = ?A1 \sqcup \{i\}$ 
let  $?A3 = ?A2 \sqcup \mathcal{A} \ e_2 \text{ let } ?A4 = ?A1 \sqcup \mathcal{A} \ e_2$ 
let  $?E1 = E @ [Class \ Object]$ 
let  $?\tau = ty_i' \ ST \ E \ A \text{ let } ?\tau s1 = compT \ E \ A \ ST \ e_1$ 
let  $?\tau 1 = ty_i' \ (U \# \ ST) \ E \ ?A1$ 
let  $?\tau 1' = ty_i' \ (Class \ Object \# \ Class \ Object \# \ ST) \ E \ ?A1$ 
let  $?\tau 1'' = ty_i' \ (Class \ Object \# \ ST) \ ?E1 \ ?A2$ 
let  $?\tau 1''' = ty_i' \ ST \ ?E1 \ ?A2$ 
let  $?\tau s2 = compT \ ?E1 \ ?A2 \ ST \ e_2$ 
let  $?\tau 2 = ty_i' \ (T \# \ ST) \ ?E1 \ ?A3 \text{ let } ?\tau 2' = ty_i' \ (Class \ Object \# \ T \# \ ST) \ ?E1 \ ?A3$ 
let  $?\tau 2'' = ?\tau 2$ 
let  $?\tau 3 = ty_i' \ (Class \ Throwable \# \ ST) \ ?E1 \ ?A2$ 
let  $?\tau 3' = ty_i' \ (Class \ Object \# \ Class \ Throwable \# \ ST) \ ?E1 \ ?A2$ 
let  $?\tau 3'' = ?\tau 3$ 
let  $?\tau' = ty_i' \ (T \# \ ST) \ E \ ?A4$ 

from  $lenE \ lenST \ max\text{-}stack1[of \ e_2] \ U$ 
have  $\vdash [Load \ i, MExit, ThrowExc], [] [::] [?\tau 3, ?\tau 3', ?\tau 3'', ?\tau']$ 
by} (auto \ simp \ add: \ ty_i'\text{-}def \ ty_1\text{-}def \ wt\text{-}defs \ hyperset\text{-}defs \ nth\text{-}Cons \ split: \ nat.split)
also have  $P, T_r, mxs, 5, [] \vdash Goto \ 4, 0 :: [?\tau 2'', ?\tau 3, ?\tau 3', ?\tau 3'', ?\tau']$ 

```

by(*auto simp: hyperset-defs ty_i'-def wt-defs intro: ty₁-antimono ty₁-incr*)
also have $P, T_r, mxs, 6, [] \vdash MExit, 0 :: [?τ 2', ?τ 2'', ?τ 3, ?τ 3', ?τ 3'', ?τ]$
by(*auto simp: hyperset-defs ty_i'-def wt-defs intro: ty₁-antimono ty₁-incr*)
also from $lenE lenST max-stack1 [of e2]$
have $P, T_r, mxs, 7, [] \vdash Load i, 0 :: [?τ 2, ?τ 2', ?τ 2'', ?τ 3, ?τ 3', ?τ 3'', ?τ]$
by(*auto simp: hyperset-defs ty_i'-def wt-defs ty₁-def intro: ty₁-antimono*)
also from $\langle D (sync_i (e1) e2) A \rangle \text{ have } D e2 (A \sqcup A e1 \sqcup [\{length E\}])$
by(*auto elim!: D-mono' simp add: hyperset-defs*)
with $\langle PROP ?P e2 ?E1 T ?A2 ST \rangle \text{ Synchronized wt2 is-class-Object [OF wf-prog]}$
have $\vdash compE2 e2, compxE2 e2 0 (size ST) [::] ?τ 1''' \# ?τ s2 @ [?τ 2]$
by(*auto simp add: after-def*)
finally have $\vdash (compE2 e2 @ [Load i, MExit, Goto 4]) @ [Load i, MExit, ThrowExc], compxE2 e2 0 (size ST) [::]$
 $(?τ 1''' \# ?τ s2 @ [?τ 2, ?τ 2', ?τ 2'']) @ [?τ 3, ?τ 3', ?τ 3'', ?τ]$
by(*simp*)
hence $\vdash (compE2 e2 @ [Load i, MExit, Goto 4]) @ [Load i, MExit, ThrowExc],$
 $compxE2 e2 0 (size ST) @ [(0, size (compE2 e2 @ [Load i, MExit, Goto 4]) - Suc 2, None,$
 $size (compE2 e2 @ [Load i, MExit, Goto 4]), size ST) [::]$
 $(?τ 1''' \# ?τ s2 @ [?τ 2, ?τ 2', ?τ 2'']) @ [?τ 3, ?τ 3', ?τ 3'', ?τ]$
proof(*rule wt-instrs-xapp-Any*)
from $lenST$ **show** $length ST < mxs$ **by** *simp*
next
show $\forall \tau \in set (?τ 1''' \# ?τ s2 @ [?τ 2, ?τ 2', ?τ 2'']). \forall ST' LT'. \tau = [(ST', LT')] \longrightarrow length ST \leq length ST' \wedge$
 $P \vdash [(drop (length ST' - length ST) ST', LT')] \leq' ty_i' ST (E @ [Class Object]) ?A2$
proof(*intro strip*)
fix $\tau ST' LT'$
assume $\tau \in set (?τ 1''' \# ?τ s2 @ [?τ 2, ?τ 2', ?τ 2'']) \tau = [(ST', LT')]$
hence $\tau: [(ST', LT')] \in set (?τ 1''' \# ?τ s2 @ [?τ 2, ?τ 2', ?τ 2''])$ **by** *simp*
show $length ST \leq length ST' \wedge P \vdash [(drop (length ST' - length ST) ST', LT')] \leq' ty_i' ST (E @ [Class Object]) ?A2$
proof(*cases [(ST', LT')] \in set ?τ s2*)
case *True*
from $compT-ST-prefix [OF suffix-order.order-refl this] compT-LT-prefix [OF this B2]$
show *?thesis unfolding postfix-conv-eq-length-drop* **by**(*simp add: ty_i'-def*)
next
case *False*
with τ **show** *?thesis*
by(*auto simp add: ty_i'-def hyperset-defs intro: ty₁-antimono*)
qed
qed
qed *simp*
hence $\vdash compE2 e2 @ [Load i, MExit, Goto 4, Load i, MExit, ThrowExc],$
 $compxE2 e2 0 (size ST) @ [(0, size (compE2 e2), None, Suc (Suc (Suc (size (compE2 e2))))),$
 $size ST) [::]$
 $?τ 1''' \# ?τ s2 @ [?τ 2, ?τ 2', ?τ 2'', ?τ 3, ?τ 3', ?τ 3'', ?τ]$ **by** *simp*
also from $wt1 \langle set E \subseteq types P \rangle$ **have** *is-type P U* **by**(*rule WT1-is-type [OF wf-prog]*)
with U **have** $P \vdash U \leq Class Object$ **by**(*auto elim!: is-refT.cases intro: subcls-C-Object [OF - wf-prog]*
widen-array-object)
with $lenE lenST max-stack1 [of e2]$
have $\vdash [Dup, Store i, MEnter], [] [::] [?τ 1, ?τ 1', ?τ 1''] @ [?τ 1''']$
by(*auto simp add: ty_i'-def ty₁-def wt-defs hyperset-defs nth-Cons nth-list-update list-all2-conv-all-nth split: nat.split*)
finally have $\vdash Dup \# Store i \# MEnter \# compE2 e2 @ [Load i, MExit, Goto 4, Load i, MExit,$

ThrowExc],
 $\text{compxE2 } e2 \ 3 \ (\text{size } ST) \ @ \ [(3, 3 + \text{size } (\text{compE2 } e2), \text{None}, 6 + \text{size } (\text{compE2 } e2), \text{size } ST)]$
 $[::] \ ?\tau1 \ # \ ?\tau1' \ # \ ?\tau1'' \ # \ ?\tau1''' \ # \ ?\tau s2 \ @ \ [?\tau2, ?\tau2', ?\tau2'', ?\tau3, ?\tau3', ?\tau3'', ?\tau]$
by (*simp add: eval-nat-numeral shift-def*)
also from $\langle PROP \ ?P \ e1 \ E \ U \ A \ ST \rangle \ wt1 \ B1 \ \langle D \ (\text{sync}_i \ (e1) \ e2) \ A \ \text{lenE} \ \text{lenST} \ \langle \text{set } E \subseteq \text{types } P \rangle$
have $\vdash \text{compE2 } e1, \text{compxE2 } e1 \ 0 \ (\text{size } ST) [::] \ ?\tau \ # \ ?\tau s1 \ @ [?\tau1]$
by (*auto simp add: after-def*)
finally show $?case$ **using** $wt1 \ wt2 \ wt$ **by** (*simp add: after-def ac-simps shift-Cons-tuple hyperUn-assoc*)
next
case new thus $?case$ **by** (*auto simp add: after-def wt-New*)
next
case ($BinOp \ E \ A \ ST \ e1 \ bop \ e2$)
have $T: P, E \vdash 1 \ e1 \ \ll bop \gg \ e2 :: T$ **by fact**
then obtain $T_1 \ T_2$ **where** $T_1: P, E \vdash 1 \ e1 :: T_1$ **and** $T_2: P, E \vdash 1 \ e2 :: T_2$ **and**
 $bopT: P \vdash T_1 \ll bop \gg T_2 :: T$ **by auto**
let $?A_1 = A \sqcup \mathcal{A} \ e1$ **let** $?A_2 = ?A_1 \sqcup \mathcal{A} \ e2$
let $?\tau = \text{ty}_i' \ ST \ E \ A$ **let** $?\tau s_1 = \text{compT } E \ A \ ST \ e1$
let $?\tau_1 = \text{ty}_i' \ (T_1 \# ST) \ E \ ?A_1$ **let** $?\tau s_2 = \text{compT } E \ ?A_1 \ (T_1 \# ST) \ e2$
let $?\tau_2 = \text{ty}_i' \ (T_2 \# T_1 \# ST) \ E \ ?A_2$ **let** $?'\tau = \text{ty}_i' \ (T \# ST) \ E \ ?A_2$
from $bopT$ **have** $\vdash [BinOpInstr \ bop], [] [::] [?\tau_2, ?'\tau]$ **by** (*rule wt-BinOpInstr*)
also from $BinOp.hyps(2)[of \ T_2] \ BinOp.premis \ T_2 \ T_1$
have $\vdash \text{compE2 } e2, \text{compxE2 } e2 \ 0 \ (\text{size } (\text{ty } E \ e1 \ # \ ST)) [::] \ ?\tau_1 \ # \ ?\tau s_2 \ @ [?\tau_2]$ **by** (*auto simp: after-def*)
also from $BinOp \ T_1$ **have** $\vdash \text{compE2 } e1, \text{compxE2 } e1 \ 0 \ (\text{size } ST) [::] \ ?\tau \ # \ ?\tau s_1 \ @ [?\tau_1]$
by (*auto simp: after-def*)
finally show $?case$ **using** $T \ T_1 \ T_2$ **by** (*simp add: after-def hyperUn-assoc*)
next
case ($Cons \ E \ A \ ST \ e \ es$)
have $P, E \vdash 1 \ e \ # \ es [::] \ Ts$ **by fact**
then obtain $T_e \ Ts'$ **where**
 $T_e: P, E \vdash 1 \ e :: T_e$ **and** $Ts': P, E \vdash 1 \ es [::] \ Ts'$ **and**
 $Ts: Ts = T_e \ # \ Ts'$ **by auto**
let $?A_e = A \sqcup \mathcal{A} \ e$
let $?\tau = \text{ty}_i' \ ST \ E \ A$ **let** $?\tau s_e = \text{compT } E \ A \ ST \ e$
let $?'\tau_e = \text{ty}_i' \ (T_e \ # \ ST) \ E \ ?A_e$ **let** $?'\tau s' = \text{compT } E \ ?A_e \ (T_e \ # \ ST) \ es$
let $?'\tau s = ?\tau \ # \ ?\tau s_e \ @ \ (?'\tau_e \ # \ ?'\tau s')$
from $Cons.hyps(2) \ Cons.premis \ T_e \ Ts'$
have $\vdash \text{compEs2 } es, \text{compxEs2 } es \ 0 \ (\text{size } (T_e \ # \ ST)) [::] \ ?\tau_e \ # \ ?'\tau s'$ **by** (*simp add: after-def*)
also from $Cons \ T_e$ **have** $\vdash \text{compE2 } e, \text{compxE2 } e \ 0 \ (\text{size } ST) [::] \ ?\tau \ # \ ?\tau s_e \ @ [?\tau_e]$ **by** (*auto simp: after-def*)
moreover
from $Cons.hyps(2)[OF \ Ts'] \ Cons.premis \ T_e \ Ts' \ Ts$
have $\text{last } ?\tau s = \text{ty}_i' \ (\text{rev } Ts \ @ \ ST) \ E \ (?A_e \sqcup \mathcal{A} \ es)$ **by simp**
ultimately show $?case$ **using** T_e
by (*auto simp add: after-def hyperUn-assoc shift-compxEs2 stack-xlift-compxEs2 simp del: compxE2-size-convs compxEs2-size-convs compxEs2-stack-xlift-convs compxE2-stack-xlift-convs intro: wt-instrs-app2*)
next
case ($FAss \ E \ A \ ST \ e1 \ F \ D \ e2$)
hence $Void: P, E \vdash 1 \ e1 \cdot F \{D\} := e2 :: Void$ **by auto**
then obtain $U \ C \ T \ T' \ \text{fm}$ **where**
 $C: P, E \vdash 1 \ e1 :: U$ **and** $U: \text{class-type-of}' \ U = [C]$ **and** $\text{sees}: P \vdash C \ \text{sees } F: T \ (\text{fm}) \ \text{in } D$ **and**
 $T': P, E \vdash 1 \ e2 :: T'$ **and** $T'-T: P \vdash T' \leq T$ **by auto**
let $?A_1 = A \sqcup \mathcal{A} \ e1$ **let** $?A_2 = ?A_1 \sqcup \mathcal{A} \ e2$
let $?\tau = \text{ty}_i' \ ST \ E \ A$ **let** $?\tau s_1 = \text{compT } E \ A \ ST \ e1$

```

let ? $\tau_1$  =  $ty_i'$  (U#ST) E ? $A_1$  let ? $\tau_{s_2}$  =  $compT$  E ? $A_1$  (U#ST)  $e_2$ 
let ? $\tau_2$  =  $ty_i'$  (T'#U#ST) E ? $A_2$  let ? $\tau_3$  =  $ty_i'$  ST E ? $A_2$ 
let ? $\tau'$  =  $ty_i'$  (Void#ST) E ? $A_2$ 
from FAss.prem $s$  sees T'-T U
have  $\vdash$  [Putfield F D,Push Unit],[] [::] [? $\tau_2$ ,? $\tau_3$ ,? $\tau'$ ]
  by (fastforce simp add: wt-Push wt-Put)
also from FAss.hyps(2)[of T'] FAss.prem $s$  T' C
have  $\vdash$   $compE2$   $e_2$ ,  $compxE2$   $e_2$  0 (size ST+1) [::] ? $\tau_1$ #? $\tau_{s_2}$ @[? $\tau_2$ ]
  by (auto simp add: after-def hyperUn-assoc)
also from FAss C have  $\vdash$   $compE2$   $e_1$ ,  $compxE2$   $e_1$  0 (size ST) [::] ? $\tau$ #? $\tau_{s_1}$ @[? $\tau_1$ ]
  by (auto simp add: after-def)
finally show ?case using Void C T' by (simp add: after-def hyperUn-assoc)
next
case Val thus ?case by(auto simp:after-def wt-Push)
next
case (Cast T exp) thus ?case by (auto simp:after-def wt-Cast)
next
case (InstanceOf E A ST e) thus ?case
  by(auto simp:after-def intro!: wt-InstanceOf wt-instrs-app3 intro: widen-refT refT-widen)
next
case (BlockNone E A ST i Ti e)
from  $\langle P, E \vdash 1 \{i:Ti=None; e\} :: T \rangle$  have  $wte: P, E@[Ti] \vdash 1 e :: T$ 
  and  $Ti: is-type P Ti$  by auto
let ? $\tau_s$  =  $ty_i'$  ST E A #  $compT$  (E @ [Ti]) (A  $\ominus$  i) ST e
from BlockNone wte Ti
have  $\vdash$   $compE2$  e,  $compxE2$  e 0 (size ST) [::] ? $\tau_s$  @ [ $ty_i'$  (T#ST) (E@[Ti]) (A  $\ominus$  (size E)  $\sqcup$  A e)]
  by(auto simp add: after-def)
also have  $P \vdash ty_i'$  (T # ST) (E@[Ti]) (A  $\ominus$  size E  $\sqcup$  A e)  $\leq'$   $ty_i'$  (T # ST) (E@[Ti]) ((A  $\sqcup$  A e)  $\ominus$  size E)
  by(auto simp add:hyperset-defs intro:  $ty_i'$ -antimono)
also have ... =  $ty_i'$  (T # ST) E (A  $\sqcup$  A e) by simp
also have  $P \vdash \dots \leq'$   $ty_i'$  (T # ST) E (A  $\sqcup$  (A e  $\ominus$  i))
  by(auto simp add:hyperset-defs intro:  $ty_i'$ -antimono)
finally show ?case using BlockNone.prem $s$  by(simp add: after-def)
next
case (BlockSome E A ST i Ti v e)
from  $\langle P, E \vdash 1 \{i:Ti=[v]; e\} :: T \rangle$  obtain Tv
  where  $Tv: P, E \vdash 1 Val v :: Tv$   $P \vdash Tv \leq Ti$ 
  and  $wte: P, E@[Ti] \vdash 1 e :: T$ 
  and  $Ti: is-type P Ti$  by auto
from  $\langle length ST + max-stack \{i:Ti=[v]; e\} \leq mxs \rangle$ 
have  $lenST: length ST + max-stack e \leq mxs$  by simp
from  $\langle length E + max-vars \{i:Ti=[v]; e\} \leq mxl \rangle$ 
have  $lenE: length (E@[Ti]) + max-vars e \leq mxl$  by simp
from  $\langle \mathcal{B} \{i:Ti=[v]; e\} (length E) \rangle$  have [simp]:  $i = length E$ 
  and  $B: \mathcal{B} e (length (E@[Ti]))$  by auto

from BlockSome wte
have  $\vdash$   $compE2$  e,  $compxE2$  e 0 (size ST) [::] ( $ty_i'$  ST (E @ [Ti]) (A  $\sqcup$  [ $\{length E\}$ ])) #  $compT$  (E @ [Ti]) (A  $\sqcup$  [ $\{i\}$ ]) ST e) @ [ $ty_i'$  (T#ST) (E@[Ti]) (A  $\sqcup$  [ $\{size E\}$ ]  $\sqcup$  A e)]
  by(auto simp add: after-def)
also have  $P \vdash ty_i'$  (T # ST) (E @ [Ti]) (A  $\sqcup$  [ $\{length E\}$ ]  $\sqcup$  A e)  $\leq'$   $ty_i'$  (T # ST) (E @ [Ti]) ((A  $\sqcup$  A e)  $\ominus$  length E)

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    by(auto simp add: hyperset-defs intro: tyi'-antimono)
  also have ... = tyi' (T # ST) E (A ⊔ A e) by simp
  also have P ⊢ ... ≤' tyi' (T # ST) E (A ⊔ (A e ⊖ i))
    by(auto simp add: hyperset-defs intro: tyi'-antimono)
  also note append-Cons
  also {
    from lenST max-stack1[of e] Tv
    have ⊢ [Push v], [] [::] [tyi' ST E A, tyi' (ty E (Val v) # ST) E A]
      by(auto intro: wt-Push)
    moreover from Tv lenE
    have ⊢ [Store (length E)], [] [::] [tyi' (Tv # ST) (E @ [Ti]) (A ⊖ length E), tyi' ST (E @ [Ti])
      (⊔ [length E] ⊔ (A ⊖ length E))]
      by -(rule wt-Store, auto)
    moreover have tyi' (Tv # ST) (E @ [Ti]) (A ⊖ length E) = tyi' (Tv # ST) E A by(simp add:
      tyi'-def)
    moreover have ⊔ [length E] ⊔ (A ⊖ length E) = A ⊔ ⊔ [length E] by(simp add: hyperset-defs)
    ultimately have ⊢ [Push v, Store (length E)], [] [::] [tyi' ST E A, tyi' (Tv # ST) E A, tyi' ST
      (E @ [Ti]) (A ⊔ ⊔ [length E])]
      using Tv by(auto intro: wt-instrs-Cons3)
  }
  finally show ?case using Tv ⟨P,E ⊢ 1 {i:Ti=[v]; e} :: T⟩ wte by(simp add: after-def)
next
  case Var thus ?case by(auto simp:after-def wt-Load)
next
  case FAcc thus ?case by(auto simp:after-def wt-Get)
next
  case (LAss E A ST i e) thus ?case using max-stack1[of e]
    by(auto simp: hyper-insert-comm after-def wt-Store wt-Push simp del: hyperUn-comm hyperUn-leftComm)
next
  case Nil thus ?case by auto
next
  case throw thus ?case by(auto simp add: after-def wt-Throw)
next
  case (While E A ST e c)
  obtain Tc where wte: P,E ⊢ 1 e :: Boolean and wtc: P,E ⊢ 1 c :: Tc
    and [simp]: T = Void using While by auto
  have [simp]: ty E (while (e) c) = Void using While by simp
  let ?A0 = A ⊔ A e let ?A1 = ?A0 ⊔ A c
  let ?τ = tyi' ST E A let ?τse = compT E A ST e
  let ?τe = tyi' (Boolean#ST) E ?A0 let ?τ1 = tyi' ST E ?A0
  let ?τsc = compT E ?A0 ST c let ?τc = tyi' (Tc#ST) E ?A1
  let ?τ2 = tyi' ST E ?A1 let ?τ' = tyi' (Void#ST) E ?A0
  let ?τs = (?τ # ?τse @ [?τe]) @ ?τ1 # ?τsc @ [?τc, ?τ2, ?τ1, ?τ']
  have ⊢ [], [] [::] [] @ ?τs by(simp add:wt-instrs-def)
  also
  from While.hyps(1)[of Boolean] While.premss
  have ⊢ compE2 e, compxE2 e 0 (size ST) [::] ?τ # ?τse @ [?τe]
    by (auto simp:after-def)
  also
  have [] @ ?τs = (?τ # ?τse) @ ?τe # ?τ1 # ?τsc @ [?τc, ?τ2, ?τ1, ?τ'] by simp
  also
  let ?ne = size(compE2 e) let ?nc = size(compE2 c)
  let ?if = IfFalse (int ?nc + 3)
  have ⊢ [?if], [] [::] ?τe # ?τ1 # ?τsc @ [?τc, ?τ2, ?τ1, ?τ']

```

by(*simp add: wt-instr-Cons wt-instr-append wt-IfFalse*
nat-add-distrib split: nat-diff-split)

also
have $(?τ \# ?τ_{s_e}) @ (?τ_e \# ?τ_1 \# ?τ_{s_c} @ [?τ_c, ?τ_2, ?τ_1, ?τ]) = ?τ_s$ **by** *simp*
also from *While.hyps(2)[of Tc] While.prem s wtc*
have $\vdash \text{compE2 } c, \text{compxE2 } c \ 0 \ (\text{size } ST) [::] ?τ_1 \# ?τ_{s_c} @ [?τ_c]$
by (*auto simp: after-def*)
also have $?τ_s = (?τ \# ?τ_{s_e} @ [?τ_e, ?τ_1] @ ?τ_{s_c}) @ [?τ_c, ?τ_2, ?τ_1, ?τ]$ **by** *simp*
also have $\vdash [Pop], [] [::] [?τ_c, ?τ_2]$ **by** (*simp add: wt-Pop*)
also have $(?τ \# ?τ_{s_e} @ [?τ_e, ?τ_1] @ ?τ_{s_c}) @ [?τ_c, ?τ_2, ?τ_1, ?τ] = ?τ_s$ **by** *simp*
also let $?go = \text{Goto } (-\text{int}(?n_c + ?n_e + 2))$
have $P \vdash ?τ_2 \leq' ?τ$ **by** (*fastforce intro: ty_i'-antimono simp: hyperset-defs*)
hence $P, T_r, \text{maxs}, \text{size } ?τ_s, [] \vdash ?go, ?n_e + ?n_c + 2 [::] ?τ_s$
by (*simp add: wt-Goto split: nat-diff-split*)
also have $?τ_s = (?τ \# ?τ_{s_e} @ [?τ_e, ?τ_1] @ ?τ_{s_c} @ [?τ_c, ?τ_2]) @ [?τ_1, ?τ]$
by *simp*
also have $\vdash [Push \ Unit], [] [::] [?τ_1, ?τ]$
using *While.prem s max-stack1 [of c] by(auto simp add: wt-Push)*
finally show $?case$ **using** *wtc wte*
by (*simp add: after-def*)

next
case (*Cond E A ST e e₁ e₂*)
obtain $T_1 \ T_2$ **where** $wte: P, E \vdash 1 \ e [::] \text{Boolean}$
and $w_{t_1}: P, E \vdash 1 \ e_1 [::] T_1$ **and** $w_{t_2}: P, E \vdash 1 \ e_2 [::] T_2$
and $sub_1: P \vdash T_1 \leq T$ **and** $sub_2: P \vdash T_2 \leq T$
using *Cond by(auto dest: is-lub-upper)*
have $[simp]: ty \ E \ (\text{if } (e) \ e_1 \ \text{else } e_2) = T$ **using** *Cond by simp*
let $?A_0 = A \sqcup \mathcal{A} \ e$ **let** $?A_2 = ?A_0 \sqcup \mathcal{A} \ e_2$ **let** $?A_1 = ?A_0 \sqcup \mathcal{A} \ e_1$
let $?A' = ?A_0 \sqcup \mathcal{A} \ e_1 \sqcap \mathcal{A} \ e_2$
let $?τ_2 = ty_i' \ ST \ E \ ?A_0$ **let** $?τ' = ty_i' \ (T \# ST) \ E \ ?A'$
let $?τ_{s_2} = \text{compT } E \ ?A_0 \ ST \ e_2$
have *PROP ?P e₂ E T₂ ?A₀ ST by fact*
hence $\vdash \text{compE2 } e_2, \text{compxE2 } e_2 \ 0 \ (\text{size } ST) [::] (?τ_2 \# ?τ_{s_2}) @ [ty_i' \ (T_2 \# ST) \ E \ ?A_2]$
using *Cond.prem s w_{t_2} by(auto simp add: after-def)*
also have $P \vdash ty_i' \ (T_2 \# ST) \ E \ ?A_2 \leq' ?τ'$ **using** *sub₂*
by (*auto simp add: hyperset-defs ty_i'-def intro!: ty_i'-antimono*)
also
let $?τ_3 = ty_i' \ (T_1 \# ST) \ E \ ?A_1$
let $?g_2 = \text{Goto}(\text{int}(\text{size}(\text{compE2 } e_2) + 1))$
from *sub₁* **have** $P, T_r, \text{maxs}, \text{size}(\text{compE2 } e_2) + 2, [] \vdash ?g_2, 0 [::] ?τ_3 \# (?τ_2 \# ?τ_{s_2}) @ [?τ]$
by (*cases length (compE2 e₂)*)
(auto simp: hyperset-defs wt-defs nth-Cons ty_i'-def neq-Nil-conv
split: nat.split intro!: ty_i'-antimono)
also let $?τ_{s_1} = \text{compT } E \ ?A_0 \ ST \ e_1$
have *PROP ?P e₁ E T₁ ?A₀ ST by fact*
hence $\vdash \text{compE2 } e_1, \text{compxE2 } e_1 \ 0 \ (\text{size } ST) [::] ?τ_2 \# ?τ_{s_1} @ [?τ_3]$
using *Cond.prem s w_{t_1} by(auto simp add: after-def)*
also
let $?τ_{s_{12}} = ?τ_2 \# ?τ_{s_1} @ ?τ_3 \# (?τ_2 \# ?τ_{s_2}) @ [?τ]$
let $?τ_1 = ty_i' \ (\text{Boolean} \# ST) \ E \ ?A_0$
let $?g_1 = \text{IfFalse}(\text{int}(\text{size}(\text{compE2 } e_1) + 2))$
let $?code = \text{compE2 } e_1 @ ?g_2 \# \text{compE2 } e_2$
have $\vdash [?g_1], [] [::] [?τ_1] @ ?τ_{s_{12}}$
by (*simp add: wt-IfFalse nat-add-distrib split: nat-diff-split*)

```

also (wt-instrs-ext2) have [ $?\tau_1$ ] @  $?\tau_{s_{12}} = ?\tau_1 \# ?\tau_{s_{12}}$  by simp also
let  $? \tau = ty_i' ST E A$ 
have PROP  $?P e E$  Boolean  $A ST$  by fact
hence  $\vdash compE2 e, compxE2 e 0 (size ST) [::] ?\tau \# compT E A ST e @ [?\tau_1]$ 
using Cond.premis wte by(auto simp add:after-def)
finally show  $?case$  using wte  $wt_1 wt_2$  by(simp add:after-def hyperUn-assoc)
next
case (Call E A ST e M es)
from  $\langle P, E \vdash 1 e \cdot M(es) :: T \rangle$ 
obtain  $U C D Ts m Ts'$ 
where  $C: P, E \vdash 1 e :: U$ 
and icto: class-type-of'  $U = [C]$ 
and method:  $P \vdash C$  sees  $M: Ts \rightarrow T = m$  in  $D$ 
and wtes:  $P, E \vdash 1 es [::] Ts'$  and subs:  $P \vdash Ts' [\leq] Ts$ 
by(cases) auto
from wtes have same-size:  $size es = size Ts'$  by(rule WTs1-same-size)
let  $?A_0 = A \sqcup \mathcal{A} e$  let  $?A_1 = ?A_0 \sqcup \mathcal{A} s es$ 
let  $? \tau = ty_i' ST E A$  let  $? \tau_{s_e} = compT E A ST e$ 
let  $? \tau_e = ty_i' (U \# ST) E ?A_0$ 
let  $? \tau_{s_{es}} = compTs E ?A_0 (U \# ST) es$ 
let  $? \tau_1 = ty_i' (rev Ts' @ U \# ST) E ?A_1$ 
let  $? \tau' = ty_i' (T \# ST) E ?A_1$ 
have  $\vdash [Invoke M (size es)], [] [::] [?\tau_1, ?\tau']$ 
by(rule wt-Invoke[OF same-size icto method subs])
also
from Call.hyps(2)[of  $Ts'$ ] Call.premis wtes  $C$ 
have  $\vdash compEs2 es, compxEs2 es 0 (size ST+1) [::] ?\tau_e \# ?\tau_{s_{es}}$ 
last ( $?\tau_e \# ?\tau_{s_{es}} = ?\tau_1$ )
by(auto simp add:after-def)
also have ( $?\tau_e \# ?\tau_{s_{es}}$ ) @  $[?\tau'] = ?\tau_e \# ?\tau_{s_{es}} @ [?\tau']$  by simp
also have  $\vdash compE2 e, compxE2 e 0 (size ST) [::] ?\tau \# ?\tau_{s_e} @ [?\tau_e]$ 
using Call  $C$  by(auto simp add:after-def)
finally show  $?case$  using Call.premis  $C$ 
by(simp add:after-def hyperUn-assoc shift-compxEs2 stack-xlift-compxEs2 del: compxEs2-stack-xlift-convs
compxEs2-size-convs)
next
case Seq thus  $?case$ 
by(auto simp:after-def)
(fastforce simp:wt-Push wt-Pop hyperUn-assoc
intro:wt-instrs-app2 wt-instrs-Cons)
next
case (NewArray E A ST Ta e)
from  $\langle P, E \vdash 1 newA Ta|e [::] T \rangle$ 
have  $\vdash [NewArray Ta], [] [::] [ty_i' (Integer \# ST) E (A \sqcup \mathcal{A} e), ty_i' (Ta[] \# ST) E (A \sqcup \mathcal{A} e)]$ 
by(auto simp:hyperset-defs  $ty_i'$ -def wt-defs  $ty_1$ -def)
with NewArray show  $?case$  by(auto simp: after-def intro: wt-instrs-app3)
next
case (ALen E A ST exp)
{ fix  $T$ 
have  $\vdash [ALength], [] [::] [ty_i' (T[] \# ST) E (A \sqcup \mathcal{A} exp), ty_i' (Integer \# ST) E (A \sqcup \mathcal{A} exp)]$ 
by(auto simp:hyperset-defs  $ty_i'$ -def wt-defs  $ty_1$ -def) }
with ALen show  $?case$  by(auto simp add: after-def)(rule wt-instrs-app2, auto)
next
case (AAcc E A ST a i)

```



```

from  $\langle P, E \vdash 1 a[i] :: T \rangle$  have  $wta: P, E \vdash 1 a :: T[]$  and  $wti: P, E \vdash 1 i :: Integer$  by auto
let  $?A1 = A \sqcup \mathcal{A} a$  let  $?A2 = ?A1 \sqcup \mathcal{A} i$ 
let  $? \tau = ty_i' ST E A$  let  $? \tau sa = compT E A ST a$ 
let  $? \tau 1 = ty_i' (T[] \# ST) E ?A1$  let  $? \tau si = compT E ?A1 (T[] \# ST) i$ 
let  $? \tau 2 = ty_i' (Integer \# T[] \# ST) E ?A2$  let  $? \tau' = ty_i' (T \# ST) E ?A2$ 
have  $\vdash [ALoad], [] [::] [? \tau 2, ? \tau \uparrow]$  by (auto simp add: ty_i'-def wt-defs)
also from  $AAcc.hyps(2)[of Integer]$   $AAcc.premis wti wta$ 
have  $\vdash compE2 i, compxE2 i 0 (size ST + 1) [::] ? \tau 1 \# ? \tau si @ [? \tau 2]$ 
  by (auto simp add: after-def)
also from  $wta AAcc$  have  $\vdash compE2 a, compxE2 a 0 (size ST) [::] ? \tau \# ? \tau sa @ [? \tau 1]$ 
  by (auto simp add: after-def)
finally show  $?case$  using  $wta wti \langle P, E \vdash 1 a[i] :: T \rangle$  by (simp add: after-def hyperUn-assoc)
next
case ( $AAss E A ST a i e$ )
note  $wt = \langle P, E \vdash 1 a[i] := e :: T \rangle$ 
then obtain  $Ta U$  where  $wta: P, E \vdash 1 a :: Ta[]$  and  $wti: P, E \vdash 1 i :: Integer$ 
  and  $wte: P, E \vdash 1 e :: U$  and  $U: P \vdash U \leq Ta$  and  $[simp]: T = Void$  by auto
let  $?A1 = A \sqcup \mathcal{A} a$  let  $?A2 = ?A1 \sqcup \mathcal{A} i$  let  $?A3 = ?A2 \sqcup \mathcal{A} e$ 
let  $? \tau = ty_i' ST E A$  let  $? \tau sa = compT E A ST a$ 
let  $? \tau 1 = ty_i' (Ta[] \# ST) E ?A1$  let  $? \tau si = compT E ?A1 (Ta[] \# ST) i$ 
let  $? \tau 2 = ty_i' (Integer \# Ta[] \# ST) E ?A2$  let  $? \tau se = compT E ?A2 (Integer \# Ta[] \# ST) e$ 
let  $? \tau 3 = ty_i' (U \# Integer \# Ta[] \# ST) E ?A3$  let  $? \tau 4 = ty_i' ST E ?A3$ 
let  $? \tau' = ty_i' (Void \# ST) E ?A3$ 
from  $\langle length ST + max-stack (a[i] := e) \leq mxs \rangle$ 
have  $\vdash [AStore, Push Unit], [] [::] [? \tau 3, ? \tau 4, ? \tau \uparrow]$ 
  by (auto simp add: ty_i'-def wt-defs nth-Cons split: nat.split)
also from  $AAss.hyps(3)[of U]$   $wte AAss.premis wta wti$ 
have  $\vdash compE2 e, compxE2 e 0 (size ST + 2) [::] ? \tau 2 \# ? \tau se @ [? \tau 3]$ 
  by (auto simp add: after-def)
also from  $AAss.hyps(2)[of Integer]$   $wti wta AAss.premis$ 
have  $\vdash compE2 i, compxE2 i 0 (size ST + 1) [::] ? \tau 1 \# ? \tau si @ [? \tau 2]$ 
  by (auto simp add: after-def)
also from  $wta AAss$  have  $\vdash compE2 a, compxE2 a 0 (size ST) [::] ? \tau \# ? \tau sa @ [? \tau 1]$ 
  by (auto simp add: after-def)
finally show  $?case$  using  $wta wti wte \langle P, E \vdash 1 a[i] := e :: T \rangle$ 
  by (simp add: after-def hyperUn-assoc)
next
case ( $CompareAndSwap E A ST e1 D F e2 e3$ )
note  $wt = \langle P, E \vdash 1 e1 \cdot compareAndSwap(D \cdot F, e2, e3) :: T \rangle$ 
then obtain  $T1 T2 T3 C fm T'$  where  $[simp]: T = Boolean$ 
  and  $wt1: P, E \vdash 1 e1 :: T1$  class-type-of'  $T1 = [C]$   $P \vdash C$  sees  $F: T' (fm)$  in  $D$  volatile fm
  and  $wt2: P, E \vdash 1 e2 :: T2$   $P \vdash T2 \leq T'$  and  $wt3: P, E \vdash 1 e3 :: T3$   $P \vdash T3 \leq T'$ 
  by auto
let  $?A1 = A \sqcup \mathcal{A} e1$  let  $?A2 = ?A1 \sqcup \mathcal{A} e2$  let  $?A3 = ?A2 \sqcup \mathcal{A} e3$ 
let  $? \tau = ty_i' ST E A$  let  $? \tau s1 = compT E A ST e1$ 
let  $? \tau 1 = ty_i' (T1 \# ST) E ?A1$  let  $? \tau s2 = compT E ?A1 (T1 \# ST) e2$ 
let  $? \tau 2 = ty_i' (T2 \# T1 \# ST) E ?A2$  let  $? \tau s3 = compT E ?A2 (T2 \# T1 \# ST) e3$ 
let  $? \tau 3 = ty_i' (T3 \# T2 \# T1 \# ST) E ?A3$ 
let  $? \tau' = ty_i' (Boolean \# ST) E ?A3$ 
from  $\langle length ST + max-stack (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) \leq mxs \rangle$ 
have  $\vdash [CAS F D], [] [::] [? \tau 3, ? \tau \uparrow]$  using  $wt1 wt2 wt3$ 
  by (cases T1) (auto simp add: ty_i'-def wt-defs nth-Cons split: nat.split intro: sees-field-idemp widen-trans[OF widen-array-object] dest: sees-field-decl-above)
also from  $CompareAndSwap.hyps(3)[of T3]$   $wt3 CompareAndSwap.premis wt1 wt2$ 

```

```

have  $\vdash \text{compE2 } e3, \text{compxE2 } e3 0 \text{ (size } ST+2) [::] ?\tau2\#\? \tau s3@[?\tau3]$ 
  by(auto simp add: after-def)
also from CompareAndSwap.hyps(2)[of T2] wt2 wt1 CompareAndSwap.prems
have  $\vdash \text{compE2 } e2, \text{compxE2 } e2 0 \text{ (size } ST+1) [::] ?\tau1\#\? \tau s2@[?\tau2]$ 
  by(auto simp add: after-def)
also from wt1 CompareAndSwap have  $\vdash \text{compE2 } e1, \text{compxE2 } e1 0 \text{ (size } ST) [::] ?\tau\#\? \tau s1@[?\tau1]$ 
  by(auto simp add: after-def)
also have ty E (e1 • compareAndSwap(D • F, e2, e3)) = T using wt by(rule ty-def2)
ultimately show ?case using wt1 wt2 wt3
  by(simp add: after-def hyperUn-assoc)
next
  case (InSynchronized i a exp) thus ?case by auto
qed

end

lemma states-compP [simp]: states (compP f P) mxs mxl = states P mxs mxl
by (simp add: JVM-states-unfold)

lemma [simp]: appi (i, compP f P, pc, mpc, T,  $\tau$ ) = appi (i, P, pc, mpc, T,  $\tau$ )
proof –
  { fix ST LT
    have  $\text{app}_i (i, \text{compP } f P, pc, mpc, T, (ST, LT)) = \text{app}_i (i, P, pc, mpc, T, (ST, LT))$ 
    proof(cases i)
      case (Invoke M n)
        have  $\bigwedge C Ts D. (\exists T m. \text{compP } f P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D) \longleftrightarrow (\exists T m. P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D)$ 
          by(auto dest!: sees-method-compPD dest: sees-method-compP)
          with Invoke show ?thesis by clarsimp
        qed(simp-all) }
    thus ?thesis by(cases  $\tau$ ) simp
  }
qed

lemma [simp]: is-relevant-entry (compP f P) i = is-relevant-entry P i
  apply (rule ext)+
  apply (unfold is-relevant-entry-def)
  apply (cases i)
  apply auto
  done

lemma [simp]: relevant-entries (compP f P) i pc xt = relevant-entries P i pc xt
by (simp add: relevant-entries-def)

lemma [simp]: app i (compP f P) mpc T pc mxl xt  $\tau$  = app i P mpc T pc mxl xt  $\tau$ 
  apply (simp add: app-def xcpt-app-def eff-def xcpt-eff-def norm-eff-def)
  apply (fastforce simp add: image-def)
  done

lemma [simp]: app i P mpc T pc mxl xt  $\tau \implies \text{eff } i (compP f P) pc xt \tau = \text{eff } i P pc xt \tau$ 
  apply (clarsimp simp add: eff-def norm-eff-def xcpt-eff-def app-def)
  apply (cases i)
  apply(auto)
  done

```

```

lemma [simp]: widen (compP f P) = widen P
  apply (rule ext)+
  apply (simp)
  done

```

```

lemma [simp]: compP f P ⊢ τ ≤' τ' = P ⊢ τ ≤' τ'
by (simp add: sup-state-opt-def sup-state-def sup-ty-opt-def)

```

```

lemma [simp]: compP f P, T, mpc, mxl, xt ⊢ i, pc :: τs = P, T, mpc, mxl, xt ⊢ i, pc :: τs
by (simp add: wt-instr-def cong: conj-cong)

```

```

declare TC0.compT-sizes[simp] TC1.ty-def2[OF TC1.intro, simp]

```

```

lemma compT-method:

```

```

  fixes e and A and C and Ts and mxl0
  defines [simp]: E ≡ Class C # Ts
    and [simp]: A ≡ [..size Ts]
    and [simp]: A' ≡ A ⊔ A e
    and [simp]: mxs ≡ max-stack e
    and [simp]: mxl0 ≡ max-vars e
    and [simp]: mxl ≡ 1 + size Ts + mxl0

```

```

  assumes wf-prog: wf-prog p P

```

```

  shows [ P, E ⊢ 1 e :: T; D e A; B e (size E); set E ⊆ types P; P ⊢ T ≤ T' ] ⇒
    wt-method (compP2 P) C Ts T' mxs mxl0 (compE2 e @ [Return]) (compxE2 e 0 0)
    (TC0.tyi' mxl [ E A # TC0.compTa P mxl E A ] e)

```

```

using wf-prog

```

```

apply(simp add:wt-method-def TC0.compTa-def TC0.after-def compP2-def compMb2-def)

```

```

apply(rule conjI)

```

```

apply(simp add:check-types-def TC0.OK-tyi'-in-statesI)

```

```

apply(rule conjI)

```

```

  apply(frule WT1-is-type[OF wf-prog])

```

```

  apply simp

```

```

  apply(insert max-stack1[of e])

```

```

  apply(fastforce intro!: TC0.OK-tyi'-in-statesI)

```

```

apply(erule (1) TC1.compT-states[OF TC1.intro])

```

```

  apply simp

```

```

  apply simp

```

```

  apply simp

```

```

apply simp

```

```

apply(rule conjI)

```

```

apply(fastforce simp add:wt-start-def TC0.tyi'-def TC0.ty1-def list-all2-conv-all-nth nth-Cons split:nat.split
dest:less-antisym)

```

```

apply (frule (1) TC3.compT-wt-instrs[OF TC3.intro[OF TC1.intro], where ST = [] and mxs =
max-stack e and mxl = 1 + size Ts + max-vars e])

```

```

  apply simp

```

```

  apply simp

```

```

  apply simp

```

```

  apply simp

```

```

apply simp

```

```

apply (clarsimp simp:TC2.wt-instrs-def TC0.after-def)

```

```

apply(rule conjI)

```

```

  apply (fastforce)

```

```

apply(clarsimp)

```

```

apply(drule (1) less-antisym)
apply(thin-tac  $\forall x. P x$  for P)
apply(clarsimp simp:TC2.wt-defs xcpt-app-pcs xcpt-eff-pcs TC0.ty_i'-def)
done

```

definition *compTP* :: 'addr J1-prog \Rightarrow ty_P

where

```

compTP P C M  $\equiv$ 
  let (D,Ts,T,meth) = method P C M;
      e = the meth;
      E = Class C # Ts;
      A = [..size Ts];
      mxl = 1 + size Ts + max-vars e
  in (TC0.ty_i' mxl [] E A # TC0.compTa P mxl E A [] e)

```

theorem *wt-compTP-compP2*:

```

wf-J1-prog P  $\implies$  wf-jvm-prog compTP P (compP2 P)
apply (simp add: wf-jvm-prog-phi-def compP2-def compMb2-def)
apply (rule wf-prog-compPI)
prefer 2 apply assumption
apply (clarsimp simp add: wf-mdecl-def)
apply (simp add: compTP-def)
apply (rule compT-method [simplified compP2-def compMb2-def, simplified])
apply assumption+
apply (drule (1) sees-wf-mdecl)
apply (simp add: wf-mdecl-def)
apply (fastforce intro: sees-method-is-class)
apply assumption
done

```

theorem *wt-compP2*:

```

wf-J1-prog P  $\implies$  wf-jvm-prog (compP2 P)
by(auto simp add: wf-jvm-prog-def intro: wt-compTP-compP2)

```

end

7.14 Unobservable steps for the JVM

theory *JVMTau* **imports**

```

  TypeComp
  ../JVM/JVMThreaded
  ../Framework/FWLTS

```

begin

```

declare nth-append [simp del]
declare Listn.lesub-list-impl-same-size[simp del]
declare listE-length [simp del]

```

```

declare match-ex-table-append-not-pcs[simp del]
  outside-pcs-not-matches-entry [simp del]

```

outside-pcs-comp α E2-not-matches-entry [simp del]
outside-pcs-comp α Es2-not-matches-entry [simp del]

context *JVM-heap-base* **begin**

primrec $\tau instr :: 'm prog \Rightarrow 'heap \Rightarrow 'addr\ val\ list \Rightarrow 'addr\ instr \Rightarrow bool$

where

$\tau instr\ P\ h\ stk\ (Load\ n) = True$
 $\tau instr\ P\ h\ stk\ (Store\ n) = True$
 $\tau instr\ P\ h\ stk\ (Push\ v) = True$
 $\tau instr\ P\ h\ stk\ (New\ C) = False$
 $\tau instr\ P\ h\ stk\ (NewArray\ T) = False$
 $\tau instr\ P\ h\ stk\ ALoad = False$
 $\tau instr\ P\ h\ stk\ AStore = False$
 $\tau instr\ P\ h\ stk\ ALength = False$
 $\tau instr\ P\ h\ stk\ (Getfield\ F\ D) = False$
 $\tau instr\ P\ h\ stk\ (Putfield\ F\ D) = False$
 $\tau instr\ P\ h\ stk\ (CAS\ F\ D) = False$
 $\tau instr\ P\ h\ stk\ (Checkcast\ T) = True$
 $\tau instr\ P\ h\ stk\ (Instanceof\ T) = True$
 $\tau instr\ P\ h\ stk\ (Invoke\ M\ n) =$
 $(n < length\ stk \wedge$
 $(stk\ !\ n = Null \vee$
 $(\forall T\ Ts\ Tr\ D.\ typeof_addr\ h\ (the_Addr\ (stk\ !\ n)) = [T] \longrightarrow P \vdash\ class_type_of\ T\ sees\ M:Ts \rightarrow Tr =$
*Native\ in\ D \longrightarrow \tau external_defs\ D\ M)))
 $\tau instr\ P\ h\ stk\ Return = True$
 $\tau instr\ P\ h\ stk\ Pop = True$
 $\tau instr\ P\ h\ stk\ Dup = True$
 $\tau instr\ P\ h\ stk\ Swap = True$
 $\tau instr\ P\ h\ stk\ (BinOpInstr\ bop) = True$
 $\tau instr\ P\ h\ stk\ (Goto\ i) = True$
 $\tau instr\ P\ h\ stk\ (IfFalse\ i) = True$
 $\tau instr\ P\ h\ stk\ ThrowExc = True$
 $\tau instr\ P\ h\ stk\ MEnter = False$
 $\tau instr\ P\ h\ stk\ MExit = False$*

inductive $\tau move2 :: 'm prog \Rightarrow 'heap \Rightarrow 'addr\ val\ list \Rightarrow 'addr\ expr1 \Rightarrow nat \Rightarrow 'addr\ option \Rightarrow bool$

and $\tau moves2 :: 'm prog \Rightarrow 'heap \Rightarrow 'addr\ val\ list \Rightarrow 'addr\ expr1\ list \Rightarrow nat \Rightarrow 'addr\ option \Rightarrow bool$

for $P :: 'm prog$ **and** $h :: 'heap$ **and** $stk :: 'addr\ val\ list$

where

$\tau move2xcp: pc < length\ (compE2\ e) \Longrightarrow \tau move2\ P\ h\ stk\ e\ pc\ [xcp]$
 $\tau move2NewArray: \tau move2\ P\ h\ stk\ e\ pc\ xcp \Longrightarrow \tau move2\ P\ h\ stk\ (newA\ T[e])\ pc\ xcp$
 $\tau move2Cast: \tau move2\ P\ h\ stk\ e\ pc\ xcp \Longrightarrow \tau move2\ P\ h\ stk\ (Cast\ T\ e)\ pc\ xcp$
 $\tau move2CastRed: \tau move2\ P\ h\ stk\ (Cast\ T\ e)\ (length\ (compE2\ e))\ None$
 $\tau move2InstanceOf: \tau move2\ P\ h\ stk\ e\ pc\ xcp \Longrightarrow \tau move2\ P\ h\ stk\ (e\ instanceof\ T)\ pc\ xcp$
 $\tau move2InstanceOfRed: \tau move2\ P\ h\ stk\ (e\ instanceof\ T)\ (length\ (compE2\ e))\ None$
 $\tau move2Val: \tau move2\ P\ h\ stk\ (Val\ v)\ 0\ None$
 $\tau move2BinOp1:$
 $\tau move2\ P\ h\ stk\ e1\ pc\ xcp \Longrightarrow \tau move2\ P\ h\ stk\ (e1 \ll bop \gg e2)\ pc\ xcp$

$\tau\text{move2BinOp2}$:
 $\tau\text{move2 } P \text{ h stk } e2 \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (e1 \ll\text{bop}\gg e2) (\text{length } (\text{compE2 } e1) + \text{pc}) \text{ } xcp$

$\tau\text{move2BinOp}$:
 $\tau\text{move2 } P \text{ h stk } (e1 \ll\text{bop}\gg e2) (\text{length } (\text{compE2 } e1) + \text{length } (\text{compE2 } e2)) \text{ } \text{None}$

$\tau\text{move2Var}$:
 $\tau\text{move2 } P \text{ h stk } (\text{Var } V) 0 \text{ } \text{None}$

$\tau\text{move2LAss}$:
 $\tau\text{move2 } P \text{ h stk } e \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (V := e) \text{ pc } xcp$

$\tau\text{move2LAssRed1}$:
 $\tau\text{move2 } P \text{ h stk } (V := e) (\text{length } (\text{compE2 } e)) \text{ } \text{None}$

$\tau\text{move2LAssRed2}$: $\tau\text{move2 } P \text{ h stk } (V := e) (\text{Suc } (\text{length } (\text{compE2 } e))) \text{ } \text{None}$

$\tau\text{move2AAcc1}$: $\tau\text{move2 } P \text{ h stk } a \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (a[i]) \text{ pc } xcp$

$\tau\text{move2AAcc2}$: $\tau\text{move2 } P \text{ h stk } i \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (a[i]) (\text{length } (\text{compE2 } a) + \text{pc}) \text{ } xcp$

$\tau\text{move2AAss1}$: $\tau\text{move2 } P \text{ h stk } a \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (a[i] := e) \text{ pc } xcp$

$\tau\text{move2AAss2}$: $\tau\text{move2 } P \text{ h stk } i \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (a[i] := e) (\text{length } (\text{compE2 } a) + \text{pc}) \text{ } xcp$

$\tau\text{move2AAss3}$: $\tau\text{move2 } P \text{ h stk } e \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (a[i] := e) (\text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + \text{pc}) \text{ } xcp$

$\tau\text{move2AAssRed}$: $\tau\text{move2 } P \text{ h stk } (a[i] := e) (\text{Suc } (\text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + \text{length } (\text{compE2 } e))) \text{ } \text{None}$

$\tau\text{move2ALength}$: $\tau\text{move2 } P \text{ h stk } a \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (a \cdot \text{length}) \text{ pc } xcp$

$\tau\text{move2FAcc}$: $\tau\text{move2 } P \text{ h stk } e \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (e \cdot F\{D\}) \text{ pc } xcp$

$\tau\text{move2FAss1}$: $\tau\text{move2 } P \text{ h stk } e \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (e \cdot F\{D\} := e') \text{ pc } xcp$

$\tau\text{move2FAss2}$: $\tau\text{move2 } P \text{ h stk } e' \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (e \cdot F\{D\} := e') (\text{length } (\text{compE2 } e) + \text{pc}) \text{ } xcp$

$\tau\text{move2FAssRed}$: $\tau\text{move2 } P \text{ h stk } (e \cdot F\{D\} := e') (\text{Suc } (\text{length } (\text{compE2 } e) + \text{length } (\text{compE2 } e'))) \text{ } \text{None}$

$\tau\text{move2CAS1}$: $\tau\text{move2 } P \text{ h stk } e \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (e \cdot \text{compareAndSwap}(D \cdot F, e', e'')) \text{ pc } xcp$

$\tau\text{move2CAS2}$: $\tau\text{move2 } P \text{ h stk } e' \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (e \cdot \text{compareAndSwap}(D \cdot F, e', e'')) (\text{length } (\text{compE2 } e) + \text{pc}) \text{ } xcp$

$\tau\text{move2CAS3}$: $\tau\text{move2 } P \text{ h stk } e'' \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (e \cdot \text{compareAndSwap}(D \cdot F, e', e'')) (\text{length } (\text{compE2 } e) + \text{length } (\text{compE2 } e') + \text{pc}) \text{ } xcp$

$\tau\text{move2CallObj}$:
 $\tau\text{move2 } P \text{ h stk } \text{obj} \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (\text{obj} \cdot M(\text{ps})) \text{ pc } xcp$

$\tau\text{move2CallParams}$:
 $\tau\text{moves2 } P \text{ h stk } \text{ps} \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (\text{obj} \cdot M(\text{ps})) (\text{length } (\text{compE2 } \text{obj}) + \text{pc}) \text{ } xcp$

$\tau\text{move2Call}$:
 $\llbracket \text{length } \text{ps} < \text{length } \text{stk};$
 $\text{stk} ! \text{length } \text{ps} = \text{Null} \vee$
 $(\forall T \text{ Ts } \text{Tr } D. \text{typeof-addr } h (\text{the-Addr } (\text{stk} ! \text{length } \text{ps})) = \llbracket T \rrbracket \longrightarrow P \vdash \text{class-type-of } T \text{ sees}$
 $M : \text{Ts} \rightarrow \text{Tr} = \text{Native in } D \longrightarrow \tau\text{external-defs } D \text{ } M \rrbracket$
 $\implies \tau\text{move2 } P \text{ h stk } (\text{obj} \cdot M(\text{ps})) (\text{length } (\text{compE2 } \text{obj}) + \text{length } (\text{compEs2 } \text{ps})) \text{ } \text{None}$

$\tau\text{move2BlockSome1}$:

$\tau\text{move2 } P \text{ h stk } \{V:T=[v]; e\} 0 \text{ None}$
 $|\ \tau\text{move2BlockSome2:}$
 $\tau\text{move2 } P \text{ h stk } \{V:T=[v]; e\} (\text{Suc } 0) \text{ None}$
 $|\ \tau\text{move2BlockSome:}$
 $\tau\text{move2 } P \text{ h stk } e \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } \{V:T=[v]; e\} (\text{Suc } (\text{Suc } pc)) \text{ xcp}$
 $|\ \tau\text{move2BlockNone:}$
 $\tau\text{move2 } P \text{ h stk } e \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } \{V:T=None; e\} pc \text{ xcp}$

$|\ \tau\text{move2Sync1:}$
 $\tau\text{move2 } P \text{ h stk } o' \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (\text{sync}_V (o') e) \text{ pc } xcp$
 $|\ \tau\text{move2Sync2:}$
 $\tau\text{move2 } P \text{ h stk } (\text{sync}_V (o') e) (\text{length } (\text{compE2 } o')) \text{ None}$
 $|\ \tau\text{move2Sync3:}$
 $\tau\text{move2 } P \text{ h stk } (\text{sync}_V (o') e) (\text{Suc } (\text{length } (\text{compE2 } o'))) \text{ None}$
 $|\ \tau\text{move2Sync4:}$
 $\tau\text{move2 } P \text{ h stk } e \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (\text{sync}_V (o') e) (\text{Suc } (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } o') + pc)))) \text{ xcp}$
 $|\ \tau\text{move2Sync5:}$
 $\tau\text{move2 } P \text{ h stk } (\text{sync}_V (o') e) (\text{Suc } (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } o') + \text{length } (\text{compE2 } e)))) \text{ None}$
 $|\ \tau\text{move2Sync6:}$
 $\tau\text{move2 } P \text{ h stk } (\text{sync}_V (o') e) (5 + \text{length } (\text{compE2 } o') + \text{length } (\text{compE2 } e)) \text{ None}$
 $|\ \tau\text{move2Sync7:}$
 $\tau\text{move2 } P \text{ h stk } (\text{sync}_V (o') e) (6 + \text{length } (\text{compE2 } o') + \text{length } (\text{compE2 } e)) \text{ None}$
 $|\ \tau\text{move2Sync8:}$
 $\tau\text{move2 } P \text{ h stk } (\text{sync}_V (o') e) (8 + \text{length } (\text{compE2 } o') + \text{length } (\text{compE2 } e)) \text{ None}$

$|\ \tau\text{move2InSync: } \tau\text{move2 } P \text{ h stk } (\text{insync}_V (a) e) 0 \text{ None}$

$|\ \tau\text{move2Seq1:}$
 $\tau\text{move2 } P \text{ h stk } e \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (e;;e') \text{ pc } xcp$
 $|\ \tau\text{move2SeqRed:}$
 $\tau\text{move2 } P \text{ h stk } (e;;e') (\text{length } (\text{compE2 } e)) \text{ None}$
 $|\ \tau\text{move2Seq2:}$
 $\tau\text{move2 } P \text{ h stk } e' \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (e;;e') (\text{Suc } (\text{length } (\text{compE2 } e) + pc)) \text{ xcp}$

$|\ \tau\text{move2Cond:}$
 $\tau\text{move2 } P \text{ h stk } e \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (\text{if } (e) e1 \text{ else } e2) \text{ pc } xcp$
 $|\ \tau\text{move2CondRed:}$
 $\tau\text{move2 } P \text{ h stk } (\text{if } (e) e1 \text{ else } e2) (\text{length } (\text{compE2 } e)) \text{ None}$
 $|\ \tau\text{move2CondThen:}$
 $\tau\text{move2 } P \text{ h stk } e1 \text{ pc } xcp$
 $\implies \tau\text{move2 } P \text{ h stk } (\text{if } (e) e1 \text{ else } e2) (\text{Suc } (\text{length } (\text{compE2 } e) + pc)) \text{ xcp}$
 $|\ \tau\text{move2CondThenExit:}$
 $\tau\text{move2 } P \text{ h stk } (\text{if } (e) e1 \text{ else } e2) (\text{Suc } (\text{length } (\text{compE2 } e) + \text{length } (\text{compE2 } e1))) \text{ None}$
 $|\ \tau\text{move2CondElse:}$
 $\tau\text{move2 } P \text{ h stk } e2 \text{ pc } xcp$
 $\implies \tau\text{move2 } P \text{ h stk } (\text{if } (e) e1 \text{ else } e2) (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e) + \text{length } (\text{compE2 } e1) + pc))) \text{ xcp}$

$|\ \tau\text{move2While1:}$
 $\tau\text{move2 } P \text{ h stk } c \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (\text{while } (c) e) \text{ pc } xcp$
 $|\ \tau\text{move2While2:}$
 $\tau\text{move2 } P \text{ h stk } e \text{ pc } xcp \implies \tau\text{move2 } P \text{ h stk } (\text{while } (c) e) (\text{Suc } (\text{length } (\text{compE2 } c) + pc)) \text{ xcp}$

$\tau\text{move2While3}$: — Jump back to condition
 $\tau\text{move2 } P \ h \ stk \ (\text{while } (c) \ e) \ (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } c) + \text{length } (\text{compE2 } e)))) \ \text{None}$
 $\tau\text{move2While4}$: — last instruction: Push Unit
 $\tau\text{move2 } P \ h \ stk \ (\text{while } (c) \ e) \ (\text{Suc } (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } c) + \text{length } (\text{compE2 } e)))) \ \text{None}$
 $\tau\text{move2While5}$: — IfFalse instruction
 $\tau\text{move2 } P \ h \ stk \ (\text{while } (c) \ e) \ (\text{length } (\text{compE2 } c)) \ \text{None}$
 $\tau\text{move2While6}$: — Pop instruction
 $\tau\text{move2 } P \ h \ stk \ (\text{while } (c) \ e) \ (\text{Suc } (\text{length } (\text{compE2 } c) + \text{length } (\text{compE2 } e))) \ \text{None}$

$\tau\text{move2Throw1}$:
 $\tau\text{move2 } P \ h \ stk \ e \ pc \ xcp \Longrightarrow \tau\text{move2 } P \ h \ stk \ (\text{throw } e) \ pc \ xcp$
 $\tau\text{move2Throw2}$:
 $\tau\text{move2 } P \ h \ stk \ (\text{throw } e) \ (\text{length } (\text{compE2 } e)) \ \text{None}$

$\tau\text{move2Try1}$:
 $\tau\text{move2 } P \ h \ stk \ e \ pc \ xcp \Longrightarrow \tau\text{move2 } P \ h \ stk \ (\text{try } e \ \text{catch}(C \ V) \ e') \ pc \ xcp$
 $\tau\text{move2TryJump}$:
 $\tau\text{move2 } P \ h \ stk \ (\text{try } e \ \text{catch}(C \ V) \ e') \ (\text{length } (\text{compE2 } e)) \ \text{None}$
 $\tau\text{move2TryCatch2}$:
 $\tau\text{move2 } P \ h \ stk \ (\text{try } e \ \text{catch}(C \ V) \ e') \ (\text{Suc } (\text{length } (\text{compE2 } e))) \ \text{None}$
 $\tau\text{move2Try2}$:
 $\tau\text{move2 } P \ h \ stk \ \{V:T=\text{None}; e'\} \ pc \ xcp$
 $\Longrightarrow \tau\text{move2 } P \ h \ stk \ (\text{try } e \ \text{catch}(C \ V) \ e') \ (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e) + pc))) \ xcp$

$\tau\text{moves2Hd}$:
 $\tau\text{move2 } P \ h \ stk \ e \ pc \ xcp \Longrightarrow \tau\text{moves2 } P \ h \ stk \ (e \ \# \ es) \ pc \ xcp$
 $\tau\text{moves2Tl}$:
 $\tau\text{moves2 } P \ h \ stk \ es \ pc \ xcp \Longrightarrow \tau\text{moves2 } P \ h \ stk \ (e \ \# \ es) \ (\text{length } (\text{compE2 } e) + pc) \ xcp$

inductive-cases $\tau\text{move2-cases}$:

$\tau\text{move2 } P \ h \ stk \ (\text{new } C) \ pc \ xcp$
 $\tau\text{move2 } P \ h \ stk \ (\text{newA } T \ [e]) \ pc \ xcp$
 $\tau\text{move2 } P \ h \ stk \ (\text{Cast } T \ e) \ pc \ xcp$
 $\tau\text{move2 } P \ h \ stk \ (e \ \text{instanceof } T) \ pc \ xcp$
 $\tau\text{move2 } P \ h \ stk \ (\text{Val } v) \ pc \ xcp$
 $\tau\text{move2 } P \ h \ stk \ (\text{Var } V) \ pc \ xcp$
 $\tau\text{move2 } P \ h \ stk \ (e1 \ \llbracket \text{bop} \rrbracket \ e2) \ pc \ xcp$
 $\tau\text{move2 } P \ h \ stk \ (V \ := \ e) \ pc \ xcp$
 $\tau\text{move2 } P \ h \ stk \ (e1 \ [e2]) \ pc \ xcp$
 $\tau\text{move2 } P \ h \ stk \ (e1 \ [e2] \ := \ e3) \ pc \ xcp$
 $\tau\text{move2 } P \ h \ stk \ (e1 \ \cdot \ \text{length}) \ pc \ xcp$
 $\tau\text{move2 } P \ h \ stk \ (e1 \ \cdot \ F\{D\}) \ pc \ xcp$
 $\tau\text{move2 } P \ h \ stk \ (e1 \ \cdot \ F\{D\} \ := \ e3) \ pc \ xcp$
 $\tau\text{move2 } P \ h \ stk \ (e1 \ \cdot \ \text{compareAndSwap}(D \cdot F, \ e2, \ e3)) \ pc \ xcp$
 $\tau\text{move2 } P \ h \ stk \ (e \ \cdot \ M(ps)) \ pc \ xcp$
 $\tau\text{move2 } P \ h \ stk \ \{V:T=vo; e\} \ pc \ xcp$
 $\tau\text{move2 } P \ h \ stk \ (\text{sync}_V \ (e1) \ e2) \ pc \ xcp$
 $\tau\text{move2 } P \ h \ stk \ (e1 \ ;; \ e2) \ pc \ xcp$
 $\tau\text{move2 } P \ h \ stk \ (\text{if } (e1) \ e2 \ \text{else } e3) \ pc \ xcp$
 $\tau\text{move2 } P \ h \ stk \ (\text{while } (e1) \ e2) \ pc \ xcp$
 $\tau\text{move2 } P \ h \ stk \ (\text{try } e1 \ \text{catch}(C \ V) \ e2) \ pc \ xcp$
 $\tau\text{move2 } P \ h \ stk \ (\text{throw } e) \ pc \ xcp$

lemma $\tau\text{moves2xcp}$: $pc < \text{length } (\text{compEs2 } es) \Longrightarrow \tau\text{moves2 } P \ h \ stk \ es \ pc \ [xcp]$


```

proof(induct es arbitrary: pc)
  case Nil thus ?case by simp
next
  case (Cons e es)
  note  $IH = \langle \wedge pc. pc < \text{length} (\text{compEs2 } es) \implies \tau \text{moves2 } P \ h \ \text{stk } es \ pc \ [xcp] \rangle$ 
  note  $pc = \langle pc < \text{length} (\text{compEs2 } (e \# es)) \rangle$ 
  show ?case
  proof(cases pc < length (compE2 e))
    case True
    thus ?thesis by(auto intro:  $\tau \text{moves2Hd } \tau \text{move2xcp}$ )
  next
  case False
  with  $pc \ IH[\text{of } pc - \text{length} (\text{compE2 } e)]$ 
  have  $\tau \text{moves2 } P \ h \ \text{stk } es \ (pc - \text{length} (\text{compE2 } e)) \ [xcp]$  by(simp)
  hence  $\tau \text{moves2 } P \ h \ \text{stk } (e \# es) \ (\text{length} (\text{compE2 } e) + (pc - \text{length} (\text{compE2 } e))) \ [xcp]$ 
    by(rule  $\tau \text{moves2Tl}$ )
  with False show ?thesis by simp
qed
qed

lemma  $\tau \text{move2-intros}'$ :
  shows  $\tau \text{move2CastRed}'$ :  $pc = \text{length} (\text{compE2 } e) \implies \tau \text{move2 } P \ h \ \text{stk} \ (\text{Cast } T \ e) \ pc \ \text{None}$ 
  and  $\tau \text{move2InstanceOfRed}'$ :  $pc = \text{length} (\text{compE2 } e) \implies \tau \text{move2 } P \ h \ \text{stk} \ (e \ \text{instanceof } T) \ pc \ \text{None}$ 
  and  $\tau \text{move2BinOp2}'$ :  $\llbracket \tau \text{move2 } P \ h \ \text{stk } e2 \ pc \ xcp; pc' = \text{length} (\text{compE2 } e1) + pc \rrbracket \implies \tau \text{move2 } P \ h \ \text{stk} \ (e1 \llbracket \text{bop} \rrbracket e2) \ pc' \ xcp$ 
  and  $\tau \text{move2BinOp}'$ :  $pc = \text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2) \implies \tau \text{move2 } P \ h \ \text{stk} \ (e1 \llbracket \text{bop} \rrbracket e2) \ pc \ \text{None}$ 
  and  $\tau \text{move2LAssRed1}'$ :  $pc = \text{length} (\text{compE2 } e) \implies \tau \text{move2 } P \ h \ \text{stk} \ (V := e) \ pc \ \text{None}$ 
  and  $\tau \text{move2LAssRed2}'$ :  $pc = \text{Suc} (\text{length} (\text{compE2 } e)) \implies \tau \text{move2 } P \ h \ \text{stk} \ (V := e) \ pc \ \text{None}$ 
  and  $\tau \text{move2AAss2}'$ :  $\llbracket \tau \text{move2 } P \ h \ \text{stk } i \ pc \ xcp; pc' = \text{length} (\text{compE2 } a) + pc \rrbracket \implies \tau \text{move2 } P \ h \ \text{stk} \ (a[i]) \ pc' \ xcp$ 
  and  $\tau \text{move2AAss2}'$ :  $\llbracket \tau \text{move2 } P \ h \ \text{stk } i \ pc \ xcp; pc' = \text{length} (\text{compE2 } a) + pc \rrbracket \implies \tau \text{move2 } P \ h \ \text{stk} \ (a[i] := e) \ pc' \ xcp$ 
  and  $\tau \text{move2AAss3}'$ :  $\llbracket \tau \text{move2 } P \ h \ \text{stk } e \ pc \ xcp; pc' = \text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i) + pc \rrbracket \implies \tau \text{move2 } P \ h \ \text{stk} \ (a[i] := e) \ pc' \ xcp$ 
  and  $\tau \text{move2AAssRed}'$ :  $pc = \text{Suc} (\text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i) + \text{length} (\text{compE2 } e)) \implies \tau \text{move2 } P \ h \ \text{stk} \ (a[i] := e) \ pc \ \text{None}$ 
  and  $\tau \text{move2FAss2}'$ :  $\llbracket \tau \text{move2 } P \ h \ \text{stk } e' \ pc \ xcp; pc' = \text{length} (\text{compE2 } e) + pc \rrbracket \implies \tau \text{move2 } P \ h \ \text{stk} \ (e \cdot F\{D\} := e') \ pc' \ xcp$ 
  and  $\tau \text{move2FAssRed}'$ :  $pc = \text{Suc} (\text{length} (\text{compE2 } e) + \text{length} (\text{compE2 } e')) \implies \tau \text{move2 } P \ h \ \text{stk} \ (e \cdot F\{D\} := e') \ pc \ \text{None}$ 
  and  $\tau \text{move2CAS2}'$ :  $\llbracket \tau \text{move2 } P \ h \ \text{stk } e2 \ pc \ xcp; pc' = \text{length} (\text{compE2 } e1) + pc \rrbracket \implies \tau \text{move2 } P \ h \ \text{stk} \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ pc' \ xcp$ 
  and  $\tau \text{move2CAS3}'$ :  $\llbracket \tau \text{move2 } P \ h \ \text{stk } e3 \ pc \ xcp; pc' = \text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2) + pc \rrbracket \implies \tau \text{move2 } P \ h \ \text{stk} \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ pc' \ xcp$ 
  and  $\tau \text{move2CallParams}'$ :  $\llbracket \tau \text{moves2 } P \ h \ \text{stk } ps \ pc \ xcp; pc' = \text{length} (\text{compE2 } obj) + pc \rrbracket \implies \tau \text{move2 } P \ h \ \text{stk} \ (obj \cdot M(ps)) \ pc' \ xcp$ 
  and  $\tau \text{move2Call}'$ :  $\llbracket pc = \text{length} (\text{compE2 } obj) + \text{length} (\text{compEs2 } ps); \text{length } ps < \text{length } \text{stk}; \text{stk} ! \text{length } ps = \text{Null} \vee (\forall T \ Ts \ Tr \ D. \ \text{typeof-addr } h \ (\text{the-Addr } (\text{stk} ! \text{length } ps)) = [T] \longrightarrow P \vdash \text{class-type-of } T \ \text{sees } M : Ts \rightarrow Tr = \text{Native in } D \longrightarrow \tau \text{external-defs } D \ M) \rrbracket \implies \tau \text{move2 } P \ h \ \text{stk} \ (obj \cdot M(ps)) \ pc \ \text{None}$ 
  and  $\tau \text{move2BlockSome2}$ :  $pc = \text{Suc } 0 \implies \tau \text{move2 } P \ h \ \text{stk} \ \{V : T = [v]; e\} \ pc \ \text{None}$ 
  and  $\tau \text{move2BlockSome}'$ :  $\llbracket \tau \text{move2 } P \ h \ \text{stk } e \ pc \ xcp; pc' = \text{Suc} (\text{Suc } pc) \rrbracket \implies \tau \text{move2 } P \ h \ \text{stk}$ 

```

$\{ V:T=[v]; e \} pc' xcp$
and $\tau move2Sync2'$: $pc = length (compE2 o') \implies \tau move2 P h stk (sync_V (o') e) pc None$
and $\tau move2Sync3'$: $pc = Suc (length (compE2 o')) \implies \tau move2 P h stk (sync_V (o') e) pc None$
and $\tau move2Sync4'$: $\llbracket \tau move2 P h stk e pc xcp; pc' = Suc (Suc (Suc (length (compE2 o') + pc))) \rrbracket \implies \tau move2 P h stk (sync_V (o') e) pc' xcp$
and $\tau move2Sync5'$: $pc = Suc (Suc (Suc (length (compE2 o') + length (compE2 e)))) \implies \tau move2 P h stk (sync_V (o') e) pc None$
and $\tau move2Sync6'$: $pc = 5 + length (compE2 o') + length (compE2 e) \implies \tau move2 P h stk (sync_V (o') e) pc None$
and $\tau move2Sync7'$: $pc = 6 + length (compE2 o') + length (compE2 e) \implies \tau move2 P h stk (sync_V (o') e) pc None$
and $\tau move2Sync8'$: $pc = 8 + length (compE2 o') + length (compE2 e) \implies \tau move2 P h stk (sync_V (o') e) pc None$
and $\tau move2SeqRed'$: $pc = length (compE2 e) \implies \tau move2 P h stk (e;;e') pc None$
and $\tau move2Seq2'$: $\llbracket \tau move2 P h stk e' pc xcp; pc' = Suc (length (compE2 e) + pc) \rrbracket \implies \tau move2 P h stk (e;;e') pc' xcp$
and $\tau move2CondRed'$: $pc = length (compE2 e) \implies \tau move2 P h stk (if (e) e1 else e2) pc None$
and $\tau move2CondThen'$: $\llbracket \tau move2 P h stk e1 pc xcp; pc' = Suc (length (compE2 e) + pc) \rrbracket \implies \tau move2 P h stk (if (e) e1 else e2) pc' xcp$
and $\tau move2CondThenExit'$: $pc = Suc (length (compE2 e) + length (compE2 e1)) \implies \tau move2 P h stk (if (e) e1 else e2) pc None$
and $\tau move2CondElse'$: $\llbracket \tau move2 P h stk e2 pc xcp; pc' = Suc (Suc (length (compE2 e) + length (compE2 e1) + pc)) \rrbracket \implies \tau move2 P h stk (if (e) e1 else e2) pc' xcp$
and $\tau move2While2'$: $\llbracket \tau move2 P h stk e pc xcp; pc' = Suc (length (compE2 c) + pc) \rrbracket \implies \tau move2 P h stk (while (c) e) pc' xcp$
and $\tau move2While3'$: $pc = Suc (Suc (length (compE2 c) + length (compE2 e))) \implies \tau move2 P h stk (while (c) e) pc None$
and $\tau move2While4'$: $pc = Suc (Suc (Suc (length (compE2 c) + length (compE2 e)))) \implies \tau move2 P h stk (while (c) e) pc None$
and $\tau move2While5'$: $pc = length (compE2 c) \implies \tau move2 P h stk (while (c) e) pc None$
and $\tau move2While6'$: $pc = Suc (length (compE2 c) + length (compE2 e)) \implies \tau move2 P h stk (while (c) e) pc None$
and $\tau move2Throw2'$: $pc = length (compE2 e) \implies \tau move2 P h stk (throw e) pc None$
and $\tau move2TryJump'$: $pc = length (compE2 e) \implies \tau move2 P h stk (try e catch(C V) e') pc None$
and $\tau move2TryCatch2'$: $pc = Suc (length (compE2 e)) \implies \tau move2 P h stk (try e catch(C V) e') pc None$
and $\tau move2Try2'$: $\llbracket \tau move2 P h stk \{ V:T=None; e' \} pc xcp; pc' = Suc (Suc (length (compE2 e) + pc)) \rrbracket \implies \tau move2 P h stk (try e catch(C V) e') pc' xcp$
and $\tau moves2Tl'$: $\llbracket \tau moves2 P h stk es pc xcp; pc' = length (compE2 e) + pc \rrbracket \implies \tau moves2 P h stk (e \# es) pc' xcp$
apply(blast intro: $\tau move2$ - $\tau moves2.intros$)+
done

lemma $\tau move2$ -iff: $\tau move2 P h stk e pc xcp \longleftrightarrow pc < length (compE2 e) \wedge (xcp = None \longrightarrow \tau instr P h stk (compE2 e ! pc))$ (**is** ?lhs1 \longleftrightarrow ?rhs1)

and $\tau moves2$ -iff: $\tau moves2 P h stk es pc xcp \longleftrightarrow pc < length (compEs2 es) \wedge (xcp = None \longrightarrow \tau instr P h stk (compEs2 es ! pc))$ (**is** ?lhs2 \longleftrightarrow ?rhs2)

proof –

have rhs1lhs1: $\llbracket \tau instr P h stk (compE2 e ! pc); pc < length (compE2 e) \rrbracket \implies \tau move2 P h stk e pc None$

and rhs2lhs2: $\llbracket \tau instr P h stk (compEs2 es ! pc); pc < length (compEs2 es) \rrbracket \implies \tau moves2 P h stk es pc None$

apply(*induct e and es arbitrary: pc and pc rule: compE2.induct compEs2.induct*)
apply(*force intro: τ move2- τ moves2.intros τ move2-intros' simp add: nth-append nth-Cons' not-less-eq*
split: if-split-asm) +
done

{ **assume** $pc < \text{length} (\text{compE2 } e) \text{ xcp} \neq \text{None}$
hence $\tau \text{move2 } P \ h \ \text{stk } e \ \text{pc } \text{xcp}$ **by**(*auto intro: τ move2xcp*) }
with *rhs1lhs1* **have** $?rhs1 \implies ?lhs1$ **by**(*cases xcp*) *auto*
moreover {
assume $pc < \text{length} (\text{compEs2 } es) \text{ xcp} \neq \text{None}$
hence $\tau \text{moves2 } P \ h \ \text{stk } es \ \text{pc } \text{xcp}$ **by**(*auto intro: τ moves2xcp*) }
with *rhs2lhs2* **have** $?rhs2 \implies ?lhs2$ **by**(*cases xcp*) *auto*
moreover **have** $?lhs1 \implies ?rhs1$ **and** $?lhs2 \implies ?rhs2$
by(*induct rule: τ move2- τ moves2.inducts*)(*fastforce simp add: nth-append eval-nat-numeral*) +
ultimately show $?lhs1 \longleftrightarrow ?rhs1$ $?lhs2 \longleftrightarrow ?rhs2$ **by** *blast+*
qed

lemma $\tau \text{move2-pc-length-compE2}: \tau \text{move2 } P \ h \ \text{stk } e \ \text{pc } \text{xcp} \implies pc < \text{length} (\text{compE2 } e)$
and $\tau \text{moves2-pc-length-compEs2}: \tau \text{moves2 } P \ h \ \text{stk } es \ \text{pc } \text{xcp} \implies pc < \text{length} (\text{compEs2 } es)$
by(*simp-all add: τ move2-iff τ moves2-iff*)

lemma $\tau \text{move2-pc-length-compE2-conv}: pc \geq \text{length} (\text{compE2 } e) \implies \neg \tau \text{move2 } P \ h \ \text{stk } e \ \text{pc } \text{xcp}$
by(*auto dest: τ move2-pc-length-compE2*)

lemma $\tau \text{moves2-pc-length-compEs2-conv}: pc \geq \text{length} (\text{compEs2 } es) \implies \neg \tau \text{moves2 } P \ h \ \text{stk } es \ \text{pc } \text{xcp}$
by(*auto dest: τ moves2-pc-length-compEs2*)

lemma $\tau \text{moves2-append}$ [*elim*]:
 $\tau \text{moves2 } P \ h \ \text{stk } es \ \text{pc } \text{xcp} \implies \tau \text{moves2 } P \ h \ \text{stk } (es \ @ \ es') \ \text{pc } \text{xcp}$
by(*auto simp add: τ moves2-iff nth-append*)

lemma $\text{append-}\tau \text{moves2}$:
 $\tau \text{moves2 } P \ h \ \text{stk } es \ \text{pc } \text{xcp} \implies \tau \text{moves2 } P \ h \ \text{stk } (es' \ @ \ es) (\text{length} (\text{compEs2 } es') + \text{pc}) \ \text{xcp}$
by(*simp add: τ moves2-iff*)

lemma [*dest*]:
shows $\tau \text{move2-NewArrayD}: \llbracket \tau \text{move2 } P \ h \ \text{stk } (\text{newA } T [e]) \ \text{pc } \text{xcp}; pc < \text{length} (\text{compE2 } e) \rrbracket \implies$
 $\tau \text{move2 } P \ h \ \text{stk } e \ \text{pc } \text{xcp}$
and $\tau \text{move2-CastD}: \llbracket \tau \text{move2 } P \ h \ \text{stk } (\text{Cast } T \ e) \ \text{pc } \text{xcp}; pc < \text{length} (\text{compE2 } e) \rrbracket \implies \tau \text{move2 } P$
 $h \ \text{stk } e \ \text{pc } \text{xcp}$
and $\tau \text{move2-InstanceOfD}: \llbracket \tau \text{move2 } P \ h \ \text{stk } (e \ \text{instanceof } T) \ \text{pc } \text{xcp}; pc < \text{length} (\text{compE2 } e) \rrbracket \implies$
 $\tau \text{move2 } P \ h \ \text{stk } e \ \text{pc } \text{xcp}$
and $\tau \text{move2-BinOp1D}: \llbracket \tau \text{move2 } P \ h \ \text{stk } (e1 \ \llbracket \text{bop} \rrbracket \ e2) \ \text{pc}' \ \text{xcp}'; \text{pc}' < \text{length} (\text{compE2 } e1) \rrbracket \implies$
 $\tau \text{move2 } P \ h \ \text{stk } e1 \ \text{pc}' \ \text{xcp}'$
and $\tau \text{move2-BinOp2D}$:
 $\llbracket \tau \text{move2 } P \ h \ \text{stk } (e1 \ \llbracket \text{bop} \rrbracket \ e2) (\text{length} (\text{compE2 } e1) + \text{pc}') \ \text{xcp}'; \text{pc}' < \text{length} (\text{compE2 } e2) \rrbracket \implies$
 $\tau \text{move2 } P \ h \ \text{stk } e2 \ \text{pc}' \ \text{xcp}'$
and $\tau \text{move2-LAssD}: \llbracket \tau \text{move2 } P \ h \ \text{stk } (V := e) \ \text{pc } \text{xcp}; pc < \text{length} (\text{compE2 } e) \rrbracket \implies \tau \text{move2 } P \ h$
 $\text{stk } e \ \text{pc } \text{xcp}$
and $\tau \text{move2-AAccD1}: \llbracket \tau \text{move2 } P \ h \ \text{stk } (a [i]) \ \text{pc } \text{xcp}; pc < \text{length} (\text{compE2 } a) \rrbracket \implies \tau \text{move2 } P \ h$
 $\text{stk } a \ \text{pc } \text{xcp}$
and $\tau \text{move2-AAccD2}: \llbracket \tau \text{move2 } P \ h \ \text{stk } (a [i]) (\text{length} (\text{compE2 } a) + \text{pc}) \ \text{xcp}; pc < \text{length} (\text{compE2}$
 $i) \rrbracket \implies \tau \text{move2 } P \ h \ \text{stk } i \ \text{pc } \text{xcp}$
and $\tau \text{move2-AAssD1}: \llbracket \tau \text{move2 } P \ h \ \text{stk } (a [i] := e) \ \text{pc } \text{xcp}; pc < \text{length} (\text{compE2 } a) \rrbracket \implies \tau \text{move2}$

$P \ h \ stk \ a \ pc \ xcp$
and $\tau_{move2-AAssD2}$: $\llbracket \tau_{move2} \ P \ h \ stk \ (a[i] := e) \ (length \ (compE2 \ a) + pc) \ xcp; \ pc < length \ (compE2 \ i) \rrbracket \implies \tau_{move2} \ P \ h \ stk \ i \ pc \ xcp$
and $\tau_{move2-AAssD3}$:
 $\llbracket \tau_{move2} \ P \ h \ stk \ (a[i] := e) \ (length \ (compE2 \ a) + length \ (compE2 \ i) + pc) \ xcp; \ pc < length \ (compE2 \ e) \rrbracket \implies \tau_{move2} \ P \ h \ stk \ e \ pc \ xcp$
and $\tau_{move2-ALengthD}$: $\llbracket \tau_{move2} \ P \ h \ stk \ (a.length) \ pc \ xcp; \ pc < length \ (compE2 \ a) \rrbracket \implies \tau_{move2} \ P \ h \ stk \ a \ pc \ xcp$
and $\tau_{move2-FAccD}$: $\llbracket \tau_{move2} \ P \ h \ stk \ (e.F\{D\}) \ pc \ xcp; \ pc < length \ (compE2 \ e) \rrbracket \implies \tau_{move2} \ P \ h \ stk \ e \ pc \ xcp$
and $\tau_{move2-FAssD1}$: $\llbracket \tau_{move2} \ P \ h \ stk \ (e.F\{D\} := e') \ pc \ xcp; \ pc < length \ (compE2 \ e) \rrbracket \implies \tau_{move2} \ P \ h \ stk \ e \ pc \ xcp$
and $\tau_{move2-FAssD2}$: $\llbracket \tau_{move2} \ P \ h \ stk \ (e.F\{D\} := e') \ (length \ (compE2 \ e) + pc) \ xcp; \ pc < length \ (compE2 \ e') \rrbracket \implies \tau_{move2} \ P \ h \ stk \ e' \ pc \ xcp$
and $\tau_{move2-CASD1}$: $\llbracket \tau_{move2} \ P \ h \ stk \ (e1.compareAndSwap(D.F, e2, e3)) \ pc \ xcp; \ pc < length \ (compE2 \ e1) \rrbracket \implies \tau_{move2} \ P \ h \ stk \ e1 \ pc \ xcp$
and $\tau_{move2-CASD2}$: $\llbracket \tau_{move2} \ P \ h \ stk \ (e1.compareAndSwap(D.F, e2, e3)) \ (length \ (compE2 \ e1) + pc) \ xcp; \ pc < length \ (compE2 \ e2) \rrbracket \implies \tau_{move2} \ P \ h \ stk \ e2 \ pc \ xcp$
and $\tau_{move2-CASD3}$:
 $\llbracket \tau_{move2} \ P \ h \ stk \ (e1.compareAndSwap(D.F, e2, e3)) \ (length \ (compE2 \ e1) + length \ (compE2 \ e2) + pc) \ xcp; \ pc < length \ (compE2 \ e3) \rrbracket \implies \tau_{move2} \ P \ h \ stk \ e3 \ pc \ xcp$
and $\tau_{move2-CallObjD}$: $\llbracket \tau_{move2} \ P \ h \ stk \ (e.M(es)) \ pc \ xcp; \ pc < length \ (compE2 \ e) \rrbracket \implies \tau_{move2} \ P \ h \ stk \ e \ pc \ xcp$
and $\tau_{move2-BlockNoneD}$: $\tau_{move2} \ P \ h \ stk \ \{V:T=None; e\} \ pc \ xcp \implies \tau_{move2} \ P \ h \ stk \ e \ pc \ xcp$
and $\tau_{move2-BlockSomeD}$: $\tau_{move2} \ P \ h \ stk \ \{V:T=[v]; e\} \ (Suc \ (Suc \ pc)) \ xcp \implies \tau_{move2} \ P \ h \ stk \ e \ pc \ xcp$
and $\tau_{move2-sync1D}$: $\llbracket \tau_{move2} \ P \ h \ stk \ (sync_V \ (o') \ e) \ pc \ xcp; \ pc < length \ (compE2 \ o') \rrbracket \implies \tau_{move2} \ P \ h \ stk \ o' \ pc \ xcp$
and $\tau_{move2-sync2D}$:
 $\llbracket \tau_{move2} \ P \ h \ stk \ (sync_V \ (o') \ e) \ (Suc \ (Suc \ (Suc \ (length \ (compE2 \ o') + pc)))) \ xcp; \ pc < length \ (compE2 \ e) \rrbracket \implies \tau_{move2} \ P \ h \ stk \ e \ pc \ xcp$
and $\tau_{move2-Seq1D}$: $\llbracket \tau_{move2} \ P \ h \ stk \ (e1;; e2) \ pc \ xcp; \ pc < length \ (compE2 \ e1) \rrbracket \implies \tau_{move2} \ P \ h \ stk \ e1 \ pc \ xcp$
and $\tau_{move2-Seq2D}$: $\tau_{move2} \ P \ h \ stk \ (e1;; e2) \ (Suc \ (length \ (compE2 \ e1) + pc')) \ xcp' \implies \tau_{move2} \ P \ h \ stk \ e2 \ pc' \ xcp'$
and $\tau_{move2-IfCondD}$: $\llbracket \tau_{move2} \ P \ h \ stk \ (if \ (e) \ e1 \ else \ e2) \ pc \ xcp; \ pc < length \ (compE2 \ e) \rrbracket \implies \tau_{move2} \ P \ h \ stk \ e \ pc \ xcp$
and $\tau_{move2-IfThenD}$:
 $\llbracket \tau_{move2} \ P \ h \ stk \ (if \ (e) \ e1 \ else \ e2) \ (Suc \ (length \ (compE2 \ e) + pc')) \ xcp'; \ pc' < length \ (compE2 \ e1) \rrbracket \implies \tau_{move2} \ P \ h \ stk \ e1 \ pc' \ xcp'$
and $\tau_{move2-IfElseD}$:
 $\tau_{move2} \ P \ h \ stk \ (if \ (e) \ e1 \ else \ e2) \ (Suc \ (Suc \ (length \ (compE2 \ e) + length \ (compE2 \ e1) + pc')) \ xcp' \implies \tau_{move2} \ P \ h \ stk \ e2 \ pc' \ xcp'$
and $\tau_{move2-WhileCondD}$: $\llbracket \tau_{move2} \ P \ h \ stk \ (while \ (c) \ b) \ pc \ xcp; \ pc < length \ (compE2 \ c) \rrbracket \implies \tau_{move2} \ P \ h \ stk \ c \ pc \ xcp$
and $\tau_{move2-ThrowD}$: $\llbracket \tau_{move2} \ P \ h \ stk \ (throw \ e) \ pc \ xcp; \ pc < length \ (compE2 \ e) \rrbracket \implies \tau_{move2} \ P \ h \ stk \ e \ pc \ xcp$
and $\tau_{move2-Try1D}$: $\llbracket \tau_{move2} \ P \ h \ stk \ (try \ e1 \ catch \ (C' \ V) \ e2) \ pc \ xcp; \ pc < length \ (compE2 \ e1) \rrbracket \implies \tau_{move2} \ P \ h \ stk \ e1 \ pc \ xcp$
apply(*auto elim!*: $\tau_{move2-cases}$ *intro*: $\tau_{move2xcp}$ *dest*: $\tau_{move2-pc-length-compE2}$)
done

lemma $\tau_{move2-Invoke}$:

$\llbracket \tau_{move2} \ P \ h \ stk \ e \ pc \ None; \ compE2 \ e \ ! \ pc = Invoke \ M \ n \rrbracket$

$\implies n < \text{length } \text{stk} \wedge (\text{stk} ! n = \text{Null} \vee (\forall T \text{Ts} \text{Tr} D. \text{typeof-addr } h (\text{the-Addr } (\text{stk} ! n)) = \lfloor T \rfloor \longrightarrow P \vdash \text{class-type-of } T \text{ sees } M:\text{Ts} \rightarrow \text{Tr} = \text{Native in } D \longrightarrow \tau \text{external-defs } D M))$

and $\tau \text{moves2-Invoke}$:

$\llbracket \tau \text{moves2 } P \ h \ \text{stk} \ es \ pc \ \text{None}; \ \text{compEs2 } es \ ! \ pc = \text{Invoke } M \ n \ \rrbracket$

$\implies n < \text{length } \text{stk} \wedge (\text{stk} ! n = \text{Null} \vee (\forall T \ C \ \text{Ts} \ \text{Tr} \ D. \text{typeof-addr } h (\text{the-Addr } (\text{stk} ! n)) = \lfloor T \rfloor \longrightarrow P \vdash \text{class-type-of } T \text{ sees } M:\text{Ts} \rightarrow \text{Tr} = \text{Native in } D \longrightarrow \tau \text{external-defs } D M))$

by($\text{simp-all add: } \tau \text{move2-iff } \tau \text{moves2-iff split-beta}$)

lemmas $\tau \text{move2-compE2-not-Invoke} = \tau \text{move2-Invoke}$

lemmas $\tau \text{moves2-compEs2-not-Invoke} = \tau \text{moves2-Invoke}$

lemma $\tau \text{move2-blocks1}$ [simp]:

$\tau \text{move2 } P \ h \ \text{stk} \ (\text{blocks1 } n \ \text{Ts} \ \text{body}) \ pc' \ xcp' = \tau \text{move2 } P \ h \ \text{stk} \ \text{body} \ pc' \ xcp'$

by($\text{simp add: } \tau \text{move2-iff}$)

lemma $\tau \text{instr-stk-append}$:

$\tau \text{instr } P \ h \ \text{stk} \ i \implies \tau \text{instr } P \ h \ (\text{stk} \ @ \ vs) \ i$

by($\text{cases } i$)($\text{auto simp add: nth-append}$)

lemma $\tau \text{move2-stk-append}$:

$\tau \text{move2 } P \ h \ \text{stk} \ e \ pc \ xcp \implies \tau \text{move2 } P \ h \ (\text{stk} \ @ \ vs) \ e \ pc \ xcp$

by($\text{simp add: } \tau \text{move2-iff } \tau \text{instr-stk-append}$)

lemma $\tau \text{moves2-stk-append}$:

$\tau \text{moves2 } P \ h \ \text{stk} \ es \ pc \ xcp \implies \tau \text{moves2 } P \ h \ (\text{stk} \ @ \ vs) \ es \ pc \ xcp$

by($\text{simp add: } \tau \text{moves2-iff } \tau \text{instr-stk-append}$)

fun $\tau \text{Move2} :: 'addr \ \text{jvm-prog} \Rightarrow ('addr, 'heap) \ \text{jvm-state} \Rightarrow \text{bool}$

where

$\tau \text{Move2 } P \ (xcp, h, []) = \text{False}$

| $\tau \text{Move2 } P \ (xcp, h, (\text{stk}, \text{loc}, C, M, pc) \# \text{frs}) =$

$(\text{let } (-,-, \text{meth}) = \text{method } P \ C \ M; \ (-,-, \text{ins}, \text{xt}) = \text{the } \text{meth}$

$\text{in } (pc < \text{length } \text{ins} \wedge (xcp = \text{None} \longrightarrow \tau \text{instr } P \ h \ \text{stk} \ (\text{ins} ! pc))))$

lemma $\tau \text{Move2-iff}$:

$\tau \text{Move2 } P \ \sigma = (\text{let } (xcp, h, \text{frs}) = \sigma$

$\text{in case } \text{frs} \ \text{of } [] \Rightarrow \text{False}$

| $(\text{stk}, \text{loc}, C, M, pc) \# \text{frs}' \Rightarrow$

$(\text{let } (-,-, \text{meth}) = \text{method } P \ C \ M; \ (-,-, \text{ins}, \text{xt}) = \text{the } \text{meth}$

$\text{in } (pc < \text{length } \text{ins} \wedge (xcp = \text{None} \longrightarrow \tau \text{instr } P \ h \ \text{stk} \ (\text{ins} ! pc))))$

by($\text{cases } \sigma$)($\text{clarsimp split: list.splits simp add: fun-eq-iff split-beta}$)

lemma $\tau \text{instr-compP}$ [simp]: $\tau \text{instr} \ (\text{compP } f \ P) \ h \ \text{stk} \ i \longleftrightarrow \tau \text{instr } P \ h \ \text{stk} \ i$

by($\text{cases } i$) auto

lemma [simp]: **fixes** $e :: 'addr \ \text{expr1}$ **and** $es :: 'addr \ \text{expr1 list}$

shows $\tau \text{move2-compP}: \tau \text{move2} \ (\text{compP } f \ P) \ h \ \text{stk} \ e = \tau \text{move2 } P \ h \ \text{stk} \ e$

and $\tau \text{moves2-compP}: \tau \text{moves2} \ (\text{compP } f \ P) \ h \ \text{stk} \ es = \tau \text{moves2 } P \ h \ \text{stk} \ es$

by($\text{auto simp add: } \tau \text{move2-iff } \tau \text{moves2-iff fun-eq-iff}$)

lemma $\tau \text{Move2-compP2}$:

$P \vdash C \ \text{sees } M:\text{Ts} \rightarrow T = \lfloor \text{body} \rfloor \ \text{in } D \implies$

$\tau \text{Move2} \ (\text{compP2 } P) \ (xcp, h, (\text{stk}, \text{loc}, C, M, pc) \# \text{frs}) =$

(*case* *xcp* of *None* \Rightarrow $\tau\text{move2 } P \ h \ \text{stk} \ \text{body} \ \text{pc} \ \text{xcp} \ \vee \ \text{pc} = \text{length} \ (\text{compE2} \ \text{body}) \mid \text{Some } a \Rightarrow \text{pc} <$
Suc ($\text{length} \ (\text{compE2} \ \text{body})$))

by(*clarsimp simp add: $\tau\text{move2-iff compP2-def compMb2-def nth-append nth-Cons' split: option.splits if-split-asm}$*)

abbreviation $\tau\text{MOVE2} ::$

'addr jvm-prog \Rightarrow ((*'addr option* \times *'addr frame list*) \times *'heap*, (*'addr*, *'thread-id*, *'heap*) *jvm-thread-action*)
trsys

where $\tau\text{MOVE2 } P \equiv \lambda((\text{xcp}, \text{frs}), h) \ \text{ta} \ s. \ \tau\text{Move2 } P \ (\text{xcp}, h, \text{frs}) \wedge \ \text{ta} = \varepsilon$

lemma $\tau\text{jvmd-heap-unchanged}$:

$\llbracket P, t \vdash \text{Normal} \ (\text{xcp}, h, \text{frs}) \ -\varepsilon\text{-jvmd} \rightarrow \text{Normal} \ (\text{xcp}', h', \text{frs}'); \ \tau\text{Move2 } P \ (\text{xcp}, h, \text{frs}) \rrbracket$
 $\implies h = h'$

apply(*erule jvmd-NormalE*)

apply(*clarsimp*)

apply(*cases xcp*)

apply(*rename-tac stk loc C M pc FRS M' Ts T meth mxs mxl ins xt*)

apply(*case-tac ins ! pc*)

prefer 19 — *BinOpInstr*

apply(*rename-tac bop*)

apply(*case-tac the (binop bop (hd (tl stk)) (hd stk))*)

apply(*auto simp add: split-beta $\tau\text{external-def split: if-split-asm}$*)

apply(*fastforce simp add: check-def has-method-def $\tau\text{external-def dest: $\tau\text{external-red-external-aggr-heap-unchanged}$$*)
done

lemma *mexecd- $\tau\text{mthr-wf}$* :

$\tau\text{multithreaded-wf JVM-final} \ (\text{mexecd } P) \ (\tau\text{MOVE2 } P)$

proof

fix *t x h ta x' h'*

assume *mexecd P t (x, h) ta (x', h')*

and $\tau\text{MOVE2 } P \ (x, h) \ \text{ta} \ (x', h')$

thus $h = h'$

by(*cases x*)(*cases x', auto dest: $\tau\text{jvmd-heap-unchanged}$*)

next

fix *s ta s'*

assume $\tau\text{MOVE2 } P \ s \ \text{ta} \ s'$ **thus** $\text{ta} = \varepsilon$ **by**(*simp add: split-beta*)

qed

end

sublocale *JVM-heap-base* < *mexecd-mthr*:

$\tau\text{multithreaded-wf}$

JVM-final

mexecd P

convert-RA

$\tau\text{MOVE2 } P$

for *P*

by(*rule mexecd- $\tau\text{mthr-wf}$*)

context *JVM-heap-base* **begin**

lemma $\tau\text{exec-1-taD}$:

assumes *exec: exec-1-d P t (Normal (xcp, h, frs)) ta (Normal (xcp', h', frs'))*

and $\tau: \tau\text{Move2 } P \ (\text{xcp}, h, \text{frs})$

```

  shows ta = ε
using assms
apply(auto elim!: jvmd-NormalE simp add: split-beta)
apply(cases xcp)
apply auto
apply(rename-tac stk loc C M pc FRS)
apply(case-tac instrs-of P C M ! pc)
apply(simp-all split: if-split-asm)
apply(auto simp add: check-def has-method-def τexternal-def dest!: τexternal-red-external-aggr-TA-empty)
done

end

end

```

7.15 JVM Semantics for the delay bisimulation proof from intermediate language to byte code

```
theory Execs imports JVMTau begin
```

```

declare match-ex-table-app [simp del]
  match-ex-table-eq-NoneI [simp del]
  compxE2-size-convs [simp del]
  compxE2-stack-xlift-convs [simp del]
  compxEs2-stack-xlift-convs [simp del]

```

```
type-synonym
```

```

('addr, 'heap) check-instr' =
  'addr instr ⇒ 'addr jvm-prog ⇒ 'heap ⇒ 'addr val list ⇒ 'addr val list ⇒ cname ⇒ mname ⇒ pc
⇒ 'addr frame list ⇒ bool

```

```
primrec check-instr' :: ('addr, 'heap) check-instr'
```

```
where
```

```
check-instr'-Load:
```

```

check-instr' (Load n) P h stk loc C M0 pc frs =
  True

```

```
| check-instr'-Store:
```

```

check-instr' (Store n) P h stk loc C0 M0 pc frs =
  (0 < length stk)

```

```
| check-instr'-Push:
```

```

check-instr' (Push v) P h stk loc C0 M0 pc frs =
  True

```

```
| check-instr'-New:
```

```

check-instr' (New C) P h stk loc C0 M0 pc frs =
  True

```

```
| check-instr'-NewArray:
```

```

check-instr' (NewArray T) P h stk loc C0 M0 pc frs =
  (0 < length stk)

```

- | *check-instr'-ALoad*:
 $check_instr' \ ALoad \ P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$
 $(1 < length \ stk)$
- | *check-instr'-AStore*:
 $check_instr' \ AStore \ P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$
 $(2 < length \ stk)$
- | *check-instr'-ALength*:
 $check_instr' \ ALength \ P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$
 $(0 < length \ stk)$
- | *check-instr'-Getfield*:
 $check_instr' \ (Getfield \ F \ C) \ P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$
 $(0 < length \ stk)$
- | *check-instr'-Putfield*:
 $check_instr' \ (Putfield \ F \ C) \ P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$
 $(1 < length \ stk)$
- | *check-instr'-CAS*:
 $check_instr' \ (CAS \ F \ C) \ P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$
 $(2 < length \ stk)$
- | *check-instr'-Checkcast*:
 $check_instr' \ (Checkcast \ T) \ P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$
 $(0 < length \ stk)$
- | *check-instr'-Instanceof*:
 $check_instr' \ (Instanceof \ T) \ P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$
 $(0 < length \ stk)$
- | *check-instr'-Invoke*:
 $check_instr' \ (Invoke \ M \ n) \ P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$
 $(n < length \ stk)$
- | *check-instr'-Return*:
 $check_instr' \ Return \ P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$
 $(0 < length \ stk)$
- | *check-instr'-Pop*:
 $check_instr' \ Pop \ P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$
 $(0 < length \ stk)$
- | *check-instr'-Dup*:
 $check_instr' \ Dup \ P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$
 $(0 < length \ stk)$
- | *check-instr'-Swap*:
 $check_instr' \ Swap \ P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$
 $(1 < length \ stk)$
- | *check-instr'-BinOpInstr*:
 $check_instr' \ (BinOpInstr \ bop) \ P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$

($1 < \text{length } \text{stk}$)

| *check-instr'-IfFalse*:

check-instr' (IfFalse b) P h stk loc C₀ M₀ pc frs =
($0 < \text{length } \text{stk} \wedge 0 \leq \text{int } \text{pc}+b$)

| *check-instr'-Goto*:

check-instr' (Goto b) P h stk loc C₀ M₀ pc frs =
($0 \leq \text{int } \text{pc}+b$)

| *check-instr'-Throw*:

check-instr' ThrowExc P h stk loc C₀ M₀ pc frs =
($0 < \text{length } \text{stk}$)

| *check-instr'-MEnter*:

check-instr' MEnter P h stk loc C₀ M₀ pc frs =
($0 < \text{length } \text{stk}$)

| *check-instr'-MExit*:

check-instr' MExit P h stk loc C₀ M₀ pc frs =
($0 < \text{length } \text{stk}$)

definition *ci-stk-offer* :: ('addr, 'heap) *check-instr'* \Rightarrow bool

where

ci-stk-offer ci =

($\forall \text{ins } P \text{ h } \text{stk } \text{stk}' \text{ loc } C \text{ M } \text{pc } \text{frs}. \text{ci } \text{ins } P \text{ h } \text{stk } \text{loc } C \text{ M } \text{pc } \text{frs} \longrightarrow \text{ci } \text{ins } P \text{ h } (\text{stk } @ \text{stk}') \text{ loc } C \text{ M } \text{pc } \text{frs}$)

lemma *ci-stk-offerI*:

($\bigwedge \text{ins } P \text{ h } \text{stk } \text{stk}' \text{ loc } C \text{ M } \text{pc } \text{frs}. \text{ci } \text{ins } P \text{ h } \text{stk } \text{loc } C \text{ M } \text{pc } \text{frs} \Longrightarrow \text{ci } \text{ins } P \text{ h } (\text{stk } @ \text{stk}') \text{ loc } C \text{ M } \text{pc } \text{frs}$) \Longrightarrow *ci-stk-offer ci*

unfolding *ci-stk-offer-def* **by** *blast*

lemma *ci-stk-offerD*:

$\llbracket \text{ci-stk-offer } \text{ci}; \text{ci } \text{ins } P \text{ h } \text{stk } \text{loc } C \text{ M } \text{pc } \text{frs} \rrbracket \Longrightarrow \text{ci } \text{ins } P \text{ h } (\text{stk } @ \text{stk}') \text{ loc } C \text{ M } \text{pc } \text{frs}$

unfolding *ci-stk-offer-def* **by** *blast*

lemma *check-instr'-stk-offer*:

ci-stk-offer check-instr'

proof(*rule ci-stk-offerI*)

fix *ins P h stk stk' loc C M pc frs*

assume *check-instr' ins P h stk loc C M pc frs*

thus *check-instr' ins P h (stk @ stk') loc C M pc frs*

by(*cases ins*) *auto*

qed

context *JVM-heap-base* **begin**

lemma *check-instr-imp-check-instr'*:

check-instr ins P h stk loc C M pc frs \Longrightarrow check-instr' ins P h stk loc C M pc frs

by(*cases ins*) *auto*

lemma *check-instr-stk-offer*:

```

ci-stk-offer check-instr
proof(rule ci-stk-offerI)
  fix ins P h stk stk' loc C M pc frs
  assume check-instr ins P h stk loc C M pc frs
  thus check-instr ins P h (stk @ stk') loc C M pc frs
  by(cases ins)(auto simp add: nth-append hd-append neq-Nil-conv tl-append split: list.split)
qed

end

```

```

primrec jump-ok :: 'addr instr list  $\Rightarrow$  nat  $\Rightarrow$  nat  $\Rightarrow$  bool
where jump-ok [] n n' = True
| jump-ok (x # xs) n n' = (jump-ok xs (Suc n) n'  $\wedge$ 
  (case x of IfFalse m  $\Rightarrow$   $\neg$  int n  $\leq$  m  $\wedge$  m  $\leq$  int (n' + length xs)
  | Goto m  $\Rightarrow$   $\neg$  int n  $\leq$  m  $\wedge$  m  $\leq$  int (n' + length xs)
  | -  $\Rightarrow$  True))

```

```

lemma jump-ok-append [simp]:
  jump-ok (xs @ xs') n n'  $\longleftrightarrow$  jump-ok xs n (n' + length xs')  $\wedge$  jump-ok xs' (n + length xs) n'
apply(induct xs arbitrary: n)
apply(simp)
apply(auto split: instr.split)
done

```

```

lemma jump-ok-GotoD:
   $\llbracket$  jump-ok ins n n'; ins ! pc = Goto m; pc < length ins  $\rrbracket \Longrightarrow \neg$  int (pc + n)  $\leq$  m  $\wedge$  m  $<$  int (length ins - pc + n')
apply(induct ins arbitrary: n n' pc)
apply(simp)
apply(clarsimp)
apply(case-tac pc)
apply(fastforce)+
done

```

```

lemma jump-ok-IfFalseD:
   $\llbracket$  jump-ok ins n n'; ins ! pc = IfFalse m; pc < length ins  $\rrbracket \Longrightarrow \neg$  int (pc + n)  $\leq$  m  $\wedge$  m  $<$  int (length ins - pc + n')
apply(induct ins arbitrary: n n' pc)
apply(simp)
apply(clarsimp)
apply(case-tac pc)
apply(fastforce)+
done

```

```

lemma fixes e :: 'addr expr1 and es :: 'addr expr1 list
  shows compE2-jump-ok [intro!]: jump-ok (compE2 e) n (Suc n')
  and compEs2-jump-ok [intro!]: jump-ok (compEs2 es) n (Suc n')
apply(induct e and es arbitrary: n n' and n n' rule: compE2.induct compEs2.induct)
apply(auto split: bop.split)
done

```

```

lemma fixes e :: 'addr expr1 and es :: 'addr expr1 list
  shows compE1-Goto-not-same:  $\llbracket$  compE2 e ! pc = Goto i; pc < length (compE2 e)  $\rrbracket \Longrightarrow$  nat (int

```

```

pc + i) ≠ pc
  and compEs2-Goto-not-same: [ compEs2 es ! pc = Goto i; pc < length (compEs2 es) ] ⇒ nat (int
pc + i) ≠ pc
apply(induct e and es arbitrary: pc i and pc i rule: compE2.induct compEs2.induct)
apply(auto simp add: nth-Cons nth-append split: if-split-asm bop.split-asm nat.splits)
apply fastforce+
done

```

```

fun ins-jump-ok :: 'addr instr ⇒ nat ⇒ bool
where
  ins-jump-ok (Goto m) l = (- (int l) ≤ m)
| ins-jump-ok (IfFalse m) l = (- (int l) ≤ m)
| ins-jump-ok - - = True

```

```

definition wf-ci :: ('addr, 'heap) check-instr' ⇒ bool
where

```

```

  wf-ci ci ↔
    ci-stk-offer ci ∧ ci ≤ check-instr' ∧
    (∀ ins P h stk loc C M pc pc' frs. ci ins P h stk loc C M pc frs → ins-jump-ok ins pc' → ci ins P
h stk loc C M pc' frs)

```

lemma wf-ciI:

```

[ ci-stk-offer ci;
  ∧ ins P h stk loc C M pc frs. ci ins P h stk loc C M pc frs ⇒ check-instr' ins P h stk loc C M pc
frs;
  ∧ ins P h stk loc C M pc pc' frs. [ ci ins P h stk loc C M pc frs; ins-jump-ok ins pc' ] ⇒ ci ins P
h stk loc C M pc' frs ]
⇒ wf-ci ci

```

```

unfolding wf-ci-def le-fun-def le-bool-def
by blast

```

lemma check-instr'-pc:

```

[ check-instr' ins P h stk loc C M pc frs; ins-jump-ok ins pc' ] ⇒ check-instr' ins P h stk loc C M
pc' frs
by(cases ins) auto

```

lemma wf-ci-check-instr' [iff]:

```

  wf-ci check-instr'
apply(rule wf-ciI)
  apply(rule check-instr'-stk-offer)
  apply(assumption)
apply(erule (1) check-instr'-pc)
done

```

lemma jump-ok-ins-jump-ok:

```

[ jump-ok ins n n'; pc < length ins ] ⇒ ins-jump-ok (ins ! pc) (pc + n)
apply(induct ins arbitrary: n n' pc)
apply(fastforce simp add: nth-Cons' gr0-conv-Suc split: instr.split-asm)+
done

```

context JVM-heap-base **begin**

lemma check-instr-pc:

```

[ check-instr ins P h stk loc C M pc frs; ins-jump-ok ins pc' ] ⇒ check-instr ins P h stk loc C M

```

pc' frs
by(cases ins) auto

lemma *wf-ci-check-instr [iff]*:
wf-ci check-instr
apply(rule *wf-ciI*)
apply(rule *check-instr-stk-offer*)
apply(erule *check-instr-imp-check-instr'*)
apply(erule (1) *check-instr-pc*)
done

end

lemma *wf-ciD1*: *wf-ci ci* \implies *ci-stk-offer ci*
unfolding *wf-ci-def* **by** *blast*

lemma *wf-ciD2*: \llbracket *wf-ci ci*; *ci ins P h stk loc C M pc frs* $\rrbracket \implies$ *check-instr' ins P h stk loc C M pc frs*
unfolding *wf-ci-def le-fun-def le-bool-def*
by *blast*

lemma *wf-ciD3*: \llbracket *wf-ci ci*; *ci ins P h stk loc C M pc frs*; *ins-jump-ok ins pc'* $\rrbracket \implies$ *ci ins P h stk loc C M pc' frs*
unfolding *wf-ci-def* **by** *blast*

lemma *check-instr'-ins-jump-ok*: *check-instr' ins P h stk loc C M pc frs* \implies *ins-jump-ok ins pc*
by(cases ins) auto

lemma *wf-ci-ins-jump-ok*:
assumes *wf: wf-ci ci*
and *ci: ci ins P h stk loc C M pc frs*
and *pc': pc \leq pc'*
shows *ins-jump-ok ins pc'*

proof –

from *wf ci* **have** *check-instr' ins P h stk loc C M pc frs* **by**(rule *wf-ciD2*)
with *pc'* **have** *check-instr' ins P h stk loc C M pc' frs* **by**(cases ins) auto
thus *?thesis* **by**(rule *check-instr'-ins-jump-ok*)

qed

lemma *wf-ciD3'*: \llbracket *wf-ci ci*; *ci ins P h stk loc C M pc frs*; *pc \leq pc'* $\rrbracket \implies$ *ci ins P h stk loc C M pc' frs*
apply(erule (2) *wf-ci-ins-jump-ok*)
apply(erule (2) *wf-ciD3*)
done

typedef ('addr, 'heap) *check-instr* = *Collect wf-ci :: ('addr, 'heap) check-instr' set*
morphisms *ci-app Abs-check-instr*
by *auto*

lemma *ci-app-check-instr' [simp]*: *ci-app (Abs-check-instr check-instr')* = *check-instr'*
by(*simp add: Abs-check-instr-inverse*)

lemma (**in** *JVM-heap-base*) *ci-app-check-instr [simp]*: *ci-app (Abs-check-instr check-instr)* = *check-instr*
by(*simp add: Abs-check-instr-inverse*)

lemma *wf-ci-stk-offerD*:
ci-app ci ins P h stk loc C M pc frs \implies *ci-app ci ins P h (stk @ stk') loc C M pc frs*

apply(rule *ci-stk-offerD*[*OF wf-ciD1*]) **back**
by(rule *ci-app* [*simplified*])

lemma *wf-ciD2-ci-app*:

ci-app ci ins P h stk loc C M pc frs \implies *check-instr' ins P h stk loc C M pc frs*
apply(cases *ci*)
apply(simp add: *Abs-check-instr-inverse*)
apply(erule (1) *wf-ciD2*)
done

lemma *wf-ciD3-ci-app*:

\llbracket *ci-app ci ins P h stk loc C M pc frs; ins-jump-ok ins pc'* $\rrbracket \implies$ *ci-app ci ins P h stk loc C M pc' frs*
apply(cases *ci*)
apply(simp add: *Abs-check-instr-inverse*)
apply(erule (2) *wf-ciD3*)
done

lemma *wf-ciD3'-ci-app*: \llbracket *ci-app ci ins P h stk loc C M pc frs; pc \leq pc'* $\rrbracket \implies$ *ci-app ci ins P h stk loc C M pc' frs*

apply(cases *ci*)
apply(simp add: *Abs-check-instr-inverse*)
apply(erule (2) *wf-ciD3'*)
done

context *JVM-heap-base* **begin**

inductive *exec-meth* ::

(*'addr, 'heap*) *check-instr* \Rightarrow *'addr jvm-prog* \Rightarrow *'addr instr list* \Rightarrow *ex-table* \Rightarrow *'thread-id*
 \Rightarrow *'heap* \Rightarrow (*'addr val list* \times *'addr val list* \times *pc* \times *'addr option*) \Rightarrow (*'addr, 'thread-id, 'heap*)
jvm-thread-action
 \Rightarrow *'heap* \Rightarrow (*'addr val list* \times *'addr val list* \times *pc* \times *'addr option*) \Rightarrow *bool*

for *ci* :: (*'addr, 'heap*) *check-instr* **and** *P* :: *'addr jvm-prog*
and *ins* :: *'addr instr list* **and** *xt* :: *ex-table* **and** *t* :: *'thread-id*

where

exec-instr:
 \llbracket (*ta, xcp, h', [(stk', loc', undefined, undefined, pc')*]) \in *exec-instr* (*ins ! pc*) *P t h stk loc undefined undefined pc* \llbracket ;
 $pc < \text{length } ins$;
ci-app ci (*ins ! pc*) *P h stk loc undefined undefined pc* \llbracket \rrbracket
 \implies *exec-meth ci P ins xt t h (stk, loc, pc, None) ta h' (stk', loc', pc', xcp)*

| *exec-catch*:

\llbracket *match-ex-table P (cname-of h xcp) pc xt = [(pc', d)]; d \leq length stk* \rrbracket
 \implies *exec-meth ci P ins xt t h (stk, loc, pc, [xcp]) ε h (Addr xcp $\#$ drop (size stk - d) stk, loc, pc', None)*

lemma *exec-meth-instr*:

exec-meth ci P ins xt t h (stk, loc, pc, None) ta h' (stk', loc', pc', xcp) \longleftrightarrow
(*ta, xcp, h', [(stk', loc', undefined, undefined, pc')*]) \in *exec-instr* (*ins ! pc*) *P t h stk loc undefined undefined pc* \llbracket \wedge $pc < \text{length } ins \wedge$ *ci-app ci* (*ins ! pc*) *P h stk loc undefined undefined pc* \llbracket
by(auto elim: *exec-meth.cases* intro: *exec-instr*)

lemma *exec-meth-xcpt*:

exec-meth ci P ins xt t h (stk, loc, pc, [xcp]) ta h (stk', loc', pc', xcp') \longleftrightarrow

$(\exists d. \text{match-ex-table } P \text{ (cname-of } h \text{ xcp) } pc \text{ xt} = \lfloor (pc', d) \rfloor \wedge ta = \varepsilon \wedge stk' = (\text{Addr } xcp \# \text{ drop } (\text{size } stk - d) \text{ stk}) \wedge loc' = loc \wedge xcp' = \text{None} \wedge d \leq \text{length } stk)$
by(*auto elim: exec-meth.cases intro: exec-catch*)

abbreviation *exec-meth-a*

where *exec-meth-a* \equiv *exec-meth* (*Abs-check-instr check-instr'*)

abbreviation *exec-meth-d*

where *exec-meth-d* \equiv *exec-meth* (*Abs-check-instr check-instr*)

lemma *exec-meth-length-compE2D* [*dest*]:

exec-meth ci P (compE2 e) (compxE2 e 0 d) t h (stk, loc, pc, xcp) ta h' s' \implies pc < length (compE2 e)

apply(*erule exec-meth.cases*)

apply(*auto dest: match-ex-table-pc-length-compE2*)

done

lemma *exec-meth-length-compEs2D* [*dest*]:

exec-meth ci P (compEs2 es) (compxEs2 es 0 0) t h (stk, loc, pc, xcp) ta h' s' \implies pc < length (compEs2 es)

apply(*erule exec-meth.cases*)

apply(*auto dest: match-ex-table-pc-length-compEs2*)

done

lemma *exec-instr-stk-offer*:

assumes *check: check-instr' (ins ! pc) P h stk loc C M pc frs*

and *exec: (ta', xcp', h', (stk', loc', C, M, pc') # frs) \in exec-instr (ins ! pc) P t h stk loc C M pc frs*

shows *(ta', xcp', h', (stk' @ stk'', loc', C, M, pc') # frs) \in exec-instr (ins ! pc) P t h (stk @ stk'')*

loc C M pc frs

using *assms*

proof(*cases ins ! pc*)

case (*Invoke M n*)

thus *?thesis using exec check*

by(*auto split: if-split-asm extCallRet.splits split del: if-split simp add: split-beta nth-append min-def extRet2JVM-def*)

qed(*force simp add: nth-append is-Ref-def has-method-def nth-Cons split-beta hd-append tl-append neq-Nil-conv split: list.split if-split-asm nat.splits sum.split-asm*)**+**

lemma *exec-meth-stk-offer*:

assumes *exec: exec-meth ci P ins xt t h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')*

shows *exec-meth ci P ins (stack-xlift (length stk'') xt) t h (stk @ stk'', loc, pc, xcp) ta h' (stk' @ stk'', loc', pc', xcp')*

using *exec*

proof(*cases*)

case (*exec-catch xcp d*)

from $\langle \text{match-ex-table } P \text{ (cname-of } h \text{ xcp) } pc \text{ xt} = \lfloor (pc', d) \rfloor \rangle$

have *match-ex-table P (cname-of h xcp) pc (stack-xlift (length stk'') xt) = $\lfloor (pc', \text{length } stk'' + d) \rfloor$*

by(*simp add: match-ex-table-stack-xlift*)

moreover have *length stk'' + d \leq length (stk @ stk'')* **using** $\langle d \leq \text{length } stk \rangle$ **by** *simp*

ultimately have *exec-meth ci P ins (stack-xlift (length stk'') xt) t h ((stk @ stk''), loc, pc, $\lfloor xcp \rfloor$) \in h ((Addr xcp # drop (length (stk @ stk'') - (length stk'' + d)) (stk @ stk'')), loc, pc', None)*

by(*rule exec-meth.exec-catch*)

with *exec-catch show ?thesis by (simp)*

next

case *exec-instr*
note $ciins = \langle ci\text{-app } ci \ (ins \ ! \ pc) \ P \ h \ stk \ loc \ undefined \ undefined \ pc \ [] \rangle$
hence $ci\text{-app } ci \ (ins \ ! \ pc) \ P \ h \ (stk \ @ \ stk'') \ loc \ undefined \ undefined \ pc \ []$
by(rule *wf-ci-stk-offerD*)
moreover from *ciins*
have $check\text{-instr}' \ (ins \ ! \ pc) \ P \ h \ stk \ loc \ undefined \ undefined \ pc \ []$
by(rule *wf-ciD2-ci-app*)
hence $(ta, xcp', h', [(stk' \ @ \ stk'', loc', undefined, undefined, pc')]) \in exec\text{-instr} \ (ins \ ! \ pc) \ P \ t \ h \ (stk \ @ \ stk'') \ loc \ undefined \ undefined \ pc \ []$
using $\langle (ta, xcp', h', [(stk', loc', undefined, undefined, pc')]) \in exec\text{-instr} \ (ins \ ! \ pc) \ P \ t \ h \ stk \ loc \ undefined \ undefined \ pc \ [] \rangle$
by(rule *exec-instr-stk-offer*)
ultimately show *?thesis using exec-instr by(auto intro: exec-meth.exec-instr)*
qed

lemma *exec-meth-append-xt* [*intro*]:
 $exec\text{-meth } ci \ P \ ins \ xt \ t \ h \ s \ ta \ h' \ s'$
 $\implies exec\text{-meth } ci \ P \ (ins \ @ \ ins') \ (xt \ @ \ xt') \ t \ h \ s \ ta \ h' \ s'$
apply(erule *exec-meth.cases*)
apply(*auto*)
apply(rule *exec-instr*)
apply(*clarsimp simp add: nth-append*)
apply(*simp*)
apply(*simp add: nth-append*)
apply(rule *exec-catch*)
by(*simp*)

lemma *exec-meth-append* [*intro*]:
 $exec\text{-meth } ci \ P \ ins \ xt \ t \ h \ s \ ta \ h' \ s' \implies exec\text{-meth } ci \ P \ (ins \ @ \ ins') \ xt \ t \ h \ s \ ta \ h' \ s'$
by(rule *exec-meth-append-xt*[**where** $xt' = []$, *simplified*])

lemma *append-exec-meth-xt*:
assumes *exec: exec-meth ci P ins xt t h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')*
and *jump: jump-ok ins 0 n*
and *pcs: pcs xt' \subseteq {0.. $length \ ins'$ }*
shows $exec\text{-meth } ci \ P \ (ins' \ @ \ ins) \ (xt' \ @ \ shift \ (length \ ins') \ xt) \ t \ h \ (stk, loc, (length \ ins' + pc), xcp)$
 $ta \ h' \ (stk', loc', (length \ ins' + pc'), xcp')$
using *exec*
proof(*cases*)
case (*exec-catch xcp d*)
from $\langle match\text{-ex-table } P \ (cname\text{-of } h \ xcp) \ pc \ xt = [(pc', d)] \rangle$
have $match\text{-ex-table } P \ (cname\text{-of } h \ xcp) \ (length \ ins' + pc) \ (shift \ (length \ ins') \ xt) = [(length \ ins' + pc', d)]$
by(*simp add: match-ex-table-shift*)
moreover from *pcs* **have** $length \ ins' + pc \notin pcs \ xt'$ **by**(*auto*)
ultimately have $match\text{-ex-table } P \ (cname\text{-of } h \ xcp) \ (length \ ins' + pc) \ (xt' \ @ \ shift \ (length \ ins') \ xt) = [(length \ ins' + pc', d)]$
by(*simp add: match-ex-table-append-not-pcs*)
with *exec-catch* **show** *?thesis by(auto dest: exec-meth.exec-catch)*
next

case *exec-instr*
note $exec = \langle (ta, xcp', h', [(stk', loc', undefined, undefined, pc')]) \in exec\text{-instr} \ (ins \ ! \ pc) \ P \ t \ h \ stk \ loc \ undefined \ undefined \ pc \ [] \rangle$
hence $(ta, xcp', h', [(stk', loc', undefined, undefined, length \ ins' + pc')]) \in exec\text{-instr} \ (ins \ ! \ pc) \ P \ t$

$h \text{ stk } loc \text{ undefined undefined } (\text{length } ins' + pc) \square$
proof(*cases ins ! pc*)
 case (*Goto i*)
 with *jump* $\langle pc < \text{length } ins \rangle$ **have** $- \text{int } pc \leq i \ i < \text{int } (\text{length } ins - pc + n)$
 by(*auto dest: jump-ok-GotoD*)
 with *exec Goto show ?thesis* **by**(*auto*)
 next
 case (*IfFalse i*)
 with *jump* $\langle pc < \text{length } ins \rangle$ **have** $- \text{int } pc \leq i \ i < \text{int } (\text{length } ins - pc + n)$
 by(*auto dest: jump-ok-IfFalseD*)
 with *exec IfFalse show ?thesis* **by**(*auto*)
 next
 case (*Invoke M n*)
 with *exec show ?thesis*
 by(*auto split: if-split-asm extCallRet.splits split del: if-split simp add: split-beta nth-append min-def extRet2JVM-def*)
 qed(*auto simp add: split-beta split: if-split-asm sum.split-asm*)
 moreover from $\langle ci\text{-app } ci \ (ins \ ! \ pc) \ P \ h \ \text{stk} \ loc \ \text{undefined} \ \text{undefined} \ pc \ \square \rangle$
 have $ci\text{-app } ci \ (ins \ ! \ pc) \ P \ h \ \text{stk} \ loc \ \text{undefined} \ \text{undefined} \ (\text{length } ins' + pc) \ \square$
 by(*rule wf-ciD3'-ci-app*) *simp*
 ultimately have *exec-meth ci P (ins' @ ins) (xt' @ shift (length ins') xt) t h (stk, loc, (length ins' + pc), None) ta h' (stk', loc', (length ins' + pc'), xcp')*
 using $\langle pc < \text{length } ins \rangle$ **by** $-(\text{rule } exec\text{-meth.}exec\text{-instr}, \text{simp-all})$
 thus ?thesis using exec-instr by(*auto*)
qed

lemma *append-exec-meth:*

assumes *exec: exec-meth ci P ins xt t h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')*
 and *jump: jump-ok ins 0 n*
 shows *exec-meth ci P (ins' @ ins) (shift (length ins') xt) t h (stk, loc, (length ins' + pc), xcp) ta h' (stk', loc', (length ins' + pc'), xcp')*
 using *assms* **by**(*rule append-exec-meth-xt [where xt' = [], simplified]*)

lemma *exec-meth-take-xt':*

$\llbracket \text{exec-meth } ci \ P \ (ins \ @ \ ins') \ (xt' \ @ \ xt) \ t \ h \ (stk, \ loc, \ pc, \ xcp) \ ta \ h' \ s';$
 $pc < \text{length } ins; pc \notin pcs \ xt \ \rrbracket$
 $\implies \text{exec-meth } ci \ P \ ins \ xt' \ t \ h \ (stk, \ loc, \ pc, \ xcp) \ ta \ h' \ s'$
apply(*erule exec-meth.cases*)
apply(*auto intro: exec-meth.intros simp add: match-ex-table-append nth-append dest: match-ex-table-pcsD*)
done

lemma *exec-meth-take-xt:*

$\llbracket \text{exec-meth } ci \ P \ (ins \ @ \ ins') \ (xt' \ @ \ \text{shift } (\text{length } ins) \ xt) \ t \ h \ (stk, \ loc, \ pc, \ xcp) \ ta \ h' \ s';$
 $pc < \text{length } ins \ \rrbracket$
 $\implies \text{exec-meth } ci \ P \ ins \ xt' \ t \ h \ (stk, \ loc, \ pc, \ xcp) \ ta \ h' \ s'$
by(*auto intro: exec-meth-take-xt'*)

lemma *exec-meth-take:*

$\llbracket \text{exec-meth } ci \ P \ (ins \ @ \ ins') \ xt \ t \ h \ (stk, \ loc, \ pc, \ xcp) \ ta \ h' \ s';$
 $pc < \text{length } ins \ \rrbracket$
 $\implies \text{exec-meth } ci \ P \ ins \ xt \ t \ h \ (stk, \ loc, \ pc, \ xcp) \ ta \ h' \ s'$
by(*auto intro: exec-meth-take-xt [where xt = []]*)

lemma *exec-meth-drop-xt*:

assumes *exec*: *exec-meth* *ci* *P* (*ins* @ *ins'*) (*xt* @ *shift* (*length ins*) *xt'*) *t h* (*stk*, *loc*, (*length ins* + *pc*), *xcp*) *ta* *h'* (*stk'*, *loc'*, *pc'*, *xcp'*)

and *xt*: *pcs* *xt* \subseteq $\{..<length\ ins\}$

and *jump*: *jump-ok* *ins'* 0 *n*

shows *exec-meth* *ci* *P* *ins'* *xt'* *t h* (*stk*, *loc*, *pc*, *xcp*) *ta* *h'* (*stk'*, *loc'*, (*pc'* - *length ins*), *xcp'*)

using *exec*

proof(*cases* *rule*: *exec-meth.cases*)

case *exec-instr*

let *?PC* = *length ins* + *pc*

note [*simp*] = $\langle xcp = None \rangle$

from $\langle ?PC < length\ (ins\ @\ ins') \rangle$ **have** *pc*: *pc* < *length ins'* **by** *simp*

moreover with $\langle (ta, xcp', h', [(stk', loc', undefined, undefined, pc')]) \in exec-instr\ ((ins\ @\ ins')\ !\ ?PC)\ P\ t\ h\ stk\ loc\ undefined\ undefined\ ?PC\ [] \rangle$

have $(ta, xcp', h', [(stk', loc', undefined, undefined, pc' - length\ ins)]) \in exec-instr\ (ins'\ !\ pc)\ P\ t\ h\ stk\ loc\ undefined\ undefined\ pc\ []$

apply(*cases* *ins'* ! *pc*)

apply(*simp-all* *add*: *split-beta* *split*: *if-split-asm* *sum.split-asm* *split* *del*: *if-split*)

apply(*force* *split*: *extCallRet.splits* *simp* *add*: *min-def* *extRet2JVM-def*) +

done

moreover from $\langle ci-app\ ci\ ((ins\ @\ ins')\ !\ ?PC)\ P\ h\ stk\ loc\ undefined\ undefined\ ?PC\ [] \rangle$ *jump* *pc*

have *ci-app* *ci* (*ins'* ! *pc*) *P* *h* *stk* *loc* *undefined* *undefined* *pc* []

by(*fastforce* *elim*: *wf-ciD3-ci-app* *dest*: *jump-ok-ins-jump-ok*)

ultimately show *?thesis* **by**(*auto* *intro*: *exec-meth.intros*)

next

case (*exec-catch* *XCP* *D*)

let *?PC* = *length ins* + *pc*

note [*simp*] = $\langle xcp = \lfloor XCP \rfloor \rangle$

$\langle ta = \varepsilon \rangle \langle h' = h \rangle \langle stk' = Addr\ XCP\ \# \ drop\ (length\ stk - D)\ stk \rangle \langle loc' = loc \rangle \langle xcp' = None \rangle$

from $\langle match-ex-table\ P\ (cname-of\ h\ XCP)\ ?PC\ (xt\ @\ shift\ (length\ ins)\ xt') = \lfloor (pc', D) \rfloor \rangle$ *xt*

have *match-ex-table* *P* (*cname-of* *h* *XCP*) *pc* *xt'* = $\lfloor (pc' - length\ ins, D) \rfloor$

by(*auto* *simp* *add*: *match-ex-table-append* *dest*: *match-ex-table-shift-pcD* *match-ex-table-pcsD*)

with $\langle D \leq length\ stk \rangle$ **show** *?thesis* **by**(*auto* *intro*: *exec-meth.intros*)

qed

lemma *exec-meth-drop*:

$\llbracket exec-meth\ ci\ P\ (ins\ @\ ins')\ (shift\ (length\ ins)\ xt)\ t\ h\ (stk, loc, (length\ ins + pc), xcp)\ ta\ h'\ (stk', loc', pc', xcp');$

$jump-ok\ ins'\ 0\ b \rrbracket$

$\implies exec-meth\ ci\ P\ ins'\ xt\ t\ h\ (stk, loc, pc, xcp)\ ta\ h'\ (stk', loc', (pc' - length\ ins), xcp')$

by(*auto* *intro*: *exec-meth-drop-xt*[**where** *xt* = []])

lemma *exec-meth-drop-xt-pc*:

assumes *exec*: *exec-meth* *ci* *P* (*ins* @ *ins'*) (*xt* @ *shift* (*length ins*) *xt'*) *t h* (*stk*, *loc*, *pc*, *xcp*) *ta* *h'* (*stk'*, *loc'*, *pc'*, *xcp'*)

and *pc*: *pc* \geq *length ins*

and *pcs*: *pcs* *xt* \subseteq $\{..<length\ ins\}$

and *jump*: *jump-ok* *ins'* 0 *n'*

shows *pc'* \geq *length ins*

using *exec*

proof(*cases* *rule*: *exec-meth.cases*)

case *exec-instr* **thus** *?thesis* **using** *jump* *pc*

apply(*cases* *ins'* ! (*pc* - *length ins*))

apply(*simp-all* *add*: *split-beta* *nth-append* *split*: *if-split-asm* *sum.split-asm*)

```

apply(force split: extCallRet.splits simp add: min-def extRet2JVM-def dest: jump-ok-GotoD jump-ok-IfFalseD)+
done
next
  case exec-catch thus ?thesis using pcs pc
  by(auto dest: match-ex-table-pcsD match-ex-table-shift-pcD simp add: match-ex-table-append)
qed

```

lemmas *exec-meth-drop-pc* = *exec-meth-drop-xt-pc*[**where** *xt*=[], *simplified*]

definition *exec-move* ::

```

('addr, 'heap) check-instr  $\Rightarrow$  'addr J1-prog  $\Rightarrow$  'thread-id  $\Rightarrow$  'addr expr1
 $\Rightarrow$  'heap  $\Rightarrow$  ('addr val list  $\times$  'addr val list  $\times$  pc  $\times$  'addr option)
 $\Rightarrow$  ('addr, 'thread-id, 'heap) jvm-thread-action
 $\Rightarrow$  'heap  $\Rightarrow$  ('addr val list  $\times$  'addr val list  $\times$  pc  $\times$  'addr option)  $\Rightarrow$  bool

```

where *exec-move* *ci P t e* \equiv *exec-meth* *ci* (*compP2* *P*) (*compE2* *e*) (*compxE2* *e* 0 0) *t*

definition *exec-moves* ::

```

('addr, 'heap) check-instr  $\Rightarrow$  'addr J1-prog  $\Rightarrow$  'thread-id  $\Rightarrow$  'addr expr1 list
 $\Rightarrow$  'heap  $\Rightarrow$  ('addr val list  $\times$  'addr val list  $\times$  pc  $\times$  'addr option)
 $\Rightarrow$  ('addr, 'thread-id, 'heap) jvm-thread-action
 $\Rightarrow$  'heap  $\Rightarrow$  ('addr val list  $\times$  'addr val list  $\times$  pc  $\times$  'addr option)  $\Rightarrow$  bool

```

where *exec-moves* *ci P t es* \equiv *exec-meth* *ci* (*compP2* *P*) (*compEs2* *es*) (*compxEs2* *es* 0 0) *t*

abbreviation *exec-move-a*

where *exec-move-a* \equiv *exec-move* (*Abs-check-instr* *check-instr'*)

abbreviation *exec-move-d*

where *exec-move-d* \equiv *exec-move* (*Abs-check-instr* *check-instr*)

abbreviation *exec-moves-a*

where *exec-moves-a* \equiv *exec-moves* (*Abs-check-instr* *check-instr'*)

abbreviation *exec-moves-d*

where *exec-moves-d* \equiv *exec-moves* (*Abs-check-instr* *check-instr*)

lemma *exec-move-newArrayI*:

exec-move *ci P t e h s ta h' s'* \Longrightarrow *exec-move* *ci P t* (*newA* *T*[*e*]) *h s ta h' s'*

unfolding *exec-move-def* **by** *auto*

lemma *exec-move-newArray*:

pc < *length* (*compE2* *e*) \Longrightarrow *exec-move* *ci P t* (*newA* *T*[*e*]) *h* (*stk*, *loc*, *pc*, *xcp*) = *exec-move* *ci P t* *e h* (*stk*, *loc*, *pc*, *xcp*)

unfolding *exec-move-def* **by**(*auto* *intro!*: *ext* *intro*: *exec-meth-take*)

lemma *exec-move-CastI*:

exec-move *ci P t e h s ta h' s'* \Longrightarrow *exec-move* *ci P t* (*Cast* *T* *e*) *h s ta h' s'*

unfolding *exec-move-def* **by** *auto*

lemma *exec-move-Cast*:

pc < *length* (*compE2* *e*) \Longrightarrow *exec-move* *ci P t* (*Cast* *T* *e*) *h* (*stk*, *loc*, *pc*, *xcp*) = *exec-move* *ci P t e h* (*stk*, *loc*, *pc*, *xcp*)

unfolding *exec-move-def* **by**(*auto* *intro!*: *ext* *intro*: *exec-meth-take*)

lemma *exec-move-InstanceOfI*:

$exec-move\ ci\ P\ t\ e\ h\ s\ ta\ h'\ s' \implies exec-move\ ci\ P\ t\ (e\ instanceof\ T)\ h\ s\ ta\ h'\ s'$
unfolding $exec-move-def$ **by** $auto$

lemma $exec-move-InstanceOf$:

$pc < length\ (compE2\ e) \implies exec-move\ ci\ P\ t\ (e\ instanceof\ T)\ h\ (stk,\ loc,\ pc,\ xcp) = exec-move\ ci\ P\ t\ e\ h\ (stk,\ loc,\ pc,\ xcp)$

unfolding $exec-move-def$ **by**($auto\ intro!$: $ext\ intro$: $exec-meth-take$)

lemma $exec-move-BinOpI1$:

$exec-move\ ci\ P\ t\ e\ h\ s\ ta\ h'\ s' \implies exec-move\ ci\ P\ t\ (e\ \llbracket bop \rrbracket\ e')\ h\ s\ ta\ h'\ s'$

unfolding $exec-move-def$ **by** $auto$

lemma $exec-move-BinOp1$:

$pc < length\ (compE2\ e) \implies exec-move\ ci\ P\ t\ (e\ \llbracket bop \rrbracket\ e')\ h\ (stk,\ loc,\ pc,\ xcp) = exec-move\ ci\ P\ t\ e\ h\ (stk,\ loc,\ pc,\ xcp)$

unfolding $exec-move-def$

by($auto\ intro!$: $ext\ intro$: $exec-meth-take-xt\ simp\ add$: $compxE2-size-convs$)

lemma $exec-move-BinOpI2$:

assumes $exec$: $exec-move\ ci\ P\ t\ e2\ h\ (stk,\ loc,\ pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$

shows $exec-move\ ci\ P\ t\ (e1\ \llbracket bop \rrbracket\ e2)\ h\ (stk\ @\ [v],\ loc,\ length\ (compE2\ e1) + pc,\ xcp)\ ta\ h'\ (stk'\ @\ [v],\ loc',\ length\ (compE2\ e1) + pc',\ xcp')$

proof –

from $exec$ **have** $exec-meth\ ci\ (compP2\ P)\ (compE2\ e2)\ (compxE2\ e2\ 0\ 0)\ t\ h\ (stk,\ loc,\ pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$

unfolding $exec-move-def$.

from $exec-meth-stk-offer[OF\ this,$ **where** $stk''=[v]$ **show** $?thesis$

by($fastforce\ split$: $bop.splits\ intro$: $append-exec-meth-xt\ simp\ add$: $exec-move-def\ compxE2-size-convs\ compxE2-stack-xlift-convs$)

qed

lemma $exec-move-LAssI$:

$exec-move\ ci\ P\ t\ e\ h\ s\ ta\ h'\ s' \implies exec-move\ ci\ P\ t\ (V\ :=\ e)\ h\ s\ ta\ h'\ s'$

unfolding $exec-move-def$ **by** $auto$

lemma $exec-move-LAss$:

$pc < length\ (compE2\ e) \implies exec-move\ ci\ P\ t\ (V\ :=\ e)\ h\ (stk,\ loc,\ pc,\ xcp) = exec-move\ ci\ P\ t\ e\ h\ (stk,\ loc,\ pc,\ xcp)$

unfolding $exec-move-def$ **by**($auto\ intro!$: $ext\ intro$: $exec-meth-take$)

lemma $exec-move-AAccI1$:

$exec-move\ ci\ P\ t\ e\ h\ s\ ta\ h'\ s' \implies exec-move\ ci\ P\ t\ (e[e'])\ h\ s\ ta\ h'\ s'$

unfolding $exec-move-def$ **by** $auto$

lemma $exec-move-AAcc1$:

$pc < length\ (compE2\ e) \implies exec-move\ ci\ P\ t\ (e[e'])\ h\ (stk,\ loc,\ pc,\ xcp) = exec-move\ ci\ P\ t\ e\ h\ (stk,\ loc,\ pc,\ xcp)$

unfolding $exec-move-def$

by($auto\ intro!$: $ext\ intro$: $exec-meth-take-xt\ simp\ add$: $compxE2-size-convs$)

lemma $exec-move-AAccI2$:

assumes $exec$: $exec-move\ ci\ P\ t\ e2\ h\ (stk,\ loc,\ pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$

shows $exec-move\ ci\ P\ t\ (e1[e2])\ h\ (stk\ @\ [v],\ loc,\ length\ (compE2\ e1) + pc,\ xcp)\ ta\ h'\ (stk'\ @\ [v],\ loc',\ length\ (compE2\ e1) + pc',\ xcp')$

proof –

from *exec* **have** *exec-meth* *ci* (*compP2* *P*) (*compE2* *e2*) (*compxE2* *e2* 0 0) *t* *h* (*stk*, *loc*, *pc*, *xcp*) *ta* *h'* (*stk'*, *loc'*, *pc'*, *xcp'*)

unfolding *exec-move-def* .

from *exec-meth-stk-offer*[*OF this*, **where** *stk''*=[*v*]] **show** *?thesis*

by(*fastforce intro: append-exec-meth-xt simp add: exec-move-def compxE2-size-convs compxE2-stack-xlift-convs*)
qed

lemma *exec-move-AAssI1*:

exec-move *ci* *P* *t* *e* *h* *s* *ta* *h'* *s'* \implies *exec-move* *ci* *P* *t* (*e*[*e'*] := *e''*) *h* *s* *ta* *h'* *s'*

unfolding *exec-move-def* **by** *auto*

lemma *exec-move-AAss1*:

assumes *pc*: *pc* < *length* (*compE2* *e*)

shows *exec-move* *ci* *P* *t* (*e*[*e'*] := *e''*) *h* (*stk*, *loc*, *pc*, *xcp*) = *exec-move* *ci* *P* *t* *e* *h* (*stk*, *loc*, *pc*, *xcp*)
(**is** *?lhs* = *?rhs*)

proof(*rule ext iffI*)+

fix *ta* *h'* *s'* **assume** *?rhs* *ta* *h'* *s'*

thus *?lhs* *ta* *h'* *s'* **by**(*rule exec-move-AAssI1*)

next

fix *ta* *h'* *s'* **assume** *?lhs* *ta* *h'* *s'*

hence *exec-meth* *ci* (*compP2* *P*) (*compE2* *e* @ *compE2* *e'* @ *compE2* *e''* @ [*AStore*, *Push Unit*])

(*compxE2* *e* 0 0 @ *shift* (*length* (*compE2* *e*)) (*compxE2* *e'* 0 (*Suc* 0) @ *compxE2* *e''* (*length* (*compE2* *e'*)) (*Suc* (*Suc* 0)))) *t*

h (*stk*, *loc*, *pc*, *xcp*) *ta* *h'* *s'* **by**(*simp add: exec-move-def shift-compxE2 ac-simps*)

thus *?rhs* *ta* *h'* *s'* **unfolding** *exec-move-def* **using** *pc* **by**(*rule exec-meth-take-xt*)

qed

lemma *exec-move-AAssI2*:

assumes *exec*: *exec-move* *ci* *P* *t* *e2* *h* (*stk*, *loc*, *pc*, *xcp*) *ta* *h'* (*stk'*, *loc'*, *pc'*, *xcp'*)

shows *exec-move* *ci* *P* *t* (*e1*[*e2*] := *e3*) *h* (*stk* @ [*v*], *loc*, *length* (*compE2* *e1*) + *pc*, *xcp*) *ta* *h'* (*stk'* @ [*v*], *loc'*, *length* (*compE2* *e1*) + *pc'*, *xcp'*)

proof –

from *exec* **have** *exec-meth* *ci* (*compP2* *P*) (*compE2* *e2*) (*compxE2* *e2* 0 0) *t* *h* (*stk*, *loc*, *pc*, *xcp*) *ta* *h'* (*stk'*, *loc'*, *pc'*, *xcp'*)

unfolding *exec-move-def* .

from *exec-meth-stk-offer*[*OF this*, **where** *stk''*=[*v*], *simplified stack-xlift-compxE2*, *simplified*]

have *exec-meth* *ci* (*compP2* *P*) (*compE2* *e2* @ *compE2* *e3* @ [*AStore*, *Push Unit*]) (*compxE2* *e2* 0 (*Suc* 0) @ *shift* (*length* (*compE2* *e2*)) (*compxE2* *e3* 0 (*Suc* (*Suc* 0)))) *t* *h* (*stk* @ [*v*], *loc*, *pc*, *xcp*) *ta* *h'* (*stk'* @ [*v*], *loc'*, *pc'*, *xcp'*)

by(*rule exec-meth-append-xt*)

hence *exec-meth* *ci* (*compP2* *P*) (*compE2* *e1* @ *compE2* *e2* @ *compE2* *e3* @ [*AStore*, *Push Unit*]) (*compxE2* *e1* 0 0 @ *shift* (*length* (*compE2* *e1*)) (*compxE2* *e2* 0 (*Suc* 0) @ *shift* (*length* (*compE2* *e2*)) (*compxE2* *e3* 0 (*Suc* (*Suc* 0)))) *t* *h* (*stk* @ [*v*], *loc*, *length* (*compE2* *e1*) + *pc*, *xcp*) *ta* *h'* (*stk'* @ [*v*], *loc'*, *length* (*compE2* *e1*) + *pc'*, *xcp'*)

by(*rule append-exec-meth-xt*) *auto*

thus *?thesis* **by**(*auto simp add: exec-move-def shift-compxE2 ac-simps*)

qed

lemma *exec-move-AAssI3*:

assumes *exec*: *exec-move* *ci* *P* *t* *e3* *h* (*stk*, *loc*, *pc*, *xcp*) *ta* *h'* (*stk'*, *loc'*, *pc'*, *xcp'*)

shows *exec-move* *ci* *P* *t* (*e1*[*e2*] := *e3*) *h* (*stk* @ [*v'*, *v*], *loc*, *length* (*compE2* *e1*) + *length* (*compE2* *e2*) + *pc*, *xcp*) *ta* *h'* (*stk'* @ [*v'*, *v*], *loc'*, *length* (*compE2* *e1*) + *length* (*compE2* *e2*) + *pc'*, *xcp'*)

proof –

from *exec* **have** *exec-meth ci (compP2 P) (compE2 e3) (compxE2 e3 0 0) t h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')*

unfolding *exec-move-def* .

from *exec-meth-stk-offer[OF this, where stk''=[v', v], simplified stack-xlift-compxE2, simplified]*
have *exec-meth ci (compP2 P) (compE2 e3 @ [AStore, Push Unit]) (compxE2 e3 0 (Suc (Suc 0))) t h (stk @ [v', v], loc, pc, xcp) ta h' (stk' @ [v', v], loc', pc', xcp')*

by(*rule exec-meth-append*)

hence *exec-meth ci (compP2 P) ((compE2 e1 @ compE2 e2) @ compE2 e3 @ [AStore, Push Unit])*

((compxE2 e1 0 0 @ compxE2 e2 (length (compE2 e1)) (Suc 0)) @ shift (length (compE2 e1 @ compE2 e2)) (compxE2 e3 0 (Suc (Suc 0)))) t h (stk @ [v', v], loc, length (compE2 e1 @ compE2 e2) + pc, xcp) ta h' (stk' @ [v', v], loc', length (compE2 e1 @ compE2 e2) + pc', xcp')

by(*rule append-exec-meth-xt*) *auto*

thus *?thesis by(auto simp add: exec-move-def shift-compxE2 ac-simps)*

qed

lemma *exec-move-ALengthI:*

exec-move ci P t e h s ta h' s' \implies exec-move ci P t (e.length) h s ta h' s'

unfolding *exec-move-def* **by** *auto*

lemma *exec-move-ALength:*

pc < length (compE2 e) \implies exec-move ci P t (e.length) h (stk, loc, pc, xcp) = exec-move ci P t e h (stk, loc, pc, xcp)

unfolding *exec-move-def* **by**(*auto intro!: ext intro: exec-meth-take*)

lemma *exec-move-FAccI:*

exec-move ci P t e h s ta h' s' \implies exec-move ci P t (e.F{D}) h s ta h' s'

unfolding *exec-move-def* **by** *auto*

lemma *exec-move-FAcc:*

pc < length (compE2 e) \implies exec-move ci P t (e.F{D}) h (stk, loc, pc, xcp) = exec-move ci P t e h (stk, loc, pc, xcp)

unfolding *exec-move-def* **by**(*auto intro!: ext intro: exec-meth-take*)

lemma *exec-move-FAssI1:*

exec-move ci P t e h s ta h' s' \implies exec-move ci P t (e.F{D} := e') h s ta h' s'

unfolding *exec-move-def* **by** *auto*

lemma *exec-move-FAss1:*

pc < length (compE2 e) \implies exec-move ci P t (e.F{D} := e') h (stk, loc, pc, xcp) = exec-move ci P t e h (stk, loc, pc, xcp)

unfolding *exec-move-def*

by(*auto intro!: ext intro: exec-meth-take-xt simp add: compxE2-size-convs*)

lemma *exec-move-FAssI2:*

assumes *exec: exec-move ci P t e2 h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')*

shows *exec-move ci P t (e1.F{D} := e2) h (stk @ [v], loc, length (compE2 e1) + pc, xcp) ta h' (stk' @ [v], loc', length (compE2 e1) + pc', xcp')*

proof –

from *exec* **have** *exec-meth ci (compP2 P) (compE2 e2) (compxE2 e2 0 0) t h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')*

unfolding *exec-move-def* .

from *exec-meth-stk-offer[OF this, where stk''=[v]]* **show** *?thesis*

by(*fastforce intro: append-exec-meth-xt simp add: exec-move-def compxE2-size-convs compxE2-stack-xlift-convs*)

qed

lemma *exec-move-CASI1*:

exec-move $ci\ P\ t\ e\ h\ s\ ta\ h'\ s' \implies exec-move\ ci\ P\ t\ (e \cdot compareAndSwap(D \cdot F, e', e''))\ h\ s\ ta\ h'\ s'$
unfolding *exec-move-def* **by** *auto*

lemma *exec-move-CASI*:

assumes *pc*: $pc < length\ (compE2\ e)$
shows *exec-move* $ci\ P\ t\ (e \cdot compareAndSwap(D \cdot F, e', e''))\ h\ (stk, loc, pc, xcp) = exec-move\ ci\ P\ t\ e\ h\ (stk, loc, pc, xcp)$
(is *?lhs = ?rhs***)**
proof(*rule ext iffI*)**+**
fix $ta\ h'\ s'$ **assume** *?rhs* $ta\ h'\ s'$
thus *?lhs* $ta\ h'\ s'$ **by**(*rule exec-move-CASI1*)
next
fix $ta\ h'\ s'$ **assume** *?lhs* $ta\ h'\ s'$
hence *exec-meth* $ci\ (compP2\ P)\ (compE2\ e\ @\ compE2\ e'\ @\ compE2\ e''\ @\ [CAS\ F\ D])\ (compxE2\ e\ 0\ 0\ @\ shift\ (length\ (compE2\ e))\ (compxE2\ e'\ 0\ (Suc\ 0)\ @\ compxE2\ e''\ (length\ (compE2\ e'))\ (Suc\ (Suc\ 0))))\ t$
 $h\ (stk, loc, pc, xcp)\ ta\ h'\ s'$ **by**(*simp add: exec-move-def shift-compxE2 ac-simps*)
thus *?rhs* $ta\ h'\ s'$ **unfolding** *exec-move-def* **using** *pc* **by**(*rule exec-meth-take-xt*)
qed

lemma *exec-move-CASI2*:

assumes *exec*: *exec-move* $ci\ P\ t\ e2\ h\ (stk, loc, pc, xcp)\ ta\ h'\ (stk', loc', pc', xcp')$
shows *exec-move* $ci\ P\ t\ (e1 \cdot compareAndSwap(D \cdot F, e2, e3))\ h\ (stk\ @\ [v], loc, length\ (compE2\ e1) + pc, xcp)\ ta\ h'\ (stk'\ @\ [v], loc', length\ (compE2\ e1) + pc', xcp')$
proof $-$
from *exec* **have** *exec-meth* $ci\ (compP2\ P)\ (compE2\ e2)\ (compxE2\ e2\ 0\ 0)\ t\ h\ (stk, loc, pc, xcp)\ ta\ h'\ (stk', loc', pc', xcp')$
unfolding *exec-move-def* .
from *exec-meth-stk-offer*[*OF this, where* $stk''=[v]$, *simplified stack-xlift-compxE2, simplified*]
have *exec-meth* $ci\ (compP2\ P)\ (compE2\ e2\ @\ compE2\ e3\ @\ [CAS\ F\ D])\ (compxE2\ e2\ 0\ (Suc\ 0)\ @\ shift\ (length\ (compE2\ e2))\ (compxE2\ e3\ 0\ (Suc\ (Suc\ 0))))\ t\ h\ (stk\ @\ [v], loc, pc, xcp)\ ta\ h'\ (stk'\ @\ [v], loc', pc', xcp')$
by(*rule exec-meth-append-xt*)
hence *exec-meth* $ci\ (compP2\ P)\ (compE2\ e1\ @\ compE2\ e2\ @\ compE2\ e3\ @\ [CAS\ F\ D])\ (compxE2\ e1\ 0\ 0\ @\ shift\ (length\ (compE2\ e1))\ (compxE2\ e2\ 0\ (Suc\ 0)\ @\ shift\ (length\ (compE2\ e2))\ (compxE2\ e3\ 0\ (Suc\ (Suc\ 0))))\ t\ h\ (stk\ @\ [v], loc, length\ (compE2\ e1) + pc, xcp)\ ta\ h'\ (stk'\ @\ [v], loc', length\ (compE2\ e1) + pc', xcp')$
by(*rule append-exec-meth-xt*) *auto*
thus *?thesis* **by**(*auto simp add: exec-move-def shift-compxE2 ac-simps*)
qed

lemma *exec-move-CASI3*:

assumes *exec*: *exec-move* $ci\ P\ t\ e3\ h\ (stk, loc, pc, xcp)\ ta\ h'\ (stk', loc', pc', xcp')$
shows *exec-move* $ci\ P\ t\ (e1 \cdot compareAndSwap(D \cdot F, e2, e3))\ h\ (stk\ @\ [v', v], loc, length\ (compE2\ e1) + length\ (compE2\ e2) + pc, xcp)\ ta\ h'\ (stk'\ @\ [v', v], loc', length\ (compE2\ e1) + length\ (compE2\ e2) + pc', xcp')$
proof $-$
from *exec* **have** *exec-meth* $ci\ (compP2\ P)\ (compE2\ e3)\ (compxE2\ e3\ 0\ 0)\ t\ h\ (stk, loc, pc, xcp)\ ta\ h'\ (stk', loc', pc', xcp')$
unfolding *exec-move-def* .
from *exec-meth-stk-offer*[*OF this, where* $stk''=[v', v]$, *simplified stack-xlift-compxE2, simplified*]

have *exec-meth* *ci* (*compP2* *P*) (*compE2* *e3* @ [*CAS F D*]) (*compxE2* *e3* 0 (*Suc* (*Suc* 0))) *t h* (*stk* @ [*v'*, *v*], *loc*, *pc*, *xcp*) *ta h'* (*stk'* @ [*v'*, *v*], *loc'*, *pc'*, *xcp'*)
by(*rule exec-meth-append*)
hence *exec-meth* *ci* (*compP2* *P*) ((*compE2* *e1* @ *compE2* *e2*) @ *compE2* *e3* @ [*CAS F D*])
((*compxE2* *e1* 0 0 @ *compxE2* *e2* (*length* (*compE2* *e1*)) (*Suc* 0)) @ *shift* (*length* (*compE2* *e1* @ *compE2* *e2*)) (*compxE2* *e3* 0 (*Suc* (*Suc* 0)))) *t h* (*stk* @ [*v'*, *v*], *loc*, *length* (*compE2* *e1* @ *compE2* *e2*) + *pc*, *xcp*) *ta h'* (*stk'* @ [*v'*, *v*], *loc'*, *length* (*compE2* *e1* @ *compE2* *e2*) + *pc'*, *xcp'*)
by(*rule append-exec-meth-xt*) *auto*
thus *?thesis* **by**(*auto simp add: exec-move-def shift-compxE2 ac-simps*)
qed

lemma *exec-move-CallI1*:

exec-move *ci P t e h s ta h' s'* \implies *exec-move* *ci P t (e.M(es)) h s ta h' s'*

unfolding *exec-move-def* **by** *auto*

lemma *exec-move-Call1*:

pc < *length* (*compE2* *e*) \implies *exec-move* *ci P t (e.M(es)) h (stk, loc, pc, xcp)* = *exec-move* *ci P t e h (stk, loc, pc, xcp)*

unfolding *exec-move-def*

by(*auto intro!: ext intro: exec-meth-take-xt simp add: compxEs2-size-convs*)

lemma *exec-move-CallI2*:

assumes *exec: exec-moves* *ci P t es h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')*

shows *exec-move* *ci P t (e.M(es)) h (stk @ [v], loc, length (compE2 e) + pc, xcp) ta h' (stk' @ [v], loc', length (compE2 e) + pc', xcp')*

proof –

from *exec* **have** *exec-meth* *ci* (*compP2* *P*) (*compEs2* *es*) (*compxEs2* *es* 0 0) *t h* (*stk, loc, pc, xcp*) *ta h' (stk', loc', pc', xcp')*

unfolding *exec-moves-def* .

from *exec-meth-stk-offer*[*OF this, where stk''=[v]*] **show** *?thesis*

by(*fastforce intro: append-exec-meth-xt simp add: exec-move-def compxEs2-size-convs compxEs2-stack-xlift-convs*)
qed

lemma *exec-move-BlockNoneI*:

exec-move *ci P t e h s ta h' s'* \implies *exec-move* *ci P t {V:T=None; e} h s ta h' s'*

unfolding *exec-move-def* **by** *simp*

lemma *exec-move-BlockNone*:

exec-move *ci P t {V:T=None; e}* = *exec-move* *ci P t e*

unfolding *exec-move-def* **by**(*simp*)

lemma *exec-move-BlockSomeI*:

assumes *exec: exec-move* *ci P t e h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')*

shows *exec-move* *ci P t {V:T=[v]; e} h (stk, loc, Suc (Suc pc), xcp) ta h' (stk', loc', Suc (Suc pc'), xcp')*

proof –

let *?ins* = [*Push v, Store V*]

from *exec* **have** *exec-meth* *ci* (*compP2* *P*) (*compE2* *e*) (*compxE2* *e* 0 0) *t h* (*stk, loc, pc, xcp*) *ta h' (stk', loc', pc', xcp')*

by(*simp add: exec-move-def*)

hence *exec-meth* *ci* (*compP2* *P*) (*?ins* @ *compE2* *e*) (*shift* (*length* *?ins*) (*compxE2* *e* 0 0)) *t h* (*stk, loc, length ?ins + pc, xcp*) *ta h' (stk', loc', length ?ins + pc', xcp')*

by(*rule append-exec-meth*) *auto*

thus *?thesis* **by**(*simp add: exec-move-def shift-compxE2*)

qed

lemma *exec-move-BlockSome*:

$exec-move\ ci\ P\ t\ \{V:T=[v];\ e\}\ h\ (stk,\ loc,\ Suc\ (Suc\ pc),\ xcp)\ ta\ h'\ (stk',\ loc',\ Suc\ (Suc\ pc'),\ xcp')$
 $=$
 $exec-move\ ci\ P\ t\ e\ h\ (stk,\ loc,\ pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$ (is ?lhs = ?rhs)

proof

assume ?rhs **thus** ?lhs **by**(rule *exec-move-BlockSomeI*)

next

let ?ins = [Push v, Store V]

assume ?lhs

hence $exec-meth\ ci\ (compP2\ P)\ (?ins\ @\ compE2\ e)\ (shift\ (length\ ?ins)\ (compxE2\ e\ 0\ 0))\ t\ h\ (stk,\ loc,\ length\ ?ins\ +\ pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ length\ ?ins\ +\ pc',\ xcp')$

by(simp add: *exec-move-def shift-compxE2*)

hence $exec-meth\ ci\ (compP2\ P)\ (compE2\ e)\ (compxE2\ e\ 0\ 0)\ t\ h\ (stk,\ loc,\ pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ length\ ?ins\ +\ pc' - length\ ?ins,\ xcp')$

by(rule *exec-meth-drop*) **auto**

thus ?rhs **by**(simp add: *exec-move-def*)

qed

lemma *exec-move-SyncI1*:

$exec-move\ ci\ P\ t\ e\ h\ s\ ta\ h'\ s' \implies exec-move\ ci\ P\ t\ (sync_V\ (e)\ e')\ h\ s\ ta\ h'\ s'$

unfolding *exec-move-def* **by** *auto*

lemma *exec-move-Sync1*:

assumes $pc: pc < length\ (compE2\ e)$

shows $exec-move\ ci\ P\ t\ (sync_V\ (e)\ e')\ h\ (stk,\ loc,\ pc,\ xcp) = exec-move\ ci\ P\ t\ e\ h\ (stk,\ loc,\ pc,\ xcp)$
 (is ?lhs = ?rhs)

proof(rule *ext iffI*)+

fix $ta\ h'\ s'$

assume ?lhs $ta\ h'\ s'$

hence $exec-meth\ ci\ (compP2\ P)\ (compE2\ e\ @\ Dup\ \#\ Store\ V\ \#\ MEnter\ \#\ compE2\ e'\ @\ [Load\ V,\ MExit,\ Goto\ 4,\ Load\ V,\ MExit,\ ThrowExc])$

$(compxE2\ e\ 0\ 0\ @\ shift\ (length\ (compE2\ e))\ (compxE2\ e'\ 3\ 0\ @\ [(3,\ 3\ +\ length\ (compE2\ e'),\ None,\ 6\ +\ length\ (compE2\ e'),\ 0)]))$

$t\ h\ (stk,\ loc,\ pc,\ xcp)\ ta\ h'\ s'$

by(simp add: *shift-compxE2 ac-simps exec-move-def*)

thus ?rhs $ta\ h'\ s'$ **unfolding** *exec-move-def* **using** pc **by**(rule *exec-meth-take-xt*)

qed(rule *exec-move-SyncI1*)

lemma *exec-move-SyncI2*:

assumes $exec: exec-move\ ci\ P\ t\ e\ h\ (stk,\ loc,\ pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$

shows $exec-move\ ci\ P\ t\ (sync_V\ (o')\ e)\ h\ (stk,\ loc,\ (Suc\ (Suc\ (Suc\ (length\ (compE2\ o')\ +\ pc))))),\ xcp)\ ta\ h'\ (stk',\ loc',\ (Suc\ (Suc\ (Suc\ (length\ (compE2\ o')\ +\ pc')))),\ xcp')$

proof –

let ?e = $compE2\ o'\ @\ [Dup,\ Store\ V,\ MEnter]$

let ?e' = [Load V, MExit, Goto 4, Load V, MExit, ThrowExc]

from $exec$ **have** $exec-meth\ ci\ (compP2\ P)\ (compE2\ e)\ (compxE2\ e\ 0\ 0)\ t\ h\ (stk,\ loc,\ pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$

by(simp add: *exec-move-def*)

hence $exec-meth\ ci\ (compP2\ P)\ ((?e\ @\ compE2\ e)\ @\ ?e')\ ((compxE2\ o'\ 0\ 0\ @\ shift\ (length\ ?e)\ (compxE2\ e\ 0\ 0))\ @\ [(length\ ?e,\ length\ ?e\ +\ length\ (compE2\ e),\ None,\ length\ ?e\ +\ length\ (compE2\ e)\ +\ 3,\ 0])\ t\ h\ (stk,\ loc,\ (length\ ?e\ +\ pc),\ xcp)\ ta\ h'\ (stk',\ loc',\ (length\ ?e\ +\ pc'),\ xcp')$

by(rule *exec-meth-append-xt[OF append-exec-meth-xt]*) **auto**

thus *?thesis* **by**(*simp add: eval-nat-numeral shift-compxE2 exec-move-def*)
qed

lemma *exec-move-SeqI1*:

exec-move ci P t e h s ta h' s' \implies exec-move ci P t (e;;e') h s ta h' s'

unfolding *exec-move-def* **by** *auto*

lemma *exec-move-Seq1*:

assumes *pc: pc < length (compE2 e)*

shows *exec-move ci P t (e;;e') h (stk, loc, pc, xcp) = exec-move ci P t e h (stk, loc, pc, xcp)*

(**is** *?lhs = ?rhs*)

proof(*rule ext iffI*)+

fix *ta h' s'*

assume *?lhs ta h' s'*

hence *exec-meth ci (compP2 P) (compE2 e @ Pop # compE2 e') (compxE2 e 0 0 @ shift (length (compE2 e)) (compxE2 e' (Suc 0) 0)) t h (stk, loc, pc, xcp) ta h' s'*

by(*simp add: exec-move-def shift-compxE2*)

thus *?rhs ta h' s'* **unfolding** *exec-move-def* **using** *pc* **by**(*rule exec-meth-take-xt*)

qed(*rule exec-move-SeqI1*)

lemma *exec-move-SeqI2*:

assumes *exec: exec-move ci P t e h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')*

shows *exec-move ci P t (e;;e) h (stk, loc, (Suc (length (compE2 e') + pc)), xcp) ta h' (stk', loc', (Suc (length (compE2 e') + pc')), xcp')*

proof –

from *exec* **have** *exec-meth ci (compP2 P) (compE2 e) (compxE2 e 0 0) t h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')*

by(*simp add: exec-move-def*)

hence *exec-meth ci (compP2 P) ((compE2 e' @ [Pop]) @ compE2 e) (compxE2 e' 0 0 @ shift (length (compE2 e' @ [Pop])) (compxE2 e 0 0)) t h (stk, loc, (length ((compE2 e') @ [Pop]) + pc), xcp) ta h' (stk', loc', (length ((compE2 e') @ [Pop]) + pc'), xcp')*

by(*rule append-exec-meth-xt*) *auto*

thus *?thesis* **by**(*simp add: shift-compxE2 exec-move-def*)

qed

lemma *exec-move-Seq2*:

assumes *pc: pc < length (compE2 e)*

shows *exec-move ci P t (e';;e) h (stk, loc, Suc (length (compE2 e') + pc), xcp) ta*

h' (stk', loc', Suc (length (compE2 e') + pc'), xcp') =

exec-move ci P t e h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')

(**is** *?lhs = ?rhs*)

proof

let *?E = compE2 e' @ [Pop]*

assume *?lhs*

hence *exec-meth ci (compP2 P) (?E @ compE2 e) (compxE2 e' 0 0 @ shift (length ?E) (compxE2 e 0 0)) t h (stk, loc, length ?E + pc, xcp) ta h' (stk', loc', length ?E + pc', xcp')*

by(*simp add: exec-move-def shift-compxE2*)

from *exec-meth-drop-xt[OF this]* **show** *?rhs* **unfolding** *exec-move-def* **by** *fastforce*

qed(*rule exec-move-SeqI2*)

lemma *exec-move-CondI1*:

exec-move ci P t e h s ta h' s' \implies exec-move ci P t (if (e) e1 else e2) h s ta h' s'

unfolding *exec-move-def* **by** *auto*

lemma *exec-move-Cond1*:

assumes *pc*: $pc < \text{length}(\text{compE2 } e)$

shows $\text{exec-move } ci \ P \ t \ (if \ (e) \ e1 \ \text{else} \ e2) \ h \ (stk, \ loc, \ pc, \ xcp) = \text{exec-move } ci \ P \ t \ e \ h \ (stk, \ loc, \ pc, \ xcp)$

(**is** $?lhs = ?rhs$)

proof(*rule ext iffI*)+

let $?E = \text{IfFalse } (2 + \text{int}(\text{length}(\text{compE2 } e1))) \# \text{compE2 } e1 \ @ \ \text{Goto } (1 + \text{int}(\text{length}(\text{compE2 } e2))) \# \text{compE2 } e2$

let $?xt = \text{compxE2 } e1 \ (\text{Suc } 0) \ 0 \ @ \ \text{compxE2 } e2 \ (\text{Suc}(\text{Suc}(\text{length}(\text{compE2 } e1)))) \ 0$

fix $ta \ h' \ s'$

assume $?lhs \ ta \ h' \ s'$

hence $\text{exec-meth } ci \ (\text{compP2 } P) \ (\text{compE2 } e \ @ \ ?E) \ (\text{compxE2 } e \ 0 \ 0 \ @ \ \text{shift}(\text{length}(\text{compE2 } e)) \ ?xt) \ t \ h \ (stk, \ loc, \ pc, \ xcp) \ ta \ h' \ s'$

by(*simp add: exec-move-def shift-compxE2 ac-simps*)

thus $?rhs \ ta \ h' \ s'$ **unfolding** *exec-move-def* **using** *pc* **by**(*rule exec-meth-take-xt*)

qed(*rule exec-move-CondI1*)

lemma *exec-move-CondI2*:

assumes *exec*: $\text{exec-move } ci \ P \ t \ e1 \ h \ (stk, \ loc, \ pc, \ xcp) \ ta \ h' \ (stk', \ loc', \ pc', \ xcp')$

shows $\text{exec-move } ci \ P \ t \ (if \ (e) \ e1 \ \text{else} \ e2) \ h \ (stk, \ loc, \ (\text{Suc}(\text{length}(\text{compE2 } e) + pc)), \ xcp) \ ta \ h' \ (stk', \ loc', \ (\text{Suc}(\text{length}(\text{compE2 } e) + pc'), \ xcp')$

proof –

from *exec* **have** $\text{exec-meth } ci \ (\text{compP2 } P) \ (\text{compE2 } e1) \ (\text{compxE2 } e1 \ 0 \ 0) \ t \ h \ (stk, \ loc, \ pc, \ xcp) \ ta \ h' \ (stk', \ loc', \ pc', \ xcp')$

by(*simp add: exec-move-def*)

hence $\text{exec-meth } ci \ (\text{compP2 } P) \ (((\text{compE2 } e \ @ \ [\text{IfFalse } (2 + \text{int}(\text{length}(\text{compE2 } e1)))] \ @ \ \text{compE2 } e1) \ @ \ \text{Goto } (1 + \text{int}(\text{length}(\text{compE2 } e2))) \# \ \text{compE2 } e2) \ (((\text{compxE2 } e \ 0 \ 0 \ @ \ \text{shift}(\text{length}(\text{compE2 } e \ @ \ [\text{IfFalse } (2 + \text{int}(\text{length}(\text{compE2 } e1)))])) \ (\text{compxE2 } e1 \ 0 \ 0)) \ @ \ (\text{compxE2 } e2 \ (\text{Suc}(\text{Suc}(\text{length}(\text{compE2 } e) + \text{length}(\text{compE2 } e1)))) \ 0)) \ t \ h \ (stk, \ loc, \ (\text{length}(\text{compE2 } e \ @ \ [\text{IfFalse } (2 + \text{int}(\text{length}(\text{compE2 } e1)))])) + pc), \ xcp) \ ta \ h' \ (stk', \ loc', \ (\text{length}(\text{compE2 } e \ @ \ [\text{IfFalse } (2 + \text{int}(\text{length}(\text{compE2 } e1)))])) + pc'), \ xcp')$

by –(*rule exec-meth-append-xt, rule append-exec-meth-xt, auto*)

thus $?thesis$ **by**(*simp add: shift-compxE2 exec-move-def*)

qed

lemma *exec-move-Cond2*:

assumes *pc*: $pc < \text{length}(\text{compE2 } e1)$

shows $\text{exec-move } ci \ P \ t \ (if \ (e) \ e1 \ \text{else} \ e2) \ h \ (stk, \ loc, \ (\text{Suc}(\text{length}(\text{compE2 } e) + pc)), \ xcp) \ ta \ h' \ (stk', \ loc', \ (\text{Suc}(\text{length}(\text{compE2 } e) + pc'), \ xcp')) = \text{exec-move } ci \ P \ t \ e1 \ h \ (stk, \ loc, \ pc, \ xcp) \ ta \ h' \ (stk', \ loc', \ pc', \ xcp')$

(**is** $?lhs = ?rhs$)

proof

let $?E1 = \text{compE2 } e \ @ \ [\text{IfFalse } (2 + \text{int}(\text{length}(\text{compE2 } e1)))]$

let $?E2 = \text{Goto } (1 + \text{int}(\text{length}(\text{compE2 } e2))) \# \ \text{compE2 } e2$

assume $?lhs$

hence $\text{exec-meth } ci \ (\text{compP2 } P) \ (?E1 \ @ \ \text{compE2 } e1 \ @ \ ?E2) \ (\text{compxE2 } e \ 0 \ 0 \ @ \ \text{shift}(\text{length } ?E1) \ (\text{compxE2 } e1 \ 0 \ 0 \ @ \ \text{shift}(\text{length}(\text{compE2 } e1)) \ (\text{compxE2 } e2 \ (\text{Suc } 0) \ 0))) \ t \ h \ (stk, \ loc, \ \text{length } ?E1 + pc, \ xcp) \ ta \ h' \ (stk', \ loc', \ \text{length } ?E1 + pc', \ xcp')$

by(*simp add: exec-move-def shift-compxE2 ac-simps*)

thus $?rhs$ **unfolding** *exec-move-def*

by –(*rule exec-meth-take-xt, drule exec-meth-drop-xt, auto simp add: pc*)

qed(*rule exec-move-CondI2*)

lemma *exec-move-CondI3*:

assumes *exec*: *exec-move ci P t e2 h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')*
shows *exec-move ci P t (if (e) e1 else e2) h (stk, loc, Suc (Suc (length (compE2 e) + length (compE2 e1) + pc)), xcp) ta h' (stk', loc', Suc (Suc (length (compE2 e) + length (compE2 e1) + pc')), xcp')*
proof –
let *?E = compE2 e @ IfFalse (2 + int (length (compE2 e1))) # compE2 e1 @ [Goto (1 + int (length (compE2 e2)))]*
let *?xt = compxE2 e 0 0 @ compxE2 e1 (Suc (length (compE2 e))) 0*
from *exec have exec-meth ci (compP2 P) (compE2 e2) (compxE2 e2 0 0) t h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')*
by(*simp add: exec-move-def*)
hence *exec-meth ci (compP2 P) (?E @ compE2 e2) (?xt @ shift (length ?E) (compxE2 e2 0 0)) t h (stk, loc, length ?E + pc, xcp) ta h' (stk', loc', length ?E + pc', xcp')*
by(*rule append-exec-meth-xt*) *auto*
thus *?thesis by (simp add: shift-compxE2 exec-move-def)*
qed

lemma *exec-move-Cond3*:

exec-move ci P t (if (e) e1 else e2) h (stk, loc, Suc (Suc (length (compE2 e) + length (compE2 e1) + pc)), xcp) ta

$$h' (stk', loc', Suc (Suc (length (compE2 e) + length (compE2 e1) + pc')), xcp') =$$
exec-move ci P t e2 h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')
(is ?lhs = ?rhs)

proof

let *?E = compE2 e @ IfFalse (2 + int (length (compE2 e1))) # compE2 e1 @ [Goto (1 + int (length (compE2 e2)))]*
let *?xt = compxE2 e 0 0 @ compxE2 e1 (Suc (length (compE2 e))) 0*
assume *?lhs*
hence *exec-meth ci (compP2 P) (?E @ compE2 e2) (?xt @ shift (length ?E) (compxE2 e2 0 0)) t h (stk, loc, length ?E + pc, xcp) ta h' (stk', loc', length ?E + pc', xcp')*
by(*simp add: shift-compxE2 exec-move-def*)
thus *?rhs unfolding exec-move-def by -(drule exec-meth-drop-xt, auto)*
qed(*rule exec-move-CondI3*)

lemma *exec-move-WhileI1*:

exec-move ci P t e h s ta h' s' \implies exec-move ci P t (while (e) e') h s ta h' s'
unfolding *exec-move-def* **by** *auto*

lemma (**in** *ab-group-add*) *uminus-minus-left-commute*:

$- a - (b + c) = - b - (a + c)$
by (*simp add: algebra-simps*)

lemma *exec-move-While1*:

assumes *pc: pc < length (compE2 e)*
shows *exec-move ci P t (while (e) e') h (stk, loc, pc, xcp) = exec-move ci P t e h (stk, loc, pc, xcp)*
(is ?lhs = ?rhs)
proof(*rule ext iffI*)
let *?E = IfFalse (3 + int (length (compE2 e'))) # compE2 e' @ [Pop, Goto (- int (length (compE2 e)) + (-2 - int (length (compE2 e')))), Push Unit]*
let *?xt = compxE2 e' (Suc 0) 0*
fix *ta h' s'*
assume *?lhs ta h' s'*
then have *exec-meth ci (compP2 P) (compE2 e @ ?E) (compxE2 e 0 0 @ shift (length (compE2 e)) ?xt) t h (stk, loc, pc, xcp) ta h' s'*

by (simp add: exec-move-def shift-compE2 algebra-simps uminus-minus-left-commute)
 thus ?rhs ta h' s' **unfolding** exec-move-def **using** pc **by**(rule exec-meth-take-xt)
qed(rule exec-move-WhileI1)

lemma exec-move-WhileI2:

assumes exec: exec-move ci P t e1 h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')
shows exec-move ci P t (while (e) e1) h (stk, loc, (Suc (length (compE2 e) + pc)), xcp) ta h' (stk', loc', (Suc (length (compE2 e) + pc')), xcp')

proof –

let ?E = compE2 e @ [IfFalse (3 + int (length (compE2 e1)))]
let ?E' = [Pop, Goto (- int (length (compE2 e)) + (-2 - int (length (compE2 e1))))], Push Unit]
from exec **have** exec-meth ci (compP2 P) (compE2 e1) (compE2 e1 0 0) t h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')

by(simp add: exec-move-def)

hence exec-meth ci (compP2 P) ((?E @ compE2 e1) @ ?E') (compE2 e 0 0 @ shift (length ?E) (compE2 e1 0 0)) t h (stk, loc, length ?E + pc, xcp) ta h' (stk', loc', length ?E + pc', xcp')

by -(rule exec-meth-append, rule append-exec-meth-xt, auto)

thus ?thesis **by** (simp add: shift-compE2 exec-move-def algebra-simps uminus-minus-left-commute)
qed

lemma exec-move-While2:

assumes pc: pc < length (compE2 e')

shows exec-move ci P t (while (e) e') h (stk, loc, (Suc (length (compE2 e) + pc)), xcp) ta h' (stk', loc', (Suc (length (compE2 e) + pc')), xcp') =
 exec-move ci P t e' h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')

(**is** ?lhs = ?rhs)

proof

let ?E = compE2 e @ [IfFalse (3 + int (length (compE2 e')))]

let ?E' = [Pop, Goto (- int (length (compE2 e)) + (-2 - int (length (compE2 e'))))], Push Unit]

assume ?lhs

hence exec-meth ci (compP2 P) ((?E @ compE2 e') @ ?E') (compE2 e 0 0 @ shift (length ?E) (compE2 e' 0 0)) t h (stk, loc, length ?E + pc, xcp) ta h' (stk', loc', length ?E + pc', xcp')

by(simp add: exec-move-def shift-compE2 algebra-simps uminus-minus-left-commute)

thus ?rhs **unfolding** exec-move-def **using** pc

by -(drule exec-meth-take, simp, drule exec-meth-drop-xt, auto)

qed(rule exec-move-WhileI2)

lemma exec-move-ThrowI:

exec-move ci P t e h s ta h' s' \implies exec-move ci P t (throw e) h s ta h' s'

unfolding exec-move-def **by** auto

lemma exec-move-Throw:

pc < length (compE2 e) \implies exec-move ci P t (throw e) h (stk, loc, pc, xcp) = exec-move ci P t e h (stk, loc, pc, xcp)

unfolding exec-move-def **by**(auto intro!: ext intro: exec-meth-take)

lemma exec-move-TryI1:

exec-move ci P t e h s ta h' s' \implies exec-move ci P t (try e catch(C V) e') h s ta h' s'

unfolding exec-move-def **by** auto

lemma exec-move-TryI2:

assumes exec: exec-move ci P t e h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')

shows exec-move ci P t (try e' catch(C V) e) h (stk, loc, Suc (Suc (length (compE2 e') + pc)), xcp) ta h' (stk', loc', Suc (Suc (length (compE2 e') + pc')), xcp')

proof –

```

let ?e = compE2 e' @ [Goto (int(size (compE2 e))+2), Store V]
from exec have exec-meth ci (compP2 P) (compE2 e) (compxE2 e 0 0) t h (stk, loc, pc, xcp) ta h'
(stk', loc', pc', xcp')
  by(simp add: exec-move-def)
  hence exec-meth ci (compP2 P) ((?e @ compE2 e) @ []) ((compxE2 e' 0 0 @ shift (length ?e)
(compxE2 e 0 0)) @ [(0, length (compE2 e'), [C], Suc (length (compE2 e')), 0)]) t h (stk, loc, (length
?e + pc), xcp) ta h' (stk', loc', (length ?e + pc'), xcp')
  by(rule exec-meth-append-xt[OF append-exec-meth-xt]) auto
  thus ?thesis by(simp add: eval-nat-numeral shift-compxE2 exec-move-def)
qed

```

lemma *exec-move-Try2*:

```

exec-move ci P t (try e catch(C V) e') h (stk, loc, Suc (Suc (length (compE2 e) + pc)), xcp) ta
h' (stk', loc', Suc (Suc (length (compE2 e) + pc')), xcp') =
exec-move ci P t e' h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')
(is ?lhs = ?rhs)

```

proof

```

let ?E = compE2 e @ [Goto (int(size (compE2 e')+2), Store V]
let ?xt = [(0, length (compE2 e), [C], Suc (length (compE2 e)), 0)]
assume lhs: ?lhs
hence pc: pc < length (compE2 e')
  by(fastforce elim!: exec-meth.cases simp add: exec-move-def match-ex-table-append match-ex-entry
dest: match-ex-table-pcsD)
  from lhs have exec-meth ci (compP2 P) ((?E @ compE2 e') @ []) ((compxE2 e' 0 0 @ shift (length
?E) (compxE2 e' 0 0)) @ ?xt) t h (stk, loc, length ?E + pc, xcp) ta h' (stk', loc', length ?E + pc',
xcp')
  by(simp add: exec-move-def shift-compxE2 ac-simps)
  thus ?rhs unfolding exec-move-def using pc
  by-(drule exec-meth-drop-xt[OF exec-meth-take-xt], auto)
qed(rule exec-move-TryI2)

```

lemma *exec-move-raise-xcp-pcD*:

```

exec-move ci P t E h (stk, loc, pc, None) ta h' (stk', loc', pc', Some a) ==> pc' = pc
apply(cases compE2 E ! pc)
apply(auto simp add: exec-move-def elim!: exec-meth.cases split: if-split-asm sum.split-asm)
apply(auto split: extCallRet.split-asm simp add: split-beta)
done

```

definition τ *exec-meth* ::

```

('addr, 'heap) check-instr => 'addr jvm-prog => 'addr instr list => ex-table => 'thread-id => 'heap
=> ('addr val list × 'addr val list × pc × 'addr option)
=> ('addr val list × 'addr val list × pc × 'addr option) => bool

```

where

```

τexec-meth ci P ins xt t h s s' <=>
exec-meth ci P ins xt t h s ε h s' ∧ (snd (snd (snd s)) = None → τinstr P h (fst s) (ins ! fst (snd
(snd s))))

```

abbreviation τ *exec-meth-a*

where τ *exec-meth-a* \equiv τ *exec-meth* (Abs-check-instr check-instr')

abbreviation τ *exec-meth-d*

where τ *exec-meth-d* \equiv τ *exec-meth* (Abs-check-instr check-instr)

lemma $\tau exec\text{-}methI$ [intro]:

$\llbracket exec\text{-}meth\ ci\ P\ ins\ xt\ t\ h\ (stk,\ loc,\ pc,\ xcp) \in h\ s';\ xcp = None \implies \tau instr\ P\ h\ stk\ (ins\ !\ pc) \rrbracket$
 $\implies \tau exec\text{-}meth\ ci\ P\ ins\ xt\ t\ h\ (stk,\ loc,\ pc,\ xcp)\ s'$

by(simp add: $\tau exec\text{-}meth\text{-}def$)

lemma $\tau exec\text{-}methE$ [elim]:

assumes $\tau exec\text{-}meth\ ci\ P\ ins\ xt\ t\ h\ s\ s'$

obtains $stk\ loc\ pc\ xcp$

where $s = (stk,\ loc,\ pc,\ xcp)$

and $exec\text{-}meth\ ci\ P\ ins\ xt\ t\ h\ (stk,\ loc,\ pc,\ xcp) \in h\ s'$

and $xcp = None \implies \tau instr\ P\ h\ stk\ (ins\ !\ pc)$

using *assms*

by(cases *s*)(auto simp add: $\tau exec\text{-}meth\text{-}def$)

abbreviation $\tau Exec\text{-}methr$::

$(addr,\ heap)\ check\text{-}instr \Rightarrow addr\ jvm\text{-}prog \Rightarrow addr\ instr\ list \Rightarrow ex\text{-}table \Rightarrow thread\text{-}id \Rightarrow heap$
 $\Rightarrow (addr\ val\ list \times addr\ val\ list \times pc \times addr\ option)$

$\Rightarrow (addr\ val\ list \times addr\ val\ list \times pc \times addr\ option) \Rightarrow bool$

where

$\tau Exec\text{-}methr\ ci\ P\ ins\ xt\ t\ h == (\tau exec\text{-}meth\ ci\ P\ ins\ xt\ t\ h)^{\wedge**}$

abbreviation $\tau Exec\text{-}methht$::

$(addr,\ heap)\ check\text{-}instr \Rightarrow addr\ jvm\text{-}prog \Rightarrow addr\ instr\ list \Rightarrow ex\text{-}table \Rightarrow thread\text{-}id \Rightarrow heap$
 $\Rightarrow (addr\ val\ list \times addr\ val\ list \times pc \times addr\ option)$

$\Rightarrow (addr\ val\ list \times addr\ val\ list \times pc \times addr\ option) \Rightarrow bool$

where

$\tau Exec\text{-}methht\ ci\ P\ ins\ xt\ t\ h == (\tau exec\text{-}meth\ ci\ P\ ins\ xt\ t\ h)^{\wedge++}$

abbreviation $\tau Exec\text{-}methr\text{-}a$

where $\tau Exec\text{-}methr\text{-}a \equiv \tau Exec\text{-}methr\ (Abs\text{-}check\text{-}instr\ check\text{-}instr')$

abbreviation $\tau Exec\text{-}methr\text{-}d$

where $\tau Exec\text{-}methr\text{-}d \equiv \tau Exec\text{-}methr\ (Abs\text{-}check\text{-}instr\ check\text{-}instr)$

abbreviation $\tau Exec\text{-}methht\text{-}a$

where $\tau Exec\text{-}methht\text{-}a \equiv \tau Exec\text{-}methht\ (Abs\text{-}check\text{-}instr\ check\text{-}instr')$

abbreviation $\tau Exec\text{-}methht\text{-}d$

where $\tau Exec\text{-}methht\text{-}d \equiv \tau Exec\text{-}methht\ (Abs\text{-}check\text{-}instr\ check\text{-}instr)$

lemma $\tau Exec\text{-}methr\text{-}refl$: $\tau Exec\text{-}methr\ ci\ P\ ins\ xt\ t\ h\ s\ s\ ..$

lemma $\tau Exec\text{-}methr\text{-}step'$:

$\llbracket \tau Exec\text{-}methr\ ci\ P\ ins\ xt\ t\ h\ s\ (stk',\ loc',\ pc',\ xcp');$
 $\tau exec\text{-}meth\ ci\ P\ ins\ xt\ t\ h\ (stk',\ loc',\ pc',\ xcp')\ s' \rrbracket$

$\implies \tau Exec\text{-}methr\ ci\ P\ ins\ xt\ t\ h\ s\ s'$

by(rule *rtranclp.rtrancl-into-rtrancl*)

lemma $\tau Exec\text{-}methr\text{-}step$:

$\llbracket \tau Exec\text{-}methr\ ci\ P\ ins\ xt\ t\ h\ s\ (stk',\ loc',\ pc',\ xcp');$
 $exec\text{-}meth\ ci\ P\ ins\ xt\ t\ h\ (stk',\ loc',\ pc',\ xcp') \in h\ s';$
 $xcp' = None \implies \tau instr\ P\ h\ stk'\ (ins\ !\ pc') \rrbracket$

$\implies \tau Exec\text{-}methr\ ci\ P\ ins\ xt\ t\ h\ s\ s'$

by(erule τ Exec-methr-step')(rule τ exec-methI)

lemmas τ Exec-methr-intros = τ Exec-methr-refl τ Exec-methr-step

lemmas τ Exec-methr1step = τ Exec-methr-step[OF τ Exec-methr-refl]

lemmas τ Exec-methr2step = τ Exec-methr-step[OF τ Exec-methr-step, OF τ Exec-methr-refl]

lemmas τ Exec-methr3step = τ Exec-methr-step[OF τ Exec-methr-step, OF τ Exec-methr-step, OF τ Exec-methr-refl]

lemma τ Exec-methr-cases [consumes 1, case-names refl step]:

assumes τ Exec-methr ci P ins xt t h s s'

obtains $s = s'$

| $stk' loc' pc' xcp'$

where τ Exec-methr ci P ins xt t h s (stk', loc', pc', xcp')

$exec\text{-}meth\ ci\ P\ ins\ xt\ t\ h\ (stk', loc', pc', xcp') \in h\ s'$

$xcp' = None \implies \tau instr\ P\ h\ stk' (ins\ !\ pc')$

using *assms*

by(rule rtranclp.cases)(auto elim!: τ exec-methE)

lemma τ Exec-methr-induct [consumes 1, case-names refl step]:

$\llbracket \tau$ Exec-methr ci P ins xt t h s s';

$Q\ s;$

$\bigwedge stk\ loc\ pc\ xcp\ s'. \llbracket \tau$ Exec-methr ci P ins xt t h s (stk, loc, pc, xcp); $exec\text{-}meth\ ci\ P\ ins\ xt\ t\ h\ (stk, loc, pc, xcp) \in h\ s';$

$xcp = None \implies \tau instr\ P\ h\ stk\ (ins\ !\ pc); Q\ (stk, loc, pc, xcp) \rrbracket \implies Q\ s' \rrbracket$

$\implies Q\ s'$

by(erule (1) rtranclp-induct)(blast elim: τ exec-methE)

lemma τ Exec-methr-trans:

$\llbracket \tau$ Exec-methr ci P ins xt t h s s'; τ Exec-methr ci P ins xt t h s' s'' $\rrbracket \implies \tau$ Exec-methr ci P ins xt t h s s''

by(rule rtranclp-trans)

lemmas τ Exec-meth-induct-split = τ Exec-methr-induct[split-format (complete), consumes 1, case-names τ Exec-refl τ Exec-step]

lemma τ Exec-methr-converse-cases [consumes 1, case-names refl step]:

assumes τ Exec-methr ci P ins xt t h s s'

obtains $s = s'$

| $stk\ loc\ pc\ xcp\ s''$

where $s = (stk, loc, pc, xcp)$

$exec\text{-}meth\ ci\ P\ ins\ xt\ t\ h\ (stk, loc, pc, xcp) \in h\ s''$

$xcp = None \implies \tau instr\ P\ h\ stk\ (ins\ !\ pc)$

τ Exec-methr ci P ins xt t h s'' s'

using *assms*

by(erule converse-rtranclpE)(blast elim: τ exec-methE)

definition τ exec-move ::

$(addr, heap)\ check\text{-}instr \Rightarrow addr\ J1\text{-}prog \Rightarrow thread\text{-}id \Rightarrow addr\ expr1 \Rightarrow heap$

$\Rightarrow (addr\ val\ list \times addr\ val\ list \times pc \times addr\ option)$

$\Rightarrow (addr\ val\ list \times addr\ val\ list \times pc \times addr\ option) \Rightarrow bool$

where

τ exec-move ci P t e h =

$(\lambda(stk, loc, pc, xcp)\ s'. exec\text{-}move\ ci\ P\ t\ e\ h\ (stk, loc, pc, xcp) \in h\ s' \wedge \tau move2\ P\ h\ stk\ e\ pc\ xcp)$

definition τ exec-moves ::

$(\text{'addr}, \text{'heap}) \text{check-instr} \Rightarrow \text{'addr J1-prog} \Rightarrow \text{'thread-id} \Rightarrow \text{'addr expr1 list} \Rightarrow \text{'heap}$
 $\Rightarrow (\text{'addr val list} \times \text{'addr val list} \times \text{pc} \times \text{'addr option})$
 $\Rightarrow (\text{'addr val list} \times \text{'addr val list} \times \text{pc} \times \text{'addr option}) \Rightarrow \text{bool}$

where

$\tau \text{exec-moves} \text{ ci } P \text{ t es } h =$

$(\lambda(\text{stk}, \text{loc}, \text{pc}, \text{xcp}) s'. \text{exec-moves} \text{ ci } P \text{ t es } h (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \varepsilon h s' \wedge \tau \text{moves2 } P \text{ h stk es pc xcp})$

lemma $\tau \text{exec-move}I$:

$\llbracket \text{exec-move} \text{ ci } P \text{ t e } h (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \varepsilon h s'; \tau \text{move2 } P \text{ h stk e pc xcp} \rrbracket$
 $\implies \tau \text{exec-move} \text{ ci } P \text{ t e } h (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) s'$

by(*simp add: $\tau \text{exec-move-def}$*)

lemma $\tau \text{exec-move}E$:

assumes $\tau \text{exec-move} \text{ ci } P \text{ t e } h (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) s'$

obtains $\text{exec-move} \text{ ci } P \text{ t e } h (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \varepsilon h s' \tau \text{move2 } P \text{ h stk e pc xcp}$

using *assms* **by**(*simp add: $\tau \text{exec-move-def}$*)

lemma $\tau \text{exec-moves}I$:

$\llbracket \text{exec-moves} \text{ ci } P \text{ t es } h (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \varepsilon h s'; \tau \text{moves2 } P \text{ h stk es pc xcp} \rrbracket$
 $\implies \tau \text{exec-moves} \text{ ci } P \text{ t es } h (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) s'$

by(*simp add: $\tau \text{exec-moves-def}$*)

lemma $\tau \text{exec-moves}E$:

assumes $\tau \text{exec-moves} \text{ ci } P \text{ t es } h (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) s'$

obtains $\text{exec-moves} \text{ ci } P \text{ t es } h (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \varepsilon h s' \tau \text{moves2 } P \text{ h stk es pc xcp}$

using *assms* **by**(*simp add: $\tau \text{exec-moves-def}$*)

lemma $\tau \text{exec-move-conv-}\tau \text{exec-meth}$:

$\tau \text{exec-move} \text{ ci } P \text{ t e} = \tau \text{exec-meth} \text{ ci } (\text{comp}P2 \text{ } P) (\text{comp}E2 \text{ } e) (\text{comp}xE2 \text{ } e \text{ } 0 \text{ } 0) t$

by(*auto simp add: $\tau \text{exec-move-def}$ exec-move-def $\tau \text{move2-iff}$ $\text{comp}P2\text{-def}$ *intro!*: *ext* $\tau \text{exec-meth}I$ *elim!*: $\tau \text{exec-meth}E$)*

lemma $\tau \text{exec-moves-conv-}\tau \text{exec-meth}$:

$\tau \text{exec-moves} \text{ ci } P \text{ t es} = \tau \text{exec-meth} \text{ ci } (\text{comp}P2 \text{ } P) (\text{comp}Es2 \text{ } es) (\text{comp}xEs2 \text{ } es \text{ } 0 \text{ } 0) t$

by(*auto simp add: $\tau \text{exec-moves-def}$ exec-moves-def $\tau \text{moves2-iff}$ $\text{comp}P2\text{-def}$ *intro!*: *ext* $\tau \text{exec-meth}I$ *elim!*: $\tau \text{exec-meth}E$)*

abbreviation $\tau \text{Exec-mover}$

where $\tau \text{Exec-mover} \text{ ci } P \text{ t e } h == (\tau \text{exec-move} \text{ ci } P \text{ t e } h) \hat{\ast}\ast$

abbreviation $\tau \text{Exec-movet}$

where $\tau \text{Exec-movet} \text{ ci } P \text{ t e } h == (\tau \text{exec-move} \text{ ci } P \text{ t e } h) \hat{\ast}\ast\ast$

abbreviation $\tau \text{Exec-mover-a}$

where $\tau \text{Exec-mover-a} \equiv \tau \text{Exec-mover} (\text{Abs-check-instr } \text{check-instr}')$

abbreviation $\tau \text{Exec-mover-d}$

where $\tau \text{Exec-mover-d} \equiv \tau \text{Exec-mover} (\text{Abs-check-instr } \text{check-instr})$

abbreviation $\tau \text{Exec-movet-a}$

where $\tau \text{Exec-movet-a} \equiv \tau \text{Exec-movet} (\text{Abs-check-instr } \text{check-instr}')$

abbreviation $\tau \text{Exec-movet-d}$

where $\tau \text{Exec-movet-d} \equiv \tau \text{Exec-movet} (\text{Abs-check-instr } \text{check-instr})$

abbreviation $\tau Exec\text{-}movesr$

where $\tau Exec\text{-}movesr\ ci\ P\ t\ e\ h == (\tau exec\text{-}moves\ ci\ P\ t\ e\ h)^{\widehat{**}}$

abbreviation $\tau Exec\text{-}movest$

where $\tau Exec\text{-}movest\ ci\ P\ t\ e\ h == (\tau exec\text{-}moves\ ci\ P\ t\ e\ h)^{\widehat{++}}$

abbreviation $\tau Exec\text{-}movesr\text{-}a$

where $\tau Exec\text{-}movesr\text{-}a \equiv \tau Exec\text{-}movesr\ (Abs\text{-}check\text{-}instr\ check\text{-}instr')$

abbreviation $\tau Exec\text{-}movesr\text{-}d$

where $\tau Exec\text{-}movesr\text{-}d \equiv \tau Exec\text{-}movesr\ (Abs\text{-}check\text{-}instr\ check\text{-}instr)$

abbreviation $\tau Exec\text{-}movest\text{-}a$

where $\tau Exec\text{-}movest\text{-}a \equiv \tau Exec\text{-}movest\ (Abs\text{-}check\text{-}instr\ check\text{-}instr')$

abbreviation $\tau Exec\text{-}movest\text{-}d$

where $\tau Exec\text{-}movest\text{-}d \equiv \tau Exec\text{-}movest\ (Abs\text{-}check\text{-}instr\ check\text{-}instr)$

lemma $\tau Execr\text{-}refl$: $\tau Exec\text{-}mover\ ci\ P\ t\ e\ h\ s\ s$

by(rule $rtranclp.rtrancl\text{-}refl$)

lemma $\tau Execsr\text{-}refl$: $\tau Exec\text{-}movesr\ ci\ P\ t\ e\ h\ s\ s$

by(rule $rtranclp.rtrancl\text{-}refl$)

lemma $\tau Execr\text{-}step$:

$\llbracket \tau Exec\text{-}mover\ ci\ P\ t\ e\ h\ s\ (stk', loc', pc', xcp');$
 $exec\text{-}move\ ci\ P\ t\ e\ h\ (stk', loc', pc', xcp') \in h\ s';$
 $\tau move2\ P\ h\ stk'\ e\ pc'\ xcp' \rrbracket$
 $\implies \tau Exec\text{-}mover\ ci\ P\ t\ e\ h\ s\ s'$

by(rule $rtranclp.rtrancl\text{-}into\text{-}rtrancl$)(auto elim: $\tau exec\text{-}moveI$)

lemma $\tau Execsr\text{-}step$:

$\llbracket \tau Exec\text{-}movesr\ ci\ P\ t\ es\ h\ s\ (stk', loc', pc', xcp');$
 $exec\text{-}moves\ ci\ P\ t\ es\ h\ (stk', loc', pc', xcp') \in h\ s';$
 $\tau moves2\ P\ h\ stk'\ es\ pc'\ xcp' \rrbracket$
 $\implies \tau Exec\text{-}movesr\ ci\ P\ t\ es\ h\ s\ s'$

by(rule $rtranclp.rtrancl\text{-}into\text{-}rtrancl$)(auto elim: $\tau exec\text{-}movesI$)

lemma $\tau Exec\text{-}step$:

$\llbracket \tau Exec\text{-}mover\ ci\ P\ t\ e\ h\ s\ (stk', loc', pc', xcp');$
 $exec\text{-}move\ ci\ P\ t\ e\ h\ (stk', loc', pc', xcp') \in h\ s';$
 $\tau move2\ P\ h\ stk'\ e\ pc'\ xcp' \rrbracket$
 $\implies \tau Exec\text{-}mover\ ci\ P\ t\ e\ h\ s\ s'$

by(rule $tranclp.trancl\text{-}into\text{-}trancl$)(auto intro: $\tau exec\text{-}moveI$)

lemma $\tau Execst\text{-}step$:

$\llbracket \tau Exec\text{-}movest\ ci\ P\ t\ es\ h\ s\ (stk', loc', pc', xcp');$
 $exec\text{-}moves\ ci\ P\ t\ es\ h\ (stk', loc', pc', xcp') \in h\ s';$
 $\tau moves2\ P\ h\ stk'\ es\ pc'\ xcp' \rrbracket$
 $\implies \tau Exec\text{-}movest\ ci\ P\ t\ es\ h\ s\ s'$

by(rule $tranclp.trancl\text{-}into\text{-}trancl$)(auto intro: $\tau exec\text{-}movesI$)

lemmas $\tau Execr1step = \tau Execr\text{-}step[OF\ \tau Execr\text{-}refl]$

lemmas $\tau Execr2step = \tau Execr\text{-}step[OF \tau Execr\text{-}step, OF \tau Execr\text{-}refl]$

lemmas $\tau Execr3step = \tau Execr\text{-}step[OF \tau Execr\text{-}step, OF \tau Execr\text{-}step, OF \tau Execr\text{-}refl]$

lemmas $\tau Execsr1step = \tau Execsr\text{-}step[OF \tau Execsr\text{-}refl]$

lemmas $\tau Execsr2step = \tau Execsr\text{-}step[OF \tau Execsr\text{-}step, OF \tau Execsr\text{-}refl]$

lemmas $\tau Execsr3step = \tau Execsr\text{-}step[OF \tau Execsr\text{-}step, OF \tau Execsr\text{-}step, OF \tau Execsr\text{-}refl]$

lemma $\tau Exec1step$:

$\llbracket exec\text{-}move \textit{ci} P t e h s \varepsilon h s';$
 $\tau move2 P h (fst s) e (fst (snd (snd s))) (snd (snd (snd s))) \rrbracket$
 $\implies \tau Exec\text{-}movet \textit{ci} P t e h s s'$

by(rule tranclp.r-into-trancl)(cases s, auto intro: $\tau exec\text{-}moveI$)

lemmas $\tau Exec2step = \tau Exec\text{-}step[OF \tau Exec1step]$

lemmas $\tau Exec3step = \tau Exec\text{-}step[OF \tau Exec\text{-}step, OF \tau Exec1step]$

lemma $\tau Execst1step$:

$\llbracket exec\text{-}moves \textit{ci} P t es h s \varepsilon h s';$
 $\tau moves2 P h (fst s) es (fst (snd (snd s))) (snd (snd (snd s))) \rrbracket$
 $\implies \tau Exec\text{-}movest \textit{ci} P t es h s s'$

by(rule tranclp.r-into-trancl)(cases s, auto intro: $\tau exec\text{-}movesI$)

lemmas $\tau Execst2step = \tau Execst\text{-}step[OF \tau Execst1step]$

lemmas $\tau Execst3step = \tau Execst\text{-}step[OF \tau Execst\text{-}step, OF \tau Execst1step]$

lemma $\tau Execr\text{-}induct$ [consumes 1, case-names refl step]:

assumes major: $\tau Exec\text{-}mover \textit{ci} P t e h (stk, loc, pc, xcp) (stk'', loc'', pc'', xcp'')$

and refl: $Q \textit{stk} \textit{loc} \textit{pc} \textit{xcp}$

and step: $\bigwedge \textit{stk}' \textit{loc}' \textit{pc}' \textit{xcp}' \textit{stk}'' \textit{loc}'' \textit{pc}'' \textit{xcp}''.$

$\llbracket \tau Exec\text{-}mover \textit{ci} P t e h (stk, loc, pc, xcp) (stk', loc', pc', xcp');$

$\tau exec\text{-}move \textit{ci} P t e h (stk', loc', pc', xcp') (stk'', loc'', pc'', xcp''); Q \textit{stk}' \textit{loc}' \textit{pc}' \textit{xcp}' \rrbracket$

$\implies Q \textit{stk}'' \textit{loc}'' \textit{pc}'' \textit{xcp}''$

shows $Q \textit{stk}'' \textit{loc}'' \textit{pc}'' \textit{xcp}''$

using major refl

by(rule rtranclp-induct4)(rule step)

lemma $\tau Execsr\text{-}induct$ [consumes 1, case-names refl step]:

assumes major: $\tau Exec\text{-}movesr \textit{ci} P t es h (stk, loc, pc, xcp) (stk'', loc'', pc'', xcp'')$

and refl: $Q \textit{stk} \textit{loc} \textit{pc} \textit{xcp}$

and step: $\bigwedge \textit{stk}' \textit{loc}' \textit{pc}' \textit{xcp}' \textit{stk}'' \textit{loc}'' \textit{pc}'' \textit{xcp}''.$

$\llbracket \tau Exec\text{-}movesr \textit{ci} P t es h (stk, loc, pc, xcp) (stk', loc', pc', xcp');$

$\tau exec\text{-}moves \textit{ci} P t es h (stk', loc', pc', xcp') (stk'', loc'', pc'', xcp''); Q \textit{stk}' \textit{loc}' \textit{pc}' \textit{xcp}' \rrbracket$

$\implies Q \textit{stk}'' \textit{loc}'' \textit{pc}'' \textit{xcp}''$

shows $Q \textit{stk}'' \textit{loc}'' \textit{pc}'' \textit{xcp}''$

using major refl

by(rule rtranclp-induct4)(rule step)

lemma $\tau Exec\text{-}induct$ [consumes 1, case-names base step]:

assumes major: $\tau Exec\text{-}movet \textit{ci} P t e h (stk, loc, pc, xcp) (stk'', loc'', pc'', xcp'')$

and base: $\bigwedge \textit{stk}' \textit{loc}' \textit{pc}' \textit{xcp}'. \tau exec\text{-}move \textit{ci} P t e h (stk, loc, pc, xcp) (stk', loc', pc', xcp') \implies Q \textit{stk}' \textit{loc}' \textit{pc}' \textit{xcp}'$

and step: $\bigwedge \textit{stk}' \textit{loc}' \textit{pc}' \textit{xcp}' \textit{stk}'' \textit{loc}'' \textit{pc}'' \textit{xcp}''.$

$\llbracket \tau Exec\text{-}movet \textit{ci} P t e h (stk, loc, pc, xcp) (stk', loc', pc', xcp');$

$\tau exec\text{-}move \textit{ci} P t e h (stk', loc', pc', xcp') (stk'', loc'', pc'', xcp''); Q \textit{stk}' \textit{loc}' \textit{pc}' \textit{xcp}' \rrbracket$

$\implies Q \text{ stk}'' \text{ loc}'' \text{ pc}'' \text{ xcp}''$
shows $Q \text{ stk}'' \text{ loc}'' \text{ pc}'' \text{ xcp}''$
using *major*
by(*rule tranclp-induct4*)(*erule base step*)+

lemma $\tau\text{Execst-induct}$ [*consumes 1, case-names base step*]:
assumes *major*: $\tau\text{Exec-movest } ci \ P \ t \ es \ h \ (stk, \text{loc}, \text{pc}, \text{xcP}) \ (stk'', \text{loc}'', \text{pc}'', \text{xcP}'')$
and *base*: $\bigwedge \text{stk}' \ \text{loc}' \ \text{pc}' \ \text{xcP}' . \ \tau\text{exec-moves } ci \ P \ t \ es \ h \ (stk, \text{loc}, \text{pc}, \text{xcP}) \ (stk', \text{loc}', \text{pc}', \text{xcP}') \implies Q \ \text{stk}' \ \text{loc}' \ \text{pc}' \ \text{xcP}'$
and *step*: $\bigwedge \text{stk}' \ \text{loc}' \ \text{pc}' \ \text{xcP}' \ \text{stk}'' \ \text{loc}'' \ \text{pc}'' \ \text{xcP}'' .$
 $\llbracket \tau\text{Exec-movest } ci \ P \ t \ es \ h \ (stk, \text{loc}, \text{pc}, \text{xcP}) \ (stk', \text{loc}', \text{pc}', \text{xcP}');$
 $\tau\text{exec-moves } ci \ P \ t \ es \ h \ (stk', \text{loc}', \text{pc}', \text{xcP}') \ (stk'', \text{loc}'', \text{pc}'', \text{xcP}''); \ Q \ \text{stk}' \ \text{loc}' \ \text{pc}' \ \text{xcP}' \rrbracket$
 $\implies Q \ \text{stk}'' \ \text{loc}'' \ \text{pc}'' \ \text{xcP}''$
shows $Q \ \text{stk}'' \ \text{loc}'' \ \text{pc}'' \ \text{xcP}''$
using *major*
by(*rule tranclp-induct4*)(*erule base step*)+

lemma $\tau\text{Exec-mover-}\tau\text{Exec-methr}$:
 $\tau\text{Exec-mover } ci \ P \ t \ e = \tau\text{Exec-methr } ci \ (compP2 \ P) \ (compE2 \ e) \ (compxE2 \ e \ 0 \ 0) \ t$
by(*simp only: \tauexec-move-conv-\tauexec-meth*)

lemma $\tau\text{Exec-movesr-}\tau\text{Exec-methr}$:
 $\tau\text{Exec-movesr } ci \ P \ t \ es = \tau\text{Exec-methr } ci \ (compP2 \ P) \ (compEs2 \ es) \ (compxEs2 \ es \ 0 \ 0) \ t$
by(*simp only: \tauexec-moves-conv-\tauexec-meth*)

lemma $\tau\text{Exec-movet-}\tau\text{Exec-metht}$:
 $\tau\text{Exec-movet } ci \ P \ t \ e = \tau\text{Exec-metht } ci \ (compP2 \ P) \ (compE2 \ e) \ (compxE2 \ e \ 0 \ 0) \ t$
by(*simp only: \tauexec-move-conv-\tauexec-meth*)

lemma $\tau\text{Exec-movest-}\tau\text{Exec-metht}$:
 $\tau\text{Exec-movest } ci \ P \ t \ es = \tau\text{Exec-metht } ci \ (compP2 \ P) \ (compEs2 \ es) \ (compxEs2 \ es \ 0 \ 0) \ t$
by(*simp only: \tauexec-moves-conv-\tauexec-meth*)

lemma $\tau\text{Exec-mover-trans}$:
 $\llbracket \tau\text{Exec-mover } ci \ P \ t \ e \ h \ s \ s'; \ \tau\text{Exec-mover } ci \ P \ t \ e \ h \ s' \ s'' \rrbracket \implies \tau\text{Exec-mover } ci \ P \ t \ e \ h \ s \ s''$
by(*rule rtranclp-trans*)

lemma $\tau\text{Exec-movesr-trans}$:
 $\llbracket \tau\text{Exec-movesr } ci \ P \ t \ es \ h \ s \ s'; \ \tau\text{Exec-movesr } ci \ P \ t \ es \ h \ s' \ s'' \rrbracket \implies \tau\text{Exec-movesr } ci \ P \ t \ es \ h \ s \ s''$
by(*rule rtranclp-trans*)

lemma $\tau\text{Exec-movet-trans}$:
 $\llbracket \tau\text{Exec-movet } ci \ P \ t \ e \ h \ s \ s'; \ \tau\text{Exec-movet } ci \ P \ t \ e \ h \ s' \ s'' \rrbracket \implies \tau\text{Exec-movet } ci \ P \ t \ e \ h \ s \ s''$
by(*rule tranclp-trans*)

lemma $\tau\text{Exec-movest-trans}$:
 $\llbracket \tau\text{Exec-movest } ci \ P \ t \ es \ h \ s \ s'; \ \tau\text{Exec-movest } ci \ P \ t \ es \ h \ s' \ s'' \rrbracket \implies \tau\text{Exec-movest } ci \ P \ t \ es \ h \ s \ s''$
by(*rule tranclp-trans*)

lemma $\tau\text{exec-move-into-}\tau\text{exec-moves}$:
 $\tau\text{exec-move } ci \ P \ t \ e \ h \ s \ s' \implies \tau\text{exec-moves } ci \ P \ t \ (e \ \# \ es) \ h \ s'$
by(*cases s*)(*auto elim!*: $\tau\text{exec-moveE intro!}$: $\tau\text{exec-movesI simp add: exec-move-def exec-moves-def intro: \taumoves2Hd}$)

lemma $\tau Exec\text{-mover}\text{-}\tau Exec\text{-movesr}$:

$\tau Exec\text{-mover} \text{ ci } P \text{ t } e \text{ h } s \text{ s}' \implies \tau Exec\text{-movesr} \text{ ci } P \text{ t } (e \# es) \text{ h } s \text{ s}'$

by(*induct rule: rtranclp-induct*)(*blast intro: rtranclp.rtrancl-into-rtrancl* $\tau exec\text{-move-into-}\tau exec\text{-moves}$)**+**

lemma $\tau Exec\text{-movet}\text{-}\tau Exec\text{-movest}$:

$\tau Exec\text{-movet} \text{ ci } P \text{ t } e \text{ h } s \text{ s}' \implies \tau Exec\text{-movest} \text{ ci } P \text{ t } (e \# es) \text{ h } s \text{ s}'$

by(*induct rule: tranclp-induct*)(*blast intro: tranclp.trancl-into-trancl* $\tau exec\text{-move-into-}\tau exec\text{-moves}$)**+**

lemma *exec-moves-append*: $exec\text{-moves} \text{ ci } P \text{ t } es \text{ h } s \text{ ta } h' \text{ s}' \implies exec\text{-moves} \text{ ci } P \text{ t } (es @ es') \text{ h } s \text{ ta } h' \text{ s}'$

by(*auto simp add: exec-moves-def*)

lemma $\tau exec\text{-moves-append}$: $\tau exec\text{-moves} \text{ ci } P \text{ t } es \text{ h } s \text{ s}' \implies \tau exec\text{-moves} \text{ ci } P \text{ t } (es @ es') \text{ h } s \text{ s}'$

by(*cases s*)(*auto elim!:* $\tau exec\text{-movesE}$ *intro!:* $\tau exec\text{-movesI}$ *exec-moves-append*)

lemma $\tau Exec\text{-movesr-append}$ [*intro*]:

$\tau Exec\text{-movesr} \text{ ci } P \text{ t } es \text{ h } s \text{ s}' \implies \tau Exec\text{-movesr} \text{ ci } P \text{ t } (es @ es') \text{ h } s \text{ s}'$

by(*induct rule: rtranclp-induct*)(*blast intro: rtranclp.rtrancl-into-rtrancl* $\tau exec\text{-moves-append}$)**+**

lemma $\tau Exec\text{-movest-append}$ [*intro*]:

$\tau Exec\text{-movest} \text{ ci } P \text{ t } es \text{ h } s \text{ s}' \implies \tau Exec\text{-movest} \text{ ci } P \text{ t } (es @ es') \text{ h } s \text{ s}'$

by(*induct rule: tranclp-induct*)(*blast intro: tranclp.trancl-into-trancl* $\tau exec\text{-moves-append}$)**+**

lemma *append-exec-moves*:

assumes *len*: $length \text{ vs} = length \text{ es}'$

and *exec*: $exec\text{-moves} \text{ ci } P \text{ t } es \text{ h } (stk, loc, pc, xcp) \text{ ta } h' (stk', loc', pc', xcp')$

shows $exec\text{-moves} \text{ ci } P \text{ t } (es' @ es) \text{ h } ((stk @ vs), loc, (length (compEs2 es') + pc), xcp) \text{ ta } h' ((stk' @ vs), loc', (length (compEs2 es) + pc'), xcp')$

proof –

from *exec* **have** $exec\text{-meth} \text{ ci } (compP2 P) (compEs2 es) (compEs2 es 0 0) \text{ t h } (stk, loc, pc, xcp) \text{ ta } h' (stk', loc', pc', xcp')$

unfolding *exec-moves-def* .

hence $exec\text{-meth} \text{ ci } (compP2 P) (compEs2 es) (stack\text{-xlift } (length \text{ vs}) (compEs2 es 0 0)) \text{ t h } ((stk @ vs), loc, pc, xcp) \text{ ta } h' ((stk' @ vs), loc', pc', xcp')$ **by**(*rule exec-meth-stk-offer*)

hence $exec\text{-meth} \text{ ci } (compP2 P) (compEs2 es' @ compEs2 es) (compEs2 es' 0 0 @ shift (length (compEs2 es') (stack\text{-xlift } (length \text{ vs})) (compEs2 es 0 0))) \text{ t h } ((stk @ vs), loc, (length (compEs2 es) + pc), xcp) \text{ ta } h' ((stk' @ vs), loc', (length (compEs2 es') + pc'), xcp')$

by(*rule append-exec-meth-xt*) *auto*

thus *?thesis* **by**(*simp add: exec-moves-def stack-xlift-compEs2 shift-compEs2 len*)

qed

lemma *append- $\tau exec\text{-moves}$* :

$\llbracket length \text{ vs} = length \text{ es}';$

$\tau exec\text{-moves} \text{ ci } P \text{ t } es \text{ h } (stk, loc, pc, xcp) (stk', loc', pc', xcp') \rrbracket$

$\implies \tau exec\text{-moves} \text{ ci } P \text{ t } (es' @ es) \text{ h } ((stk @ vs), loc, (length (compEs2 es') + pc), xcp) ((stk' @ vs), loc', (length (compEs2 es) + pc'), xcp')$

by(*auto elim!:* $\tau exec\text{-movesE}$ *intro: $\tau exec\text{-movesI}$ append-exec-moves $\tau moves2\text{-stk-append}$ append- $\tau moves2$*)

lemma *append- $\tau Exec\text{-movesr}$* :

assumes *len*: $length \text{ vs} = length \text{ es}'$

shows $\tau Exec\text{-movesr} \text{ ci } P \text{ t } es \text{ h } (stk, loc, pc, xcp) (stk', loc', pc', xcp')$

$\implies \tau Exec\text{-movesr} \text{ ci } P \text{ t } (es' @ es) \text{ h } ((stk @ vs), loc, (length (compEs2 es') + pc), xcp) ((stk' @ vs), loc', (length (compEs2 es) + pc'), xcp')$

by(*induct rule*: *rtranclp-induct4*)(*blast intro*: *rtranclp.rtrancl-into-rtrancl append- τ exec-moves*[*OF len*])+

lemma *append- τ Exec-move*:

assumes *len*: $\text{length } vs = \text{length } es'$

shows $\tau\text{Exec-move}$ *ci P t es h* (*stk*, *loc*, *pc*, *xcp*) (*stk'*, *loc'*, *pc'*, *xcp'*)

$\implies \tau\text{Exec-move}$ *ci P t (es' @ es) h* ((*stk* @ *vs*), *loc*, ($\text{length } (\text{compEs2 } es') + pc$), *xcp*) ((*stk'* @ *vs*), *loc'*, ($\text{length } (\text{compEs2 } es') + pc'$), *xcp'*)

by(*induct rule*: *tranclp-induct4*)(*blast intro*: *tranclp.trancl-into-trancl append- τ exec-moves*[*OF len*])+

lemma *NewArray- τ execI*:

$\tau\text{exec-move}$ *ci P t e h s s'* $\implies \tau\text{exec-move}$ *ci P t (newA T[e]) h s s'*

by(*cases s*)(*blast elim*: $\tau\text{exec-moveE}$ *intro*: $\tau\text{exec-moveI}$ $\tau\text{move2-}\tau\text{moves2.intros exec-move-newArrayI}$)

lemma *Cast- τ execI*:

$\tau\text{exec-move}$ *ci P t e h s s'* $\implies \tau\text{exec-move}$ *ci P t (Cast T e) h s s'*

by(*cases s*)(*blast elim*: $\tau\text{exec-moveE}$ *intro*: $\tau\text{exec-moveI}$ $\tau\text{move2-}\tau\text{moves2.intros exec-move-CastI}$)

lemma *InstanceOf- τ execI*:

$\tau\text{exec-move}$ *ci P t e h s s'* $\implies \tau\text{exec-move}$ *ci P t (e instanceof T) h s s'*

by(*cases s*)(*blast elim*: $\tau\text{exec-moveE}$ *intro*: $\tau\text{exec-moveI}$ $\tau\text{move2-}\tau\text{moves2.intros exec-move-InstanceOfI}$)

lemma *BinOp- τ execI1*:

$\tau\text{exec-move}$ *ci P t e h s s'* $\implies \tau\text{exec-move}$ *ci P t (e «bop» e')* *h s s'*

by(*cases s*)(*blast elim*: $\tau\text{exec-moveE}$ *intro*: $\tau\text{exec-moveI}$ $\tau\text{move2-}\tau\text{moves2.intros exec-move-BinOpI1}$)

lemma *BinOp- τ execI2*:

$\tau\text{exec-move}$ *ci P t e' h* (*stk*, *loc*, *pc*, *xcp*) (*stk'*, *loc'*, *pc'*, *xcp'*)

$\implies \tau\text{exec-move}$ *ci P t (e «bop» e')* *h* ((*stk* @ [*v*]), *loc*, ($\text{length } (\text{compE2 } e) + pc$), *xcp*) ((*stk'* @ [*v*]), *loc'*, ($\text{length } (\text{compE2 } e) + pc'$), *xcp'*)

by(*blast elim*: $\tau\text{exec-moveE}$ *intro*: $\tau\text{exec-moveI}$ $\tau\text{move2-}\tau\text{moves2.intros exec-move-BinOpI2}$ $\tau\text{move2-stk-append}$)

lemma *LAss- τ execI*:

$\tau\text{exec-move}$ *ci P t e h s s'* $\implies \tau\text{exec-move}$ *ci P t (V := e) h s s'*

by(*cases s*)(*blast elim*: $\tau\text{exec-moveE}$ *intro*: $\tau\text{exec-moveI}$ $\tau\text{move2-}\tau\text{moves2.intros exec-move-LAssI}$)

lemma *AAcc- τ execI1*:

$\tau\text{exec-move}$ *ci P t e h s s'* $\implies \tau\text{exec-move}$ *ci P t (e[i]) h s s'*

by(*cases s*)(*blast elim*: $\tau\text{exec-moveE}$ *intro*: $\tau\text{exec-moveI}$ $\tau\text{move2-}\tau\text{moves2.intros exec-move-AAccI1}$)

lemma *AAcc- τ execI2*:

$\tau\text{exec-move}$ *ci P t e' h* (*stk*, *loc*, *pc*, *xcp*) (*stk'*, *loc'*, *pc'*, *xcp'*)

$\implies \tau\text{exec-move}$ *ci P t (e[e']) h* ((*stk* @ [*v*]), *loc*, ($\text{length } (\text{compE2 } e) + pc$), *xcp*) ((*stk'* @ [*v*]), *loc'*, ($\text{length } (\text{compE2 } e) + pc'$), *xcp'*)

by(*blast elim*: $\tau\text{exec-moveE}$ *intro*: $\tau\text{exec-moveI}$ $\tau\text{move2-}\tau\text{moves2.intros exec-move-AAccI2}$ $\tau\text{move2-stk-append}$)

lemma *AAss- τ execI1*:

$\tau\text{exec-move}$ *ci P t e h s s'* $\implies \tau\text{exec-move}$ *ci P t (e[i] := e')* *h s s'*

by(*cases s*)(*blast elim*: $\tau\text{exec-moveE}$ *intro*: $\tau\text{exec-moveI}$ $\tau\text{move2-}\tau\text{moves2.intros exec-move-AAssI1}$)

lemma *AAss- τ execI2*:

$\tau\text{exec-move}$ *ci P t e' h* (*stk*, *loc*, *pc*, *xcp*) (*stk'*, *loc'*, *pc'*, *xcp'*)

$\implies \tau\text{exec-move}$ *ci P t (e[e'] := e')* *h* ((*stk* @ [*v*]), *loc*, ($\text{length } (\text{compE2 } e) + pc$), *xcp*) ((*stk'* @ [*v*]), *loc'*, ($\text{length } (\text{compE2 } e) + pc'$), *xcp'*)

by(*blast elim*: $\tau\text{exec-move}E$ *intro*: $\tau\text{exec-move}I$ $\tau\text{move2-}\tau\text{moves2}$.*intros exec-move-AAssI2* $\tau\text{move2-stk-append}$)

lemma *AAss- τexecI3* :

$\tau\text{exec-move}$ *ci P t e'' h* (*stk*, *loc*, *pc*, *xcp*) (*stk'*, *loc'*, *pc'*, *xcp'*)
 $\implies \tau\text{exec-move}$ *ci P t* ($e[e'] := e''$) *h* ((*stk* @ [*v*, *v'*]), *loc*, ($\text{length}(\text{compE2 } e) + \text{length}(\text{compE2 } e')$ + *pc*), *xcp*) ((*stk'* @ [*v*, *v'*]), *loc'*, ($\text{length}(\text{compE2 } e) + \text{length}(\text{compE2 } e') + \text{pc}'$), *xcp'*)

by(*blast elim*: $\tau\text{exec-move}E$ *intro*: $\tau\text{exec-move}I$ $\tau\text{move2-}\tau\text{moves2}$.*intros exec-move-AAssI3* $\tau\text{move2-stk-append}$)

lemma *ALength- τexecI* :

$\tau\text{exec-move}$ *ci P t e h s s'* $\implies \tau\text{exec-move}$ *ci P t* ($e \cdot \text{length}$) *h s s'*

by(*cases s*)(*blast elim*: $\tau\text{exec-move}E$ *intro*: $\tau\text{exec-move}I$ $\tau\text{move2-}\tau\text{moves2}$.*intros exec-move-ALengthI*)

lemma *FAcc- τexecI* :

$\tau\text{exec-move}$ *ci P t e h s s'* $\implies \tau\text{exec-move}$ *ci P t* ($e \cdot F\{D\}$) *h s s'*

by(*cases s*)(*blast elim*: $\tau\text{exec-move}E$ *intro*: $\tau\text{exec-move}I$ $\tau\text{move2-}\tau\text{moves2}$.*intros exec-move-FAccI*)

lemma *FAss- τexecI1* :

$\tau\text{exec-move}$ *ci P t e h s s'* $\implies \tau\text{exec-move}$ *ci P t* ($e \cdot F\{D\} := e'$) *h s s'*

by(*cases s*)(*blast elim*: $\tau\text{exec-move}E$ *intro*: $\tau\text{exec-move}I$ $\tau\text{move2-}\tau\text{moves2}$.*intros exec-move-FAssI1*)

lemma *FAss- τexecI2* :

$\tau\text{exec-move}$ *ci P t e' h* (*stk*, *loc*, *pc*, *xcp*) (*stk'*, *loc'*, *pc'*, *xcp'*)
 $\implies \tau\text{exec-move}$ *ci P t* ($e \cdot F\{D\} := e'$) *h* ((*stk* @ [*v*]), *loc*, ($\text{length}(\text{compE2 } e) + \text{pc}$), *xcp*) ((*stk'* @ [*v*]), *loc'*, ($\text{length}(\text{compE2 } e) + \text{pc}'$), *xcp'*)

by(*blast elim*: $\tau\text{exec-move}E$ *intro*: $\tau\text{exec-move}I$ $\tau\text{move2-}\tau\text{moves2}$.*intros exec-move-FAssI2* $\tau\text{move2-stk-append}$)

lemma *CAS- τexecI1* :

$\tau\text{exec-move}$ *ci P t e h s s'* $\implies \tau\text{exec-move}$ *ci P t* ($e \cdot \text{compareAndSwap}(D \cdot F, e', e'')$) *h s s'*

by(*cases s*)(*blast elim*: $\tau\text{exec-move}E$ *intro*: $\tau\text{exec-move}I$ $\tau\text{move2-}\tau\text{moves2}$.*intros exec-move-CASI1*)

lemma *CAS- τexecI2* :

$\tau\text{exec-move}$ *ci P t e' h* (*stk*, *loc*, *pc*, *xcp*) (*stk'*, *loc'*, *pc'*, *xcp'*)
 $\implies \tau\text{exec-move}$ *ci P t* ($e \cdot \text{compareAndSwap}(D \cdot F, e', e'')$) *h* ((*stk* @ [*v*]), *loc*, ($\text{length}(\text{compE2 } e) + \text{pc}$), *xcp*) ((*stk'* @ [*v*]), *loc'*, ($\text{length}(\text{compE2 } e) + \text{pc}'$), *xcp'*)

by(*blast elim*: $\tau\text{exec-move}E$ *intro*: $\tau\text{exec-move}I$ $\tau\text{move2-}\tau\text{moves2}$.*intros exec-move-CASI2* $\tau\text{move2-stk-append}$)

lemma *CAS- τexecI3* :

$\tau\text{exec-move}$ *ci P t e'' h* (*stk*, *loc*, *pc*, *xcp*) (*stk'*, *loc'*, *pc'*, *xcp'*)
 $\implies \tau\text{exec-move}$ *ci P t* ($e \cdot \text{compareAndSwap}(D \cdot F, e', e'')$) *h* ((*stk* @ [*v*, *v'*]), *loc*, ($\text{length}(\text{compE2 } e) + \text{length}(\text{compE2 } e') + \text{pc}$), *xcp*) ((*stk'* @ [*v*, *v'*]), *loc'*, ($\text{length}(\text{compE2 } e) + \text{length}(\text{compE2 } e') + \text{pc}'$), *xcp'*)

by(*blast elim*: $\tau\text{exec-move}E$ *intro*: $\tau\text{exec-move}I$ $\tau\text{move2-}\tau\text{moves2}$.*intros exec-move-CASI3* $\tau\text{move2-stk-append}$)

lemma *Call- τexecI1* :

$\tau\text{exec-move}$ *ci P t e h s s'* $\implies \tau\text{exec-move}$ *ci P t* ($e \cdot M(es)$) *h s s'*

by(*cases s*)(*blast elim*: $\tau\text{exec-move}E$ *intro*: $\tau\text{exec-move}I$ $\tau\text{move2-}\tau\text{moves2}$.*intros exec-move-CallI1*)

lemma *Call- τexecI2* :

$\tau\text{exec-moves}$ *ci P t es h* (*stk*, *loc*, *pc*, *xcp*) (*stk'*, *loc'*, *pc'*, *xcp'*)
 $\implies \tau\text{exec-move}$ *ci P t* ($e \cdot M(es)$) *h* ((*stk* @ [*v*]), *loc*, ($\text{length}(\text{compE2 } e) + \text{pc}$), *xcp*) ((*stk'* @ [*v*]), *loc'*, ($\text{length}(\text{compE2 } e) + \text{pc}'$), *xcp'*)

by(*blast elim*: $\tau\text{exec-moves}E$ *intro*: $\tau\text{exec-move}I$ $\tau\text{move2-}\tau\text{moves2}$.*intros exec-move-CallI2* $\tau\text{moves2-stk-append}$)

lemma *Block- $\tau\text{execI-Some}$* :

$\tau_{exec-move} ci P t e h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$
 $\implies \tau_{exec-move} ci P t \{V:T=[v]; e\} h (stk, loc, Suc (Suc pc), xcp) (stk', loc', Suc (Suc pc'), xcp')$
by(blast elim: $\tau_{exec-moveE}$ intro: $\tau_{exec-moveI} \tau_{move2-\tau_{moves2}}$.intros $exec-move-BlockSomeI$)

lemma *Block- $\tau_{execI-None}$:*

$\tau_{exec-move} ci P t e h s s' \implies \tau_{exec-move} ci P t \{V:T=None; e\} h s s'$
by(cases s)(blast elim: $\tau_{exec-moveE}$ intro: $\tau_{exec-moveI} \tau_{move2-\tau_{moves2}}$.intros $exec-move-BlockNoneI$)

lemma *Sync- τ_{execI} :*

$\tau_{exec-move} ci P t e h s s' \implies \tau_{exec-move} ci P t (sync_V (e) e') h s s'$
by(cases s)(blast elim: $\tau_{exec-moveE}$ intro: $\tau_{exec-moveI} \tau_{move2-\tau_{moves2}}$.intros $exec-move-SyncI1$)

lemma *Insync- τ_{execI} :*

$\tau_{exec-move} ci P t e' h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$
 $\implies \tau_{exec-move} ci P t (sync_V (e) e') h (stk, loc, Suc (Suc (Suc (length (compE2 e) + pc))), xcp)$
 $(stk', loc', Suc (Suc (Suc (length (compE2 e) + pc'))), xcp')$
by(blast elim: $\tau_{exec-moveE}$ intro: $\tau_{exec-moveI} \tau_{move2-\tau_{moves2}}$.intros $exec-move-SyncI2$)

lemma *Seq- τ_{execI1} :*

$\tau_{exec-move} ci P t e h s s' \implies \tau_{exec-move} ci P t (e;; e') h s s'$
by(cases s)(blast elim: $\tau_{exec-moveE}$ intro: $\tau_{exec-moveI} \tau_{move2-\tau_{moves2}}$.intros $exec-move-SeqI1$)

lemma *Seq- τ_{execI2} :*

$\tau_{exec-move} ci P t e' h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$
 $\implies \tau_{exec-move} ci P t (e;; e') h (stk, loc, Suc (length (compE2 e) + pc), xcp) (stk', loc', Suc (length (compE2 e) + pc'), xcp')$
by(blast elim: $\tau_{exec-moveE}$ intro: $\tau_{exec-moveI} \tau_{move2-\tau_{moves2}}$.intros $exec-move-SeqI2$)

lemma *Cond- τ_{execI1} :*

$\tau_{exec-move} ci P t e h s s' \implies \tau_{exec-move} ci P t (if (e) e' else e'') h s s'$
by(cases s)(blast elim: $\tau_{exec-moveE}$ intro: $\tau_{exec-moveI} \tau_{move2-\tau_{moves2}}$.intros $exec-move-CondI1$)

lemma *Cond- τ_{execI2} :*

$\tau_{exec-move} ci P t e' h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$
 $\implies \tau_{exec-move} ci P t (if (e) e' else e'') h (stk, loc, Suc (length (compE2 e) + pc), xcp) (stk', loc', Suc (length (compE2 e) + pc'), xcp')$
by(blast elim: $\tau_{exec-moveE}$ intro: $\tau_{exec-moveI} \tau_{move2-\tau_{moves2}}$.intros $exec-move-CondI2$)

lemma *Cond- τ_{execI3} :*

$\tau_{exec-move} ci P t e'' h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$
 $\implies \tau_{exec-move} ci P t (if (e) e' else e'') h (stk, loc, Suc (Suc (length (compE2 e) + length (compE2 e') + pc)), xcp) (stk', loc', Suc (Suc (length (compE2 e) + length (compE2 e') + pc')), xcp')$
by(blast elim: $\tau_{exec-moveE}$ intro: $\tau_{exec-moveI} \tau_{move2-\tau_{moves2}}$.intros $exec-move-CondI3$)

lemma *While- τ_{execI1} :*

$\tau_{exec-move} ci P t e h s s' \implies \tau_{exec-move} ci P t (while (e) e') h s s'$
by(cases s)(blast elim: $\tau_{exec-moveE}$ intro: $\tau_{exec-moveI} \tau_{move2-\tau_{moves2}}$.intros $exec-move-WhileI1$)

lemma *While- τ_{execI2} :*

$\tau_{exec-move} ci P t e' h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$
 $\implies \tau_{exec-move} ci P t (while (e) e') h (stk, loc, Suc (length (compE2 e) + pc), xcp) (stk', loc', Suc (length (compE2 e) + pc'), xcp')$
by(blast elim: $\tau_{exec-moveE}$ intro: $\tau_{exec-moveI} \tau_{move2-\tau_{moves2}}$.intros $exec-move-WhileI2$)

lemma *Throw- τ execI*:

$$\tau\text{exec-move } ci P t e h s s' \Longrightarrow \tau\text{exec-move } ci P t (\text{throw } e) h s s'$$

by(cases s)(blast elim: $\tau\text{exec-move}E$ intro: $\tau\text{exec-move}I$ $\tau\text{move2-}\tau\text{moves2.intros}$ $\text{exec-move-Throw}I$)

lemma *Try- τ execI1*:

$$\tau\text{exec-move } ci P t e h s s' \Longrightarrow \tau\text{exec-move } ci P t (\text{try } e \text{ catch } (C V) e') h s s'$$

by(cases s)(blast elim: $\tau\text{exec-move}E$ intro: $\tau\text{exec-move}I$ $\tau\text{move2-}\tau\text{moves2.intros}$ $\text{exec-move-Try}I1$)

lemma *Try- τ execI2*:

$$\tau\text{exec-move } ci P t e' h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$$

$$\Longrightarrow \tau\text{exec-move } ci P t (\text{try } e \text{ catch } (C V) e') h (stk, loc, \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e) + pc)), xcp) (stk', loc', \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e) + pc')), xcp')$$

by(blast elim: $\tau\text{exec-move}E$ intro: $\tau\text{exec-move}I$ $\tau\text{move2-}\tau\text{moves2.intros}$ $\text{exec-move-Try}I2$)

lemma *NewArray- τ ExecrI*:

$$\tau\text{Exec-mover } ci P t e h s s' \Longrightarrow \tau\text{Exec-mover } ci P t (\text{newA } T[e]) h s s'$$

by(induct rule: $r\text{tranclp-induct}$)(blast intro: $r\text{tranclp.rtrancl-into-rtrancl}$ $\text{NewArray-}\tau\text{exec}I$) $+$

lemma *Cast- τ ExecrI*:

$$\tau\text{Exec-mover } ci P t e h s s' \Longrightarrow \tau\text{Exec-mover } ci P t (\text{Cast } T e) h s s'$$

by(induct rule: $r\text{tranclp-induct}$)(blast intro: $r\text{tranclp.rtrancl-into-rtrancl}$ $\text{Cast-}\tau\text{exec}I$) $+$

lemma *InstanceOf- τ ExecrI*:

$$\tau\text{Exec-mover } ci P t e h s s' \Longrightarrow \tau\text{Exec-mover } ci P t (e \text{ instanceof } T) h s s'$$

by(induct rule: $r\text{tranclp-induct}$)(blast intro: $r\text{tranclp.rtrancl-into-rtrancl}$ $\text{InstanceOf-}\tau\text{exec}I$) $+$

lemma *BinOp- τ ExecrI1*:

$$\tau\text{Exec-mover } ci P t e1 h s s' \Longrightarrow \tau\text{Exec-mover } ci P t (e1 \ll\text{bop}\gg e2) h s s'$$

by(induct rule: $r\text{tranclp-induct}$)(blast intro: $r\text{tranclp.rtrancl-into-rtrancl}$ $\text{BinOp-}\tau\text{exec}I1$) $+$

lemma *BinOp- τ ExecrI2*:

$$\tau\text{Exec-mover } ci P t e2 h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$$

$$\Longrightarrow \tau\text{Exec-mover } ci P t (e \ll\text{bop}\gg e2) h ((stk @ [v]), loc, (\text{length } (\text{compE2 } e) + pc), xcp) ((stk' @ [v]), loc', (\text{length } (\text{compE2 } e) + pc'), xcp')$$

by(induct rule: $\tau\text{Execr-induct}$)(blast intro: $r\text{tranclp.rtrancl-into-rtrancl}$ $\text{BinOp-}\tau\text{exec}I2$) $+$

lemma *LAss- τ ExecrI*:

$$\tau\text{Exec-mover } ci P t e h s s' \Longrightarrow \tau\text{Exec-mover } ci P t (V := e) h s s'$$

by(induct rule: $r\text{tranclp-induct}$)(blast intro: $r\text{tranclp.rtrancl-into-rtrancl}$ $\text{LAss-}\tau\text{exec}I$) $+$

lemma *AAcc- τ ExecrI1*:

$$\tau\text{Exec-mover } ci P t e h s s' \Longrightarrow \tau\text{Exec-mover } ci P t (e[i]) h s s'$$

by(induct rule: $r\text{tranclp-induct}$)(blast intro: $r\text{tranclp.rtrancl-into-rtrancl}$ $\text{AAcc-}\tau\text{exec}I1$) $+$

lemma *AAcc- τ ExecrI2*:

$$\tau\text{Exec-mover } ci P t i h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$$

$$\Longrightarrow \tau\text{Exec-mover } ci P t (a[i]) h ((stk @ [v]), loc, (\text{length } (\text{compE2 } a) + pc), xcp) ((stk' @ [v]), loc', (\text{length } (\text{compE2 } a) + pc'), xcp')$$

by(induct rule: $\tau\text{Execr-induct}$)(blast intro: $r\text{tranclp.rtrancl-into-rtrancl}$ $\text{AAcc-}\tau\text{exec}I2$) $+$

lemma *AAss- τ ExecrI1*:

$$\tau\text{Exec-mover } ci P t e h s s' \Longrightarrow \tau\text{Exec-mover } ci P t (e[i] := e') h s s'$$

by(*induct rule*: rtranclp-induct)(*blast intro*: rtranclp.rtrancl-into-rtrancl AAss- τ execI1)+

lemma AAss- τ ExecrI2:

τ Exec-mover ci P t i h (stk, loc, pc, xcp) (stk', loc', pc', xcp')
 $\implies \tau$ Exec-mover ci P t (a[i] := e) h ((stk @ [v]), loc, (length (compE2 a) + pc), xcp) ((stk' @ [v]), loc', (length (compE2 a) + pc'), xcp')

by(*induct rule*: τ Execr-induct)(*blast intro*: rtranclp.rtrancl-into-rtrancl AAss- τ execI2)+

lemma AAss- τ ExecrI3:

τ Exec-mover ci P t e h (stk, loc, pc, xcp) (stk', loc', pc', xcp')
 $\implies \tau$ Exec-mover ci P t (a[i] := e) h ((stk @ [v, v']), loc, (length (compE2 a) + length (compE2 i) + pc), xcp) ((stk' @ [v, v']), loc', (length (compE2 a) + length (compE2 i) + pc'), xcp')

by(*induct rule*: τ Execr-induct)(*blast intro*: rtranclp.rtrancl-into-rtrancl AAss- τ execI3)+

lemma ALength- τ ExecrI:

τ Exec-mover ci P t e h s s' $\implies \tau$ Exec-mover ci P t (e.length) h s s'

by(*induct rule*: rtranclp-induct)(*blast intro*: rtranclp.rtrancl-into-rtrancl ALength- τ execI)+

lemma FAcc- τ ExecrI:

τ Exec-mover ci P t e h s s' $\implies \tau$ Exec-mover ci P t (e.F{D}) h s s'

by(*induct rule*: rtranclp-induct)(*blast intro*: rtranclp.rtrancl-into-rtrancl FAcc- τ execI)+

lemma FAss- τ ExecrI1:

τ Exec-mover ci P t e h s s' $\implies \tau$ Exec-mover ci P t (e.F{D} := e') h s s'

by(*induct rule*: rtranclp-induct)(*blast intro*: rtranclp.rtrancl-into-rtrancl FAss- τ execI1)+

lemma FAss- τ ExecrI2:

τ Exec-mover ci P t e' h (stk, loc, pc, xcp) (stk', loc', pc', xcp')
 $\implies \tau$ Exec-mover ci P t (e.F{D} := e') h ((stk @ [v]), loc, (length (compE2 e) + pc), xcp) ((stk' @ [v]), loc', (length (compE2 e) + pc'), xcp')

by(*induct rule*: τ Execr-induct)(*blast intro*: rtranclp.rtrancl-into-rtrancl FAss- τ execI2)+

lemma CAS- τ ExecrI1:

τ Exec-mover ci P t e h s s' $\implies \tau$ Exec-mover ci P t (e.compareAndSwap(D.F, e', e'')) h s s'

by(*induct rule*: rtranclp-induct)(*blast intro*: rtranclp.rtrancl-into-rtrancl CAS- τ execI1)+

lemma CAS- τ ExecrI2:

τ Exec-mover ci P t e' h (stk, loc, pc, xcp) (stk', loc', pc', xcp')
 $\implies \tau$ Exec-mover ci P t (e.compareAndSwap(D.F, e', e'')) h ((stk @ [v]), loc, (length (compE2 e) + pc), xcp) ((stk' @ [v]), loc', (length (compE2 e) + pc'), xcp')

by(*induct rule*: τ Execr-induct)(*blast intro*: rtranclp.rtrancl-into-rtrancl CAS- τ execI2)+

lemma CAS- τ ExecrI3:

τ Exec-mover ci P t e'' h (stk, loc, pc, xcp) (stk', loc', pc', xcp')
 $\implies \tau$ Exec-mover ci P t (e.compareAndSwap(D.F, e', e'')) h ((stk @ [v, v']), loc, (length (compE2 e) + length (compE2 e') + pc), xcp) ((stk' @ [v, v']), loc', (length (compE2 e) + length (compE2 e') + pc'), xcp')

by(*induct rule*: τ Execr-induct)(*blast intro*: rtranclp.rtrancl-into-rtrancl CAS- τ execI3)+

lemma Call- τ ExecrI1:

τ Exec-mover ci P t obj h s s' $\implies \tau$ Exec-mover ci P t (obj.M'(es)) h s s'

by(*induct rule*: rtranclp-induct)(*blast intro*: rtranclp.rtrancl-into-rtrancl Call- τ execI1)+

lemma Call- τ ExecrI2:

$\tau Exec\text{-movesr}$ $ci P t es h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$
 $\implies \tau Exec\text{-mover}$ $ci P t (obj \cdot M'(es)) h ((stk @ [v]), loc, (length (compE2 obj) + pc), xcp) ((stk' @ [v]), loc', (length (compE2 obj) + pc'), xcp')$
by(*induct rule*: $\tau Execsr\text{-induct}$)(*blast intro*: $rtranclp.rtrancl\text{-into-rtrancl Call}\text{-}\tau execI2$)**+**

lemma *Block- $\tau ExecrI\text{-Some}$* :

$\tau Exec\text{-mover}$ $ci P t e h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$
 $\implies \tau Exec\text{-mover}$ $ci P t \{V:T=[v]; e\} h (stk, loc, (Suc (Suc pc)), xcp) (stk', loc', (Suc (Suc pc')), xcp')$
by(*induct rule*: $\tau Execr\text{-induct}$)(*blast intro*: $rtranclp.rtrancl\text{-into-rtrancl Block}\text{-}\tau execI\text{-Some}$)**+**

lemma *Block- $\tau ExecrI\text{-None}$* :

$\tau Exec\text{-mover}$ $ci P t e h s s' \implies \tau Exec\text{-mover}$ $ci P t \{V:T=None; e\} h s s'$
by(*induct rule*: $rtranclp\text{-induct}$)(*blast intro*: $rtranclp.rtrancl\text{-into-rtrancl Block}\text{-}\tau execI\text{-None}$)**+**

lemma *Sync- $\tau ExecrI$* :

$\tau Exec\text{-mover}$ $ci P t e h s s' \implies \tau Exec\text{-mover}$ $ci P t (sync_V (e) e') h s s'$
by(*induct rule*: $rtranclp\text{-induct}$)(*blast intro*: $rtranclp.rtrancl\text{-into-rtrancl Sync}\text{-}\tau execI$)**+**

lemma *Insync- $\tau ExecrI$* :

$\tau Exec\text{-mover}$ $ci P t e' h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$
 $\implies \tau Exec\text{-mover}$ $ci P t (sync_V (e) e') h (stk, loc, (Suc (Suc (Suc (length (compE2 e) + pc))))), xcp) (stk', loc', (Suc (Suc (Suc (length (compE2 e) + pc')))), xcp')$
by(*induct rule*: $\tau Execr\text{-induct}$)(*blast intro*: $rtranclp.rtrancl\text{-into-rtrancl Insync}\text{-}\tau execI$)**+**

lemma *Seq- $\tau ExecrI1$* :

$\tau Exec\text{-mover}$ $ci P t e h s s' \implies \tau Exec\text{-mover}$ $ci P t (e;;e') h s s'$
by(*induct rule*: $rtranclp\text{-induct}$)(*blast intro*: $rtranclp.rtrancl\text{-into-rtrancl Seq}\text{-}\tau execI1$)**+**

lemma *Seq- $\tau ExecrI2$* :

$\tau Exec\text{-mover}$ $ci P t e h (stk, loc, pc, xcp) (stk', loc', pc', xcp') \implies$
 $\tau Exec\text{-mover}$ $ci P t (e';;e) h (stk, loc, (Suc (length (compE2 e') + pc)), xcp) (stk', loc', (Suc (length (compE2 e') + pc')), xcp')$
by(*induct rule*: $\tau Execr\text{-induct}$)(*blast intro*: $rtranclp.rtrancl\text{-into-rtrancl Seq}\text{-}\tau execI2$)**+**

lemma *Cond- $\tau ExecrI1$* :

$\tau Exec\text{-mover}$ $ci P t e h s s' \implies \tau Exec\text{-mover}$ $ci P t (if (e) e1 else e2) h s s'$
by(*induct rule*: $rtranclp\text{-induct}$)(*blast intro*: $rtranclp.rtrancl\text{-into-rtrancl Cond}\text{-}\tau execI1$)**+**

lemma *Cond- $\tau ExecrI2$* :

$\tau Exec\text{-mover}$ $ci P t e1 h (stk, loc, pc, xcp) (stk', loc', pc', xcp') \implies$
 $\tau Exec\text{-mover}$ $ci P t (if (e) e1 else e2) h (stk, loc, (Suc (length (compE2 e) + pc)), xcp) (stk', loc', (Suc (length (compE2 e) + pc')), xcp')$
by(*induct rule*: $\tau Execr\text{-induct}$)(*blast intro*: $rtranclp.rtrancl\text{-into-rtrancl Cond}\text{-}\tau execI2$)**+**

lemma *Cond- $\tau ExecrI3$* :

$\tau Exec\text{-mover}$ $ci P t e2 h (stk, loc, pc, xcp) (stk', loc', pc', xcp') \implies$
 $\tau Exec\text{-mover}$ $ci P t (if (e) e1 else e2) h (stk, loc, (Suc (Suc (length (compE2 e) + length (compE2 e1) + pc))), xcp) (stk', loc', (Suc (Suc (length (compE2 e) + length (compE2 e1) + pc'))), xcp')$
by(*induct rule*: $\tau Execr\text{-induct}$)(*blast intro*: $rtranclp.rtrancl\text{-into-rtrancl Cond}\text{-}\tau execI3$)**+**

lemma *While- $\tau ExecrI1$* :

$\tau Exec\text{-mover}$ $ci P t c h s s' \implies \tau Exec\text{-mover}$ $ci P t (while (c) e) h s s'$
by(*induct rule*: $rtranclp\text{-induct}$)(*blast intro*: $rtranclp.rtrancl\text{-into-rtrancl While}\text{-}\tau execI1$)**+**

lemma *While- τ ExecrI2*:

τ Exec-mover $ci P t E h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$
 $\implies \tau$ Exec-mover $ci P t (while (c) E) h (stk, loc, (Suc (length (compE2 c) + pc)), xcp) (stk', loc', (Suc (length (compE2 c) + pc')), xcp')$
by(*induct rule: τ Execr-induct*)(*blast intro: rtranclp.rtrancl-into-rtrancl While- τ execI2*)+

lemma *Throw- τ ExecrI*:

τ Exec-mover $ci P t e h s s' \implies \tau$ Exec-mover $ci P t (throw e) h s s'$
by(*induct rule: rtranclp-induct*)(*blast intro: rtranclp.rtrancl-into-rtrancl Throw- τ execI*)+

lemma *Try- τ ExecrI1*:

τ Exec-mover $ci P t E h s s' \implies \tau$ Exec-mover $ci P t (try E catch (C' V) e) h s s'$
by(*induct rule: rtranclp-induct*)(*blast intro: rtranclp.rtrancl-into-rtrancl Try- τ execI1*)+

lemma *Try- τ ExecrI2*:

τ Exec-mover $ci P t e h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$
 $\implies \tau$ Exec-mover $ci P t (try E catch (C' V) e) h (stk, loc, (Suc (Suc (length (compE2 E) + pc))), xcp) (stk', loc', (Suc (Suc (length (compE2 E) + pc')), xcp')$
by(*induct rule: τ Execr-induct*)(*blast intro: rtranclp.rtrancl-into-rtrancl Try- τ execI2*)+

lemma *NewArray- τ ExecI*:

τ Exec-mover $ci P t e h s s' \implies \tau$ Exec-mover $ci P t (newA T[e]) h s s'$
by(*induct rule: tranclp-induct*)(*blast intro: tranclp.trancl-into-trancl NewArray- τ execI*)+

lemma *Cast- τ ExecI*:

τ Exec-mover $ci P t e h s s' \implies \tau$ Exec-mover $ci P t (Cast T e) h s s'$
by(*induct rule: tranclp-induct*)(*blast intro: tranclp.trancl-into-trancl Cast- τ execI*)+

lemma *InstanceOf- τ ExecI*:

τ Exec-mover $ci P t e h s s' \implies \tau$ Exec-mover $ci P t (e \text{ instanceof } T) h s s'$
by(*induct rule: tranclp-induct*)(*blast intro: tranclp.trancl-into-trancl InstanceOf- τ execI*)+

lemma *BinOp- τ ExecI1*:

τ Exec-mover $ci P t e1 h s s' \implies \tau$ Exec-mover $ci P t (e1 \llbracket \text{bop} \rrbracket e2) h s s'$
by(*induct rule: tranclp-induct*)(*blast intro: tranclp.trancl-into-trancl BinOp- τ execI1*)+

lemma *BinOp- τ ExecI2*:

τ Exec-mover $ci P t e2 h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$
 $\implies \tau$ Exec-mover $ci P t (e \llbracket \text{bop} \rrbracket e2) h ((stk @ [v]), loc, (length (compE2 e) + pc), xcp) ((stk' @ [v]), loc', (length (compE2 e) + pc'), xcp')$
by(*induct rule: τ ExecI-induct*)(*blast intro: tranclp.trancl-into-trancl BinOp- τ execI2*)+

lemma *LAss- τ ExecI*:

τ Exec-mover $ci P t e h s s' \implies \tau$ Exec-mover $ci P t (V := e) h s s'$
by(*induct rule: tranclp-induct*)(*blast intro: tranclp.trancl-into-trancl LAss- τ execI*)+

lemma *AAcc- τ ExecI1*:

τ Exec-mover $ci P t e h s s' \implies \tau$ Exec-mover $ci P t (e[i]) h s s'$
by(*induct rule: tranclp-induct*)(*blast intro: tranclp.trancl-into-trancl AAcc- τ execI1*)+

lemma *AAcc- τ ExecI2*:

τ Exec-mover $ci P t i h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$

$\implies \tau Exec\text{-movet } ci P t (a[i]) h ((stk @ [v]), loc, (length (compE2 a) + pc), xcp) ((stk' @ [v]), loc', (length (compE2 a) + pc'), xcp')$

by(*induct rule*: $\tau Exec\text{-induct}$)(*blast intro*: $tranclp.trancl\text{-into}\text{-trancl } AAcc\text{-}\tau execI2$)+

lemma *AAss- $\tau ExecI1$* :

$\tau Exec\text{-movet } ci P t e h s s' \implies \tau Exec\text{-movet } ci P t (e[i] := e') h s s'$

by(*induct rule*: $tranclp\text{-induct}$)(*blast intro*: $tranclp.trancl\text{-into}\text{-trancl } AAss\text{-}\tau execI1$)+

lemma *AAss- $\tau ExecI2$* :

$\tau Exec\text{-movet } ci P t i h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$

$\implies \tau Exec\text{-movet } ci P t (a[i] := e) h ((stk @ [v]), loc, (length (compE2 a) + pc), xcp) ((stk' @ [v]), loc', (length (compE2 a) + pc'), xcp')$

by(*induct rule*: $\tau Exec\text{-induct}$)(*blast intro*: $tranclp.trancl\text{-into}\text{-trancl } AAss\text{-}\tau execI2$)+

lemma *AAss- $\tau ExecI3$* :

$\tau Exec\text{-movet } ci P t e h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$

$\implies \tau Exec\text{-movet } ci P t (a[i] := e) h ((stk @ [v, v']), loc, (length (compE2 a) + length (compE2 i) + pc), xcp) ((stk' @ [v, v']), loc', (length (compE2 a) + length (compE2 i) + pc'), xcp')$

by(*induct rule*: $\tau Exec\text{-induct}$)(*blast intro*: $tranclp.trancl\text{-into}\text{-trancl } AAss\text{-}\tau execI3$)+

lemma *ALength- $\tau ExecI$* :

$\tau Exec\text{-movet } ci P t e h s s' \implies \tau Exec\text{-movet } ci P t (e \cdot length) h s s'$

by(*induct rule*: $tranclp\text{-induct}$)(*blast intro*: $tranclp.trancl\text{-into}\text{-trancl } ALength\text{-}\tau execI$)+

lemma *FAcc- $\tau ExecI$* :

$\tau Exec\text{-movet } ci P t e h s s' \implies \tau Exec\text{-movet } ci P t (e \cdot F\{D\}) h s s'$

by(*induct rule*: $tranclp\text{-induct}$)(*blast intro*: $tranclp.trancl\text{-into}\text{-trancl } FAcc\text{-}\tau execI$)+

lemma *FAss- $\tau ExecI1$* :

$\tau Exec\text{-movet } ci P t e h s s' \implies \tau Exec\text{-movet } ci P t (e \cdot F\{D\} := e') h s s'$

by(*induct rule*: $tranclp\text{-induct}$)(*blast intro*: $tranclp.trancl\text{-into}\text{-trancl } FAss\text{-}\tau execI1$)+

lemma *FAss- $\tau ExecI2$* :

$\tau Exec\text{-movet } ci P t e' h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$

$\implies \tau Exec\text{-movet } ci P t (e \cdot F\{D\} := e') h ((stk @ [v]), loc, (length (compE2 e) + pc), xcp) ((stk' @ [v]), loc', (length (compE2 e) + pc'), xcp')$

by(*induct rule*: $\tau Exec\text{-induct}$)(*blast intro*: $tranclp.trancl\text{-into}\text{-trancl } FAss\text{-}\tau execI2$)+

lemma *CAS- $\tau ExecI1$* :

$\tau Exec\text{-movet } ci P t e h s s' \implies \tau Exec\text{-movet } ci P t (e \cdot compareAndSwap(D \cdot F, e', e'')) h s s'$

by(*induct rule*: $tranclp\text{-induct}$)(*blast intro*: $tranclp.trancl\text{-into}\text{-trancl } CAS\text{-}\tau execI1$)+

lemma *CAS- $\tau ExecI2$* :

$\tau Exec\text{-movet } ci P t e' h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$

$\implies \tau Exec\text{-movet } ci P t (e \cdot compareAndSwap(D \cdot F, e', e'')) h ((stk @ [v]), loc, (length (compE2 e) + pc), xcp) ((stk' @ [v]), loc', (length (compE2 e) + pc'), xcp')$

by(*induct rule*: $\tau Exec\text{-induct}$)(*blast intro*: $tranclp.trancl\text{-into}\text{-trancl } CAS\text{-}\tau execI2$)+

lemma *CAS- $\tau ExecI3$* :

$\tau Exec\text{-movet } ci P t e'' h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$

$\implies \tau Exec\text{-movet } ci P t (e \cdot compareAndSwap(D \cdot F, e', e'')) h ((stk @ [v, v']), loc, (length (compE2 e) + length (compE2 e') + pc), xcp) ((stk' @ [v, v']), loc', (length (compE2 e) + length (compE2 e') + pc'), xcp')$

by(*induct rule*: $\tau Exec\text{-induct}$)(*blast intro*: $tranclp.trancl\text{-into}\text{-trancl } CAS\text{-}\tau execI3$)+

lemma *Call- τ ExecI1*:

$$\tauExec\text{-movet}\ ci\ P\ t\ obj\ h\ s\ s' \implies \tauExec\text{-movet}\ ci\ P\ t\ (obj.M'(es))\ h\ s\ s'$$

by(*induct rule*: *tranclp-induct*)(*blast intro*: *tranclp.trancl-into-trancl Call- τ execI1*)+

lemma *Call- τ ExecI2*:

$$\tauExec\text{-movest}\ ci\ P\ t\ es\ h\ (stk, loc, pc, xcp)\ (stk', loc', pc', xcp')$$

$$\implies \tauExec\text{-movet}\ ci\ P\ t\ (obj.M'(es))\ h\ ((stk\ @\ [v]), loc, (length\ (compE2\ obj) + pc), xcp)\ ((stk'\ @\ [v]), loc', (length\ (compE2\ obj) + pc'), xcp')$$

by(*induct rule*: \tauExecst -*induct*)(*blast intro*: *tranclp.trancl-into-trancl Call- τ execI2*)+

lemma *Block- τ ExecI-Some*:

$$\tauExec\text{-movet}\ ci\ P\ t\ e\ h\ (stk, loc, pc, xcp)\ (stk', loc', pc', xcp')$$

$$\implies \tauExec\text{-movet}\ ci\ P\ t\ \{V:T=[v];\ e\}\ h\ (stk, loc, (Suc\ (Suc\ pc)), xcp)\ (stk', loc', (Suc\ (Suc\ pc')), xcp')$$

by(*induct rule*: \tauExec -*induct*)(*blast intro*: *tranclp.trancl-into-trancl Block- τ execI-Some*)+

lemma *Block- τ ExecI-None*:

$$\tauExec\text{-movet}\ ci\ P\ t\ e\ h\ s\ s' \implies \tauExec\text{-movet}\ ci\ P\ t\ \{V:T=None;\ e\}\ h\ s\ s'$$

by(*induct rule*: *tranclp-induct*)(*blast intro*: *tranclp.trancl-into-trancl Block- τ execI-None*)+

lemma *Sync- τ ExecI*:

$$\tauExec\text{-movet}\ ci\ P\ t\ e\ h\ s\ s' \implies \tauExec\text{-movet}\ ci\ P\ t\ (sync_V\ (e)\ e')\ h\ s\ s'$$

by(*induct rule*: *tranclp-induct*)(*blast intro*: *tranclp.trancl-into-trancl Sync- τ execI*)+

lemma *Insync- τ ExecI*:

$$\tauExec\text{-movet}\ ci\ P\ t\ e'\ h\ (stk, loc, pc, xcp)\ (stk', loc', pc', xcp')$$

$$\implies \tauExec\text{-movet}\ ci\ P\ t\ (sync_V\ (e)\ e')\ h\ (stk, loc, (Suc\ (Suc\ (Suc\ (length\ (compE2\ e) + pc))))), xcp)\ (stk', loc', (Suc\ (Suc\ (Suc\ (length\ (compE2\ e) + pc')))), xcp')$$

by(*induct rule*: \tauExec -*induct*)(*blast intro*: *tranclp.trancl-into-trancl Insync- τ execI*)+

lemma *Seq- τ ExecI1*:

$$\tauExec\text{-movet}\ ci\ P\ t\ e\ h\ s\ s' \implies \tauExec\text{-movet}\ ci\ P\ t\ (e;;e')\ h\ s\ s'$$

by(*induct rule*: *tranclp-induct*)(*blast intro*: *tranclp.trancl-into-trancl Seq- τ execI1*)+

lemma *Seq- τ ExecI2*:

$$\tauExec\text{-movet}\ ci\ P\ t\ e\ h\ (stk, loc, pc, xcp)\ (stk', loc', pc', xcp') \implies$$

$$\tauExec\text{-movet}\ ci\ P\ t\ (e;;e)\ h\ (stk, loc, (Suc\ (length\ (compE2\ e') + pc)), xcp)\ (stk', loc', (Suc\ (length\ (compE2\ e') + pc')), xcp')$$

by(*induct rule*: \tauExec -*induct*)(*blast intro*: *tranclp.trancl-into-trancl Seq- τ execI2*)+

lemma *Cond- τ ExecI1*:

$$\tauExec\text{-movet}\ ci\ P\ t\ e\ h\ s\ s' \implies \tauExec\text{-movet}\ ci\ P\ t\ (if\ (e)\ e1\ else\ e2)\ h\ s\ s'$$

by(*induct rule*: *tranclp-induct*)(*blast intro*: *tranclp.trancl-into-trancl Cond- τ execI1*)+

lemma *Cond- τ ExecI2*:

$$\tauExec\text{-movet}\ ci\ P\ t\ e1\ h\ (stk, loc, pc, xcp)\ (stk', loc', pc', xcp') \implies$$

$$\tauExec\text{-movet}\ ci\ P\ t\ (if\ (e)\ e1\ else\ e2)\ h\ (stk, loc, (Suc\ (length\ (compE2\ e) + pc)), xcp)\ (stk', loc', (Suc\ (length\ (compE2\ e) + pc')), xcp')$$

by(*induct rule*: \tauExec -*induct*)(*blast intro*: *tranclp.trancl-into-trancl Cond- τ execI2*)+

lemma *Cond- τ ExecI3*:

$$\tauExec\text{-movet}\ ci\ P\ t\ e2\ h\ (stk, loc, pc, xcp)\ (stk', loc', pc', xcp') \implies$$

$$\tauExec\text{-movet}\ ci\ P\ t\ (if\ (e)\ e1\ else\ e2)\ h\ (stk, loc, (Suc\ (Suc\ (length\ (compE2\ e) + length\ (compE2\ e2))))), xcp)\ (stk', loc', (Suc\ (Suc\ (length\ (compE2\ e) + length\ (compE2\ e2')))), xcp')$$

$e1) + pc)))$, $xcp)$ (stk' , loc' , ($Suc (Suc (length (compE2 e) + length (compE2 e1) + pc'))$), xcp')
by(*induct rule: $\tau Exec$ -induct*)(*blast intro: tranclp.trancl-into-trancl Cond- $\tau execI3$*)+

lemma *While- $\tau ExecI1$* :

$\tau Exec$ -movet $ci P t c h s s' \implies \tau Exec$ -movet $ci P t (while (c) e) h s s'$

by(*induct rule: tranclp-induct*)(*blast intro: tranclp.trancl-into-trancl While- $\tau execI1$*)+

lemma *While- $\tau ExecI2$* :

$\tau Exec$ -movet $ci P t E h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$

$\implies \tau Exec$ -movet $ci P t (while (c) E) h (stk, loc, (Suc (length (compE2 c) + pc)), xcp) (stk', loc', (Suc (length (compE2 c) + pc')), xcp')$

by(*induct rule: $\tau Exec$ -induct*)(*blast intro: tranclp.trancl-into-trancl While- $\tau execI2$*)+

lemma *Throw- $\tau ExecI1$* :

$\tau Exec$ -movet $ci P t e h s s' \implies \tau Exec$ -movet $ci P t (throw e) h s s'$

by(*induct rule: tranclp-induct*)(*blast intro: tranclp.trancl-into-trancl Throw- $\tau execI1$*)+

lemma *Try- $\tau ExecI1$* :

$\tau Exec$ -movet $ci P t E h s s' \implies \tau Exec$ -movet $ci P t (try E catch (C' V) e) h s s'$

by(*induct rule: tranclp-induct*)(*blast intro: tranclp.trancl-into-trancl Try- $\tau execI1$*)+

lemma *Try- $\tau ExecI2$* :

$\tau Exec$ -movet $ci P t e h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$

$\implies \tau Exec$ -movet $ci P t (try E catch (C' V) e) h (stk, loc, (Suc (Suc (length (compE2 E) + pc))), xcp) (stk', loc', (Suc (Suc (length (compE2 E) + pc')), xcp')$

by(*induct rule: $\tau Exec$ -induct*)(*blast intro: tranclp.trancl-into-trancl Try- $\tau execI2$*)+

lemma *$\tau Exec$ -movesr-map-Val*:

$\tau Exec$ -movesr-a $P t (map Val vs) h ([], xs, 0, None) ((rev vs), xs, (length (compEs2 (map Val vs))), None)$

proof(*induct vs arbitrary: pc stk Ts rule: rev-induct*)

case Nil thus ?case **by**(*auto*)

next

case (*snoc v vs'*)

let $?E = compEs2 (map Val vs')$

from *snoc* **have** $\tau Exec$ -movesr-a $P t (map Val (vs' @ [v])) h ([], xs, 0, None) ((rev vs'), xs, (length ?E), None)$

by *auto*

also {

have *exec-meth-a* (*compP2 P*) ($?E @ [Push v]$) (*compEs2* (*map Val vs'*) 0 0 @ *shift* (*length ?E*) []) *t h* ((*rev vs'*), *xs*, (*length ?E* + 0), *None*) εh ((*v # rev vs'*), *xs*, (*length ?E* + *Suc 0*), *None*)

by $-(rule \textit{append-exec-meth-xt}, auto \textit{simp add: exec-meth-instr})$

moreover **have** $\tau moves2 (compP2 P) h (rev vs') (map Val vs' @ [Val v]) (length (compEs2 (map Val vs') + 0) None)$

by(*rule append- $\tau moves2 \tau moves2Hd \tau move2Val$*)+

ultimately **have** $\tau Exec$ -movesr-a $P t (map Val (vs' @ [v])) h ((rev vs'), xs, (length ?E), None) ((rev (vs' @ [v])), xs, (length (compEs2 (map Val (vs' @ [v])))), None)$

by $-(rule \tau Execsr1step, auto \textit{simp add: exec-moves-def compP2-def})$ }

finally **show** *?case* .

qed

lemma *$\tau Exec$ -mover-blocks1* [*simp*]:

$\tau Exec$ -mover $ci P t (blocks1 n Ts body) h s s' = \tau Exec$ -mover $ci P t body h s s'$

by(*simp add: $\tau exec$ -move-conv- $\tau exec$ -meth*)

lemma $\tauExec\text{-}move\text{-}blocks1$ [simp]:

$\tauExec\text{-}move\text{-}ci\ P\ t\ (blocks1\ n\ Ts\ body)\ h\ s\ s' = \tauExec\text{-}move\text{-}ci\ P\ t\ body\ h\ s\ s'$
by(simp add: $\tauexec\text{-}move\text{-}conv\text{-}\tauexec\text{-}meth$)

definition $\tauexec\text{-}1 :: 'addr\ jvm\text{-}prog \Rightarrow 'thread\text{-}id \Rightarrow ('addr,\ heap)\ jvm\text{-}state \Rightarrow ('addr,\ heap)\ jvm\text{-}state \Rightarrow bool$

where $\tauexec\text{-}1\ P\ t\ \sigma\ \sigma' \longleftrightarrow exec\text{-}1\ P\ t\ \sigma\ \varepsilon\ \sigma' \wedge \tauMove2\ P\ \sigma$

lemma $\tauexec\text{-}1I$ [intro]:

$\llbracket exec\text{-}1\ P\ t\ \sigma\ \varepsilon\ \sigma'; \tauMove2\ P\ \sigma \rrbracket \Longrightarrow \tauexec\text{-}1\ P\ t\ \sigma\ \sigma'$
by(simp add: $\tauexec\text{-}1\text{-}def$)

lemma $\tauexec\text{-}1E$ [elim]:

assumes $\tauexec\text{-}1\ P\ t\ \sigma\ \sigma'$
obtains $exec\text{-}1\ P\ t\ \sigma\ \varepsilon\ \sigma' \tauMove2\ P\ \sigma$
using *assms* **by**(auto simp add: $\tauexec\text{-}1\text{-}def$)

abbreviation $\tauExec\text{-}1r :: 'addr\ jvm\text{-}prog \Rightarrow 'thread\text{-}id \Rightarrow ('addr,\ heap)\ jvm\text{-}state \Rightarrow ('addr,\ heap)\ jvm\text{-}state \Rightarrow bool$

where $\tauExec\text{-}1r\ P\ t == (\tauexec\text{-}1\ P\ t)^{**}$

abbreviation $\tauExec\text{-}1t :: 'addr\ jvm\text{-}prog \Rightarrow 'thread\text{-}id \Rightarrow ('addr,\ heap)\ jvm\text{-}state \Rightarrow ('addr,\ heap)\ jvm\text{-}state \Rightarrow bool$

where $\tauExec\text{-}1t\ P\ t == (\tauexec\text{-}1\ P\ t)^{++}$

definition $\tauexec\text{-}1\text{-}d :: 'addr\ jvm\text{-}prog \Rightarrow 'thread\text{-}id \Rightarrow ('addr,\ heap)\ jvm\text{-}state \Rightarrow ('addr,\ heap)\ jvm\text{-}state \Rightarrow bool$

where $\tauexec\text{-}1\text{-}d\ P\ t\ \sigma\ \sigma' \longleftrightarrow exec\text{-}1\ P\ t\ \sigma\ \varepsilon\ \sigma' \wedge \tauMove2\ P\ \sigma \wedge check\ P\ \sigma$

lemma $\tauexec\text{-}1\text{-}dI$ [intro]:

$\llbracket exec\text{-}1\ P\ t\ \sigma\ \varepsilon\ \sigma'; check\ P\ \sigma; \tauMove2\ P\ \sigma \rrbracket \Longrightarrow \tauexec\text{-}1\text{-}d\ P\ t\ \sigma\ \sigma'$
by(simp add: $\tauexec\text{-}1\text{-}d\text{-}def$)

lemma $\tauexec\text{-}1\text{-}dE$ [elim]:

assumes $\tauexec\text{-}1\text{-}d\ P\ t\ \sigma\ \sigma'$
obtains $exec\text{-}1\ P\ t\ \sigma\ \varepsilon\ \sigma' check\ P\ \sigma \tauMove2\ P\ \sigma$
using *assms* **by**(auto simp add: $\tauexec\text{-}1\text{-}d\text{-}def$)

abbreviation $\tauExec\text{-}1\text{-}dr :: 'addr\ jvm\text{-}prog \Rightarrow 'thread\text{-}id \Rightarrow ('addr,\ heap)\ jvm\text{-}state \Rightarrow ('addr,\ heap)\ jvm\text{-}state \Rightarrow bool$

where $\tauExec\text{-}1\text{-}dr\ P\ t == (\tauexec\text{-}1\text{-}d\ P\ t)^{**}$

abbreviation $\tauExec\text{-}1\text{-}dt :: 'addr\ jvm\text{-}prog \Rightarrow 'thread\text{-}id \Rightarrow ('addr,\ heap)\ jvm\text{-}state \Rightarrow ('addr,\ heap)\ jvm\text{-}state \Rightarrow bool$

where $\tauExec\text{-}1\text{-}dt\ P\ t == (\tauexec\text{-}1\text{-}d\ P\ t)^{++}$

declare $compxE2\text{-}size\text{-}conv[simp\ del]$ $compxEs2\text{-}size\text{-}conv[simp\ del]$

declare $compxE2\text{-}stack\text{-}xlift\text{-}conv[simp\ del]$ $compxEs2\text{-}stack\text{-}xlift\text{-}conv[simp\ del]$

lemma $exec\text{-}instr\text{-}frs\text{-}offer$:

$(ta,\ xcp',\ h',\ (stk',\ loc',\ C,\ M,\ pc') \# frs) \in exec\text{-}instr\ ins\ P\ t\ h\ stk\ loc\ C\ M\ pc\ frs$
 $\Longrightarrow (ta,\ xcp',\ h',\ (stk',\ loc',\ C,\ M,\ pc') \# frs @ frs') \in exec\text{-}instr\ ins\ P\ t\ h\ stk\ loc\ C\ M\ pc\ (frs @$

```

frs')
apply(cases ins)
apply(simp-all add: nth-append split-beta split: if-split-asm sum.split-asm)
apply(force split: extCallRet.split-asm simp add: extRet2JVM-def)+
done

```

lemma *check-instr-frs-offer*:

```

[[ check-instr ins P h stk loc C M pc frs; ins ≠ Return ]]
  ⇒ check-instr ins P h stk loc C M pc (frs @ frs')

```

by(cases ins)(simp-all split: if-split-asm)

lemma *exec-instr-CM-change*:

```

(ta, xcp', h', (stk', loc', C, M, pc') # frs) ∈ exec-instr ins P t h stk loc C M pc frs
  ⇒ (ta, xcp', h', (stk', loc', C', M', pc') # frs) ∈ exec-instr ins P t h stk loc C' M' pc frs

```

apply(cases ins)

apply(simp-all add: nth-append split-beta neq-Nil-conv split: if-split-asm sum.split-asm)

apply(force split: extCallRet.split-asm simp add: extRet2JVM-def)+

done

lemma *check-instr-CM-change*:

```

[[ check-instr ins P h stk loc C M pc frs; ins ≠ Return ]]
  ⇒ check-instr ins P h stk loc C' M' pc frs

```

by(cases ins)(simp-all split: if-split-asm)

lemma *exec-move-exec-1*:

assumes *exec*: *exec-move* ci P t body h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')

and *sees*: P ⊢ C sees M : Ts → T = [body] in D

shows *exec-1* (compP2 P) t (xcp, h, (stk, loc, C, M, pc) # frs) ta (xcp', h', (stk', loc', C, M, pc') # frs)

using *exec unfolding exec-move-def*

proof(cases)

case *exec-instr*

note [simp] = ⟨xcp = None⟩

and *exec* = ⟨(ta, xcp', h', [(stk', loc', undefined, undefined, pc')])

∈ *exec-instr* (compE2 body ! pc) (compP2 P) t h stk loc undefined undefined pc []⟩

from *exec* **have** (ta, xcp', h', [(stk', loc', C, M, pc')])

∈ *exec-instr* (compE2 body ! pc) (compP2 P) t h stk loc C M pc []

by(rule *exec-instr-CM-change*)

from *exec-instr-frs-offer*[OF this, of frs]

have (ta, xcp', h', (stk', loc', C, M, pc') # frs)

∈ *exec-instr* (compE2 body ! pc) (compP2 P) t h stk loc C M pc frs **by** *simp*

with *sees* ⟨pc < length (compE2 body)⟩ **show** ?thesis

by(*simp add: exec-1-iff compP2-def compMb2-def nth-append*)

next

case *exec-catch*

thus ?thesis **using** *sees-method-compP*[OF *sees*, of λC M Ts T. compMb2]

by(*simp add: exec-1-iff compMb2-def compP2-def*)

qed

lemma *τexec-move-τexec-1*:

assumes *exec*: τ*exec-move* ci P t body h (stk, loc, pc, xcp) (stk', loc', pc', xcp')

and *sees*: P ⊢ C sees M : Ts → T = [body] in D

shows τ*exec-1* (compP2 P) t (xcp, h, (stk, loc, C, M, pc) # frs) (xcp', h, (stk', loc', C, M, pc') # frs)

proof(rule $\tau exec-1I$)
from $exec$ **obtain** $exec'$: $exec-move\ ci\ P\ t\ body\ h\ (stk,\ loc,\ pc,\ xcp) \varepsilon\ h\ (stk',\ loc',\ pc',\ xcp')$
and τ : $\tau move2\ P\ h\ stk\ body\ pc\ xcp$ **by**(rule $\tau exec-moveE$)
have $exec-1\ (compP2\ P)\ t\ (xcp,\ h,\ (stk,\ loc,\ C,\ M,\ pc) \# frs) \varepsilon\ (xcp',\ h,\ (stk',\ loc',\ C,\ M,\ pc') \# frs)$
using $exec'$ **sees** **by**(rule $exec-move-exec-1$)
thus $compP2\ P,t \vdash (xcp,\ h,\ (stk,\ loc,\ C,\ M,\ pc) \# frs) \rightarrow \varepsilon-jvm \rightarrow (xcp',\ h,\ (stk',\ loc',\ C,\ M,\ pc') \# frs)$ **by** *auto*
{ **fix** a
assume [$simp$]: $xcp = [a]$
from $sees-method-compP[OF\ sees,\ of\ \lambda C\ M\ Ts\ T.\ compMb2]$
have $ex-table-of\ (compP2\ P)\ C\ M = compxE2\ body\ 0\ 0$ **by**($simp\ add:\ compP2-def\ compMb2-def$)
hence $match-ex-table\ (compP2\ P)\ (cname-of\ h\ a)\ pc\ (ex-table-of\ (compP2\ P)\ C\ M) \neq None\ pc < length\ (compE2\ body)$
using $exec'$ **sees** **by**($auto\ simp\ add:\ exec-move-def\ elim:\ exec-meth.cases$) }
with $\tau\ sees\ sees-method-compP[OF\ sees,\ of\ \lambda C\ M\ Ts\ T.\ compMb2]$
show $\tau Move2\ (compP2\ P)\ (xcp,\ h,\ (stk,\ loc,\ C,\ M,\ pc) \# frs)$
unfolding $\tau Move2-compP2[OF\ sees]$ **by**($fastforce\ simp\ add:\ compP2-def\ compMb2-def$)
qed

lemma $\tau Exec-mover-\tau Exec-1r$:

assumes $move:\ \tau Exec-mover\ ci\ P\ t\ body\ h\ (stk,\ loc,\ pc,\ xcp)\ (stk',\ loc',\ pc',\ xcp')$
and $sees:\ P \vdash C\ sees\ M : Ts \rightarrow T = [body]$ *in* D
shows $\tau Exec-1r\ (compP2\ P)\ t\ (xcp,\ h,\ (stk,\ loc,\ C,\ M,\ pc) \# frs')\ (xcp',\ h,\ (stk',\ loc',\ C,\ M,\ pc') \# frs')$
using $move$
by($induct\ rule:\ \tau Execr-induct$)($blast\ intro:\ rtranclp.rtrancl-into-rtrancl\ \tau exec-move-\tau exec-1[OF - sees]$)+

lemma $\tau Exec-movet-\tau Exec-1t$:

assumes $move:\ \tau Exec-movet\ ci\ P\ t\ body\ h\ (stk,\ loc,\ pc,\ xcp)\ (stk',\ loc',\ pc',\ xcp')$
and $sees:\ P \vdash C\ sees\ M : Ts \rightarrow T = [body]$ *in* D
shows $\tau Exec-1t\ (compP2\ P)\ t\ (xcp,\ h,\ (stk,\ loc,\ C,\ M,\ pc) \# frs')\ (xcp',\ h,\ (stk',\ loc',\ C,\ M,\ pc') \# frs')$
using $move$
by($induct\ rule:\ \tau Exec-1t-induct$)($blast\ intro:\ tranclp.trancl-into-trancl\ \tau exec-move-\tau exec-1[OF - sees]$)+

lemma $\tau Exec-1r-rtranclpD$:

$\tau Exec-1r\ P\ t\ (xcp,\ h,\ frs)\ (xcp',\ h',\ frs')$
 $\implies (\lambda((xcp,\ frs), h)\ ((xcp',\ frs'), h')).\ exec-1\ P\ t\ (xcp,\ h,\ frs) \varepsilon\ (xcp',\ h',\ frs') \wedge \tau Move2\ P\ (xcp,\ h,\ frs) \hat{~}^{**}\ ((xcp,\ frs), h)\ ((xcp',\ frs'), h')$
by($induct\ rule:\ rtranclp-induct3$)($fastforce\ intro:\ rtranclp.rtrancl-into-rtrancl$)+

lemma $\tau Exec-1t-rtranclpD$:

$\tau Exec-1t\ P\ t\ (xcp,\ h,\ frs)\ (xcp',\ h',\ frs')$
 $\implies (\lambda((xcp,\ frs), h)\ ((xcp',\ frs'), h')).\ exec-1\ P\ t\ (xcp,\ h,\ frs) \varepsilon\ (xcp',\ h',\ frs') \wedge \tau Move2\ P\ (xcp,\ h,\ frs) \hat{~}^{++}\ ((xcp,\ frs), h)\ ((xcp',\ frs'), h')$
by($induct\ rule:\ tranclp-induct3$)($fastforce\ intro:\ tranclp.trancl-into-trancl$)+

lemma $exec-meth-length-compE2-stack-xliftD$:

$exec-meth\ ci\ P\ (compE2\ e)\ (stack-xlift\ d\ (compxE2\ e\ 0\ 0))\ t\ h\ (stk,\ loc,\ pc,\ xcp)\ ta\ h'\ s'$
 $\implies pc < length\ (compE2\ e)$
by($cases\ s'$)($auto\ simp\ add:\ stack-xlift-compxE2$)

lemma $exec-meth-length-pc-xt-Nil$:

$exec\text{-}meth\ ci\ P\ ins\ []\ t\ h\ (stk,\ loc,\ pc,\ xcp)\ ta\ h'\ s' \implies pc < length\ ins$
apply(erule $exec\text{-}meth.cases$)
apply(auto dest: $match\text{-}ex\text{-}table\text{-}pc\text{-}length\text{-}compE2$)
done

lemma *BinOp-exec2D*:

assumes $exec: exec\text{-}meth\ ci\ (compP2\ P)\ (compE2\ (e1\ \llbracket bop \rrbracket\ e2))\ (compxE2\ (e1\ \llbracket bop \rrbracket\ e2)\ 0\ 0)\ t\ h$
 $(stk\ @\ [v1],\ loc,\ length\ (compE2\ e1) + pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$

and $pc: pc < length\ (compE2\ e2)$

shows $exec\text{-}meth\ ci\ (compP2\ P)\ (compE2\ e2)\ (stack\text{-}xlift\ (length\ [v1])\ (compxE2\ e2\ 0\ 0))\ t\ h$
 $(stk\ @\ [v1],\ loc,\ pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc' - length\ (compE2\ e1),\ xcp') \wedge pc' \geq length\ (compE2\ e1)$

proof

from $exec$ **have** $exec\text{-}meth\ ci\ (compP2\ P)\ ((compE2\ e1\ @\ compE2\ e2)\ @\ [BinOpInstr\ bop])$
 $(compxE2\ e1\ 0\ 0\ @\ shift\ (length\ (compE2\ e1))\ (stack\text{-}xlift\ (length\ [v1])\ (compxE2\ e2\ 0\ 0)))\ t\ h$
 $(stk\ @\ [v1],\ loc,\ length\ (compE2\ e1) + pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$

by(simp add: $compxE2\text{-}size\text{-}conv\ compxE2\text{-}stack\text{-}xlift\text{-}conv$)

hence $exec': exec\text{-}meth\ ci\ (compP2\ P)\ (compE2\ e1\ @\ compE2\ e2)\ (compxE2\ e1\ 0\ 0\ @\ shift\ (length$
 $(compE2\ e1))\ (stack\text{-}xlift\ (length\ [v1])\ (compxE2\ e2\ 0\ 0)))\ t\ h$

$(stk\ @\ [v1],\ loc,\ length\ (compE2\ e1) + pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$

by(rule $exec\text{-}meth\text{-}take$) (simp add: pc)

thus $exec\text{-}meth\ ci\ (compP2\ P)\ (compE2\ e2)\ (stack\text{-}xlift\ (length\ [v1])\ (compxE2\ e2\ 0\ 0))\ t\ h$
 $(stk\ @\ [v1],\ loc,\ pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc' - length\ (compE2\ e1),\ xcp')$

by(rule $exec\text{-}meth\text{-}drop\text{-}xt$) auto

from $exec'$ **show** $pc' \geq length\ (compE2\ e1)$

by(rule $exec\text{-}meth\text{-}drop\text{-}xt\text{-}pc$)(auto)

qed

lemma *Call-execParamD*:

assumes $exec: exec\text{-}meth\ ci\ (compP2\ P)\ (compE2\ (obj.M'(ps)))\ (compxE2\ (obj.M'(ps))\ 0\ 0)\ t\ h$
 $(stk\ @\ [v],\ loc,\ length\ (compE2\ obj) + pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$

and $pc: pc < length\ (compEs2\ ps)$

shows $exec\text{-}meth\ ci\ (compP2\ P)\ (compEs2\ ps)\ (stack\text{-}xlift\ (length\ [v])\ (compxEs2\ ps\ 0\ 0))\ t\ h$
 $(stk\ @\ [v],\ loc,\ pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc' - length\ (compE2\ obj),\ xcp') \wedge pc' \geq length\ (compE2\ obj)$

proof

from $exec$ **have** $exec\text{-}meth\ ci\ (compP2\ P)\ ((compE2\ obj\ @\ compEs2\ ps)\ @\ [Invoke\ M'\ (length\ ps)])$
 $(compxE2\ obj\ 0\ 0\ @\ shift\ (length\ (compE2\ obj))\ (stack\text{-}xlift\ (length\ [v])\ (compxEs2\ ps\ 0\ 0)))\ t\ h$
 $(stk\ @\ [v],\ loc,\ length\ (compE2\ obj) + pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$

by(simp add: $compxEs2\text{-}size\text{-}conv\ compxEs2\text{-}stack\text{-}xlift\text{-}conv$)

hence $exec': exec\text{-}meth\ ci\ (compP2\ P)\ (compE2\ obj\ @\ compEs2\ ps)\ (compxE2\ obj\ 0\ 0\ @\ shift\ (length$
 $(compE2\ obj))\ (stack\text{-}xlift\ (length\ [v])\ (compxEs2\ ps\ 0\ 0)))\ t\ h$

$(stk\ @\ [v],\ loc,\ length\ (compE2\ obj) + pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$

by(rule $exec\text{-}meth\text{-}take$)(simp add: pc)

thus $exec\text{-}meth\ ci\ (compP2\ P)\ (compEs2\ ps)\ (stack\text{-}xlift\ (length\ [v])\ (compxEs2\ ps\ 0\ 0))\ t\ h$
 $(stk\ @\ [v],\ loc,\ pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc' - length\ (compE2\ obj),\ xcp')$

by(rule $exec\text{-}meth\text{-}drop\text{-}xt$) auto

from $exec'$ **show** $pc' \geq length\ (compE2\ obj)$

by(rule $exec\text{-}meth\text{-}drop\text{-}xt\text{-}pc$)(auto)

qed

lemma *exec-move-length-compE2D* [$dest$]:

$exec\text{-}move\ ci\ P\ t\ e\ h\ (stk,\ loc,\ pc,\ xcp)\ ta\ h'\ s' \implies pc < length\ (compE2\ e)$
by(cases s')(auto simp add: $exec\text{-}move\text{-}def$)

lemma *exec-moves-length-compEs2D* [$dest$]:

$exec\text{-moves } ci P t es h (stk, loc, pc, xcp) ta h' s' \implies pc < length (compEs2 es)$
by(cases s')(auto simp add: exec-moves-def)

lemma *exec-meth-ci-appD*:

$\llbracket exec\text{-meth } ci P ins xt t h (stk, loc, pc, None) ta h' fr' \rrbracket$
 $\implies ci\text{-app } ci (ins ! pc) P h stk loc undefined undefined pc \llbracket$
by(cases fr')(simp add: exec-meth-instr)

lemma *exec-move-ci-appD*:

$exec\text{-move } ci P t E h (stk, loc, pc, None) ta h' fr'$
 $\implies ci\text{-app } ci (compE2 E ! pc) (compP2 P) h stk loc undefined undefined pc \llbracket$
unfolding *exec-move-def* **by**(rule *exec-meth-ci-appD*)

lemma *exec-moves-ci-appD*:

$exec\text{-moves } ci P t Es h (stk, loc, pc, None) ta h' fr'$
 $\implies ci\text{-app } ci (compEs2 Es ! pc) (compP2 P) h stk loc undefined undefined pc \llbracket$
unfolding *exec-moves-def* **by**(rule *exec-meth-ci-appD*)

lemma τ *instr-stk-append-check*:

$check\text{-instr}' i P h stk loc C M pc frs \implies \tau instr P h (stk @ vs) i = \tau instr P h stk i$
by(cases i)(simp-all add: nth-append)

lemma τ *instr-stk-drop-exec-move*:

$exec\text{-move } ci P t e h (stk, loc, pc, None) ta h' fr'$
 $\implies \tau instr (compP2 P) h (stk @ vs) (compE2 e ! pc) = \tau instr (compP2 P) h stk (compE2 e ! pc)$
apply(drule *exec-move-ci-appD*)
apply(drule *wf-ciD2-ci-app*)
apply(erule τ *instr-stk-append-check*)
done

lemma τ *instr-stk-drop-exec-moves*:

$exec\text{-moves } ci P t es h (stk, loc, pc, None) ta h' fr'$
 $\implies \tau instr (compP2 P) h (stk @ vs) (compEs2 es ! pc) = \tau instr (compP2 P) h stk (compEs2 es !$
 $pc)$
apply(drule *exec-moves-ci-appD*)
apply(drule *wf-ciD2-ci-app*)
apply(erule τ *instr-stk-append-check*)
done

end

end

7.16 The delay bisimulation between intermediate language and JVM

theory *J1JVMbisim* **imports**

Execs
../BV/BVNoTypeError
J1

begin

declare *Listn.lesub-list-impl-same-size*[simp del]

lemma (in *JVM-heap-conf-base'*) $\tau exec-1$ - $\tau exec-1-d$:

$\llbracket wf-jvm-prog_{\Phi} P; \tau exec-1 P t \sigma \sigma'; \Phi \mid- t:\sigma [ok] \rrbracket \implies \tau exec-1-d P t \sigma \sigma'$
by(*auto simp add: $\tau exec-1-def$ $\tau exec-1-d-def$ welltyped-commute[symmetric] elim: jvmd-NormalE*)

context *JVM-conf-read* **begin**

lemma $\tau Exec-1r$ -preserves-correct-state:

assumes $wf: wf-jvm-prog_{\Phi} P$
and $exec: \tau Exec-1r P t \sigma \sigma'$
shows $\Phi \mid- t:\sigma [ok] \implies \Phi \mid- t:\sigma' [ok]$

using $exec$

by(*induct*)(*blast intro: BV-correct-1[OF wf]*)+

lemma $\tau Exec-1t$ -preserves-correct-state:

assumes $wf: wf-jvm-prog_{\Phi} P$
and $exec: \tau Exec-1t P t \sigma \sigma'$
shows $\Phi \mid- t:\sigma [ok] \implies \Phi \mid- t:\sigma' [ok]$

using $exec$

by(*induct*)(*blast intro: BV-correct-1[OF wf]*)+

lemma $\tau Exec-1r$ - $\tau Exec-1-dr$:

assumes $wf: wf-jvm-prog_{\Phi} P$
shows $\llbracket \tau Exec-1r P t \sigma \sigma'; \Phi \mid- t:\sigma [ok] \rrbracket \implies \tau Exec-1-dr P t \sigma \sigma'$

apply(*induct rule: rtranclp-induct*)

apply(*blast intro: rtranclp.rtrancl-into-rtrancl $\tau exec-1$ - $\tau exec-1-d$ [OF wf] $\tau Exec-1r$ -preserves-correct-state[OF wf]*)+

done

lemma $\tau Exec-1t$ - $\tau Exec-1-dt$:

assumes $wf: wf-jvm-prog_{\Phi} P$
shows $\llbracket \tau Exec-1t P t \sigma \sigma'; \Phi \mid- t:\sigma [ok] \rrbracket \implies \tau Exec-1-dt P t \sigma \sigma'$

apply(*induct rule: tranclp-induct*)

apply(*blast intro: tranclp.trancl-into-trancl $\tau exec-1$ - $\tau exec-1-d$ [OF wf] $\tau Exec-1t$ -preserves-correct-state[OF wf]*)+

done

lemma $\tau Exec-1-dr$ -preserves-correct-state:

assumes $wf: wf-jvm-prog_{\Phi} P$
and $exec: \tau Exec-1-dr P t \sigma \sigma'$
shows $\Phi \mid- t:\sigma [ok] \implies \Phi \mid- t:\sigma' [ok]$

using $exec$

by(*induct*)(*blast intro: BV-correct-1[OF wf]*)+

lemma $\tau Exec-1-dt$ -preserves-correct-state:

assumes $wf: wf-jvm-prog_{\Phi} P$
and $exec: \tau Exec-1-dt P t \sigma \sigma'$
shows $\Phi \mid- t:\sigma [ok] \implies \Phi \mid- t:\sigma' [ok]$

using $exec$

by(*induct*)(*blast intro: BV-correct-1[OF wf]*)+

end

locale *J1-JVM-heap-base* =

```

J1-heap-base +
JVM-heap-base +
constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
begin

inductive bisim1 ::
'm prog ⇒ 'heap ⇒ 'addr expr1 ⇒ ('addr expr1 × 'addr locals1)
⇒ ('addr val list × 'addr val list × pc × 'addr option) ⇒ bool

and bisims1 ::
'm prog ⇒ 'heap ⇒ 'addr expr1 list ⇒ ('addr expr1 list × 'addr locals1)
⇒ ('addr val list × 'addr val list × pc × 'addr option) ⇒ bool

and bisim1-syntax ::
'm prog ⇒ 'addr expr1 ⇒ 'heap ⇒ ('addr expr1 × 'addr locals1)
⇒ ('addr val list × 'addr val list × pc × 'addr option) ⇒ bool
(⟦-, -, ⊢ - ↔ -> [50, 0, 0, 0, 50] 100)

and bisims1-syntax ::
'm prog ⇒ 'addr expr1 list ⇒ 'heap ⇒ ('addr expr1 list × 'addr locals1)
⇒ ('addr val list × 'addr val list × pc × 'addr option) ⇒ bool
(⟦-, -, ⊢ - [↔] -> [50, 0, 0, 0, 50] 100)
for P :: 'm prog and h :: 'heap
where
P, e, h ⊢ exs ↔ s ≡ bisim1 P h e exs s
| P, es, h ⊢ esxs [↔] s ≡ bisims1 P h es esxs s

| bisim1Val2:
pc = length (compE2 e) ⇒ P, e, h ⊢ (Val v, xs) ↔ (v # [], xs, pc, None)

| bisim1New:
P, new C, h ⊢ (new C, xs) ↔ ([], xs, 0, None)

| bisim1NewThrow:
P, new C, h ⊢ (THROW OutOfMemory, xs) ↔ ([], xs, 0, [addr-of-sys-xcpt OutOfMemory])

| bisim1NewArray:
P, e, h ⊢ (e', xs) ↔ (stk, loc, pc, xcp) ⇒ P, newA T[e], h ⊢ (newA T[e'], xs) ↔ (stk, loc, pc, xcp)

| bisim1NewArrayThrow:
P, e, h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a]) ⇒ P, newA T[e], h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])

| bisim1NewArrayFail:
P, newA T[e], h ⊢ (Throw a, xs) ↔ ([v], xs, length (compE2 e), [a])

```

- | *bisim1Cast*:
 $P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \implies P, \text{Cast } T e, h \vdash (\text{Cast } T e', xs) \leftrightarrow (stk, loc, pc, xcp)$
- | *bisim1CastThrow*:
 $P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \implies P, \text{Cast } T e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a])$
- | *bisim1CastFail*:
 $P, \text{Cast } T e, h \vdash (\text{THROW } \text{ClassCast}, xs) \leftrightarrow ([v], xs, \text{length } (\text{compE2 } e), [\text{addr-of-sys-xcpt } \text{ClassCast}])$
- | *bisim1InstanceOf*:
 $P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \implies P, e \text{ instanceof } T, h \vdash (e' \text{ instanceof } T, xs) \leftrightarrow (stk, loc, pc, xcp)$
- | *bisim1InstanceOfThrow*:
 $P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \implies P, e \text{ instanceof } T, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a])$
- | *bisim1Val*: $P, \text{Val } v, h \vdash (\text{Val } v, xs) \leftrightarrow ([], xs, 0, \text{None})$
- | *bisim1Var*: $P, \text{Var } V, h \vdash (\text{Var } V, xs) \leftrightarrow ([], xs, 0, \text{None})$
- | *bisim1BinOp1*:
 $P, e1, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \implies P, e1 \llbracket \text{bop} \rrbracket e2, h \vdash (e' \llbracket \text{bop} \rrbracket e2, xs) \leftrightarrow (stk, loc, pc, xcp)$
- | *bisim1BinOp2*:
 $P, e2, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \implies P, e1 \llbracket \text{bop} \rrbracket e2, h \vdash (\text{Val } v1 \llbracket \text{bop} \rrbracket e', xs) \leftrightarrow (stk @ [v1], loc, \text{length } (\text{compE2 } e1) + pc, xcp)$
- | *bisim1BinOpThrow1*:
 $P, e1, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \implies P, e1 \llbracket \text{bop} \rrbracket e2, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a])$
- | *bisim1BinOpThrow2*:
 $P, e2, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \implies P, e1 \llbracket \text{bop} \rrbracket e2, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk @ [v1], loc, \text{length } (\text{compE2 } e1) + pc, [a])$
- | *bisim1BinOpThrow*:
 $P, e1 \llbracket \text{bop} \rrbracket e2, h \vdash (\text{Throw } a, xs) \leftrightarrow ([v1, v2], xs, \text{length } (\text{compE2 } e1) + \text{length } (\text{compE2 } e2), [a])$
- | *bisim1LAss1*:
 $P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \implies P, V := e, h \vdash (V := e', xs) \leftrightarrow (stk, loc, pc, xcp)$
- | *bisim1LAss2*:
 $P, V := e, h \vdash (\text{unit}, xs) \leftrightarrow ([], xs, \text{Suc } (\text{length } (\text{compE2 } e)), \text{None})$
- | *bisim1LAssThrow*:
 $P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \implies P, V := e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a])$

- | *bisim1AAcc1*:
 $P, a, h \vdash (a', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, a[i], h \vdash (a'[i], xs) \leftrightarrow (stk, loc, pc, xcp)$
- | *bisim1AAcc2*:
 $P, i, h \vdash (i', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, a[i], h \vdash (Val\ v[i'], xs) \leftrightarrow (stk\ @\ [v], loc, length\ (compE2\ a) + pc, xcp)$
- | *bisim1AAccThrow1*:
 $P, a, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$
 $\implies P, a[i], h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$
- | *bisim1AAccThrow2*:
 $P, i, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$
 $\implies P, a[i], h \vdash (Throw\ ad, xs) \leftrightarrow (stk\ @\ [v], loc, length\ (compE2\ a) + pc, [ad])$
- | *bisim1AAccFail*:
 $P, a[i], h \vdash (Throw\ ad, xs) \leftrightarrow ([v, v'], xs, length\ (compE2\ a) + length\ (compE2\ i), [ad])$
- | *bisim1AAss1*:
 $P, a, h \vdash (a', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, a[i] := e, h \vdash (a'[i] := e, xs) \leftrightarrow (stk, loc, pc, xcp)$
- | *bisim1AAss2*:
 $P, i, h \vdash (i', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, a[i] := e, h \vdash (Val\ v[i'] := e, xs) \leftrightarrow (stk\ @\ [v], loc, length\ (compE2\ a) + pc, xcp)$
- | *bisim1AAss3*:
 $P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, a[i] := e, h \vdash (Val\ v[Val\ v'] := e', xs) \leftrightarrow (stk\ @\ [v', v], loc, length\ (compE2\ a) + length\ (compE2\ i) + pc, xcp)$
- | *bisim1AAssThrow1*:
 $P, a, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$
 $\implies P, a[i] := e, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$
- | *bisim1AAssThrow2*:
 $P, i, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$
 $\implies P, a[i] := e, h \vdash (Throw\ ad, xs) \leftrightarrow (stk\ @\ [v], loc, length\ (compE2\ a) + pc, [ad])$
- | *bisim1AAssThrow3*:
 $P, e, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$
 $\implies P, a[i] := e, h \vdash (Throw\ ad, xs) \leftrightarrow (stk\ @\ [v', v], loc, length\ (compE2\ a) + length\ (compE2\ i) + pc, [ad])$
- | *bisim1AAssFail*:
 $P, a[i] := e, h \vdash (Throw\ ad, xs) \leftrightarrow ([v', v, v''], xs, length\ (compE2\ a) + length\ (compE2\ i) + length\ (compE2\ e), [ad])$
- | *bisim1AAss4*:
 $P, a[i] := e, h \vdash (unit, xs) \leftrightarrow ([], xs, Suc\ (length\ (compE2\ a) + length\ (compE2\ i) + length\ (compE2\ e)), None)$

| *bisim1ALength*:

$$P, a, h \vdash (a', xs) \leftrightarrow (stk, loc, pc, xcp) \\ \implies P, a \cdot length, h \vdash (a' \cdot length, xs) \leftrightarrow (stk, loc, pc, xcp)$$

| *bisim1ALengthThrow*:

$$P, a, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor) \\ \implies P, a \cdot length, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor)$$

| *bisim1ALengthNull*:

$$P, a \cdot length, h \vdash (THROW\ NullPointer, xs) \leftrightarrow ([Null], xs, length\ (compE2\ a), \lfloor addr-of-sys-xcpt\ NullPointer \rfloor)$$

| *bisim1FAcc*:

$$P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \\ \implies P, e \cdot F\{D\}, h \vdash (e' \cdot F\{D\}, xs) \leftrightarrow (stk, loc, pc, xcp)$$

| *bisim1FAccThrow*:

$$P, e, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor) \\ \implies P, e \cdot F\{D\}, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor)$$

| *bisim1FAccNull*:

$$P, e \cdot F\{D\}, h \vdash (THROW\ NullPointer, xs) \leftrightarrow ([Null], xs, length\ (compE2\ e), \lfloor addr-of-sys-xcpt\ NullPointer \rfloor)$$

| *bisim1FAss1*:

$$P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \\ \implies P, e \cdot F\{D\} := e2, h \vdash (e' \cdot F\{D\} := e2, xs) \leftrightarrow (stk, loc, pc, xcp)$$

| *bisim1FAss2*:

$$P, e2, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \\ \implies P, e \cdot F\{D\} := e2, h \vdash (Val\ v \cdot F\{D\} := e', xs) \leftrightarrow (stk\ @\ [v], loc, length\ (compE2\ e) + pc, xcp)$$

| *bisim1FAssThrow1*:

$$P, e, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor) \\ \implies P, e \cdot F\{D\} := e2, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor)$$

| *bisim1FAssThrow2*:

$$P, e2, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor) \\ \implies P, e \cdot F\{D\} := e2, h \vdash (Throw\ ad, xs) \leftrightarrow (stk\ @\ [v], loc, length\ (compE2\ e) + pc, \lfloor ad \rfloor)$$

| *bisim1FAssNull*:

$$P, e \cdot F\{D\} := e2, h \vdash (THROW\ NullPointer, xs) \leftrightarrow ([v, Null], xs, length\ (compE2\ e) + length\ (compE2\ e2), \lfloor addr-of-sys-xcpt\ NullPointer \rfloor)$$

| *bisim1FAss3*:

$$P, e \cdot F\{D\} := e2, h \vdash (unit, xs) \leftrightarrow ([], xs, Suc\ (length\ (compE2\ e) + length\ (compE2\ e2)), None)$$

| *bisim1CAS1*:

$P, e1, h \vdash (e1', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h \vdash (e1' \cdot compareAndSwap(D \cdot F, e2, e3), xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1CAS2*:

$P, e2, h \vdash (e2', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h \vdash (Val\ v \cdot compareAndSwap(D \cdot F, e2', e3), xs) \leftrightarrow (stk\ @\ [v], loc, length\ (compE2\ e1) + pc, xcp)$

| *bisim1CAS3*:

$P, e3, h \vdash (e3', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h \vdash (Val\ v \cdot compareAndSwap(D \cdot F, Val\ v', e3'), xs) \leftrightarrow (stk\ @\ [v', v], loc, length\ (compE2\ e1) + length\ (compE2\ e2) + pc, xcp)$

| *bisim1CASThrow1*:

$P, e1, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$
 $\implies P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$

| *bisim1CASThrow2*:

$P, e2, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$
 $\implies P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h \vdash (Throw\ ad, xs) \leftrightarrow (stk\ @\ [v], loc, length\ (compE2\ e1) + pc, [ad])$

| *bisim1CASThrow3*:

$P, e3, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$
 $\implies P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h \vdash (Throw\ ad, xs) \leftrightarrow (stk\ @\ [v', v], loc, length\ (compE2\ e1) + length\ (compE2\ e2) + pc, [ad])$

| *bisim1CASFail*:

$P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h \vdash (Throw\ ad, xs) \leftrightarrow ([v', v, v'], xs, length\ (compE2\ e1) + length\ (compE2\ e2) + length\ (compE2\ e3), [ad])$

| *bisim1Call1*:

$P, obj, h \vdash (obj', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, obj \cdot M(ps), h \vdash (obj' \cdot M(ps), xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1CallParams*:

$P, ps, h \vdash (ps', xs) [\leftrightarrow] (stk, loc, pc, xcp)$
 $\implies P, obj \cdot M(ps), h \vdash (Val\ v \cdot M(ps'), xs) \leftrightarrow (stk\ @\ [v], loc, length\ (compE2\ obj) + pc, xcp)$

| *bisim1CallThrowObj*:

$P, obj, h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a])$
 $\implies P, obj \cdot M(ps), h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a])$

| *bisim1CallThrowParams*:

$P, ps, h \vdash (map\ Val\ vs\ @\ Throw\ a\ \# ps', xs) [\leftrightarrow] (stk, loc, pc, [a])$
 $\implies P, obj \cdot M(ps), h \vdash (Throw\ a, xs) \leftrightarrow (stk\ @\ [v], loc, length\ (compE2\ obj) + pc, [a])$

| *bisim1CallThrow*:

$length\ ps = length\ vs$
 $\implies P, obj \cdot M(ps), h \vdash (Throw\ a, xs) \leftrightarrow (vs\ @\ [v], xs, length\ (compE2\ obj) + length\ (compEs2\ ps), [a])$

- | *bisim1BlockSome1*:
 $P, \{V:T=[v]; e\}, h \vdash (\{V:T=[v]; e\}, xs) \leftrightarrow ([], xs, 0, None)$
- | *bisim1BlockSome2*:
 $P, \{V:T=[v]; e\}, h \vdash (\{V:T=[v]; e\}, xs) \leftrightarrow ([v], xs, Suc\ 0, None)$
- | *bisim1BlockSome4*:
 $P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, \{V:T=[v]; e\}, h \vdash (\{V:T=None; e'\}, xs) \leftrightarrow (stk, loc, Suc\ (Suc\ pc), xcp)$
- | *bisim1BlockThrowSome*:
 $P, e, h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a])$
 $\implies P, \{V:T=[v]; e\}, h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, Suc\ (Suc\ pc), [a])$
- | *bisim1BlockNone*:
 $P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, \{V:T=None; e\}, h \vdash (\{V:T=None; e'\}, xs) \leftrightarrow (stk, loc, pc, xcp)$
- | *bisim1BlockThrowNone*:
 $P, e, h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a])$
 $\implies P, \{V:T=None; e\}, h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a])$
- | *bisim1Sync1*:
 $P, e1, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, sync_V\ (e1)\ e2, h \vdash (sync_V\ (e')\ e2, xs) \leftrightarrow (stk, loc, pc, xcp)$
- | *bisim1Sync2*:
 $P, sync_V\ (e1)\ e2, h \vdash (sync_V\ (Val\ v)\ e2, xs) \leftrightarrow ([v, v], xs, Suc\ (length\ (compE2\ e1)), None)$
- | *bisim1Sync3*:
 $P, sync_V\ (e1)\ e2, h \vdash (sync_V\ (Val\ v)\ e2, xs) \leftrightarrow ([v], xs[V := v], Suc\ (Suc\ (length\ (compE2\ e1))), None)$
- | *bisim1Sync4*:
 $P, e2, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, sync_V\ (e1)\ e2, h \vdash (insync_V\ (a)\ e', xs) \leftrightarrow (stk, loc, Suc\ (Suc\ (Suc\ (length\ (compE2\ e1) + pc))), xcp)$
- | *bisim1Sync5*:
 $P, sync_V\ (e1)\ e2, h \vdash (insync_V\ (a)\ Val\ v, xs) \leftrightarrow ([xs ! V, v], xs, 4 + length\ (compE2\ e1) + length\ (compE2\ e2), None)$
- | *bisim1Sync6*:
 $P, sync_V\ (e1)\ e2, h \vdash (Val\ v, xs) \leftrightarrow ([v], xs, 5 + length\ (compE2\ e1) + length\ (compE2\ e2), None)$
- | *bisim1Sync7*:
 $P, sync_V\ (e1)\ e2, h \vdash (insync_V\ (a)\ Throw\ a', xs) \leftrightarrow ([Addr\ a'], xs, 6 + length\ (compE2\ e1) + length\ (compE2\ e2), None)$
- | *bisim1Sync8*:
 $P, sync_V\ (e1)\ e2, h \vdash (insync_V\ (a)\ Throw\ a', xs) \leftrightarrow ([xs ! V, Addr\ a'], xs, 7 + length\ (compE2\ e1) + length\ (compE2\ e2), None)$

| *bisim1Sync9*:
 $P, \text{sync}_V(e1) e2, h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], xs, 8 + \text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2), \text{None})$

| *bisim1Sync10*:
 $P, \text{sync}_V(e1) e2, h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], xs, 8 + \text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2), [a])$

| *bisim1Sync11*:
 $P, \text{sync}_V(e1) e2, h \vdash (\text{THROW } \text{NullPointer}, xs) \leftrightarrow ([\text{Null}], xs, \text{Suc}(\text{Suc}(\text{length}(\text{compE2 } e1))), [\text{addr-of-sys-xcpt } \text{NullPointer}])$

| *bisim1Sync12*:
 $P, \text{sync}_V(e1) e2, h \vdash (\text{Throw } a, xs) \leftrightarrow ([v, v'], xs, 4 + \text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2), [a])$

| *bisim1Sync14*:
 $P, \text{sync}_V(e1) e2, h \vdash (\text{Throw } a, xs) \leftrightarrow ([v, \text{Addr } a'], xs, 7 + \text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2), [a])$

| *bisim1SyncThrow*:
 $P, e1, h \vdash (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a])$
 $\implies P, \text{sync}_V(e1) e2, h \vdash (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a])$

| *bisim1InSync*: — This rule only exists such that $P, e, h \vdash (e, xs) \leftrightarrow ([], xs, 0, \text{None})$ holds for all e
 $P, \text{insync}_V(a) e, h \vdash (\text{insync}_V(a) e, xs) \leftrightarrow ([], xs, 0, \text{None})$

| *bisim1Seq1*:
 $P, e1, h \vdash (e', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \implies P, e1;;e2, h \vdash (e';;e2, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp})$

| *bisim1SeqThrow1*:
 $P, e1, h \vdash (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a]) \implies P, e1;;e2, h \vdash (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a])$

| *bisim1Seq2*:
 $P, e2, h \vdash \text{exs} \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp})$
 $\implies P, e1;;e2, h \vdash \text{exs} \leftrightarrow (\text{stk}, \text{loc}, \text{Suc}(\text{length}(\text{compE2 } e1) + \text{pc}), \text{xcp})$

| *bisim1Cond1*:
 $P, e, h \vdash (e', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp})$
 $\implies P, \text{if}(e) e1 \text{ else } e2, h \vdash (\text{if}(e') e1 \text{ else } e2, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp})$

| *bisim1CondThen*:
 $P, e1, h \vdash \text{exs} \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp})$
 $\implies P, \text{if}(e) e1 \text{ else } e2, h \vdash \text{exs} \leftrightarrow (\text{stk}, \text{loc}, \text{Suc}(\text{length}(\text{compE2 } e) + \text{pc}), \text{xcp})$

| *bisim1CondElse*:
 $P, e2, h \vdash \text{exs} \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp})$
 $\implies P, \text{if}(e) e1 \text{ else } e2, h \vdash \text{exs} \leftrightarrow (\text{stk}, \text{loc}, \text{Suc}(\text{Suc}(\text{length}(\text{compE2 } e) + \text{length}(\text{compE2 } e1) + \text{pc})), \text{xcp})$

| *bisim1CondThrow*:

$$P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$$

$$\implies P, \text{if } (e) e1 \text{ else } e2, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$$

| *bisim1While1*:

$$P, \text{while } (c) e, h \vdash (\text{while } (c) e, xs) \leftrightarrow ([], xs, 0, \text{None})$$

| *bisim1While3*:

$$P, c, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp)$$

$$\implies P, \text{while } (c) e, h \vdash (\text{if } (e') (e;; \text{while } (c) e) \text{ else unit}, xs) \leftrightarrow (stk, loc, pc, xcp)$$

| *bisim1While4*:

$$P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp)$$

$$\implies P, \text{while } (c) e, h \vdash (e';; \text{while } (c) e, xs) \leftrightarrow (stk, loc, \text{Suc } (\text{length } (\text{compE2 } c) + pc), xcp)$$

| *bisim1While6*:

$$P, \text{while } (c) e, h \vdash (\text{while } (c) e, xs) \leftrightarrow ([], xs, \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } c) + \text{length } (\text{compE2 } e))), \text{None})$$

| *bisim1While7*:

$$P, \text{while } (c) e, h \vdash (\text{unit}, xs) \leftrightarrow ([], xs, \text{Suc } (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } c) + \text{length } (\text{compE2 } e))))), \text{None})$$

| *bisim1WhileThrow1*:

$$P, c, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$$

$$\implies P, \text{while } (c) e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$$

| *bisim1WhileThrow2*:

$$P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$$

$$\implies P, \text{while } (c) e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, \text{Suc } (\text{length } (\text{compE2 } c) + pc), \lfloor a \rfloor)$$

| *bisim1Throw1*:

$$P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \implies P, \text{throw } e, h \vdash (\text{throw } e', xs) \leftrightarrow (stk, loc, pc, xcp)$$

| *bisim1Throw2*:

$$P, \text{throw } e, h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], xs, \text{length } (\text{compE2 } e), \lfloor a \rfloor)$$

| *bisim1ThrowNull*:

$$P, \text{throw } e, h \vdash (\text{THROW } \text{NullPointer}, xs) \leftrightarrow ([\text{Null}], xs, \text{length } (\text{compE2 } e), \lfloor \text{addr-of-sys-xcpt NullPointer} \rfloor)$$

| *bisim1ThrowThrow*:

$$P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor) \implies P, \text{throw } e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$$

| *bisim1Try*:

$$P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp)$$

$$\implies P, \text{try } e \text{ catch}(C V) e2, h \vdash (\text{try } e' \text{ catch}(C V) e2, xs) \leftrightarrow (stk, loc, pc, xcp)$$

| *bisim1TryCatch1*:

$$\llbracket P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor); \text{typeof-addr } h a = \lfloor \text{Class-type } C' \rfloor; P \vdash C' \preceq^* C \rrbracket$$

$$\implies P, \text{try } e \text{ catch}(C V) e2, h \vdash (\{V:\text{Class } C=\text{None}; e2\}, xs[V := \text{Addr } a]) \leftrightarrow ([\text{Addr } a], loc, \text{Suc } (\text{length } (\text{compE2 } e)), \text{None})$$

| *bisim1TryCatch2*:
 $P, e2, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, \text{try } e \text{ catch}(C V) e2, h \vdash (\{V:Class C=None; e'\}, xs) \leftrightarrow (stk, loc, Suc (Suc (length (compE2 e) + pc)), xcp)$

| *bisim1TryFail*:
 $\llbracket P, e, h \vdash (Throw a, xs) \leftrightarrow (stk, loc, pc, [a]); \text{typeof-addr } h a = [Class\text{-type } C']; \neg P \vdash C' \preceq^* C \rrbracket$
 $\implies P, \text{try } e \text{ catch}(C V) e2, h \vdash (Throw a, xs) \leftrightarrow (stk, loc, pc, [a])$

| *bisim1TryCatchThrow*:
 $P, e2, h \vdash (Throw a, xs) \leftrightarrow (stk, loc, pc, [a])$
 $\implies P, \text{try } e \text{ catch}(C V) e2, h \vdash (Throw a, xs) \leftrightarrow (stk, loc, Suc (Suc (length (compE2 e) + pc)), [a])$

| *bisims1Nil*: $P, [], h \vdash ([], xs) [\leftrightarrow] ([], xs, 0, None)$

| *bisims1List1*:
 $P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \implies P, e\#es, h \vdash (e'\#es, xs) [\leftrightarrow] (stk, loc, pc, xcp)$

| *bisims1List2*:
 $P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, xcp)$
 $\implies P, e\#es, h \vdash (Val v \# es', xs) [\leftrightarrow] (stk @ [v], loc, length (compE2 e) + pc, xcp)$

inductive-cases *bisim1-cases*:

$P, e, h \vdash (Val v, xs) \leftrightarrow (stk, loc, pc, xcp)$

lemma *bisim1-refl*: $P, e, h \vdash (e, xs) \leftrightarrow ([], xs, 0, None)$
and *bisims1-refl*: $P, es, h \vdash (es, xs) [\leftrightarrow] ([], xs, 0, None)$
apply(*induct e and es rule: call.induct calls.induct*)
apply(*auto intro: bisim1-bisims1.intros simp add: nat-fun-sum-eq-conv*)
apply(*rename-tac option a*)
apply(*case-tac option*)
apply(*auto intro: bisim1-bisims1.intros split: if-split-asm*)
done

lemma *bisims1-lengthD*: $P, es, h \vdash (es', xs) [\leftrightarrow] s \implies length\ es = length\ es'$
apply(*induct es arbitrary: es' s*)
apply(*auto elim: bisims1.cases*)
done

Derive an alternative induction rule for *bisim1* such that (i) induction hypothesis are generated for all subexpressions and (ii) the number of surrounding blocks is passed through.

inductive *bisim1'* ::

$'m\ prog \Rightarrow 'heap \Rightarrow 'addr\ expr1 \Rightarrow nat \Rightarrow ('addr\ expr1 \times 'addr\ locals1)$
 $\Rightarrow ('addr\ val\ list \times 'addr\ val\ list \times pc \times 'addr\ option) \Rightarrow bool$

and *bisims1'* ::

$'m\ prog \Rightarrow 'heap \Rightarrow 'addr\ expr1\ list \Rightarrow nat \Rightarrow ('addr\ expr1\ list \times 'addr\ locals1)$
 $\Rightarrow ('addr\ val\ list \times 'addr\ val\ list \times pc \times 'addr\ option) \Rightarrow bool$

and *bisim1'-syntax* ::

'm prog \Rightarrow 'addr expr1 \Rightarrow nat \Rightarrow 'heap \Rightarrow ('addr expr1 \times 'addr locals1)
 \Rightarrow ('addr val list \times 'addr val list \times pc \times 'addr option) \Rightarrow bool
 ($\langle -, -, -, - \vdash'' - \leftrightarrow - \rangle$ \rightarrow [50, 0, 0, 0, 0, 50] 100)

and bisims1'-syntax ::

'm prog \Rightarrow 'addr expr1 list \Rightarrow nat \Rightarrow 'heap \Rightarrow ('addr expr1 list \times 'addr val list)
 \Rightarrow ('addr val list \times 'addr val list \times pc \times 'addr option) \Rightarrow bool
 ($\langle -, -, -, - \vdash'' - [\leftrightarrow] - \rangle$ \rightarrow [50, 0, 0, 0, 0, 50] 100)

for P :: 'm prog **and** h :: 'heap

where

P, e, n, h \vdash' exs \leftrightarrow s \equiv bisim1' P h e n exs s

| P, es, n, h \vdash' esxs $[\leftrightarrow]$ s \equiv bisims1' P h es n esxs s

| bisim1Val2':

P, e, n, h \vdash' (Val v, xs) \leftrightarrow (v # [], xs, length (compE2 e), None)

| bisim1New':

P, new C, n, h \vdash' (new C, xs) \leftrightarrow ([], xs, 0, None)

| bisim1NewThrow':

P, new C, n, h \vdash' (THROW OutOfMemory, xs) \leftrightarrow ([], xs, 0, [addr-of-sys-xcpt OutOfMemory])

| bisim1NewArray':

P, e, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp)
 \Rightarrow P, newA T[e], n, h \vdash' (newA T[e], xs) \leftrightarrow (stk, loc, pc, xcp)

| bisim1NewArrayThrow':

P, e, n, h \vdash' (Throw a, xs) \leftrightarrow (stk, loc, pc, [a])
 \Rightarrow P, newA T[e], n, h \vdash' (Throw a, xs) \leftrightarrow (stk, loc, pc, [a])

| bisim1NewArrayFail':

(\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None))
 \Rightarrow P, newA T[e], n, h \vdash' (Throw a, xs) \leftrightarrow ([v], xs, length (compE2 e), [a])

| bisim1Cast':

P, e, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp)
 \Rightarrow P, Cast T e, n, h \vdash' (Cast T e', xs) \leftrightarrow (stk, loc, pc, xcp)

| bisim1CastThrow':

P, e, n, h \vdash' (Throw a, xs) \leftrightarrow (stk, loc, pc, [a])
 \Rightarrow P, Cast T e, n, h \vdash' (Throw a, xs) \leftrightarrow (stk, loc, pc, [a])

| bisim1CastFail':

(\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None))
 \Rightarrow P, Cast T e, n, h \vdash' (THROW ClassCast, xs) \leftrightarrow ([v], xs, length (compE2 e), [addr-of-sys-xcpt ClassCast])

| bisim1InstanceOf':

P, e, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp)
 \Rightarrow P, e instanceof T, n, h \vdash' (e' instanceof T, xs) \leftrightarrow (stk, loc, pc, xcp)

- | *bisim1InstanceOfThrow'*:
 $P, e, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a])$
 $\implies P, e\ instanceof\ T, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a])$
- | *bisim1Val'*: $P, Val\ v, n, h \vdash' (Val\ v, xs) \leftrightarrow ([], xs, 0, None)$
- | *bisim1Var'*: $P, Var\ V, n, h \vdash' (Var\ V, xs) \leftrightarrow ([], xs, 0, None)$
- | *bisim1BinOp1'*:
 $\llbracket P, e1, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp);$
 $\bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, e1 \llbracket bop \rrbracket e2, n, h \vdash' (e' \llbracket bop \rrbracket e2, xs) \leftrightarrow (stk, loc, pc, xcp)$
- | *bisim1BinOp2'*:
 $\llbracket P, e2, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp);$
 $\bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, e1 \llbracket bop \rrbracket e2, n, h \vdash' (Val\ v1 \llbracket bop \rrbracket e', xs) \leftrightarrow (stk\ @\ [v1], loc, length\ (compE2\ e1) + pc, xcp)$
- | *bisim1BinOpThrow1'*:
 $\llbracket P, e1, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a]);$
 $\bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, e1 \llbracket bop \rrbracket e2, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a])$
- | *bisim1BinOpThrow2'*:
 $\llbracket P, e2, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a]);$
 $\bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, e1 \llbracket bop \rrbracket e2, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk\ @\ [v1], loc, length\ (compE2\ e1) + pc, [a])$
- | *bisim1BinOpThrow'*:
 $\llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None);$
 $\bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, e1 \llbracket bop \rrbracket e2, n, h \vdash' (Throw\ a, xs) \leftrightarrow ([v1, v2], xs, length\ (compE2\ e1) + length\ (compE2\ e2), [a])$
- | *bisim1LAss1'*:
 $P, e, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, V:=e, n, h \vdash' (V:=e', xs) \leftrightarrow (stk, loc, pc, xcp)$
- | *bisim1LAss2'*:
 $(\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None))$
 $\implies P, V:=e, n, h \vdash' (unit, xs) \leftrightarrow ([], xs, Suc\ (length\ (compE2\ e)), None)$
- | *bisim1LAssThrow'*:
 $P, e, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a])$
 $\implies P, V:=e, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a])$
- | *bisim1AAcc1'*:
 $\llbracket P, a, n, h \vdash' (a', xs) \leftrightarrow (stk, loc, pc, xcp); \bigwedge xs. P, i, n, h \vdash' (i, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, a[i], n, h \vdash' (a'[i], xs) \leftrightarrow (stk, loc, pc, xcp)$
- | *bisim1AAcc2'*:

$$\begin{aligned} & \llbracket P, i, n, h \vdash' (i', xs) \leftrightarrow (stk, loc, pc, xcp); \bigwedge xs. P, a, n, h \vdash' (a, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket \\ & \implies P, a[i], n, h \vdash' (Val v[i'], xs) \leftrightarrow (stk @ [v], loc, length (compE2 a) + pc, xcp) \end{aligned}$$

| *bisim1AAccThrow1'*:

$$\begin{aligned} & \llbracket P, a, n, h \vdash' (Throw ad, xs) \leftrightarrow (stk, loc, pc, [ad]); \\ & \quad \bigwedge xs. P, i, n, h \vdash' (i, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket \\ & \implies P, a[i], n, h \vdash' (Throw ad, xs) \leftrightarrow (stk, loc, pc, [ad]) \end{aligned}$$

| *bisim1AAccThrow2'*:

$$\begin{aligned} & \llbracket P, i, n, h \vdash' (Throw ad, xs) \leftrightarrow (stk, loc, pc, [ad]); \\ & \quad \bigwedge xs. P, a, n, h \vdash' (a, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket \\ & \implies P, a[i], n, h \vdash' (Throw ad, xs) \leftrightarrow (stk @ [v], loc, length (compE2 a) + pc, [ad]) \end{aligned}$$

| *bisim1AAccFail'*:

$$\begin{aligned} & \llbracket \bigwedge xs. P, a, n, h \vdash' (a, xs) \leftrightarrow (\llbracket, xs, 0, None); \bigwedge xs. P, i, n, h \vdash' (i, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket \\ & \implies P, a[i], n, h \vdash' (Throw ad, xs) \leftrightarrow ([v, v'], xs, length (compE2 a) + length (compE2 i), [ad]) \end{aligned}$$

| *bisim1AAss1'*:

$$\begin{aligned} & \llbracket P, a, n, h \vdash' (a', xs) \leftrightarrow (stk, loc, pc, xcp); \\ & \quad \bigwedge xs. P, i, n, h \vdash' (i, xs) \leftrightarrow (\llbracket, xs, 0, None); \\ & \quad \bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket \\ & \implies P, a[i] := e, n, h \vdash' (a'[i] := e, xs) \leftrightarrow (stk, loc, pc, xcp) \end{aligned}$$

| *bisim1AAss2'*:

$$\begin{aligned} & \llbracket P, i, n, h \vdash' (i', xs) \leftrightarrow (stk, loc, pc, xcp); \\ & \quad \bigwedge xs. P, a, n, h \vdash' (a, xs) \leftrightarrow (\llbracket, xs, 0, None); \\ & \quad \bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket \\ & \implies P, a[i] := e, n, h \vdash' (Val v[i'] := e, xs) \leftrightarrow (stk @ [v], loc, length (compE2 a) + pc, xcp) \end{aligned}$$

| *bisim1AAss3'*:

$$\begin{aligned} & \llbracket P, e, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp); \\ & \quad \bigwedge xs. P, a, n, h \vdash' (a, xs) \leftrightarrow (\llbracket, xs, 0, None); \\ & \quad \bigwedge xs. P, i, n, h \vdash' (i, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket \\ & \implies P, a[i] := e, n, h \vdash' (Val v[Val v'] := e', xs) \leftrightarrow (stk @ [v', v], loc, length (compE2 a) + length (compE2 i) + pc, xcp) \end{aligned}$$

| *bisim1AAssThrow1'*:

$$\begin{aligned} & \llbracket P, a, n, h \vdash' (Throw ad, xs) \leftrightarrow (stk, loc, pc, [ad]); \\ & \quad \bigwedge xs. P, i, n, h \vdash' (i, xs) \leftrightarrow (\llbracket, xs, 0, None); \\ & \quad \bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket \\ & \implies P, a[i] := e, n, h \vdash' (Throw ad, xs) \leftrightarrow (stk, loc, pc, [ad]) \end{aligned}$$

| *bisim1AAssThrow2'*:

$$\begin{aligned} & \llbracket P, i, n, h \vdash' (Throw ad, xs) \leftrightarrow (stk, loc, pc, [ad]); \\ & \quad \bigwedge xs. P, a, n, h \vdash' (a, xs) \leftrightarrow (\llbracket, xs, 0, None); \\ & \quad \bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket \\ & \implies P, a[i] := e, n, h \vdash' (Throw ad, xs) \leftrightarrow (stk @ [v], loc, length (compE2 a) + pc, [ad]) \end{aligned}$$

| *bisim1AAssThrow3'*:

$$\begin{aligned} & \llbracket P, e, n, h \vdash' (Throw ad, xs) \leftrightarrow (stk, loc, pc, [ad]); \\ & \quad \bigwedge xs. P, a, n, h \vdash' (a, xs) \leftrightarrow (\llbracket, xs, 0, None); \\ & \quad \bigwedge xs. P, i, n, h \vdash' (i, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket \\ & \implies P, a[i] := e, n, h \vdash' (Throw ad, xs) \leftrightarrow (stk @ [v', v], loc, length (compE2 a) + length (compE2 i) + pc, [ad]) \end{aligned}$$

$i) + pc, [ad]$)

| *bisim1AAssFail'*:

$\llbracket \bigwedge xs. P, a, n, h \vdash' (a, xs) \leftrightarrow ([], xs, 0, None);$
 $\bigwedge xs. P, i, n, h \vdash' (i, xs) \leftrightarrow ([], xs, 0, None);$
 $\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, a[i] := e, n, h \vdash' (Throw\ ad, xs) \leftrightarrow ([v', v, v'], xs, length(compE2\ a) + length(compE2\ i)$
 $+ length(compE2\ e), [ad])$

| *bisim1AAss4'*:

$\llbracket \bigwedge xs. P, a, n, h \vdash' (a, xs) \leftrightarrow ([], xs, 0, None);$
 $\bigwedge xs. P, i, n, h \vdash' (i, xs) \leftrightarrow ([], xs, 0, None);$
 $\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, a[i] := e, n, h \vdash' (unit, xs) \leftrightarrow ([], xs, Suc(length(compE2\ a) + length(compE2\ i) + length$
 $(compE2\ e)), None)$

| *bisim1ALength'*:

$P, a, n, h \vdash' (a', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, a \cdot length, n, h \vdash' (a' \cdot length, xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1ALengthThrow'*:

$P, a, n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$
 $\implies P, a \cdot length, n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$

| *bisim1ALengthNull'*:

$(\bigwedge xs. P, a, n, h \vdash' (a, xs) \leftrightarrow ([], xs, 0, None))$
 $\implies P, a \cdot length, n, h \vdash' (THROW\ NullPointer, xs) \leftrightarrow ([Null], xs, length(compE2\ a), [addr-of-sys-xcpt$
 $NullPointer])$

| *bisim1FAcc'*:

$P, e, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp)$
 $\implies P, e \cdot F\{D\}, n, h \vdash' (e' \cdot F\{D\}, xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1FAccThrow'*:

$P, e, n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$
 $\implies P, e \cdot F\{D\}, n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$

| *bisim1FAccNull'*:

$(\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None))$
 $\implies P, e \cdot F\{D\}, n, h \vdash' (THROW\ NullPointer, xs) \leftrightarrow ([Null], xs, length(compE2\ e), [addr-of-sys-xcpt$
 $NullPointer])$

| *bisim1FAss1'*:

$\llbracket P, e, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp);$
 $\bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, e \cdot F\{D\} := e2, n, h \vdash' (e' \cdot F\{D\} := e2, xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1FAss2'*:

$\llbracket P, e2, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp);$
 $\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$

$\implies P, e \cdot F\{D\} := e2, n, h \vdash' (Val\ v \cdot F\{D\} := e', xs) \leftrightarrow (stk \ @ \ [v], loc, length\ (compE2\ e) + pc, xcp)$

| *bisim1FAssThrow1'*:

$\llbracket P, e, n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad]);$
 $\bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, e \cdot F\{D\} := e2, n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$

| *bisim1FAssThrow2'*:

$\llbracket P, e2, n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad]);$
 $\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, e \cdot F\{D\} := e2, n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk \ @ \ [v], loc, length\ (compE2\ e) + pc, [ad])$

| *bisim1FAssNull'*:

$\llbracket \bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None);$
 $\bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, e \cdot F\{D\} := e2, n, h \vdash' (THROW\ NullPointer, xs) \leftrightarrow ([v, Null], xs, length\ (compE2\ e) + length\ (compE2\ e2), [addr-of-sys-xcpt\ NullPointer])$

| *bisim1FAss3'*:

$\llbracket \bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None);$
 $\bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, e \cdot F\{D\} := e2, n, h \vdash' (unit, xs) \leftrightarrow ([], xs, Suc\ (length\ (compE2\ e) + length\ (compE2\ e2)), None)$

| *bisim1CAS1'*:

$\llbracket P, e1, n, h \vdash' (e1', xs) \leftrightarrow (stk, loc, pc, xcp);$
 $\bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None);$
 $\bigwedge xs. P, e3, n, h \vdash' (e3, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), n, h \vdash' (e1' \cdot compareAndSwap(D \cdot F, e2, e3), xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1CAS2'*:

$\llbracket P, e2, n, h \vdash' (e2', xs) \leftrightarrow (stk, loc, pc, xcp);$
 $\bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None);$
 $\bigwedge xs. P, e3, n, h \vdash' (e3, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), n, h \vdash' (Val\ v \cdot compareAndSwap(D \cdot F, e2', e3), xs) \leftrightarrow (stk \ @ \ [v], loc, length\ (compE2\ e1) + pc, xcp)$

| *bisim1CAS3'*:

$\llbracket P, e3, n, h \vdash' (e3', xs) \leftrightarrow (stk, loc, pc, xcp);$
 $\bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None);$
 $\bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), n, h \vdash' (Val\ v \cdot compareAndSwap(D \cdot F, Val\ v', e3'), xs) \leftrightarrow (stk \ @ \ [v', v], loc, length\ (compE2\ e1) + length\ (compE2\ e2) + pc, xcp)$

| *bisim1CASThrow1'*:

$\llbracket P, e1, n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad]);$
 $\bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None);$
 $\bigwedge xs. P, e3, n, h \vdash' (e3, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$

| *bisim1CASThrow2'*:

$\llbracket P, e2, n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad]);$
 $\bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None);$
 $\bigwedge xs. P, e3, n, h \vdash' (e3, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk\ @\ [v], loc, length\ (compE2\ e1) + pc, [ad])$

| *bisim1CASThrow3'*:

$\llbracket P, e3, n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad]);$
 $\bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None);$
 $\bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk\ @\ [v', v], loc, length\ (compE2\ e1) + length\ (compE2\ e2) + pc, [ad])$

| *bisim1CASFail'*:

$\llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None);$
 $\bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None);$
 $\bigwedge xs. P, e3, n, h \vdash' (e3, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), n, h \vdash' (Throw\ ad, xs) \leftrightarrow ([v', v, v'], xs, length\ (compE2\ e1) + length\ (compE2\ e2) + length\ (compE2\ e3), [ad])$

| *bisim1Call1'*:

$\llbracket P, obj, n, h \vdash' (obj', xs) \leftrightarrow (stk, loc, pc, xcp);$
 $\bigwedge xs. P, ps, n, h \vdash' (ps, xs) [\leftrightarrow] ([], xs, 0, None) \rrbracket$
 $\implies P, obj \cdot M(ps), n, h \vdash' (obj' \cdot M(ps), xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1CallParams'*:

$\llbracket P, ps, n, h \vdash' (ps', xs) [\leftrightarrow] (stk, loc, pc, xcp); ps \neq [];$
 $\bigwedge xs. P, obj, n, h \vdash' (obj, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, obj \cdot M(ps), n, h \vdash' (Val\ v \cdot M(ps'), xs) \leftrightarrow (stk\ @\ [v], loc, length\ (compE2\ obj) + pc, xcp)$

| *bisim1CallThrowObj'*:

$\llbracket P, obj, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a]);$
 $\bigwedge xs. P, ps, n, h \vdash' (ps, xs) [\leftrightarrow] ([], xs, 0, None) \rrbracket$
 $\implies P, obj \cdot M(ps), n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a])$

| *bisim1CallThrowParams'*:

$\llbracket P, ps, n, h \vdash' (map\ Val\ vs\ @\ Throw\ a\ \# ps', xs) [\leftrightarrow] (stk, loc, pc, [a]);$
 $\bigwedge xs. P, obj, n, h \vdash' (obj, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\implies P, obj \cdot M(ps), n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk\ @\ [v], loc, length\ (compE2\ obj) + pc, [a])$

| *bisim1CallThrow'*:

$\llbracket length\ ps = length\ vs;$
 $\bigwedge xs. P, obj, n, h \vdash' (obj, xs) \leftrightarrow ([], xs, 0, None); \bigwedge xs. P, ps, n, h \vdash' (ps, xs) [\leftrightarrow] ([], xs, 0, None)$
 \rrbracket
 $\implies P, obj \cdot M(ps), n, h \vdash' (Throw\ a, xs) \leftrightarrow (vs\ @\ [v], xs, length\ (compE2\ obj) + length\ (compEs2\ ps), [a])$

| *bisim1BlockSome1'*:

$(\bigwedge xs. P, e, Suc\ n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None))$
 $\implies P, \{V:T=[v]; e\}, n, h \vdash' (\{V:T=[v]; e\}, xs) \leftrightarrow ([], xs, 0, None)$

| *bisim1BlockSome2'*:

$(\bigwedge xs. P, e, Suc\ n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None))$

$$\implies P, \{V:T=[v]; e\}, n, h \vdash' (\{V:T=[v]; e\}, xs) \leftrightarrow ([v], xs, \text{Suc } 0, \text{None})$$

| *bisim1BlockSome4'*:

$$P, e, \text{Suc } n, h \vdash' (e', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp})$$

$$\implies P, \{V:T=[v]; e\}, n, h \vdash' (\{V:T=\text{None}; e'\}, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{Suc } (\text{Suc } \text{pc}), \text{xcp})$$

| *bisim1BlockThrowSome'*:

$$P, e, \text{Suc } n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a])$$

$$\implies P, \{V:T=[v]; e\}, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{Suc } (\text{Suc } \text{pc}), [a])$$

| *bisim1BlockNone'*:

$$P, e, \text{Suc } n, h \vdash' (e', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp})$$

$$\implies P, \{V:T=\text{None}; e\}, n, h \vdash' (\{V:T=\text{None}; e'\}, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp})$$

| *bisim1BlockThrowNone'*:

$$P, e, \text{Suc } n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a])$$

$$\implies P, \{V:T=\text{None}; e\}, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a])$$

| *bisim1Sync1'*:

$$\llbracket P, e1, n, h \vdash' (e', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp});$$

$$\bigwedge xs. P, e2, \text{Suc } n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, \text{None}) \rrbracket$$

$$\implies P, \text{sync}_V (e1) e2, n, h \vdash' (\text{sync}_V (e') e2, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp})$$

| *bisim1Sync2'*:

$$\llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, \text{None});$$

$$\bigwedge xs. P, e2, \text{Suc } n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, \text{None}) \rrbracket$$

$$\implies P, \text{sync}_V (e1) e2, n, h \vdash' (\text{sync}_V (\text{Val } v) e2, xs) \leftrightarrow ([v, v], xs, \text{Suc } (\text{length } (\text{compE2 } e1)), \text{None})$$

| *bisim1Sync3'*:

$$\llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, \text{None});$$

$$\bigwedge xs. P, e2, \text{Suc } n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, \text{None}) \rrbracket$$

$$\implies P, \text{sync}_V (e1) e2, n, h \vdash' (\text{sync}_V (\text{Val } v) e2, xs) \leftrightarrow ([v], xs[V := v], \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e1))), \text{None})$$

| *bisim1Sync4'*:

$$\llbracket P, e2, \text{Suc } n, h \vdash' (e', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp});$$

$$\bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, \text{None}) \rrbracket$$

$$\implies P, \text{sync}_V (e1) e2, n, h \vdash' (\text{insync}_V (a) e', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{Suc } (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e1) + \text{pc}))), \text{xcp})$$

| *bisim1Sync5'*:

$$\llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, \text{None});$$

$$\bigwedge xs. P, e2, \text{Suc } n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, \text{None}) \rrbracket$$

$$\implies P, \text{sync}_V (e1) e2, n, h \vdash' (\text{insync}_V (a) \text{Val } v, xs) \leftrightarrow ([xs ! V, v], xs, 4 + \text{length } (\text{compE2 } e1) + \text{length } (\text{compE2 } e2), \text{None})$$

| *bisim1Sync6'*:

$$\llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, \text{None});$$

$$\bigwedge xs. P, e2, \text{Suc } n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, \text{None}) \rrbracket$$

$$\implies P, \text{sync}_V (e1) e2, n, h \vdash' (\text{Val } v, xs) \leftrightarrow ([v], xs, 5 + \text{length } (\text{compE2 } e1) + \text{length } (\text{compE2 } e2), \text{None})$$

| *bisim1Sync7'*:

$$\begin{aligned} & \llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None); \\ & \quad \bigwedge xs. P, e2, Suc\ n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ & \implies P, sync_V (e1)\ e2, n, h \vdash' (insync_V (a)\ Throw\ a', xs) \leftrightarrow ([Addr\ a'], xs, 6 + length (compE2\ e1) + length (compE2\ e2), None) \end{aligned}$$

| *bisim1Sync8'*:

$$\begin{aligned} & \llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None); \\ & \quad \bigwedge xs. P, e2, Suc\ n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ & \implies P, sync_V (e1)\ e2, n, h \vdash' (insync_V (a)\ Throw\ a', xs) \leftrightarrow \\ & \quad ([xs\ !\ V, Addr\ a'], xs, 7 + length (compE2\ e1) + length (compE2\ e2), None) \end{aligned}$$

| *bisim1Sync9'*:

$$\begin{aligned} & \llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None); \\ & \quad \bigwedge xs. P, e2, Suc\ n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ & \implies P, sync_V (e1)\ e2, n, h \vdash' (Throw\ a, xs) \leftrightarrow ([Addr\ a], xs, 8 + length (compE2\ e1) + length (compE2\ e2), None) \end{aligned}$$

| *bisim1Sync10'*:

$$\begin{aligned} & \llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None); \\ & \quad \bigwedge xs. P, e2, Suc\ n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ & \implies P, sync_V (e1)\ e2, n, h \vdash' (Throw\ a, xs) \leftrightarrow ([Addr\ a], xs, 8 + length (compE2\ e1) + length (compE2\ e2), [a]) \end{aligned}$$

| *bisim1Sync11'*:

$$\begin{aligned} & \llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None); \\ & \quad \bigwedge xs. P, e2, Suc\ n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ & \implies P, sync_V (e1)\ e2, n, h \vdash' (THROW\ NullPointer, xs) \leftrightarrow ([Null], xs, Suc (Suc (length (compE2\ e1))), [addr-of-sys-xcpt\ NullPointer]) \end{aligned}$$

| *bisim1Sync12'*:

$$\begin{aligned} & \llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None); \\ & \quad \bigwedge xs. P, e2, Suc\ n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ & \implies P, sync_V (e1)\ e2, n, h \vdash' (Throw\ a, xs) \leftrightarrow ([v, v'], xs, 4 + length (compE2\ e1) + length (compE2\ e2), [a]) \end{aligned}$$

| *bisim1Sync14'*:

$$\begin{aligned} & \llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None); \\ & \quad \bigwedge xs. P, e2, Suc\ n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ & \implies P, sync_V (e1)\ e2, n, h \vdash' (Throw\ a, xs) \leftrightarrow \\ & \quad ([v, Addr\ a'], xs, 7 + length (compE2\ e1) + length (compE2\ e2), [a]) \end{aligned}$$

| *bisim1SyncThrow'*:

$$\begin{aligned} & \llbracket P, e1, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a]); \\ & \quad \bigwedge xs. P, e2, Suc\ n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ & \implies P, sync_V (e1)\ e2, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a]) \end{aligned}$$

| *bisim1InSync'*:

$$P, insync_V (a)\ e, n, h \vdash' (insync_V (a)\ e, xs) \leftrightarrow ([], xs, 0, None)$$

| *bisim1Seq1'*:

$$\begin{aligned} & \llbracket P, e1, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp); \\ & \quad \bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \end{aligned}$$

$$\implies P, e1;;e2, n, h \vdash' (e';e2, xs) \leftrightarrow (stk, loc, pc, xcp)$$

| *bisim1SeqThrow1'*:

$$\begin{aligned} & \llbracket P, e1, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a]); \\ & \quad \bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ & \implies P, e1;;e2, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a]) \end{aligned}$$

| *bisim1Seq2'*:

$$\begin{aligned} & \llbracket P, e2, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp); \\ & \quad \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ & \implies P, e1;;e2, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, Suc\ (length\ (compE2\ e1) + pc), xcp) \end{aligned}$$

| *bisim1Cond1'*:

$$\begin{aligned} & \llbracket P, e, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp); \\ & \quad \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None); \\ & \quad \bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ & \implies P, if\ (e)\ e1\ else\ e2, n, h \vdash' (if\ (e')\ e1\ else\ e2, xs) \leftrightarrow (stk, loc, pc, xcp) \end{aligned}$$

| *bisim1CondThen'*:

$$\begin{aligned} & \llbracket P, e1, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp); \\ & \quad \bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None); \\ & \quad \bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ & \implies P, if\ (e)\ e1\ else\ e2, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, Suc\ (length\ (compE2\ e) + pc), xcp) \end{aligned}$$

| *bisim1CondElse'*:

$$\begin{aligned} & \llbracket P, e2, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp); \\ & \quad \bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None); \\ & \quad \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ & \implies P, if\ (e)\ e1\ else\ e2, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, Suc\ (Suc\ (length\ (compE2\ e) + length\ (compE2\ e1) + pc)), xcp) \end{aligned}$$

| *bisim1CondThrow'*:

$$\begin{aligned} & \llbracket P, e, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a]); \\ & \quad \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None); \\ & \quad \bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ & \implies P, if\ (e)\ e1\ else\ e2, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a]) \end{aligned}$$

| *bisim1While1'*:

$$\begin{aligned} & \llbracket \bigwedge xs. P, c, n, h \vdash' (c, xs) \leftrightarrow ([], xs, 0, None); \\ & \quad \bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ & \implies P, while\ (c)\ e, n, h \vdash' (while\ (c)\ e, xs) \leftrightarrow ([], xs, 0, None) \end{aligned}$$

| *bisim1While3'*:

$$\begin{aligned} & \llbracket P, c, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp); \\ & \quad \bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ & \implies P, while\ (c)\ e, n, h \vdash' (if\ (e')\ (e;;\ while\ (c)\ e)\ else\ unit, xs) \leftrightarrow (stk, loc, pc, xcp) \end{aligned}$$

| *bisim1While4'*:

$$\begin{aligned} & \llbracket P, e, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp); \\ & \quad \bigwedge xs. P, c, n, h \vdash' (c, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \\ & \implies P, while\ (c)\ e, n, h \vdash' (e';\ while\ (c)\ e, xs) \leftrightarrow (stk, loc, Suc\ (length\ (compE2\ c) + pc), xcp) \end{aligned}$$

| *bisim1While6'*:

$\llbracket \bigwedge xs. P, c, n, h \vdash' (c, xs) \leftrightarrow ([], xs, 0, None);$
 $\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \Longrightarrow$
 $P, \text{while } (c) e, n, h \vdash' (\text{while } (c) e, xs) \leftrightarrow ([], xs, \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } c) + \text{length } (\text{compE2 } e))), None)$

| *bisim1While7'*:

$\llbracket \bigwedge xs. P, c, n, h \vdash' (c, xs) \leftrightarrow ([], xs, 0, None);$
 $\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \Longrightarrow$
 $P, \text{while } (c) e, n, h \vdash' (\text{unit}, xs) \leftrightarrow ([], xs, \text{Suc } (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } c) + \text{length } (\text{compE2 } e))))), None)$

| *bisim1WhileThrow1'*:

$\llbracket P, c, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a]);$
 $\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\Longrightarrow P, \text{while } (c) e, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a])$

| *bisim1WhileThrow2'*:

$\llbracket P, e, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a]);$
 $\bigwedge xs. P, c, n, h \vdash' (c, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\Longrightarrow P, \text{while } (c) e, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{Suc } (\text{length } (\text{compE2 } c) + \text{pc}), [a])$

| *bisim1Throw1'*:

$P, e, n, h \vdash' (e', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcpt})$
 $\Longrightarrow P, \text{throw } e, n, h \vdash' (\text{throw } e', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcpt})$

| *bisim1Throw2'*:

$(\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None))$
 $\Longrightarrow P, \text{throw } e, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], xs, \text{length } (\text{compE2 } e), [a])$

| *bisim1ThrowNull'*:

$(\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None))$
 $\Longrightarrow P, \text{throw } e, n, h \vdash' (\text{THROW } \text{NullPointer}, xs) \leftrightarrow ([\text{Null}], xs, \text{length } (\text{compE2 } e), [\text{addr-of-sys-xcpt } \text{NullPointer}])$

| *bisim1ThrowThrow'*:

$P, e, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a])$
 $\Longrightarrow P, \text{throw } e, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a])$

| *bisim1Try'*:

$\llbracket P, e, n, h \vdash' (e', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcpt});$
 $\bigwedge xs. P, e2, \text{Suc } n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\Longrightarrow P, \text{try } e \text{ catch } (C \ V) e2, n, h \vdash' (\text{try } e' \text{ catch } (C \ V) e2, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcpt})$

| *bisim1TryCatch1'*:

$\llbracket P, e, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a]); \text{typeof-addr } h \ a = [\text{Class-type } C']; P \vdash C' \preceq^* C;$
 $\bigwedge xs. P, e2, \text{Suc } n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$
 $\Longrightarrow P, \text{try } e \text{ catch } (C \ V) e2, n, h \vdash' (\{V:\text{Class } C=\text{None}; e2\}, xs[V := \text{Addr } a]) \leftrightarrow ([\text{Addr } a], \text{loc}, \text{Suc } (\text{length } (\text{compE2 } e)), None)$

| *bisim1TryCatch2'*:

$\llbracket P, e2, \text{Suc } n, h \vdash' (e', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcpt});$

$\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None)]$
 $\implies P, \text{try } e \text{ catch}(C V) e2, n, h \vdash' (\{V:Class C=None; e'\}, xs) \leftrightarrow (stk, loc, Suc (Suc (length (compE2 e) + pc)), xcp)$

| *bisim1TryFail'*:

$\llbracket P, e, n, h \vdash' (Throw a, xs) \leftrightarrow (stk, loc, pc, [a]); \text{typeof-addr } h a = [Class\text{-type } C']; \neg P \vdash C' \preceq^* C; \rrbracket$

$\bigwedge xs. P, e2, Suc n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None)]$
 $\implies P, \text{try } e \text{ catch}(C V) e2, n, h \vdash' (Throw a, xs) \leftrightarrow (stk, loc, pc, [a])$

| *bisim1TryCatchThrow'*:

$\llbracket P, e2, Suc n, h \vdash' (Throw a, xs) \leftrightarrow (stk, loc, pc, [a]); \rrbracket$
 $\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None)]$
 $\implies P, \text{try } e \text{ catch}(C V) e2, n, h \vdash' (Throw a, xs) \leftrightarrow (stk, loc, Suc (Suc (length (compE2 e) + pc)), [a])$

| *bisims1Nil'*: $P, [], n, h \vdash' ([], xs) [\leftrightarrow] ([], xs, 0, None)$

| *bisims1List1'*:

$\llbracket P, e, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp); \rrbracket$
 $\bigwedge xs. P, es, n, h \vdash' (es, xs) [\leftrightarrow] ([], xs, 0, None)]$
 $\implies P, e\#es, n, h \vdash' (e'\#es, xs) [\leftrightarrow] (stk, loc, pc, xcp)$

| *bisims1List2'*:

$\llbracket P, es, n, h \vdash' (es', xs) [\leftrightarrow] (stk, loc, pc, xcp); \rrbracket$
 $\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None)]$
 $\implies P, e\#es, n, h \vdash' (Val v \# es', xs) [\leftrightarrow] (stk @ [v], loc, length (compE2 e) + pc, xcp)$

lemma *bisim1'-refl*: $P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None)$

and *bisims1'-refl*: $P, es, n, h \vdash' (es, xs) [\leftrightarrow] ([], xs, 0, None)$

apply(*induct e and es arbitrary: n xs and n xs rule: call.induct calls.induct*)

apply(*auto intro: bisim1'-bisims1'.intros simp add: nat-fun-sum-eq-conv*)

apply(*rename-tac option a b c*)

apply(*case-tac option*)

apply(*auto intro: bisim1'-bisims1'.intros simp add: fun-eq-iff split: if-split-asm*)

done

lemma *bisim1-imp-bisim1'*: $P, e, h \vdash exs \leftrightarrow s \implies P, e, n, h \vdash' exs \leftrightarrow s$

and *bisims1-imp-bisims1'*: $P, es, h \vdash esxs [\leftrightarrow] s \implies P, es, n, h \vdash' esxs [\leftrightarrow] s$

proof(*induct arbitrary: n and n rule: bisim1-bisims1.inducts*)

case (*bisim1CallParams ps ps' xs stk loc pc xcp obj M v*)

show ?*case*

proof(*cases ps = []*)

case *True*

with $\langle P, ps, h \vdash (ps', xs) [\leftrightarrow] (stk, loc, pc, xcp) \rangle$ **have** $ps' = [] \text{ } pc = 0 \text{ } stk = [] \text{ } loc = xs \text{ } xcp = None$

by(*auto elim: bisims1.cases*)

moreover **have** $P, obj, n, h \vdash' (Val v, xs) \leftrightarrow ([v], xs, length (compE2 obj), None)$

by(*blast intro: bisim1Val2' bisim1'-refl*)

hence $P, obj \cdot M([], n, h \vdash' (Val v \cdot M([], xs) \leftrightarrow ([v], xs, length (compE2 obj), None)$

by-(*rule bisim1Call1', auto intro!: bisims1Nil' simp add: bsoks-def*)

ultimately show ?*thesis using True by simp*

next

case *False* **with** *bisim1CallParams* **show** ?*thesis*

by(*auto intro: bisim1CallParams' bisims1'-refl bisim1'-refl*)


```

qed
qed(auto intro: bisim1'-bisims1'.intros bisim1'-refl bisims1'-refl)

lemma bisim1'-imp-bisim1:  $P, e, n, h \vdash' \text{exs} \leftrightarrow s \implies P, e, h \vdash \text{exs} \leftrightarrow s$ 
  and bisims1'-imp-bisims1:  $P, es, n, h \vdash' \text{esxs} [\leftrightarrow] s \implies P, es, h \vdash \text{esxs} [\leftrightarrow] s$ 
apply(induct rule: bisim1'-bisims1'.inducts)
apply(blast intro: bisim1-bisims1.intros)+
done

lemma bisim1'-eq-bisim1:  $\text{bisim1}' P h e n = \text{bisim1} P h e$ 
  and bisims1'-eq-bisims1:  $\text{bisims1}' P h es n = \text{bisims1} P h es$ 
by(blast intro!: ext bisim1-imp-bisim1' bisims1-imp-bisims1' bisim1'-imp-bisim1 bisims1'-imp-bisims1)

end

```

```

lemmas bisim1-bisims1-inducts =
  J1-JVM-heap-base.bisim1'-bisims1'.inducts
  [simplified J1-JVM-heap-base.bisim1'-eq-bisim1 J1-JVM-heap-base.bisims1'-eq-bisims1,
  consumes 1,
  case-names bisim1Val2 bisim1New bisim1NewThrow
  bisim1NewArray bisim1NewArrayThrow bisim1NewArrayFail bisim1Cast bisim1CastThrow bisim1CastFail
  bisim1InstanceOf bisim1InstanceOfThrow
  bisim1Val bisim1Var bisim1BinOp1 bisim1BinOp2 bisim1BinOpThrow1 bisim1BinOpThrow2 bisim1BinOpThrow
  bisim1LAss1 bisim1LAss2 bisim1LAssThrow
  bisim1AAcc1 bisim1AAcc2 bisim1AAccThrow1 bisim1AAccThrow2 bisim1AAccFail
  bisim1AAss1 bisim1AAss2 bisim1AAss3 bisim1AAssThrow1 bisim1AAssThrow2
  bisim1AAssThrow3 bisim1AAssFail bisim1AAss4
  bisim1ALength bisim1ALengthThrow bisim1ALengthNull
  bisim1FAcc bisim1FAccThrow bisim1FAccNull
  bisim1FAss1 bisim1FAss2 bisim1FAssThrow1 bisim1FAssThrow2 bisim1FAssNull bisim1FAss3
  bisim1CAS1 bisim1CAS2 bisim1CAS3 bisim1CASThrow1 bisim1CASThrow2
  bisim1CASThrow3 bisim1CASFail
  bisim1Call1 bisim1CallParams bisim1CallThrowObj bisim1CallThrowParams
  bisim1CallThrow
  bisim1BlockSome1 bisim1BlockSome2 bisim1BlockSome4 bisim1BlockThrowSome
  bisim1BlockNone bisim1BlockThrowNone
  bisim1Sync1 bisim1Sync2 bisim1Sync3 bisim1Sync4 bisim1Sync5 bisim1Sync6
  bisim1Sync7 bisim1Sync8 bisim1Sync9 bisim1Sync10 bisim1Sync11 bisim1Sync12
  bisim1Sync14 bisim1SyncThrow bisim1InSync
  bisim1Seq1 bisim1SeqThrow1 bisim1Seq2
  bisim1Cond1 bisim1CondThen bisim1CondElse bisim1CondThrow
  bisim1While1 bisim1While3 bisim1While4
  bisim1While6 bisim1While7 bisim1WhileThrow1 bisim1WhileThrow2
  bisim1Throw1 bisim1Throw2 bisim1ThrowNull bisim1ThrowThrow
  bisim1Try bisim1TryCatch1 bisim1TryCatch2 bisim1TryFail bisim1TryCatchThrow
  bisims1Nil bisims1List1 bisims1List2]

```

```

lemmas bisim1-bisims1-inducts-split = bisim1-bisims1-inducts[split-format (complete)]

```

```

context J1-JVM-heap-base begin

```

lemma *bisim1-pc-length-compE2*: $P, E, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp) \implies pc \leq \text{length} (\text{compE2 } E)$
and *bisims1-pc-length-compEs2*: $P, Es, h \vdash (es, xs) [\leftrightarrow] (stk, loc, pc, xcp) \implies pc \leq \text{length} (\text{compEs2 } Es)$

apply(*induct* (*stk, loc, pc, xcp*) **and** (*stk, loc, pc, xcp*)
arbitrary: stk loc pc xcp and stk loc pc xcp rule: bisim1-bisims1.inducts)
apply(*auto*)
done

lemma *bisim1-pc-length-compE2D*:

$P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, \text{length} (\text{compE2 } e), xcp)$
 $\implies xcp = \text{None} \wedge \text{call1 } e' = \text{None} \wedge (\exists v. stk = [v] \wedge (\text{is-val } e' \longrightarrow e' = \text{Val } v \wedge xs = loc))$

and *bisims1-pc-length-compEs2D*:

$P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, \text{length} (\text{compEs2 } es), xcp)$
 $\implies xcp = \text{None} \wedge \text{calls1 } es' = \text{None} \wedge (\exists vs. stk = \text{rev } vs \wedge \text{length } vs = \text{length } es \wedge (\text{is-vals } es' \longrightarrow es' = \text{map Val } vs \wedge xs = loc))$

proof(*induct* (*e', xs*) (*stk, loc, length (compE2 e), xcp*)
and (*es', xs*) (*stk, loc, length (compEs2 es), xcp*)
arbitrary: e' xs stk loc xcp and es' xs stk loc xcp rule: bisim1-bisims1.inducts)
case (*bisims1List2 es es' xs stk loc pc xcp e v*)
then obtain *vs where* $xcp = \text{None}$ $\text{calls1 } es' = \text{None}$
 $stk = \text{rev } vs$ $\text{length } vs = \text{length } es$ $\text{is-vals } es' \longrightarrow es' = \text{map Val } vs \wedge xs = loc$ **by** *auto*
thus *?case*
by(*clarsimp*)(*rule-tac x=v#vs in exI, auto*)
qed(*simp-all (no-asm-use), (fastforce dest: bisim1-pc-length-compE2 bisims1-pc-length-compEs2 split: bop.split-asm if-split-asm)+*)

corollary *bisim1-call-pcD*: $\llbracket P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp); \text{call1 } e' = [aMvs] \rrbracket \implies pc < \text{length} (\text{compE2 } e)$

and *bisims1-calls-pcD*: $\llbracket P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, xcp); \text{calls1 } es' = [aMvs] \rrbracket \implies pc < \text{length} (\text{compEs2 } es)$

proof –

assume *bisim*: $P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp)$
and *call*: $\text{call1 } e' = [aMvs]$

{ **assume** $pc = \text{length} (\text{compE2 } e)$
with *bisim call* **have** *False*
by(*auto dest: bisim1-pc-length-compE2D*) }
moreover from *bisim* **have** $pc \leq \text{length} (\text{compE2 } e)$
by(*rule bisim1-pc-length-compE2*)
ultimately show $pc < \text{length} (\text{compE2 } e)$
by(*cases pc < length (compE2 e)*)(*auto*)

next

assume *bisim*: $P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, xcp)$
and *call*: $\text{calls1 } es' = [aMvs]$
{ **assume** $pc = \text{length} (\text{compEs2 } es)$
with *bisim call* **have** *False*
by(*auto dest: bisims1-pc-length-compEs2D*) }
moreover from *bisim* **have** $pc \leq \text{length} (\text{compEs2 } es)$
by(*rule bisims1-pc-length-compEs2*)
ultimately show $pc < \text{length} (\text{compEs2 } es)$
by(*cases pc < length (compEs2 es)*)(*auto*)

qed

lemma *bisim1-length-xs*: $P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \implies \text{length } xs = \text{length } loc$
and *bisims1-length-xs*: $P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, xcp) \implies \text{length } xs = \text{length } loc$
by(*induct* (e', xs) (stk, loc, pc, xcp) **and** (es', xs) (stk, loc, pc, xcp))
arbitrary: $e' \ xs \ stk \ loc \ pc \ xcp$ **and** $es' \ xs \ stk \ loc \ pc \ xcp$ *rule*: *bisim1-bisims1.inducts*)
auto

lemma *bisim1-Val-length-compE2D*:

$P, e, h \vdash (Val \ v, xs) \leftrightarrow (stk, loc, \text{length } (compE2 \ e), xcp) \implies stk = [v] \wedge xs = loc \wedge xcp = None$

and *bisims1-Val-length-compEs2D*:

$P, es, h \vdash (map \ Val \ vs, xs) [\leftrightarrow] (stk, loc, \text{length } (compEs2 \ es), xcp) \implies stk = rev \ vs \wedge xs = loc \wedge xcp = None$

by(*auto dest*: *bisim1-pc-length-compE2D bisims1-pc-length-compEs2D*)

lemma *bisim-Val-loc-eq-xcp-None*:

$P, e, h \vdash (Val \ v, xs) \leftrightarrow (stk, loc, pc, xcp) \implies xs = loc \wedge xcp = None$

and *bisims-Val-loc-eq-xcp-None*:

$P, es, h \vdash (map \ Val \ vs, xs) [\leftrightarrow] (stk, loc, pc, xcp) \implies xs = loc \wedge xcp = None$

apply(*induct* ($Val \ v :: 'addr \ expr1, xs$) (stk, loc, pc, xcp))

and ($map \ Val \ vs :: 'addr \ expr1 \ list, xs$) (stk, loc, pc, xcp)

arbitrary: $v \ xs \ stk \ loc \ pc \ xcp$ **and** $vs \ xs \ stk \ loc \ pc \ xcp$ *rule*: *bisim1-bisims1.inducts*)

apply(*auto*)

done

lemma *bisim-Val-pc-not-Invoke*:

$\llbracket P, e, h \vdash (Val \ v, xs) \leftrightarrow (stk, loc, pc, xcp); pc < \text{length } (compE2 \ e) \rrbracket \implies compE2 \ e \ ! \ pc \neq \text{Invoke } M \ n'$

and *bisims-Val-pc-not-Invoke*:

$\llbracket P, es, h \vdash (map \ Val \ vs, xs) [\leftrightarrow] (stk, loc, pc, xcp); pc < \text{length } (compEs2 \ es) \rrbracket \implies compEs2 \ es \ ! \ pc \neq \text{Invoke } M \ n'$

apply(*induct* ($Val \ v :: 'addr \ expr1, xs$) (stk, loc, pc, xcp))

and ($map \ Val \ vs :: 'addr \ expr1 \ list, xs$) (stk, loc, pc, xcp)

arbitrary: $v \ xs \ stk \ loc \ pc \ xcp$ **and** $vs \ xs \ stk \ loc \ pc \ xcp$ *rule*: *bisim1-bisims1.inducts*)

apply(*auto simp add*: *nth-append compEs2-map-Val dest*: *bisim1-pc-length-compE2*)

done

lemma *bisim1-VarD*: $P, E, h \vdash (Var \ V, xs) \leftrightarrow (stk, loc, pc, xcp) \implies xs = loc$

and $P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, xcp) \implies True$

by(*induct* ($Var \ V :: 'addr \ expr1, xs$) (stk, loc, pc, xcp) **and** *arbitrary*: $V \ xs \ stk \ loc \ pc \ xcp$ **and** *rule*: *bisim1-bisims1.inducts*) *auto*

lemma *bisim1-ThrowD*:

$P, e, h \vdash (Throw \ a, xs) \leftrightarrow (stk, loc, pc, xcp)$

$\implies pc < \text{length } (compE2 \ e) \wedge (xcp = [a] \vee xcp = None) \wedge xs = loc$

and *bisims1-ThrowD*:

$P, es, h \vdash (map \ Val \ vs \ @ \ Throw \ a \ \# \ es', xs) [\leftrightarrow] (stk, loc, pc, xcp)$

$\implies pc < \text{length } (compEs2 \ es) \wedge (xcp = [a] \vee xcp = None) \wedge xs = loc$

apply(*induct* ($Throw \ a :: 'addr \ expr1, xs$) (stk, loc, pc, xcp))

and ($map \ Val \ vs \ @ \ Throw \ a \ \# \ es', xs$) (stk, loc, pc, xcp)

arbitrary: $xs \ stk \ loc \ pc \ xcp$ **and** $vs \ es' \ xs \ stk \ loc \ pc \ xcp$ *rule*: *bisim1-bisims1.inducts*)

apply(*fastforce dest*: *bisim1-pc-length-compE2 bisim-Val-loc-eq-xcp-None simp add*: *Cons-eq-append-conv*)

done

lemma fixes $P :: 'addr\ J1\ prog$

shows $bisim1\ Invoke\ stkD:$

$\llbracket P, e, h \vdash exs \leftrightarrow (stk, loc, pc, None); pc < length\ (compE2\ e); compE2\ e\ !\ pc = Invoke\ M\ n' \rrbracket$
 $\implies \exists\ vs\ v\ stk'.\ stk = vs\ @\ v\ \# \ stk' \wedge length\ vs = n'$

and $bisims1\ Invoke\ stkD:$

$\llbracket P, es, h \vdash esxs\ [\leftrightarrow]\ (stk, loc, pc, None); pc < length\ (compEs2\ es); compEs2\ es\ !\ pc = Invoke\ M\ n' \rrbracket$
 $\implies \exists\ vs\ v\ stk'.\ stk = vs\ @\ v\ \# \ stk' \wedge length\ vs = n'$

proof($induct\ (stk, loc, pc, None :: 'addr\ option)$ **and** ($stk, loc, pc, None :: 'addr\ option$)

arbitrary: stk loc pc and stk loc pc rule: bisim1-bisims1.inducts)

case $bisim1Call1$

thus $?case$

apply($clarsimp\ simp\ add: nth\ append\ append\ eq\ append\ conv2\ neq\ Nil\ conv\ split: if\ split\ asm$)

apply($drule\ bisim1\ pc\ length\ compE2, clarsimp\ simp\ add: neq\ Nil\ conv\ nth\ append$)

apply($frule\ bisim1\ pc\ length\ compE2, clarsimp$)

apply($drule\ bisim1\ pc\ length\ compE2D, fastforce$)

done

next

case $bisim1CallParams$ **thus** $?case$

apply($clarsimp\ simp\ add: nth\ append\ append\ eq\ Cons\ conv\ split: if\ split\ asm$)

apply($fastforce\ simp\ add: append\ eq\ append\ conv2\ Cons\ eq\ append\ conv$)

apply($frule\ bisims1\ pc\ length\ compEs2, clarsimp$)

apply($drule\ bisims1\ pc\ length\ compEs2D, fastforce\ simp\ add: append\ eq\ append\ conv2$)

done

qed($fastforce\ simp\ add: nth\ append\ append\ eq\ append\ conv2\ neq\ Nil\ conv\ split: if\ split\ asm\ bop.\ split\ asm$
 $dest: bisim1\ pc\ length\ compE2\ bisims1\ pc\ length\ compEs2$) $+$

lemma fixes $P :: 'addr\ J1\ prog$

shows $bisim1\ call\ xcpNone: P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, [a]) \implies call1\ e' = None$

and $bisims1\ calls\ xcpNone: P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, [a]) \implies calls1\ es' = None$

apply($induct\ (e', xs)\ (stk, loc, pc, [a :: 'addr])$ **and** ($es', xs)\ (stk, loc, pc, [a :: 'addr])$)

arbitrary: e' xs stk loc pc and es' xs stk loc pc rule: bisim1-bisims1.inducts)

apply($auto\ dest: bisim\ Val\ loc\ eq\ xcp\ None\ bisims\ Val\ loc\ eq\ xcp\ None\ simp\ add: is\ vals\ conv$)

done

lemma $bisims1\ map\ Val\ append:$

assumes $bisim: P, es', h \vdash (es'', xs) [\leftrightarrow] (stk, loc, pc, xcp)$

shows $length\ es = length\ vs$

$\implies P, es\ @\ es', h \vdash (map\ Val\ vs\ @\ es'', xs) [\leftrightarrow] (stk\ @\ rev\ vs, loc, length\ (compEs2\ es) +$

$pc, xcp)$

proof($induction\ vs\ arbitrary: es$)

case Nil **thus** $?case$ **using** $bisim$ **by** $simp$

next

case ($Cons\ v\ vs$)

from $\langle length\ es = length\ (v\ \# \ vs) \rangle$ **obtain** $e\ es'''$ **where** $[simp]: es = e\ \# \ es'''$ **by**($cases\ es, auto$)

with $\langle length\ es = length\ (v\ \# \ vs) \rangle$ **have** $len: length\ es''' = length\ vs$ **by** $simp$

from $Cons.IH[OF\ len]$

show $?case$ **by**($simp\ add: add.\ assoc\ append\ assoc[symmetric]\ del: append\ assoc$)($rule\ bisims1List2,$

$auto$)

qed

lemma $bisim1\ hext\ mono: \llbracket P, e, h \vdash exs \leftrightarrow s; hext\ h\ h' \rrbracket \implies P, e, h' \vdash exs \leftrightarrow s$ (**is** $PROP\ ?thesis1$)

and $bisims1\ hext\ mono: \llbracket P, es, h \vdash esxs [\leftrightarrow] s; hext\ h\ h' \rrbracket \implies P, es, h' \vdash esxs [\leftrightarrow] s$ (**is** $PROP$)

```

?thesis2)
proof -
  assume hext: hext h h'
  have  $P, e, h \vdash \text{exs} \leftrightarrow s \implies P, e, h' \vdash \text{exs} \leftrightarrow s$ 
  and  $P, es, h \vdash \text{esxs} [\leftrightarrow] s \implies P, es, h' \vdash \text{esxs} [\leftrightarrow] s$ 
  apply (induct rule: bisim1-bisims1.inducts)
  apply (insert hext)
  apply (auto intro: bisim1-bisims1.intros dest: hext-objD)
  done
  thus PROP ?thesis1 and PROP ?thesis2 by auto
qed

declare match-ex-table-append-not-pcs [simp]
  match-ex-table-eq-NoneI [simp]
  outside-pcs-compxE2-not-matches-entry [simp]
  outside-pcs-compxEs2-not-matches-entry [simp]

lemma bisim1-xcp-Some-not-caught:
   $P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a])$ 
 $\implies \text{match-ex-table } (compP f P) (cname-of h a) (pc' + pc) (compxE2 e pc' d) = None$ 

  and bisims1-xcp-Some-not-caught:
   $P, es, h \vdash (\text{map Val vs } @ \text{ Throw } a \# es', xs) [\leftrightarrow] (stk, loc, pc, [a])$ 
 $\implies \text{match-ex-table } (compP f P) (cname-of h a) (pc' + pc) (compxEs2 es pc' d) = None$ 
proof (induct (Throw a :: 'addr expr1, xs) (stk, loc, pc, [a :: 'addr']))
  and (map Val vs @ Throw a # es' :: 'addr expr1 list, xs) (stk, loc, pc, [a :: 'addr'])
  arbitrary: xs stk loc pc pc' d and xs stk loc pc vs es' pc' d rule: bisim1-bisims1.inducts)
  case bisim1Sync10
  thus ?case by (simp add: matches-ex-entry-def)
next
  case bisim1Sync11
  thus ?case by (simp add: matches-ex-entry-def)
next
  case (bisim1SyncThrow e1 xs stk loc pc e2)
  note  $IH = \langle \text{match-ex-table } (compP f P) (cname-of h a) (pc' + pc) (compxE2 e1 pc' d) = None \rangle$ 
  from  $\langle P, e1, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$  have  $pc < \text{length } (compE2 e1)$  by (auto dest:
bisim1-ThrowD)
  with IH show ?case
  by (auto simp add: match-ex-table-append matches-ex-entry-def dest: match-ex-table-pc-length-compE2
intro: match-ex-table-not-pcs-None)
next
  case bisims1List1 thus ?case
  by (auto simp add: Cons-eq-append-conv dest: bisim1-ThrowD bisim-Val-loc-eq-xcp-None)
next
  case (bisims1List2 es es'' xs stk loc pc e v)
  hence  $\bigwedge pc'. \text{match-ex-table } (compP f P) (cname-of h a) (pc' + pc) (compxEs2 es pc' d) = None$ 
  by (auto simp add: Cons-eq-append-conv)
  from this[of  $pc' + \text{length } (compE2 e)$  Suc d] show ?case by (auto simp add: add.assoc)
next
  case (bisim1BlockThrowSome e xs stk loc pc T v)
  hence  $\bigwedge pc'. \text{match-ex-table } (compP f P) (cname-of h a) (pc' + pc) (compxE2 e pc' d) = None$  by
auto
  from this[of  $2+pc$ ] show ?case by (auto)
next

```

```

    case (bisim1Seq2 e2 stk loc pc e1 xs)
    hence  $\bigwedge pc'. \text{match-ex-table } (compP f P) (cname-of h a) (pc' + pc) (compxE2 e2 pc' d) = None$  by
    auto
    from this[of Suc (pc' + length (compE2 e1))] show ?case by(simp add: add.assoc)
  next
    case (bisim1CondThen e1 stk loc pc e e2 xs)
    hence  $\bigwedge pc'. \text{match-ex-table } (compP f P) (cname-of h a) (pc' + pc) (compxE2 e1 pc' d) = None$  by
    auto
    note this[of Suc (pc' + length (compE2 e))]
    moreover from  $\langle P, e1, h \vdash (Throw a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$ 
    have  $pc < \text{length } (compE2 e1)$  by(auto dest: bisim1-ThrowD)
    ultimately show ?case by(simp add: add.assoc match-ex-table-eq-NoneI outside-pcs-compxE2-not-matches-entry)
  next
    case (bisim1CondElse e2 stk loc pc e e1 xs)
    hence  $\bigwedge pc'. \text{match-ex-table } (compP f P) (cname-of h a) (pc' + pc) (compxE2 e2 pc' d) = None$  by
    auto
    note this[of Suc (Suc (pc' + (length (compE2 e) + length (compE2 e1)))]
    thus ?case by(simp add: add.assoc)
  next
    case (bisim1WhileThrow2 e xs stk loc pc c)
    hence  $\bigwedge pc'. \text{match-ex-table } (compP f P) (cname-of h a) (pc' + pc) (compxE2 e pc' d) = None$  by
    auto
    from this[of Suc (pc' + (length (compE2 c)))]
    show ?case by(simp add: add.assoc)
  next
    case (bisim1Throw1 e xs stk loc pc)
    thus ?case by(auto dest: bisim-Val-loc-eq-xcp-None)
  next
    case (bisim1TryFail e xs stk loc pc C' C e2)
    hence  $\text{match-ex-table } (compP f P) (cname-of h a) (pc' + pc) (compxE2 e pc' d) = None$  by auto
    moreover from  $\langle P, e, h \vdash (Throw a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$  have  $pc < \text{length } (compE2 e)$ 
    by(auto dest: bisim1-ThrowD)
    ultimately show ?case using  $\langle \text{typeof-addr } h a = [Class\text{-type } C'] \rangle \langle \neg P \vdash C' \preceq^* C \rangle$ 
    by(simp add: matches-ex-entry-def cname-of-def)
  next
    case (bisim1TryCatchThrow e2 xs stk loc pc e C)
    hence  $\bigwedge pc'. \text{match-ex-table } (compP f P) (cname-of h a) (pc' + pc) (compxE2 e2 pc' d) = None$  by
    auto
    from this[of Suc (Suc (pc' + (length (compE2 e)))]
    show ?case by(simp add: add.assoc matches-ex-entry-def)
  next
    case bisim1Sync12 thus ?case
    by(auto dest: bisim1-ThrowD simp add: match-ex-table-append eval-nat-numeral, simp add: matches-ex-entry-def)
  next
    case bisim1Sync14 thus ?case
    by(auto dest: bisim1-ThrowD simp add: match-ex-table-append eval-nat-numeral, simp add: matches-ex-entry-def)
qed(fastforce dest: bisim1-ThrowD simp add: add.assoc[symmetric])+

declare match-ex-table-append-not-pcs [simp del]
    match-ex-table-eq-NoneI [simp del]
    outside-pcs-compxE2-not-matches-entry [simp del]
    outside-pcs-compxEs2-not-matches-entry [simp del]

```

lemma bisim1-xcp-pcD: $P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, [a]) \implies pc < \text{length } (compE2 e)$

and *bisims1-xcp-pcD*: $P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, [a]) \implies pc < length (compEs2 es)$
by(*induct* (e', xs) ($stk, loc, pc, [a :: 'addr]$)) **and** (es', xs) ($stk, loc, pc, [a :: 'addr]$)
arbitrary: $e' xs stk loc pc$ **and** $es' xs stk loc pc$ *rule*: *bisim1-bisims1.inducts*)
auto

declare *nth-append* [*simp*]

lemma *bisim1-Val-Exec-move*:

$\llbracket P, E, h \vdash (Val v, xs) \leftrightarrow (stk, loc, pc, xcp); pc < length (compE2 E) \rrbracket$
 $\implies xs = loc \wedge xcp = None \wedge$
 $\tauExec-mover-a P t E h (stk, xs, pc, None) ([v], xs, length (compE2 E), None)$

and *bisims1-Val-Exec-moves*:

$\llbracket P, Es, h \vdash (map Val vs, xs) [\leftrightarrow] (stk, loc, pc, xcp); pc < length (compEs2 Es) \rrbracket$
 $\implies xs = loc \wedge xcp = None \wedge$
 $\tauExec-moves-r-a P t Es h (stk, xs, pc, None) (rev vs, xs, length (compEs2 Es), None)$

proof(*induct* ($Val v :: 'addr expr1, xs$) (stk, loc, pc, xcp))

and ($map Val vs :: 'addr expr1 list, xs$) (stk, loc, pc, xcp)

arbitrary: $v xs stk loc pc xcp$ **and** $vs xs stk loc pc xcp$ *rule*: *bisim1-bisims1.inducts*)

case *bisim1Val* **thus** ?*case* **by**(*auto intro!*: $\tauExecr1step exec-instr \tau move2Val simp add: exec-move-def$)

next

case (*bisim1LAss2* $V e xs$)

have $\tauExec-mover-a P t (V := e) h ([], xs, Suc (length (compE2 e)), None) ([Unit], xs, Suc (Suc (length (compE2 e))), None)$

by(*auto intro!*: $\tauExecr1step exec-instr \tau move2LAssRed2 simp add: exec-move-def$)

with *bisim1LAss2* **show** ?*case* **by** *simp*

next

case (*bisim1AAss4* $a i e xs$)

have $\tauExec-mover-a P t (a[i] := e) h ([], xs, Suc (length (compE2 a) + length (compE2 i) + length (compE2 e)), None) ([Unit], xs, Suc (Suc (length (compE2 a) + length (compE2 i) + length (compE2 e))), None)$

by(*auto intro!*: $\tauExecr1step exec-instr \tau move2AAssRed simp add: exec-move-def$)

with *bisim1AAss4* **show** ?*case* **by**(*simp add: ac-simps*)

next

case (*bisim1FAss3* $e F D e2 xs$)

have $\tauExec-mover-a P t (e \cdot F\{D\} := e2) h ([], xs, Suc (length (compE2 e) + length (compE2 e2)), None) ([Unit], xs, Suc (Suc (length (compE2 e) + length (compE2 e2))), None)$

by(*auto intro!*: $\tauExecr1step exec-instr \tau move2FAssRed simp add: exec-move-def$)

with *bisim1FAss3* **show** ?*case* **by** *simp*

next

case (*bisim1Sync6* $V e1 e2 v xs$)

have $\tauExec-mover-a P t (sync_V (e1) e2) h ([v], xs, 5 + length (compE2 e1) + length (compE2 e2), None)$

$([v], xs, 9 + length (compE2 e1) + length (compE2 e2), None)$

by(*rule* $\tauExecr1step$)(*auto intro: exec-instr \tau move2Sync6 simp add: exec-move-def*)

with *bisim1Sync6* **show** ?*case* **by**(*auto simp add: eval-nat-numeral*)

next

case (*bisim1Seq2* $e2 stk loc pc xcp e1 v xs$)

from $\langle Suc (length (compE2 e1) + pc) < length (compE2 (e1;; e2)) \rangle$ **have** $pc: pc < length (compE2 e2)$ **by** *simp*

with $\langle pc < length (compE2 e2) \implies xs = loc \wedge xcp = None \wedge \tauExec-mover-a P t e2 h (stk, xs, pc, None) ([v], xs, length (compE2 e2), None) \rangle$

have $xs = loc xcp = None$

$\tauExec-mover-a P t e2 h (stk, xs, pc, None) ([v], xs, length (compE2 e2), None)$ **by** *auto*

moreover
hence $\tau\text{Exec-mover-a } P \ t \ (e1;;e2) \ h \ (stk, \ xs, \ Suc \ (\text{length} \ (\text{compE2} \ e1) \ + \ pc), \ None) \ ([v], \ xs, \ Suc \ (\text{length} \ (\text{compE2} \ e1) \ + \ \text{length} \ (\text{compE2} \ e2)), \ None)$
by $-(\text{rule } \text{Seq-}\tau\text{ExecrI2})$
ultimately show $?case$ **by**(*simp*)

next
case (*bisim1CondThen* $e1 \ stk \ loc \ pc \ xcp \ e \ e2 \ v \ xs$)
from $\langle P, \ e1, \ h \vdash \ (Val \ v, \ xs) \leftrightarrow \ (stk, \ loc, \ pc, \ xcp) \rangle$
have $pc \leq \text{length} \ (\text{compE2} \ e1)$ **by**(*rule bisim1-pc-length-compE2*)

have $e: \tau\text{Exec-mover-a } P \ t \ (\text{if} \ (e) \ e1 \ \text{else} \ e2) \ h$
 $([v], \ xs, \ Suc \ (\text{length} \ (\text{compE2} \ e) \ + \ (\text{length} \ (\text{compE2} \ e1))), \ None)$
 $([v], \ xs, \ \text{length} \ (\text{compE2} \ (\text{if} \ (e) \ e1 \ \text{else} \ e2)), \ None)$
by(*rule* $\tau\text{Execr1step}$)(*auto simp add: exec-move-def intro!: exec-instr* $\tau\text{move2CondThenExit}$)

show $?case$
proof(*cases* $pc < \text{length} \ (\text{compE2} \ e1)$)
case *True*
with $\langle pc < \text{length} \ (\text{compE2} \ e1) \rangle$
 $\implies xs = loc \wedge xcp = None \wedge \tau\text{Exec-mover-a } P \ t \ e1 \ h \ (stk, \ xs, \ pc, \ None) \ ([v], \ xs, \ \text{length} \ (\text{compE2} \ e1), \ None) \rangle$
have $s: xs = loc \ xcp = None$
and $\tau\text{Exec-mover-a } P \ t \ e1 \ h \ (stk, \ xs, \ pc, \ None) \ ([v], \ xs, \ \text{length} \ (\text{compE2} \ e1), \ None)$ **by** *auto*
hence $\tau\text{Exec-mover-a } P \ t \ (\text{if} \ (e) \ e1 \ \text{else} \ e2) \ h$
 $(stk, \ xs, \ Suc \ (\text{length} \ (\text{compE2} \ e) \ + \ pc), \ None)$
 $([v], \ xs, \ Suc \ (\text{length} \ (\text{compE2} \ e) \ + \ \text{length} \ (\text{compE2} \ e1)), \ None)$
by $-(\text{rule } \text{Cond-}\tau\text{ExecrI2})$
with $e \ True \ s$ **show** $?thesis$ **by**(*simp*)

next
case *False*
with $\langle pc \leq \text{length} \ (\text{compE2} \ e1) \rangle$ **have** $pc: pc = \text{length} \ (\text{compE2} \ e1)$ **by** *auto*
with $\langle P, \ e1, \ h \vdash \ (Val \ v, \ xs) \leftrightarrow \ (stk, \ loc, \ pc, \ xcp) \rangle$
have $stk = [v] \ xs = loc \ xcp = None$ **by**(*auto dest: bisim1-Val-length-compE2D*)
with $pc \ e$ **show** $?thesis$ **by**(*simp*)

qed

next
case (*bisim1CondElse* $e2 \ stk \ loc \ pc \ xcp \ e \ e1 \ v \ xs$)
from $\langle P, \ e2, \ h \vdash \ (Val \ v, \ xs) \leftrightarrow \ (stk, \ loc, \ pc, \ xcp) \rangle$
have $pc \leq \text{length} \ (\text{compE2} \ e2)$ **by**(*rule bisim1-pc-length-compE2*)

show $?case$
proof(*cases* $pc < \text{length} \ (\text{compE2} \ e2)$)
case *True*
with $\langle pc < \text{length} \ (\text{compE2} \ e2) \rangle$
 $\implies xs = loc \wedge xcp = None \wedge \tau\text{Exec-mover-a } P \ t \ e2 \ h \ (stk, \ xs, \ pc, \ None) \ ([v], \ xs, \ \text{length} \ (\text{compE2} \ e2), \ None) \rangle$
have $s: xs = loc \ xcp = None$
and $e: \tau\text{Exec-mover-a } P \ t \ e2 \ h \ (stk, \ xs, \ pc, \ None) \ ([v], \ xs, \ \text{length} \ (\text{compE2} \ e2), \ None)$ **by** *auto*
from e **have** $\tau\text{Exec-mover-a } P \ t \ (\text{if} \ (e) \ e1 \ \text{else} \ e2) \ h \ (stk, \ xs, \ Suc \ (\text{Suc} \ (\text{length} \ (\text{compE2} \ e) \ + \ \text{length} \ (\text{compE2} \ e1) \ + \ pc)), \ None) \ ([v], \ xs, \ Suc \ (\text{Suc} \ (\text{length} \ (\text{compE2} \ e) \ + \ \text{length} \ (\text{compE2} \ e1) \ + \ \text{length} \ (\text{compE2} \ e2))), \ None)$
by(*rule* $\text{Cond-}\tau\text{ExecrI3}$)
with $True \ s$ **show** $?thesis$ **by**(*simp add: add.assoc*)

next
case *False*

with $\langle pc \leq \text{length}(\text{compE2 } e2) \rangle$ **have** $pc: pc = \text{length}(\text{compE2 } e2)$ **by** *auto*
with $\langle P, e2, h \vdash (\text{Val } v, xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
have $stk = [v]$ $xs = loc$ $xcp = \text{None}$ **by**(*auto dest: bisim1-Val-length-compE2D*)
with pc **show** *?thesis* **by**(*simp add: add.assoc*)
qed
next
case (*bisim1While7 c e xs*)
have $\tau\text{Exec-mover-a } P t (\text{while } (c) e) h$
 $([], xs, \text{Suc}(\text{Suc}(\text{Suc}(\text{length}(\text{compE2 } c) + \text{length}(\text{compE2 } e))))$, *None*)
 $([\text{Unit}], xs, \text{length}(\text{compE2 } (\text{while } (c) e))$, *None*)
by(*auto intro!: \tauExecr1step exec-instr \tau move2While4 simp add: exec-move-def*)
thus *?case* **by**(*simp*)
next
case (*bisims1List1 e e' xs stk loc pc xcp es*)
from $\langle e' \# es = \text{map Val } vs \rangle$ **obtain** v vs' **where** [*simp*]: $vs = v \# vs'$ $e' = \text{Val } v$ $es = \text{map Val } vs'$
by *auto*
from $\langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
have $\text{length}: pc \leq \text{length}(\text{compE2 } e)$ **by**(*auto dest: bisim1-pc-length-compE2*)
hence $xs = loc \wedge xcp = \text{None} \wedge \tau\text{Exec-mover-a } P t e h (stk, xs, pc, \text{None}) ([v], xs, \text{length}(\text{compE2 } e), \text{None})$
proof(*cases pc < length(compE2 e)*)
case *True*
with $\langle [e' = \text{Val } v; pc < \text{length}(\text{compE2 } e)] \implies xs = loc \wedge xcp = \text{None} \wedge \tau\text{Exec-mover-a } P t e h (stk, xs, pc, \text{None}) ([v], xs, \text{length}(\text{compE2 } e), \text{None}) \rangle$
show *?thesis* **by** *auto*
next
case *False*
with length **have** $pc: pc = \text{length}(\text{compE2 } e)$ **by** *auto*
with $\langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ **have** $stk = [v]$ $xs = loc$ $xcp = \text{None}$
by(*auto dest: bisim1-Val-length-compE2D*)
with pc **show** *?thesis* **by**(*auto*)
qed
hence $s: xs = loc$ $xcp = \text{None}$
and $\text{exec1}: \tau\text{Exec-mover-a } P t e h (stk, xs, pc, \text{None}) ([v], xs, \text{length}(\text{compE2 } e), \text{None})$ **by** *auto*
from exec1 **have** $\tau\text{Exec-movesr-a } P t (e \# es) h (stk, xs, pc, \text{None}) ([v], xs, \text{length}(\text{compE2 } e), \text{None})$
by(*auto intro: \tauExec-mover-\tauExec-movesr*)
moreover **have** $\tau\text{Exec-movesr-a } P t (\text{map Val } vs') h ([], xs, 0, \text{None}) (\text{rev } vs', xs, \text{length}(\text{compEs2 } (\text{map Val } vs')), \text{None})$
by(*rule \tauExec-movesr-map-Val*)
hence $\tau\text{Exec-movesr-a } P t ([e] @ \text{map Val } vs') h ([] @ [v], xs, \text{length}(\text{compEs2 } [e]) + 0, \text{None}) (\text{rev } vs' @ [v], xs, \text{length}(\text{compEs2 } [e]) + \text{length}(\text{compEs2 } (\text{map Val } vs')), \text{None})$
by $-(\text{rule append-}\tau\text{Exec-movesr}, \text{auto})$
ultimately **show** *?case* **using** s **by**(*auto*)
next
case (*bisims1List2 es es' xs stk loc pc xcp e v*)
from $\langle \text{Val } v \# es' = \text{map Val } vs \rangle$ **obtain** vs' **where** [*simp*]: $vs = v \# vs'$ $es' = \text{map Val } vs'$ **by** *auto*
from $\langle P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, xcp) \rangle$
have $\text{length}: pc \leq \text{length}(\text{compEs2 } es)$ **by**(*auto dest: bisims1-pc-length-compEs2*)
hence $xs = loc \wedge xcp = \text{None} \wedge \tau\text{Exec-movesr-a } P t es h (stk, xs, pc, \text{None}) (\text{rev } vs', xs, \text{length}(\text{compEs2 } es), \text{None})$
proof(*cases pc < length(compEs2 es)*)
case *True*
with $\langle [es' = \text{map Val } vs'; pc < \text{length}(\text{compEs2 } es)] \implies xs = loc \wedge xcp = \text{None} \wedge \tau\text{Exec-movesr-a } P t es h (stk, xs, pc, \text{None}) (\text{rev } vs', xs, \text{length}(\text{compEs2 } es), \text{None}) \rangle$

```

P t e s h (stk, xs, pc, None)
  (rev vs', xs, length (compEs2 es), None)
  show ?thesis by auto
next
case False
with length have pc: pc = length (compEs2 es) by auto
with ⟨P, es, h ⊢ (es', xs) [↔] (stk, loc, pc, xcp)⟩ have stk = rev vs' xs = loc xcp = None
  by (auto dest: bisims1-Val-length-compEs2D)
with pc show ?thesis by (auto)
qed
hence s: xs = loc xcp = None
and exec1: τExec-movesr-a P t e s h (stk, xs, pc, None) (rev vs', xs, length (compEs2 es), None)
by auto
from exec1 have τExec-movesr-a P t ([e] @ es) h (stk @ [v], xs, length (compEs2 [e]) + pc, None)
(rev vs' @ [v], xs, length (compEs2 [e]) + length (compEs2 es), None)
  by -(rule append-τExec-movesr, auto)
thus ?case using s by (auto)
qed(auto)

```

lemma *bisim1Val2D1*:

```

assumes bisim: P, e, h ⊢ (Val v, xs) ↔ (stk, loc, pc, xcp)
shows xcp = None ∧ xs = loc ∧ τExec-mover-a P t e h (stk, loc, pc, xcp) ([v], loc, length (compE2
e), None)
proof -
from bisim have xcp = None xs = loc by (auto dest: bisim-Val-loc-eq-xcp-None)
moreover
have τExec-mover-a P t e h (stk, loc, pc, xcp) ([v], loc, length (compE2 e), None)
proof (cases pc < length (compE2 e))
case True
from bisim1-Val-τExec-move[OF bisim True] show ?thesis by auto
next
case False
from bisim have pc ≤ length (compE2 e) by (auto dest: bisim1-pc-length-compE2)
with False have pc = length (compE2 e) by auto
with bisim have stk = [v] loc = xs xcp=None by (auto dest: bisim1-Val-length-compE2D)
with ⟨pc = length (compE2 e)⟩ show ?thesis by (auto)
qed
ultimately show ?thesis by simp
qed

```

lemma *bisim1-Throw-τExec-movet*:

```

[[ P, e, h ⊢ (Throw a, xs) ↔ (stk, loc, pc, None) ]]
⇒ ∃ pc'. τExec-movet-a P t e h (stk, loc, pc, None) ([Addr a], loc, pc', [a]) ∧
P, e, h ⊢ (Throw a, xs) ↔ ([Addr a], loc, pc', [a]) ∧ xs = loc

```

and *bisims1-Throw-τExec-movest*:

```

[[ P, es, h ⊢ (map Val vs @ Throw a # es', xs) [↔] (stk, loc, pc, None) ]]
⇒ ∃ pc'. τExec-movest-a P t e s h (stk, loc, pc, None) (Addr a # rev vs, loc, pc', [a]) ∧
P, es, h ⊢ (map Val vs @ Throw a # es', xs) [↔] (Addr a # rev vs, loc, pc', [a]) ∧ xs = loc

```

proof (induct e n :: nat Throw a :: 'addr expr1 xs stk loc pc None :: 'addr option

and es n :: nat map Val vs @ Throw a # es' :: 'addr expr1 list xs stk loc pc None :: 'addr option
arbitrary: and vs rule: bisim1-bisims1-inducts-split)

case (bisim1Sync9 e1 n e2 V xs)

let ?pc = 8 + length (compE2 e1) + length (compE2 e2)

have $\tauExec\text{-}movet\text{-}a\ P\ t\ (sync\ \vee\ (e1)\ e2)\ h\ ([Addr\ a],\ xs,\ ?pc,\ None)\ ([Addr\ a],\ xs,\ ?pc,\ [a])$
by(rule $\tauExec1step$)(auto intro: *exec-instr* $\tau move2\text{-}\tau moves2$.intros simp add: *is-Ref-def* *exec-move-def*)
moreover
have $P,\ sync\ \vee\ (e1)\ e2,\ h\ \vdash\ (Throw\ a,\ xs)\ \leftrightarrow\ ([Addr\ a],\ xs,\ ?pc,\ [a])$ **by**(rule *bisim1Sync10*)
ultimately show *?case* **by** auto
next
case (*bisim1Seq2* $e2\ n\ xs\ stk\ loc\ pc\ e1$)
then obtain pc' **where** $\tauExec\text{-}movet\text{-}a\ P\ t\ e2\ h\ (stk,\ loc,\ pc,\ None)\ ([Addr\ a],\ loc,\ pc',\ [a])$
 $P,\ e2,\ h\ \vdash\ (Throw\ a,\ xs)\ \leftrightarrow\ ([Addr\ a],\ loc,\ pc',\ [a])\ xs = loc$ **by** auto
thus *?case* **by**(auto intro: *Seq-ExecI2* *bisim1-bisims1*.*bisim1Seq2*)
next
case (*bisim1CondThen* $e1\ n\ xs\ stk\ loc\ pc\ e\ e2$)
then obtain pc' **where** $exec:\ \tauExec\text{-}movet\text{-}a\ P\ t\ e1\ h\ (stk,\ loc,\ pc,\ None)\ ([Addr\ a],\ loc,\ pc',\ [a])$
and *bisim*: $P,\ e1,\ h\ \vdash\ (Throw\ a,\ xs)\ \leftrightarrow\ ([Addr\ a],\ loc,\ pc',\ [a])$ **and** $s:\ xs = loc$ **by** auto
from *exec* **have** $\tauExec\text{-}movet\text{-}a\ P\ t\ (if\ (e)\ e1\ else\ e2)\ h\ (stk,\ loc,\ Suc\ (length\ (compE2\ e)\ +\ pc),\ None)\ ([Addr\ a],\ loc,\ Suc\ (length\ (compE2\ e)\ +\ pc'),\ [a])$
by(rule *Cond-ExecI2*)
moreover from *bisim*
have $P,\ if\ (e)\ e1\ else\ e2,\ h\ \vdash\ (Throw\ a,\ xs)\ \leftrightarrow\ ([Addr\ a],\ loc,\ Suc\ (length\ (compE2\ e)\ +\ pc'),\ [a])$
by(rule *bisim1-bisims1*.*bisim1CondThen*)
ultimately show *?case* **using** s **by**(auto)
next
case (*bisim1CondElse* $e2\ n\ xs\ stk\ loc\ pc\ e\ e1$)
then obtain pc' **where** $exec:\ \tauExec\text{-}movet\text{-}a\ P\ t\ e2\ h\ (stk,\ loc,\ pc,\ None)\ ([Addr\ a],\ loc,\ pc',\ [a])$
and *bisim*: $P,\ e2,\ h\ \vdash\ (Throw\ a,\ xs)\ \leftrightarrow\ ([Addr\ a],\ loc,\ pc',\ [a])$ **and** $s:\ xs = loc$ **by** auto
let $?pc\ pc = Suc\ (Suc\ (length\ (compE2\ e)\ +\ length\ (compE2\ e1)\ +\ pc))$
from *exec* **have** $\tauExec\text{-}movet\text{-}a\ P\ t\ (if\ (e)\ e1\ else\ e2)\ h\ (stk,\ loc,\ (?pc\ pc),\ None)\ ([Addr\ a],\ loc,\ ?pc\ pc',\ [a])$
by(rule *Cond-ExecI3*)
moreover from *bisim*
have $P,\ if\ (e)\ e1\ else\ e2,\ h\ \vdash\ (Throw\ a,\ xs)\ \leftrightarrow\ ([Addr\ a],\ loc,\ ?pc\ pc',\ [a])$
by(rule *bisim1-bisims1*.*bisim1CondElse*)
ultimately show *?case* **using** s **by** auto
next
case (*bisim1Throw1* $e\ n\ xs\ stk\ loc\ pc$)
note $bisim = \langle P,\ e,\ h\ \vdash\ (addr\ a,\ xs)\ \leftrightarrow\ (stk,\ loc,\ pc,\ None) \rangle$
hence $s:\ xs = loc$
and $exec:\ \tauExec\text{-}mover\text{-}a\ P\ t\ e\ h\ (stk,\ loc,\ pc,\ None)\ ([Addr\ a],\ loc,\ length\ (compE2\ e),\ None)$
by(auto dest: *bisim1Val2D1*)
from *exec* **have** $\tauExec\text{-}mover\text{-}a\ P\ t\ (throw\ e)\ h\ (stk,\ loc,\ pc,\ None)\ ([Addr\ a],\ loc,\ length\ (compE2\ e),\ None)$
by(rule *Throw-ExecI*)
also have $\tauExec\text{-}movet\text{-}a\ P\ t\ (throw\ e)\ h\ ([Addr\ a],\ loc,\ length\ (compE2\ e),\ None)\ ([Addr\ a],\ loc,\ length\ (compE2\ e),\ [a])$
by(rule $\tauExec1step$, auto intro: *exec-instr* $\tau move2\ Throw2$ simp add: *is-Ref-def* *exec-move-def*)
also have $P,\ throw\ e,\ h\ \vdash\ (Throw\ a,\ loc)\ \leftrightarrow\ ([Addr\ a],\ loc,\ length\ (compE2\ e),\ [a])$
by(rule *bisim1Throw2*)
ultimately show *?case* **using** s **by** auto
next
case (*bisims1List1* $e\ n\ e'\ xs\ stk\ loc\ pc\ es\ vs$)
note $bisim = \langle P,\ e,\ h\ \vdash\ (e',\ xs)\ \leftrightarrow\ (stk,\ loc,\ pc,\ None) \rangle$
show *?case*
proof(cases *is-val* e')
case *True*

with $\langle e' \# es = \text{map Val } vs \ @ \ \text{Throw } a \ # \ es' \rangle$ **obtain** $v \ vs'$ **where** $vs = v \ # \ vs' \ e' = \text{Val } v$
and $es: es = \text{map Val } vs' \ @ \ \text{Throw } a \ # \ es'$ **by** $(\text{auto simp add: Cons-eq-append-conv})$
with bisim **have** $P, e, h \vdash (\text{Val } v, xs) \leftrightarrow (stk, loc, pc, \text{None})$ **by** simp
from $\text{bisim1Val2D1}[OF \ \text{this}]$ **have** $[\text{simp}]: xs = loc$
and $\text{exec}: \tau \text{Exec-mover-a } P \ t \ e \ h \ (stk, loc, pc, \text{None}) \ ([v], loc, \text{length}(\text{compE2 } e), \text{None})$
by auto
from exec **have** $\tau \text{Exec-movesr-a } P \ t \ (e \ # \ es) \ h \ (stk, loc, pc, \text{None}) \ ([v], loc, \text{length}(\text{compE2 } e), \text{None})$
by $(\text{rule } \tau \text{Exec-mover-}\tau \text{Exec-movesr})$
also from $es \ \langle es = \text{map Val } vs' \ @ \ \text{Throw } a \ # \ es' \rangle$
 $\implies \exists pc'. \tau \text{Exec-movest-a } P \ t \ es \ h \ ([], loc, 0, \text{None}) \ (\text{Addr } a \ # \ \text{rev } vs', loc, pc', [a]) \wedge$
 $P, es, h \vdash (\text{map Val } vs' \ @ \ \text{Throw } a \ # \ es', loc) \ [\leftrightarrow] \ (\text{Addr } a \ # \ \text{rev } vs', loc, pc', [a]) \wedge loc = loc$
obtain pc' **where** $\text{exec}: \tau \text{Exec-movest-a } P \ t \ es \ h \ ([], loc, 0, \text{None}) \ (\text{Addr } a \ # \ \text{rev } vs', loc, pc', [a])$
 $[a]$
and $\text{bisim}': P, es, h \vdash (\text{map Val } vs' \ @ \ \text{Throw } a \ # \ es', loc) \ [\leftrightarrow] \ (\text{Addr } a \ # \ \text{rev } vs', loc, pc', [a])$
by auto
from $\text{append-}\tau \text{Exec-movest}[OF \ - \ \text{exec}, \text{ of } [v] \ [e]]$
have $\tau \text{Exec-movest-a } P \ t \ (e \ # \ es) \ h \ ([v], loc, \text{length}(\text{compE2 } e), \text{None}) \ (\text{Addr } a \ # \ \text{rev } vs' \ @ \ [v], loc, \text{length}(\text{compE2 } e) + pc', [a])$ **by** simp
also from $\text{bisims1List2}[OF \ \text{bisim}', \text{ of } e \ v] \ es \ \langle e' = \text{Val } v \rangle \ \langle vs = v \ # \ vs' \rangle$
have $P, e \ # \ es, h \vdash (e' \ # \ es, xs) \ [\leftrightarrow] \ ((\text{Addr } a \ # \ \text{rev } vs), loc, \text{length}(\text{compE2 } e) + pc', [a])$ **by** simp
ultimately show $?thesis$ **using** $\langle vs = v \ # \ vs' \rangle \ es \ \langle e' = \text{Val } v \rangle$ **by** auto
next
case False
with $\langle e' \ # \ es = \text{map Val } vs \ @ \ \text{Throw } a \ # \ es' \rangle$ **have** $[\text{simp}]: e' = \text{Throw } a \ es = es' \ vs = []$
by $(\text{auto simp add: Cons-eq-append-conv})$
from $\langle e' = \text{Throw } a \implies \exists pc'. \tau \text{Exec-movet-a } P \ t \ e \ h \ (stk, loc, pc, \text{None}) \ ([\text{Addr } a], loc, pc', [a])$
 $\wedge P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], loc, pc', [a]) \wedge xs = loc$
obtain pc' **where** $\tau \text{Exec-movet-a } P \ t \ e \ h \ (stk, loc, pc, \text{None}) \ ([\text{Addr } a], loc, pc', [a])$
and $\text{bisim}: P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], loc, pc', [a])$ **and** $s: xs = loc$ **by** auto
hence $\tau \text{Exec-movest-a } P \ t \ (e \ # \ es) \ h \ (stk, loc, pc, \text{None}) \ ([\text{Addr } a], loc, pc', [a])$
by $(\text{rule } \tau \text{Exec-movet-}\tau \text{Exec-movest})$
moreover from bisim
have $P, e \ # \ es, h \vdash (\text{Throw } a \ # \ es, xs) \ [\leftrightarrow] \ ([\text{Addr } a], loc, pc', [a])$ **by** $(\text{rule } \text{bisim1-bisims1.bisims1List1})$
ultimately show $?thesis$ **using** s **by** auto
qed
next
case $(\text{bisims1List2 } es \ n \ es'' \ xs \ stk \ loc \ pc \ e \ v)$
note $IH = \langle \wedge vs. es'' = \text{map Val } vs \ @ \ \text{Throw } a \ # \ es' \rangle$
 $\implies \exists pc'. \tau \text{Exec-movest-a } P \ t \ es \ h \ (stk, loc, pc, \text{None}) \ (\text{Addr } a \ # \ \text{rev } vs, loc, pc', [a]) \wedge$
 $P, es, h \vdash (\text{map Val } vs \ @ \ \text{Throw } a \ # \ es', xs) \ [\leftrightarrow] \ (\text{Addr } a \ # \ \text{rev } vs, loc, pc', [a]) \wedge xs = loc$
from $\langle \text{Val } v \ # \ es'' = \text{map Val } vs \ @ \ \text{Throw } a \ # \ es' \rangle$
obtain vs' **where** $[\text{simp}]: vs = v \ # \ vs' \ es'' = \text{map Val } vs' \ @ \ \text{Throw } a \ # \ es'$ **by** $(\text{auto simp add: Cons-eq-append-conv})$
from $IH[OF \ \langle es'' = \text{map Val } vs' \ @ \ \text{Throw } a \ # \ es' \rangle]$
obtain pc' **where** $\text{exec}: \tau \text{Exec-movest-a } P \ t \ es \ h \ (stk, loc, pc, \text{None}) \ (\text{Addr } a \ # \ \text{rev } vs', loc, pc', [a])$
and $\text{bisim}: P, es, h \vdash (\text{map Val } vs' \ @ \ \text{Throw } a \ # \ es', xs) \ [\leftrightarrow] \ (\text{Addr } a \ # \ \text{rev } vs', loc, pc', [a])$
and $[\text{simp}]: xs = loc$ **by** auto
from $\text{append-}\tau \text{Exec-movest}[OF \ - \ \text{exec}, \text{ of } [v] \ [e]]$
have $\tau \text{Exec-movest-a } P \ t \ (e \ # \ es) \ h \ (stk \ @ \ [v], loc, \text{length}(\text{compE2 } e) + pc, \text{None}) \ (\text{Addr } a \ # \ \text{rev } vs, loc, \text{length}(\text{compE2 } e) + pc', [a])$ **by** simp
moreover from bisim
have $P, e \ # \ es, h \vdash (\text{Val } v \ # \ \text{map Val } vs' \ @ \ \text{Throw } a \ # \ es', xs) \ [\leftrightarrow] \ ((\text{Addr } a \ # \ \text{rev } vs') \ @ \ [v], loc, \text{length}(\text{compE2 } e) + pc', [a])$

```
(compE2 e) + pc', [a])
  by(rule bisim1-bisims1.bisims1List2)
  ultimately show ?case by(auto)
qed(auto)
```

lemma *bisim1-Throw- τ Exec-mover*:

```
[[ P, e, h  $\vdash$  (Throw a, xs)  $\leftrightarrow$  (stk, loc, pc, None) ]]
 $\implies \exists pc'. \tau$ Exec-mover-a P t e h (stk, loc, pc, None) ([Addr a], loc, pc', [a])  $\wedge$ 
  P, e, h  $\vdash$  (Throw a, xs)  $\leftrightarrow$  ([Addr a], loc, pc', [a])  $\wedge$  xs = loc
by(drule bisim1-Throw- $\tau$ Exec-movet)(blast intro: tranclp-into-rtranclp)
```

lemma *bisims1-Throw- τ Exec-movesr*:

```
[[ P, es, h  $\vdash$  (map Val vs @ Throw a # es', xs) [ $\leftrightarrow$ ] (stk, loc, pc, None) ]]
 $\implies \exists pc'. \tau$ Exec-movesr-a P t es h (stk, loc, pc, None) (Addr a # rev vs, loc, pc', [a])  $\wedge$ 
  P, es, h  $\vdash$  (map Val vs @ Throw a # es', xs) [ $\leftrightarrow$ ] (Addr a # rev vs, loc, pc', [a])  $\wedge$  xs = loc
by(drule bisims1-Throw- $\tau$ Exec-movest)(blast intro: tranclp-into-rtranclp)
```

declare *split-beta* [simp]

lemma *bisim1-inline-call-Throw*:

```
[[ P, e, h  $\vdash$  (e', xs)  $\leftrightarrow$  (stk, loc, pc, None); call1 e' = [(a, M, vs)];
  compE2 e ! pc = Invoke M n0; pc < length (compE2 e) ]]
 $\implies n0 = \text{length } vs \wedge P, e, h \vdash (\text{inline-call } (\text{Throw } A) e', xs) \leftrightarrow (stk, loc, pc, [A])$ 
(is [[ -; -; - ]  $\implies$  ?concl e n e' xs pc stk loc)
```

and *bisims1-inline-calls-Throw*:

```
[[ P, es, h  $\vdash$  (es', xs) [ $\leftrightarrow$ ] (stk, loc, pc, None); calls1 es' = [(a, M, vs)];
  compEs2 es ! pc = Invoke M n0; pc < length (compEs2 es) ]]
 $\implies n0 = \text{length } vs \wedge P, es, h \vdash (\text{inline-calls } (\text{Throw } A) es', xs) [ $\leftrightarrow$ ] (stk, loc, pc, [A])$ 
(is [[ -; -; - ]  $\implies$  ?concls es n es' xs pc stk loc)
```

proof(*induct e n :: nat e' xs stk loc pc None :: 'addr option*

and *es n :: nat es' xs stk loc pc None :: 'addr option*

rule: bisim1-bisims1-inducts-split)

case (*bisim1BinOp1 e1 n e' xs stk loc pc e2 bop*)

note *IH1* = \langle [[call1 e' = [(a, M, vs)]; compE2 e1 ! pc = Invoke M n0; pc < length (compE2 e1)]]
 \implies ?concl e1 n e' xs pc stk loc

note *bisim1* = $\langle P, e1, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, None) \rangle$

note *ins* = $\langle \text{compE2 } (e1 \ll bop \gg e2) ! pc = \text{Invoke } M n0 \rangle$

note *call* = $\langle \text{call1 } (e' \ll bop \gg e2) = [(a, M, vs)] \rangle$

show ?case

proof(*cases is-val e'*)

case *False*

with *bisim1 call* **have** *pc < length (compE2 e1)*

by(*auto intro: bisim1-call-pcD*)

with *call ins IH1 False* **show** ?thesis

by(*auto intro: bisim1-bisims1.bisim1BinOp1*)

next

case *True*

then obtain *v* **where** [simp]: *e' = Val v* **by** *auto*

from *bisim1* **have** *pc \leq length (compE2 e1)* **by**(*auto dest: bisim1-pc-length-compE2*)

moreover {

assume *pc: pc < length (compE2 e1)*

with *bisim1 ins* **have** *False*

by(*auto dest: bisim-Val-pc-not-Invoke*) }

```

ultimately have [simp]: pc = length (compE2 e1) by(cases pc < length (compE2 e1)) auto
from call ins show ?thesis by simp
qed
next
case bisim1BinOp2 thus ?case
  by(auto split: if-split-asm bop.split-asm dest: bisim1-bisims1.bisim1BinOp2)
next
case (bisim1AAcc1 A n a' xs stk loc pc i)
note IH1 = ⟨[call1 a' = [(a, M, vs)]; compE2 A ! pc = Invoke M n0; pc < length (compE2 A) ]
  ⇒ ?concl A n a' xs pc stk loc⟩
note bisim1 = ⟨P,A,h ⊢ (a', xs) ↔ (stk, loc, pc, None)⟩
note ins = ⟨compE2 (A[i]) ! pc = Invoke M n0⟩
note call = ⟨call1 (a'[i]) = [(a, M, vs)]⟩
show ?case
proof(cases is-val a')
  case False
  with bisim1 call have pc < length (compE2 A)
  by(auto intro: bisim1-call-pcD)
  with call ins IH1 False show ?thesis
  by(auto intro: bisim1-bisims1.bisim1AAcc1)
next
case True
then obtain v where [simp]: a' = Val v by auto
from bisim1 have pc ≤ length (compE2 A) by(auto dest: bisim1-pc-length-compE2)
moreover {
  assume pc: pc < length (compE2 A)
  with bisim1 ins have False
  by(auto dest: bisim-Val-pc-not-Invoke) }
ultimately have [simp]: pc = length (compE2 A) by(cases pc < length (compE2 A)) auto
from call ins show ?thesis by simp
qed
next
case bisim1AAcc2 thus ?case
  by(auto split: if-split-asm dest: bisim1-bisims1.bisim1AAcc2)
next
case (bisim1AAss1 A n a' xs stk loc pc i e)
note IH1 = ⟨[call1 a' = [(a, M, vs)]; compE2 A ! pc = Invoke M n0; pc < length (compE2 A) ]
  ⇒ ?concl A n a' xs pc stk loc⟩
note bisim1 = ⟨P,A,h ⊢ (a', xs) ↔ (stk, loc, pc, None)⟩
note ins = ⟨compE2 (A[i] := e) ! pc = Invoke M n0⟩
note call = ⟨call1 (a'[i] := e) = [(a, M, vs)]⟩
show ?case
proof(cases is-val a')
  case False
  with bisim1 call have pc < length (compE2 A)
  by(auto intro: bisim1-call-pcD)
  with call ins IH1 False show ?thesis
  by(auto intro: bisim1-bisims1.bisim1AAss1)
next
case True
then obtain v where [simp]: a' = Val v by auto
from bisim1 have pc ≤ length (compE2 A) by(auto dest: bisim1-pc-length-compE2)
moreover {
  assume pc: pc < length (compE2 A)

```

with *bisim1 ins* **have** *False*
by(*auto dest: bisim-Val-pc-not-Invoke*) }
ultimately have [*simp*]: *pc = length (compE2 A)* **by**(*cases pc < length (compE2 A)*) *auto*
from *call ins* **show** ?*thesis* **by** *simp*
qed
next
case (*bisim1AAss2 i n i' xs stk loc pc A e v*)
note *IH1 = ⟨[call1 i' = [(a, M, vs)]; compE2 i ! pc = Invoke M n0; pc < length (compE2 i)]*
 \implies ?*concl i n i' xs pc stk loc*
note *bisim1 = ⟨P, i, h ⊢ (i', xs) ↔ (stk, loc, pc, None)⟩*
note *ins = ⟨compE2 (A[i] := e) ! (length (compE2 A) + pc) = Invoke M n0⟩*
note *call = ⟨call1 (Val v[i] := e) = [(a, M, vs)]⟩*
show ?*case*
proof(*cases is-val i'*)
case *False*
with *bisim1 call* **have** *pc < length (compE2 i)*
by(*auto intro: bisim1-call-pcD*)
with *call ins IH1 False* **show** ?*thesis*
by(*auto intro: bisim1-bisims1.bisim1AAss2*)
next
case *True*
then obtain *v* **where** [*simp*]: *i' = Val v* **by** *auto*
from *bisim1* **have** *pc ≤ length (compE2 i)* **by**(*auto dest: bisim1-pc-length-compE2*)
moreover {
assume *pc: pc < length (compE2 i)*
with *bisim1 ins* **have** *False*
by(*auto dest: bisim-Val-pc-not-Invoke*) }
ultimately have [*simp*]: *pc = length (compE2 i)* **by**(*cases pc < length (compE2 i)*) *auto*
from *call ins* **show** ?*thesis* **by** *simp*
qed
next
case *bisim1AAss3* **thus** ?*case*
by(*auto split: if-split-asm nat.split-asm simp add: nth-Cons dest: bisim1-bisims1.bisim1AAss3*)
next
case (*bisim1FAss1 e n e' xs stk loc pc e2 F D*)
note *IH1 = ⟨[call1 e' = [(a, M, vs)]; compE2 e ! pc = Invoke M n0; pc < length (compE2 e)]*
 \implies ?*concl e n e' xs pc stk loc*
note *bisim1 = ⟨P, e, h ⊢ (e', xs) ↔ (stk, loc, pc, None)⟩*
note *ins = ⟨compE2 (e·F{D} := e2) ! pc = Invoke M n0⟩*
note *call = ⟨call1 (e'·F{D} := e2) = [(a, M, vs)]⟩*
show ?*case*
proof(*cases is-val e'*)
case *False*
with *bisim1 call* **have** *pc < length (compE2 e)*
by(*auto intro: bisim1-call-pcD*)
with *call ins IH1 False* **show** ?*thesis*
by(*auto intro: bisim1-bisims1.bisim1FAss1*)
next
case *True*
then obtain *v* **where** [*simp*]: *e' = Val v* **by** *auto*
from *bisim1* **have** *pc ≤ length (compE2 e)* **by**(*auto dest: bisim1-pc-length-compE2*)
moreover {
assume *pc: pc < length (compE2 e)*
with *bisim1 ins* **have** *False*

```

      by(auto dest: bisim-Val-pc-not-Invoke) }
    ultimately have [simp]: pc = length (compE2 e) by(cases pc < length (compE2 e)) auto
  from call ins show ?thesis by simp
qed
next
case bisim1FAss2 thus ?case
  by(auto split: if-split-asm nat.split-asm simp add: nth-Cons dest: bisim1-bisims1.bisim1FAss2)
next
case (bisim1CAS1 E n e' xs stk loc pc e2 e3 D F)
note IH1 = ⟨[call1 e' = [(a, M, vs)]; compE2 E ! pc = Invoke M n0; pc < length (compE2 E) ]
  ⇒ ?concl E n e' xs pc stk loc⟩
note bisim1 = ⟨P,E,h ⊢ (e', xs) ↔ (stk, loc, pc, None)⟩
note ins = ⟨compE2 - ! pc = Invoke M n0⟩
note call = ⟨call1 - = [(a, M, vs)]⟩
show ?case
proof(cases is-val e')
  case False
  with bisim1 call have pc < length (compE2 E)
    by(auto intro: bisim1-call-pcD)
  with call ins IH1 False show ?thesis
    by(auto intro: bisim1-bisims1.bisim1CAS1)
next
case True
  then obtain v where [simp]: e' = Val v by auto
  from bisim1 have pc ≤ length (compE2 E) by(auto dest: bisim1-pc-length-compE2)
  moreover {
    assume pc: pc < length (compE2 E)
    with bisim1 ins have False
      by(auto dest: bisim-Val-pc-not-Invoke) }
  ultimately have [simp]: pc = length (compE2 E) by(cases pc < length (compE2 E)) auto
  from call ins show ?thesis by simp
qed
next
case (bisim1CAS2 e2 n e2' xs stk loc pc e1 e3 D F v)
note IH1 = ⟨[call1 e2' = [(a, M, vs)]; compE2 e2 ! pc = Invoke M n0; pc < length (compE2 e2) ]
  ⇒ ?concl e2 n e2' xs pc stk loc⟩
note bisim1 = ⟨P,e2,h ⊢ (e2', xs) ↔ (stk, loc, pc, None)⟩
note ins = ⟨compE2 - ! (length (compE2 e1) + pc) = Invoke M n0⟩
note call = ⟨call1 - = [(a, M, vs)]⟩
show ?case
proof(cases is-val e2')
  case False
  with bisim1 call have pc < length (compE2 e2)
    by(auto intro: bisim1-call-pcD)
  with call ins IH1 False show ?thesis
    by(auto intro: bisim1-bisims1.bisim1CAS2)
next
case True
  then obtain v where [simp]: e2' = Val v by auto
  from bisim1 have pc ≤ length (compE2 e2) by(auto dest: bisim1-pc-length-compE2)
  moreover {
    assume pc: pc < length (compE2 e2)
    with bisim1 ins have False
      by(auto dest: bisim-Val-pc-not-Invoke) }

```


ultimately have [simp]: $pc = \text{length}(\text{compE2 } e2)$ by(cases $pc < \text{length}(\text{compE2 } e2)$) auto
 from call ins show ?thesis by simp

qed

next

case (bisim1Call1 obj n obj' xs stk loc pc ps M')

note IH1 = $\langle \llbracket \text{call1 } obj' = \llbracket (a, M, vs) \rrbracket; \text{compE2 } obj ! pc = \text{Invoke } M n0; pc < \text{length}(\text{compE2 } obj) \rrbracket \Rightarrow ?\text{concl } obj \ n \ obj' \ xs \ pc \ stk \ loc \rangle$

note IH2 = $\langle \bigwedge xs. \llbracket \text{calls1 } ps = \llbracket (a, M, vs) \rrbracket; \text{compEs2 } ps ! 0 = \text{Invoke } M n0; 0 < \text{length}(\text{compEs2 } ps) \rrbracket \Rightarrow ?\text{concls } ps \ n \ ps \ xs \ 0 \ [] \ xs \rangle$

note ins = $\langle \text{compE2 } (obj \cdot M'(ps)) ! pc = \text{Invoke } M n0 \rangle$

note bisim1 = $\langle P, obj, h \vdash (obj', xs) \leftrightarrow (stk, loc, pc, None) \rangle$

note call = $\langle \text{call1 } (obj' \cdot M'(ps)) = \llbracket (a, M, vs) \rrbracket \rangle$

thus ?case

proof(cases rule: call1-callE)

case CallObj

with bisim1 call have $pc < \text{length}(\text{compE2 } obj)$ by(auto intro: bisim1-call-pcD)

with call ins CallObj IH1 show ?thesis

by(auto intro: bisim1-bisims1.bisim1Call1)

next

case (CallParams v)

from bisim1 have $pc \leq \text{length}(\text{compE2 } obj)$ by(auto dest: bisim1-pc-length-compE2)

moreover {

assume $pc: pc < \text{length}(\text{compE2 } obj)$

with bisim1 ins CallParams have False by(auto dest: bisim-Val-pc-not-Invoke) }

ultimately have [simp]: $pc = \text{length}(\text{compE2 } obj)$ by(cases $pc < \text{length}(\text{compE2 } obj)$) auto

with bisim1 CallParams have [simp]: $stk = [v] \ loc = xs$ by(auto dest: bisim1-Val-length-compE2D)

from IH2[of loc] CallParams ins

show ?thesis

apply(clarsimp simp add: compEs2-map-Val is-vals-conv split: if-split-asm)

apply(drule bisim1-bisims1.bisim1CallParams)

apply(auto simp add: neq-Nil-conv)

done

next

case [simp]: Call

from bisim1 have $pc \leq \text{length}(\text{compE2 } obj)$ by(auto dest: bisim1-pc-length-compE2)

moreover {

assume $pc: pc < \text{length}(\text{compE2 } obj)$

with bisim1 ins have False by(auto dest: bisim-Val-pc-not-Invoke) }

ultimately have [simp]: $pc = \text{length}(\text{compE2 } obj)$ by(cases $pc < \text{length}(\text{compE2 } obj)$) auto

with ins have [simp]: $vs = []$ by(auto simp add: compEs2-map-Val split: if-split-asm)

from bisim1 have [simp]: $stk = [\text{Addr } a] \ xs = loc$ by(auto dest: bisim1-Val-length-compE2D)

from ins show ?thesis by(auto intro: bisim1CallThrow[of [] [], simplified])

qed

next

case (bisim1CallParams ps n ps' xs stk loc pc obj M' v)

note IH2 = $\langle \llbracket \text{calls1 } ps' = \llbracket (a, M, vs) \rrbracket; \text{compEs2 } ps ! pc = \text{Invoke } M n0; pc < \text{length}(\text{compEs2 } ps) \rrbracket \Rightarrow ?\text{concls } ps \ n \ ps' \ xs \ pc \ stk \ loc \rangle$

note ins = $\langle \text{compE2 } (obj \cdot M'(ps)) ! (\text{length}(\text{compE2 } obj) + pc) = \text{Invoke } M n0 \rangle$

note bisim2 = $\langle P, ps, h \vdash (ps', xs) [\leftrightarrow] (stk, loc, pc, None) \rangle$

note call = $\langle \text{call1 } (\text{Val } v \cdot M'(ps')) = \llbracket (a, M, vs) \rrbracket \rangle$

thus ?case

```

proof(cases rule: call1-callE)
  case CallObj thus ?thesis by simp
next
  case (CallParams v')
  hence [simp]: v' = v and call': calls1 ps' = [(a, M, vs)] by auto
  from bisim2 call' have pc < length (compEs2 ps) by(auto intro: bisims1-calls-pcD)
  with IH2 CallParams ins show ?thesis
    by(auto simp add: is-vals-conv split: if-split-asm intro: bisim1-bisims1.bisim1CallParams)
next
  case Call
  hence [simp]: v = Addr a M' = M ps' = map Val vs by auto
  from bisim2 have pc ≤ length (compEs2 ps) by(auto dest: bisims1-pc-length-compEs2)
  moreover {
    assume pc: pc < length (compEs2 ps)
    with bisim2 ins have False by(auto dest: bisims-Val-pc-not-Invoke) }
  ultimately have [simp]: pc = length (compEs2 ps) by(cases pc < length (compEs2 ps)) auto
  from bisim2 have [simp]: stk = rev vs xs = loc by(auto dest: bisims1-Val-length-compEs2D)
  from bisim2 have length ps = length vs by(auto dest: bisims1-lengthD)
  with ins show ?thesis by(auto intro: bisim1CallThrow)
qed
next
  case (bisims1List1 e n e' xs stk loc pc es)
  note IH1 = ⟨[[call1 e' = [(a, M, vs)]; compE2 e ! pc = Invoke M n0; pc < length (compE2 e) ]
    ⇒ ?concl e n e' xs pc stk loc⟩
  note IH2 = ⟨∧xs. [[calls1 es = [(a, M, vs)]; compEs2 es ! 0 = Invoke M n0; 0 < length (compEs2
    es) ]
    ⇒ ?concls es n es xs 0 [] xs⟩
  note bisim1 = ⟨P,e,h ⊢ (e', xs) ↔ (stk, loc, pc, None)⟩
  note call = ⟨calls1 (e' # es) = [(a, M, vs)]⟩
  note ins = ⟨compEs2 (e # es) ! pc = Invoke M n0⟩
  show ?case
  proof(cases is-val e')
    case False
    with bisim1 call have pc < length (compE2 e) by(auto intro: bisim1-call-pcD)
    with call ins False IH1 show ?thesis
      by(auto intro: bisim1-bisims1.bisims1List1)
  next
    case True
    then obtain v where [simp]: e' = Val v by auto
    from bisim1 have pc ≤ length (compE2 e) by(auto dest: bisim1-pc-length-compE2)
    moreover {
      assume pc: pc < length (compE2 e)
      with bisim1 ins have False by(auto dest: bisim-Val-pc-not-Invoke) }
    ultimately have [simp]: pc = length (compE2 e) by(cases pc < length (compE2 e)) auto
    with bisim1 have [simp]: stk = [v] loc = xs by(auto dest: bisim1-Val-length-compE2D)
    from call have es ≠ [] by(cases es) simp-all
    with IH2[of loc] call ins
    show ?thesis by(auto split: if-split-asm dest: bisims1List2)
  qed
qed(auto split: if-split-asm bop.split-asm intro: bisim1-bisims1.intros dest: bisim1-pc-length-compE2)

lemma bisim1-max-stack: P,e,h ⊢ (e', xs) ↔ (stk, loc, pc, xcp) ⇒ length stk ≤ max-stack e
  and bisims1-max-stacks: P,es,h ⊢ (es', xs) [↔] (stk, loc, pc, xcp) ⇒ length stk ≤ max-stacks es
apply(induct (e', xs) (stk, loc, pc, xcp) and (es', xs) (stk, loc, pc, xcp)

```

arbitrary: $e' \text{ xs stk loc pc xcp}$ and $es' \text{ xs stk loc pc xcp}$ rule: *bisim1-bisims1.inducts*)
apply(auto simp add: max-stack1 [simplified] max-def max-stacks-ge-length)
apply(drule sym, simp add: max-stacks-ge-length, drule sym, simp, rule le-trans[OF max-stacks-ge-length],
simp)
done

inductive bisim1-fr :: 'addr J1-prog \Rightarrow 'heap \Rightarrow 'addr expr1 \times 'addr locals1 \Rightarrow 'addr frame \Rightarrow bool
for P :: 'addr J1-prog and h :: 'heap

where

$\llbracket P \vdash C \text{ sees } M:Ts \rightarrow T = \lfloor \text{body} \rfloor \text{ in } D;$
 $P, \text{blocks1 } 0 \text{ (Class } D \# Ts) \text{ body, } h \vdash (e, xs) \leftrightarrow (stk, loc, pc, \text{None});$
 $\text{call1 } e = \lfloor (a, M', vs) \rfloor;$
 $\text{max-vars } e \leq \text{length } xs \rrbracket$
 $\Rightarrow \text{bisim1-fr } P \ h \ (e, xs) \ (stk, loc, C, M, pc)$

declare bisim1-fr.intros [intro]

declare bisim1-fr.cases [elim]

lemma bisim1-fr-heap-mono:

$\llbracket \text{bisim1-fr } P \ h \ \text{exs } \text{fr}; \text{heap } h \ h' \rrbracket \Rightarrow \text{bisim1-fr } P \ h' \ \text{exs } \text{fr}$
by(auto intro: bisim1-heap-mono)

lemma bisim1-max-vars: $P, E, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp) \Rightarrow \text{max-vars } E \geq \text{max-vars } e$
and bisims1-max-varss: $P, Es, h \vdash (es, xs) [\leftrightarrow] (stk, loc, pc, xcp) \Rightarrow \text{max-varss } Es \geq \text{max-varss } es$
apply(induct E (e, xs) (stk, loc, pc, xcp) and Es (es, xs) (stk, loc, pc, xcp)

arbitrary: $e \ \text{xs stk loc pc xcp}$ and $es \ \text{xs stk loc pc xcp}$ rule: *bisim1-bisims1.inducts*)
apply(auto)
done

lemma bisim1-call- τ Exec-move:

$\llbracket P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, \text{None}); \text{call1 } e' = \lfloor (a, M', vs) \rfloor; n + \text{max-vars } e' \leq \text{length } xs; \neg$
contains-insync e \rrbracket
 $\Rightarrow \exists pc' \ \text{loc}' \ \text{stk}'. \ \tau \text{Exec-mover-a } P \ t \ e \ h \ (stk, loc, pc, \text{None}) \ (\text{rev } vs \ @ \ \text{Addr } a \ \# \ \text{stk}', \ \text{loc}', \ pc',$
None) \wedge

$pc' < \text{length } (\text{compE2 } e) \wedge \text{compE2 } e \ ! \ pc' = \text{Invoke } M' \ (\text{length } vs) \wedge$
 $P, e, h \vdash (e', xs) \leftrightarrow (\text{rev } vs \ @ \ \text{Addr } a \ \# \ \text{stk}', \ \text{loc}', \ pc', \ \text{None})$

(is $\llbracket _ ; _ ; _ ; _ \rrbracket \Rightarrow ?\text{concl } e \ n \ e' \ \text{xs } pc \ \text{stk } \ \text{loc}$)

and bisims1-calls- τ Exec-moves:

$\llbracket P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, \text{None}); \text{calls1 } es' = \lfloor (a, M', vs) \rfloor;$
 $n + \text{max-varss } es' \leq \text{length } xs; \neg \text{contains-insyncs } es \rrbracket$
 $\Rightarrow \exists pc' \ \text{stk}' \ \text{loc}'. \ \tau \text{Exec-movesr-a } P \ t \ es \ h \ (stk, loc, pc, \text{None}) \ (\text{rev } vs \ @ \ \text{Addr } a \ \# \ \text{stk}', \ \text{loc}', \ pc',$
None) \wedge

$pc' < \text{length } (\text{compEs2 } es) \wedge \text{compEs2 } es \ ! \ pc' = \text{Invoke } M' \ (\text{length } vs) \wedge$
 $P, es, h \vdash (es', xs) [\leftrightarrow] (\text{rev } vs \ @ \ \text{Addr } a \ \# \ \text{stk}', \ \text{loc}', \ pc', \ \text{None})$

(is $\llbracket _ ; _ ; _ ; _ \rrbracket \Rightarrow ?\text{concls } es \ n \ es' \ \text{xs } pc \ \text{stk } \ \text{loc}$)

proof(induct e n :: nat e' xs stk loc pc xcp \equiv None :: 'addr option

and es n :: nat es' xs stk loc pc xcp \equiv None :: 'addr option

rule: *bisim1-bisims1.inducts-split*)

case bisim1Val2 **thus** ?case **by** auto

next

case bisim1New **thus** ?case **by** auto

next

case bisim1NewArray **thus** ?case

```

  by auto (fastforce intro: bisim1-bisims1.bisim1NewArray elim!: NewArray- $\tau$ ExecrI intro!: exI)
next
  case bisim1Cast thus ?case
    by(auto)(fastforce intro: bisim1-bisims1.bisim1Cast elim!: Cast- $\tau$ ExecrI intro!: exI)+
next
  case bisim1InstanceOf thus ?case
    by(auto)(fastforce intro: bisim1-bisims1.bisim1InstanceOf elim!: InstanceOf- $\tau$ ExecrI intro!: exI)+
next
  case bisim1Val thus ?case by auto
next
  case bisim1Var thus ?case by auto
next
  case (bisim1BinOp1 e1 n e' xs stk loc pc e2 bop)
    note IH1 =  $\langle \llbracket \text{call1 } e' = \llbracket (a, M', vs) \rrbracket; n + \text{max-vars } e' \leq \text{length } xs; \neg \text{contains-insync } e1 \rrbracket \implies$ 
 $\text{?concl } e1 \ n \ e' \ xs \ pc \ stk \ loc \rangle$ 
    note IH2 =  $\langle \bigwedge xs. \llbracket \text{call1 } e2 = \llbracket (a, M', vs) \rrbracket; n + \text{max-vars } e2 \leq \text{length } xs; \neg \text{contains-insync } e2 \rrbracket$ 
 $\implies \text{?concl } e2 \ n \ e2 \ xs \ 0 \ \llbracket xs \rrbracket \rangle$ 
    note call =  $\langle \text{call1 } (e' \llbracket \text{bop} \rrbracket e2) = \llbracket (a, M', vs) \rrbracket \rangle$ 
    note len =  $\langle n + \text{max-vars } (e' \llbracket \text{bop} \rrbracket e2) \leq \text{length } xs \rangle$ 
    note bisim1 =  $\langle P, e1, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, \text{None}) \rangle$ 
    note cs =  $\langle \neg \text{contains-insync } (e1 \llbracket \text{bop} \rrbracket e2) \rangle$ 
    show ?case
    proof(cases is-val e')
      case True
        then obtain v where [simp]:  $e' = \text{Val } v$  by auto
        from bisim1 have  $\tau \text{Exec-mover-a } P \ t \ e1 \ h \ (stk, loc, pc, \text{None}) \ ([v], loc, \text{length } (\text{compE2 } e1), \text{None})$ 
          and [simp]:  $xs = loc$  by(auto dest!: bisim1Val2D1)
        hence  $\tau \text{Exec-mover-a } P \ t \ (e1 \llbracket \text{bop} \rrbracket e2) \ h \ (stk, loc, pc, \text{None}) \ ([v], loc, \text{length } (\text{compE2 } e1), \text{None})$ 
          by-(rule BinOp- $\tau$ ExecrI1)
        also from call IH2[of loc] len cs obtain  $pc' \ stk' \ loc'$ 
          where exec:  $\tau \text{Exec-mover-a } P \ t \ e2 \ h \ (\llbracket \rrbracket, xs, 0, \text{None}) \ (\text{rev } vs \ @ \ \text{Addr } a \ \# \ stk', loc', pc', \text{None})$ 
            and ins:  $\text{compE2 } e2 \ ! \ pc' = \text{Invoke } M' \ (\text{length } vs) \ pc' < \text{length } (\text{compE2 } e2)$ 
            and bisim':  $P, e2, h \vdash (e2, xs) \leftrightarrow (\text{rev } vs \ @ \ \text{Addr } a \ \# \ stk', loc', pc', \text{None})$  by auto
        from BinOp- $\tau$ ExecrI2[OF exec, of e1 bop v]
        have  $\tau \text{Exec-mover-a } P \ t \ (e1 \llbracket \text{bop} \rrbracket e2) \ h \ ([v], loc, \text{length } (\text{compE2 } e1), \text{None}) \ (\text{rev } vs \ @ \ \text{Addr } a \ \#$ 
 $(stk' \ @ \ [v]), loc', \text{length } (\text{compE2 } e1) + pc', \text{None})$  by simp
        also (rtranclp-trans) from bisim'
        have  $P, e1 \llbracket \text{bop} \rrbracket e2, h \vdash (\text{Val } v \llbracket \text{bop} \rrbracket e2, xs) \leftrightarrow ((\text{rev } vs \ @ \ \text{Addr } a \ \# \ stk') \ @ \ [v], loc', \text{length}$ 
 $(\text{compE2 } e1) + pc', \text{None})$ 
          by(rule bisim1BinOp2)
        ultimately show ?thesis using ins by fastforce
      next
      case False with IH1 len False call cs show ?thesis
        by(clarsimp)(fastforce intro: bisim1-bisims1.bisim1BinOp1 elim!: BinOp- $\tau$ ExecrI1 intro!: exI)
    qed
next
  case (bisim1BinOp2 e2 n e' xs stk loc pc e1 bop v1)
    then obtain  $pc' \ loc' \ stk'$  where  $pc': pc' < \text{length } (\text{compE2 } e2) \ \text{compE2 } e2 \ ! \ pc' = \text{Invoke } M' \ (\text{length}$ 
 $vs)$ 
    and exec:  $\tau \text{Exec-mover-a } P \ t \ e2 \ h \ (stk, loc, pc, \text{None}) \ (\text{rev } vs \ @ \ \text{Addr } a \ \# \ stk', loc', pc', \text{None})$ 
    and bisim':  $P, e2, h \vdash (e', xs) \leftrightarrow (\text{rev } vs \ @ \ \text{Addr } a \ \# \ stk', loc', pc', \text{None})$  by fastforce
    from exec have  $\tau \text{Exec-mover-a } P \ t \ (e1 \llbracket \text{bop} \rrbracket e2) \ h \ (stk \ @ \ [v1], loc, \text{length } (\text{compE2 } e1) + pc,$ 
 $\text{None})$ 
 $((\text{rev } vs \ @ \ \text{Addr } a \ \# \ stk') \ @ \ [v1], loc', \text{length } (\text{compE2 } e1) + pc',$ 

```

None)

by(rule BinOp- τ ExecrI2)

moreover from bisim'

have $P, e1 \llcorner \text{bop} \gg e2, h \vdash (\text{Val } v1 \llcorner \text{bop} \gg e', xs) \leftrightarrow ((\text{rev } vs \text{ @ Addr } a \# \text{stk}') \text{ @ } [v1], \text{loc}', \text{length} (\text{compE2 } e1) + \text{pc}', \text{None})$

by(rule bisim1-bisims1.bisim1BinOp2)

ultimately show ?case using pc' by(fastforce)

next

case bisim1LAss1 thus ?case

by(auto)(fastforce intro: bisim1-bisims1.bisim1LAss1 elim!: LAss- τ ExecrI intro!: exI)

next

case bisim1LAss2 thus ?case by simp

next

case (bisim1AAcc1 A n a' xs stk loc pc i)

note IH1 = $\langle \llbracket \text{call1 } a' = \llbracket (a, M', vs) \rrbracket; n + \text{max-vars } a' \leq \text{length } xs; \neg \text{contains-insync } A \rrbracket \Rightarrow ?\text{concl } A \ n \ a' \ xs \ pc \ stk \ loc \rangle$

note IH2 = $\langle \bigwedge xs. \llbracket \text{call1 } i = \llbracket (a, M', vs) \rrbracket; n + \text{max-vars } i \leq \text{length } xs; \neg \text{contains-insync } i \rrbracket \Rightarrow ?\text{concl } i \ n \ i \ xs \ 0 \ \square \ xs \rangle$

note call = $\langle \text{call1 } (a'[i]) = \llbracket (a, M', vs) \rrbracket \rangle$

note len = $\langle n + \text{max-vars } (a'[i]) \leq \text{length } xs \rangle$

note bisim1 = $\langle P, A, h \vdash (a', xs) \leftrightarrow (stk, loc, pc, \text{None}) \rangle$

note cs = $\langle \neg \text{contains-insync } (A[i]) \rangle$

show ?case

proof(cases is-val a')

case True

then obtain v where [simp]: $a' = \text{Val } v$ by auto

from bisim1 have $\tau\text{Exec-mover-a } P \ t \ A \ h \ (stk, \text{loc}, \text{pc}, \text{None}) \ ([v], \text{loc}, \text{length} (\text{compE2 } A), \text{None})$

and [simp]: $xs = \text{loc}$ by(auto dest!: bisim1Val2D1)

hence $\tau\text{Exec-mover-a } P \ t \ (A[i]) \ h \ (stk, \text{loc}, \text{pc}, \text{None}) \ ([v], \text{loc}, \text{length} (\text{compE2 } A), \text{None})$

by-(rule AAcc- τ ExecrI1)

also from call IH2[of loc] len cs obtain pc' stk' loc'

where exec: $\tau\text{Exec-mover-a } P \ t \ i \ h \ (\square, xs, 0, \text{None}) \ (\text{rev } vs \text{ @ Addr } a \# \text{stk}', \text{loc}', \text{pc}', \text{None})$

and ins: $\text{compE2 } i \ ! \ \text{pc}' = \text{Invoke } M' \ (\text{length } vs) \ \text{pc}' < \text{length} (\text{compE2 } i)$

and bisim': $P, i, h \vdash (i, xs) \leftrightarrow (\text{rev } vs \text{ @ Addr } a \# \text{stk}', \text{loc}', \text{pc}', \text{None})$ by auto

from AAcc- τ ExecrI2[OF exec, of A v]

have $\tau\text{Exec-mover-a } P \ t \ (A[i]) \ h \ ([v], \text{loc}, \text{length} (\text{compE2 } A), \text{None}) \ (\text{rev } vs \text{ @ Addr } a \# \text{stk}' \text{ @ } [v], \text{loc}', \text{length} (\text{compE2 } A) + \text{pc}', \text{None})$ by simp

also (rtranclp-trans) from bisim'

have $P, A[i], h \vdash (\text{Val } v[i], xs) \leftrightarrow ((\text{rev } vs \text{ @ Addr } a \# \text{stk}') \text{ @ } [v], \text{loc}', \text{length} (\text{compE2 } A) + \text{pc}', \text{None})$

None)

by(rule bisim1AAcc2)

ultimately show ?thesis using ins by fastforce

next

case False with IH1 len False call cs show ?thesis

by(clarsimp)(fastforce intro: bisim1-bisims1.bisim1AAcc1 elim!: AAcc- τ ExecrI1 intro!: exI)

qed

next

case (bisim1AAcc2 i n i' xs stk loc pc A v)

then obtain pc' loc' stk' where pc': $\text{pc}' < \text{length} (\text{compE2 } i) \ \text{compE2 } i \ ! \ \text{pc}' = \text{Invoke } M' \ (\text{length } vs)$

and exec: $\tau\text{Exec-mover-a } P \ t \ i \ h \ (stk, \text{loc}, \text{pc}, \text{None}) \ (\text{rev } vs \text{ @ Addr } a \# \text{stk}', \text{loc}', \text{pc}', \text{None})$

and bisim': $P, i, h \vdash (i', xs) \leftrightarrow (\text{rev } vs \text{ @ Addr } a \# \text{stk}', \text{loc}', \text{pc}', \text{None})$ by fastforce

from exec have $\tau\text{Exec-mover-a } P \ t \ (A[i]) \ h \ (stk \text{ @ } [v], \text{loc}, \text{length} (\text{compE2 } A) + \text{pc}, \text{None})$

$((\text{rev } vs \text{ @ Addr } a \# \text{stk}') \text{ @ } [v], \text{loc}', \text{length} (\text{compE2 } A) + \text{pc}', \text{None})$

```

  by(rule AAcc-τExecrI2)
  moreover from bisim'
  have P,A[i],h ⊢ (Val v[i], xs) ↔ ((rev vs @ Addr a # stk') @ [v], loc', length (compE2 A) + pc',
None)
  by(rule bisim1-bisims1.bisim1AAcc2)
  ultimately show ?case using pc' by(fastforce)
next
  case (bisim1AAss1 A n a' xs stk loc pc i e)
  note IH1 = ⟨[call1 a' = [(a, M', vs)]; n + max-vars a' ≤ length xs; ¬ contains-insync A ] ⟩ ⇒
?concl A n a' xs pc stk loc⟩
  note IH2 = ⟨∧xs. [call1 i = [(a, M', vs)]; n + max-vars i ≤ length xs; ¬ contains-insync i] ⟩ ⇒
?concl i n i xs 0 [] xs⟩
  note IH3 = ⟨∧xs. [call1 e = [(a, M', vs)]; n + max-vars e ≤ length xs; ¬ contains-insync e] ⟩ ⇒
?concl e n e xs 0 [] xs⟩
  note call = ⟨call1 (a'[i] := e) = [(a, M', vs)]⟩
  note len = ⟨n + max-vars (a'[i] := e) ≤ length xs⟩
  note bisim1 = ⟨P,A,h ⊢ (a', xs) ↔ (stk, loc, pc, None)⟩
  note bisim2 = ⟨P,i,h ⊢ (i, loc) ↔ ([], loc, 0, None)⟩
  note cs = ⟨¬ contains-insync (A[i] := e)⟩
  show ?case
  proof(cases is-val a')
    case True
    then obtain v where [simp]: a' = Val v by auto
    from bisim1 have τExec-mover-a P t A h (stk, loc, pc, None) ([v], loc, length (compE2 A), None)
      and [simp]: xs = loc by(auto dest!: bisim1Val2D1)
    hence exec: τExec-mover-a P t (A[i] := e) h (stk, loc, pc, None) ([v], loc, length (compE2 A),
None)
    by-(rule AAss-τExecrI1)
    show ?thesis
    proof(cases is-val i)
      case True
      then obtain v' where [simp]: i = Val v' by auto
      note exec also from bisim2
      have τExec-mover-a P t i h ([], loc, 0, None) ([v'], loc, length (compE2 i), None)
        by(auto dest!: bisim1Val2D1)
      from AAss-τExecrI2[OF this, of A e v]
      have τExec-mover-a P t (A[i] := e) h ([v], loc, length (compE2 A), None) ([v', v], loc, length
(compE2 A) + length (compE2 i), None) by simp
      also (rtranclp-trans) from call IH3[of loc] len cs obtain pc' stk' loc'
        where exec: τExec-mover-a P t e h ([], loc, 0, None) (rev vs @ Addr a # stk', loc', pc', None)
          and ins: compE2 e ! pc' = Invoke M' (length vs) pc' < length (compE2 e)
          and bisim': P,e,h ⊢ (e, loc) ↔ (rev vs @ Addr a # stk', loc', pc', None) by auto
      from AAss-τExecrI3[OF exec, of A i v' v]
      have τExec-mover-a P t (A[i] := e) h ([v', v], loc, length (compE2 A) + length (compE2 i),
None)
        ((rev vs @ Addr a # stk') @ [v', v], loc', length (compE2 A) + length (compE2 i)
+ pc', None) by simp
      also (rtranclp-trans) from bisim'
      have P,A[i] := e,h ⊢ (Val v[Val v'] := e, xs) ↔ ((rev vs @ Addr a # stk') @ [v', v], loc', length
(compE2 A) + length (compE2 i) + pc', None)
        by - (rule bisim1AAss3, simp)
      ultimately show ?thesis using ins by fastforce
    next
    case False
  end

```

note *exec* **also from** *False call IH2[of loc] len cs* **obtain** $pc' \text{ stk}' \text{ loc}'$
where *exec*: $\tau \text{Exec-mover-a } P \text{ t i h } ([], xs, 0, \text{None})$ (*rev vs* @ *Addr a* # *stk'*, *loc'*, *pc'*, *None*)
and *ins*: $\text{compE2 } i ! pc' = \text{Invoke } M' (\text{length } vs) \text{ pc}' < \text{length } (\text{compE2 } i)$
and *bisim'*: $P, i, h \vdash (i, xs) \leftrightarrow (\text{rev } vs \text{ @ } \text{Addr } a \# \text{stk}', \text{loc}', \text{pc}', \text{None})$ **by** *auto*
from *AAss- τ ExecrI2[OF exec, of A e v]*
have $\tau \text{Exec-mover-a } P \text{ t } (A[i] := e) \text{ h } ([v], \text{loc}, \text{length } (\text{compE2 } A), \text{None})$ (*rev vs* @ *Addr a* # $\text{stk}' \text{ @ } [v]$), *loc'*, $\text{length } (\text{compE2 } A) + \text{pc}'$, *None*) **by** *simp*
also (*rtranclp-trans*) **from** *bisim'*
have $P, A[i] := e, h \vdash (\text{Val } v[i] := e, xs) \leftrightarrow ((\text{rev } vs \text{ @ } \text{Addr } a \# \text{stk}') \text{ @ } [v], \text{loc}', \text{length } (\text{compE2 } A) + \text{pc}', \text{None})$
by(*rule bisim1AAss2*)
ultimately show *?thesis using ins False by(fastforce intro!: exI)*
qed
next
case *False with IH1 len False call cs* **show** *?thesis*
by(*clarsimp*)(*fastforce intro: bisim1-bisims1.bisim1AAss1 elim!: AAss- τ ExecrI1 intro!: exI*)
qed
next
case (*bisim1AAss2 i n i' xs stk loc pc A e v*)
note $IH2 = \langle \llbracket \text{call1 } i' = [(a, M', vs)]; n + \text{max-vars } i' \leq \text{length } xs; \neg \text{contains-insync } i \rrbracket \implies ?\text{concl } i \text{ n } i' \text{ xs } pc \text{ stk } \text{loc} \rangle$
note $IH3 = \langle \bigwedge xs. \llbracket \text{call1 } e = [(a, M', vs)]; n + \text{max-vars } e \leq \text{length } xs; \neg \text{contains-insync } e \rrbracket \implies ?\text{concl } e \text{ n } e \text{ xs } 0 \llbracket xs \rrbracket \rangle$
note $\text{call} = \langle \text{call1 } (\text{Val } v[i'] := e) = [(a, M', vs)] \rangle$
note $\text{len} = \langle n + \text{max-vars } (\text{Val } v[i'] := e) \leq \text{length } xs \rangle$
note $\text{bisim2} = \langle P, i, h \vdash (i', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{None}) \rangle$
note $\text{cs} = \langle \neg \text{contains-insync } (A[i] := e) \rangle$
show *?case*
proof(*cases is-val i'*)
case *True*
then obtain v' **where** [*simp*]: $i' = \text{Val } v'$ **by** *auto*
from *bisim2* **have** *exec*: $\tau \text{Exec-mover-a } P \text{ t i h } (\text{stk}, \text{loc}, \text{pc}, \text{None})$ ($[v']$, *loc*, $\text{length } (\text{compE2 } i)$, *None*)
and [*simp*]: $xs = \text{loc}$ **by**(*auto dest!: bisim1Val2D1*)
from *AAss- τ ExecrI2[OF exec, of A e v]*
have $\tau \text{Exec-mover-a } P \text{ t } (A[i] := e) \text{ h } (\text{stk} \text{ @ } [v], \text{loc}, \text{length } (\text{compE2 } A) + \text{pc}, \text{None})$ ($[v', v]$, *loc*, $\text{length } (\text{compE2 } A) + \text{length } (\text{compE2 } i)$, *None*) **by** *simp*
also from *call IH3[of loc] len cs* **obtain** $pc' \text{ stk}' \text{ loc}'$
where *exec*: $\tau \text{Exec-mover-a } P \text{ t e h } ([], xs, 0, \text{None})$ (*rev vs* @ *Addr a* # *stk'*, *loc'*, *pc'*, *None*)
and *ins*: $\text{compE2 } e ! pc' = \text{Invoke } M' (\text{length } vs) \text{ pc}' < \text{length } (\text{compE2 } e)$
and *bisim'*: $P, e, h \vdash (e, xs) \leftrightarrow (\text{rev } vs \text{ @ } \text{Addr } a \# \text{stk}', \text{loc}', \text{pc}', \text{None})$ **by** *auto*
from *AAss- τ ExecrI3[OF exec, of A i v' v]*
have $\tau \text{Exec-mover-a } P \text{ t } (A[i] := e) \text{ h } ([v', v], \text{loc}, \text{length } (\text{compE2 } A) + \text{length } (\text{compE2 } i), \text{None})$
 $((\text{rev } vs \text{ @ } \text{Addr } a \# \text{stk}') \text{ @ } [v', v], \text{loc}', \text{length } (\text{compE2 } A) + \text{length } (\text{compE2 } i) + \text{pc}', \text{None})$ **by** *simp*
also (*rtranclp-trans*) **from** *bisim'*
have $P, A[i] := e, h \vdash (\text{Val } v[\text{Val } v'] := e, xs) \leftrightarrow ((\text{rev } vs \text{ @ } \text{Addr } a \# \text{stk}') \text{ @ } [v', v], \text{loc}', \text{length } (\text{compE2 } A) + \text{length } (\text{compE2 } i) + \text{pc}', \text{None})$
by(*rule bisim1AAss3*)
ultimately show *?thesis using ins by(fastforce intro!: exI)*
next
case *False*
with *IH2 len call cs* **obtain** $pc' \text{ loc}' \text{ stk}'$
where *ins*: $pc' < \text{length } (\text{compE2 } i) \text{ compE2 } i ! pc' = \text{Invoke } M' (\text{length } vs)$

and $exec: \tau Exec\text{-mover-}a\ P\ t\ e\ h\ (stk, loc, pc, None)\ (rev\ vs\ @\ Addr\ a\ \# \ stk', loc', pc', None)$
and $bisim': P, i, h \vdash (i', xs) \leftrightarrow (rev\ vs\ @\ Addr\ a\ \# \ stk', loc', pc', None)$ **by** *fastforce*
from $bisim'$ **have** $P, A[i] := e, h \vdash (Val\ v[i^\wedge] := e, xs) \leftrightarrow ((rev\ vs\ @\ Addr\ a\ \# \ stk') @ [v], loc',$
 $length\ (compE2\ A) + pc', None)$
by(*rule bisim1-bisims1.bisim1AAss2*)
with $AAss\text{-}\tau\ ExecrI2[OF\ exec, of\ A\ e\ v]\ ins\ False$ **show** *?thesis* **by**(*auto intro!: exI*)
qed
next
case ($bisim1AAss3\ e\ n\ e'\ xs\ stk\ loc\ pc\ A\ i\ v\ v'$)
then obtain $pc'\ loc'\ stk'$ **where** $pc': pc' < length\ (compE2\ e)\ compE2\ e ! pc' = Invoke\ M'\ (length\ vs)$
and $exec: \tau Exec\text{-mover-}a\ P\ t\ e\ h\ (stk, loc, pc, None)\ (rev\ vs\ @\ Addr\ a\ \# \ stk', loc', pc', None)$
and $bisim': P, e, h \vdash (e', xs) \leftrightarrow (rev\ vs\ @\ Addr\ a\ \# \ stk', loc', pc', None)$ **by** *fastforce*
from $exec$ **have** $\tau Exec\text{-mover-}a\ P\ t\ (A[i] := e)\ h\ (stk\ @\ [v', v], loc, length\ (compE2\ A) + length\ (compE2\ i) + pc, None)$
 $((rev\ vs\ @\ Addr\ a\ \# \ stk') @ [v', v], loc', length\ (compE2\ A) + length\ (compE2\ i) + pc', None)$
by(*rule AAss-ExecrI3*)
moreover from $bisim'$
have $P, A[i] := e, h \vdash (Val\ v\ [Val\ v^\wedge] := e', xs) \leftrightarrow ((rev\ vs\ @\ Addr\ a\ \# \ stk') @ [v', v], loc', length\ (compE2\ A) + length\ (compE2\ i) + pc', None)$
by(*rule bisim1-bisims1.bisim1AAss3*)
ultimately show *?case* **using** pc' **by**(*fastforce intro!: exI*)
next
case $bisim1AAss4$ **thus** *?case* **by** *simp*
next
case $bisim1ALength$ **thus** *?case*
by(*auto*)(*fastforce intro: bisim1-bisims1.bisim1ALength elim!: ALength-ExecrI intro!: exI*)
next
case $bisim1FAcc$ **thus** *?case*
by(*auto*)(*fastforce intro: bisim1-bisims1.bisim1FAcc elim!: FAcc-ExecrI intro!: exI*)
next
case ($bisim1FAss1\ e\ n\ e'\ xs\ stk\ loc\ pc\ e2\ F\ D$)
note $IH1 = \langle \llbracket call1\ e' = \llbracket (a, M', vs) \rrbracket; n + max\text{-vars}\ e' \leq length\ xs; \neg\ contains\text{-insync}\ e \rrbracket \implies ?concl\ e\ n\ e'\ xs\ pc\ stk\ loc \rangle$
note $IH2 = \langle \bigwedge xs. \llbracket call1\ e2 = \llbracket (a, M', vs) \rrbracket; n + max\text{-vars}\ e2 \leq length\ xs; \neg\ contains\text{-insync}\ e2 \rrbracket \implies ?concl\ e2\ n\ e2\ xs\ 0\ []\ xs \rangle$
note $call = \langle call1\ (e' \cdot F\{D\} := e2) = \llbracket (a, M', vs) \rrbracket \rangle$
note $len = \langle n + max\text{-vars}\ (e' \cdot F\{D\} := e2) \leq length\ xs \rangle$
note $bisim1 = \langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, None) \rangle$
note $cs = \langle \neg\ contains\text{-insync}\ (e \cdot F\{D\} := e2) \rangle$
show *?case*
proof(*cases is-val e'*)
case *True*
then obtain v **where** [*simp*]: $e' = Val\ v$ **by** *auto*
from $bisim1$ **have** $\tau Exec\text{-mover-}a\ P\ t\ e\ h\ (stk, loc, pc, None)\ ([v], loc, length\ (compE2\ e), None)$
and [*simp*]: $xs = loc$ **by**(*auto dest!: bisim1Val2D1*)
hence $\tau Exec\text{-mover-}a\ P\ t\ (e \cdot F\{D\} := e2)\ h\ (stk, loc, pc, None)\ ([v], loc, length\ (compE2\ e), None)$
by-(*rule FAss-ExecrI1*)
also from $call\ IH2[of\ loc]\ len\ cs$ **obtain** $pc'\ stk'\ loc'$
where $exec: \tau Exec\text{-mover-}a\ P\ t\ e2\ h\ ([], xs, 0, None)\ (rev\ vs\ @\ Addr\ a\ \# \ stk', loc', pc', None)$
and $ins: compE2\ e2 ! pc' = Invoke\ M'\ (length\ vs)\ pc' < length\ (compE2\ e2)$
and $bisim': P, e2, h \vdash (e2, xs) \leftrightarrow (rev\ vs\ @\ Addr\ a\ \# \ stk', loc', pc', None)$ **by** *auto*
from $FAss\text{-}\tau\ ExecrI2[OF\ exec, of\ e\ F\ D\ v]$


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have  $\tauExec\text{-mover}\text{-}a\ P\ t\ (e\cdot F\{D\} := e2)\ h\ ([v],\ loc,\ length\ (compE2\ e),\ None)\ (rev\ vs\ @\ Addr\ a\ \#)\ (stk'\ @\ [v]),\ loc',\ length\ (compE2\ e) + pc',\ None)$  by simp
also (rtranclp-trans) from bisim'
have  $P, e\cdot F\{D\} := e2, h \vdash (Val\ v\cdot F\{D\} := e2, xs) \leftrightarrow ((rev\ vs\ @\ Addr\ a\ \#)\ stk')\ @\ [v],\ loc',\ length\ (compE2\ e) + pc',\ None)$ 
by(rule\ bisim1FAss2)
ultimately show ?thesis using ins by fastforce
next
case False with IH1\ len\ False\ call\ cs show ?thesis
by(clarsimp)(fastforce\ intro: bisim1-bisims1.bisim1FAss1\ elim!: FAss-ExecrI1\ intro!: exI)
qed
next
case (bisim1FAss2\ e2\ n\ e'\ xs\ stk\ loc\ pc\ e\ F\ D\ v)
then obtain  $pc'\ loc'\ stk'$  where  $pc': pc' < length\ (compE2\ e2)\ compE2\ e2\ !\ pc' = Invoke\ M'\ (length\ vs)$ 
and  $exec: \tauExec\text{-mover}\text{-}a\ P\ t\ e2\ h\ (stk,\ loc,\ pc,\ None)\ (rev\ vs\ @\ Addr\ a\ \#)\ stk',\ loc',\ pc',\ None)$ 
and  $bisim': P, e2, h \vdash (e', xs) \leftrightarrow (rev\ vs\ @\ Addr\ a\ \#)\ stk',\ loc',\ pc',\ None)$  by fastforce
from exec have  $\tauExec\text{-mover}\text{-}a\ P\ t\ (e\cdot F\{D\} := e2)\ h\ (stk\ @\ [v],\ loc,\ length\ (compE2\ e) + pc,\ None)$ 
 $((rev\ vs\ @\ Addr\ a\ \#)\ stk')\ @\ [v],\ loc',\ length\ (compE2\ e) + pc',\ None)$ 
by(rule\ FAss-ExecrI2)
moreover from bisim'
have  $P, e\cdot F\{D\} := e2, h \vdash (Val\ v\cdot F\{D\} := e', xs) \leftrightarrow ((rev\ vs\ @\ Addr\ a\ \#)\ stk')\ @\ [v],\ loc',\ length\ (compE2\ e) + pc',\ None)$ 
by(rule\ bisim1-bisims1.bisim1FAss2)
ultimately show ?case using  $pc'$  by(fastforce)
next
case bisim1FAss3 thus ?case by simp
next
case (bisim1CAS1\ e1\ n\ e'\ xs\ stk\ loc\ pc\ e2\ e3\ D\ F)
note  $IH1 = \langle \llbracket call1\ e' = \llbracket (a, M', vs) \rrbracket; n + max\text{-}vars\ e' \leq length\ xs; \neg\ contains\text{-}insync\ e1 \rrbracket \implies ?concl\ e1\ n\ e'\ xs\ pc\ stk\ loc \rangle$ 
note  $IH2 = \langle \bigwedge xs. \llbracket call1\ e2 = \llbracket (a, M', vs) \rrbracket; n + max\text{-}vars\ e2 \leq length\ xs; \neg\ contains\text{-}insync\ e2 \rrbracket \implies ?concl\ e2\ n\ e2\ xs\ 0 \llbracket xs \rrbracket \rangle$ 
note  $IH3 = \langle \bigwedge xs. \llbracket call1\ e3 = \llbracket (a, M', vs) \rrbracket; n + max\text{-}vars\ e3 \leq length\ xs; \neg\ contains\text{-}insync\ e3 \rrbracket \implies ?concl\ e3\ n\ e3\ xs\ 0 \llbracket xs \rrbracket \rangle$ 
note  $call = \langle call1\ - = \llbracket (a, M', vs) \rrbracket \rangle$ 
note  $len = \langle n + max\text{-}vars - \leq length\ xs \rangle$ 
note  $bisim1 = \langle P, e1, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, None) \rangle$ 
note  $bisim2 = \langle P, e2, h \vdash (e2, loc) \leftrightarrow (\llbracket \rrbracket, loc, 0, None) \rangle$ 
note  $cs = \langle \neg\ contains\text{-}insync\ \rightarrow \rangle$ 
show ?case
proof(cases\ is-val\ e')
case True
then obtain  $v$  where [simp]:  $e' = Val\ v$  by auto
from bisim1 have  $\tauExec\text{-mover}\text{-}a\ P\ t\ e1\ h\ (stk,\ loc,\ pc,\ None)\ ([v],\ loc,\ length\ (compE2\ e1),\ None)$ 
and [simp]:  $xs = loc$  by(auto\ dest!: bisim1Val2D1)
hence  $exec: \tauExec\text{-mover}\text{-}a\ P\ t\ (e1\cdot compareAndSwap(D\cdot F, e2, e3))\ h\ (stk,\ loc,\ pc,\ None)\ ([v],\ loc,\ length\ (compE2\ e1),\ None)$ 
by-(rule\ CAS-ExecrI1)
show ?thesis
proof(cases\ is-val\ e2)
case True
then obtain  $v'$  where [simp]:  $e2 = Val\ v'$  by auto
note exec also from bisim2

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have  $\tau\text{Exec-mover-a } P t e2 h$  ( $\square$ ,  $loc$ ,  $0$ ,  $None$ ) ( $[v']$ ,  $loc$ ,  $length (compE2 e2)$ ,  $None$ )
  by(auto dest!: bisim1Val2D1)
from CAS- $\tau\text{ExecrI2}$ [OF this, of e1 D F e3]
  have  $\tau\text{Exec-mover-a } P t (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) h$  ( $[v]$ ,  $loc$ ,  $length (compE2 e1)$ ,
   $None$ ) ( $[v', v]$ ,  $loc$ ,  $length (compE2 e1) + length (compE2 e2)$ ,  $None$ ) by simp
  also (rtranclp-trans) from call IH3[of loc] len cs obtain  $pc' stk' loc'$ 
  where exec:  $\tau\text{Exec-mover-a } P t e3 h$  ( $\square$ ,  $loc$ ,  $0$ ,  $None$ ) ( $rev vs @ Addr a \# stk', loc', pc', None$ )
  and ins:  $compE2 e3 ! pc' = Invoke M' (length vs) pc' < length (compE2 e3)$ 
  and bisim':  $P, e3, h \vdash (e3, loc) \leftrightarrow (rev vs @ Addr a \# stk', loc', pc', None)$  by auto
from CAS- $\tau\text{ExecrI3}$ [OF exec, of e1 D F e2 v' v]
have  $\tau\text{Exec-mover-a } P t (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) h$  ( $[v', v]$ ,  $loc$ ,  $length (compE2 e1)$ 
   $+ length (compE2 e2)$ ,  $None$ )
  ( $(rev vs @ Addr a \# stk') @ [v', v], loc', length (compE2 e1) + length (compE2$ 
   $e2) + pc', None$ ) by simp
  also (rtranclp-trans) from bisim'
  have  $P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h \vdash (Val v \cdot compareAndSwap(D \cdot F, Val v', e3), xs) \leftrightarrow$ 
  ( $(rev vs @ Addr a \# stk') @ [v', v], loc', length (compE2 e1) + length (compE2 e2) + pc', None$ )
  by  $-$  (rule bisim1CAS3, simp)
  ultimately show ?thesis using ins by fastforce
next
case False
note exec also from False call IH2[of loc] len cs obtain  $pc' stk' loc'$ 
  where exec:  $\tau\text{Exec-mover-a } P t e2 h$  ( $\square$ ,  $xs$ ,  $0$ ,  $None$ ) ( $rev vs @ Addr a \# stk', loc', pc', None$ )
  and ins:  $compE2 e2 ! pc' = Invoke M' (length vs) pc' < length (compE2 e2)$ 
  and bisim':  $P, e2, h \vdash (e2, xs) \leftrightarrow (rev vs @ Addr a \# stk', loc', pc', None)$  by auto
from CAS- $\tau\text{ExecrI2}$ [OF exec, of e1 D F e3 v]
have  $\tau\text{Exec-mover-a } P t (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) h$  ( $[v]$ ,  $loc$ ,  $length (compE2 e1)$ ,
   $None$ ) ( $rev vs @ Addr a \# (stk' @ [v]), loc', length (compE2 e1) + pc', None$ ) by simp
  also (rtranclp-trans) from bisim'
  have  $P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h \vdash (Val v \cdot compareAndSwap(D \cdot F, e2, e3), xs) \leftrightarrow$ 
  ( $(rev vs @ Addr a \# stk') @ [v], loc', length (compE2 e1) + pc', None$ )
  by(rule bisim1CAS2)
  ultimately show ?thesis using ins False by(fastforce intro!: exI)
qed
next
case False with IH1 len False call cs show ?thesis
  by(clarsimp)(fastforce intro!: bisim1-bisims1.bisim1CAS1 elim!: CAS- $\tau\text{ExecrI1}$  intro!: exI)
qed
next
case (bisim1CAS2 e2 n e2' xs stk loc pc e1 e3 D F v)
  note  $IH2 = \langle \llbracket call1 e2' = \lfloor (a, M', vs) \rfloor; n + max-vars e2' \leq length xs; \neg contains-insync e2 \rrbracket \implies$ 
   $?concl e2 n e2' xs pc stk loc \rangle$ 
  note  $IH3 = \langle \bigwedge xs. \llbracket call1 e3 = \lfloor (a, M', vs) \rfloor; n + max-vars e3 \leq length xs; \neg contains-insync e3 \rrbracket$ 
   $\implies ?concl e3 n e3 xs 0 \square xs \rangle$ 
  note  $call = \langle call1 - = \lfloor (a, M', vs) \rfloor \rangle$ 
  note  $len = \langle n + max-vars - \leq length xs \rangle$ 
  note  $bisim2 = \langle P, e2, h \vdash (e2', xs) \leftrightarrow (stk, loc, pc, None) \rangle$ 
  note  $cs = \langle \neg contains-insync - \rangle$ 
show ?case
proof(cases is-val e2')
  case True
  then obtain  $v'$  where [simp]:  $e2' = Val v'$  by auto
  from bisim2 have exec:  $\tau\text{Exec-mover-a } P t e2 h$  ( $stk, loc, pc, None$ ) ( $[v']$ ,  $loc$ ,  $length (compE2 e2)$ ,
   $None$ )

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and $[simp]: xs = loc$ **by** $(auto\ dest!: bisim1Val2D1)$
from $CAS\text{-}\tau\text{ExecrI2}[OF\ exec, of\ e1\ D\ F\ e3\ v]$
have $\tau\text{Exec-mover-a}\ P\ t\ (e1\cdot\text{compareAndSwap}(D\cdot F, e2, e3))\ h\ (stk\ @\ [v], loc, length\ (compE2\ e1)$
 $+ pc, None)\ ([v', v], loc, length\ (compE2\ e1) + length\ (compE2\ e2), None)$ **by** $simp$
also from $call\ IH3[of\ loc]\ len\ cs$ **obtain** $pc'\ stk'\ loc'$
where $exec: \tau\text{Exec-mover-a}\ P\ t\ e3\ h\ ([], xs, 0, None)\ (rev\ vs\ @\ Addr\ a\ \#\ stk', loc', pc', None)$
and $ins: compE2\ e3\ !\ pc' = Invoke\ M'\ (length\ vs)\ pc' < length\ (compE2\ e3)$
and $bisim': P, e3, h \vdash (e3, xs) \leftrightarrow (rev\ vs\ @\ Addr\ a\ \#\ stk', loc', pc', None)$ **by** $auto$
from $CAS\text{-}\tau\text{ExecrI3}[OF\ exec, of\ e1\ D\ F\ e2\ v'\ v]$
have $\tau\text{Exec-mover-a}\ P\ t\ (e1\cdot\text{compareAndSwap}(D\cdot F, e2, e3))\ h\ ([v', v], loc, length\ (compE2\ e1)$
 $+ length\ (compE2\ e2), None)$
 $((rev\ vs\ @\ Addr\ a\ \#\ stk')\ @\ [v', v], loc', length\ (compE2\ e1) + length\ (compE2\ e2)$
 $+ pc', None)$ **by** $simp$
also $(rtranclp\text{-}trans)$ **from** $bisim'$
have $P, e1\cdot\text{compareAndSwap}(D\cdot F, e2, e3), h \vdash (Val\ v\cdot\text{compareAndSwap}(D\cdot F, Val\ v', e3), xs) \leftrightarrow$
 $((rev\ vs\ @\ Addr\ a\ \#\ stk')\ @\ [v', v], loc', length\ (compE2\ e1) + length\ (compE2\ e2) + pc', None)$
by $(rule\ bisim1CAS3)$
ultimately show $?thesis$ **using** ins **by** $(fastforce\ intro!: exI)$
next
case $False$
with $IH2\ len\ call\ cs$ **obtain** $pc'\ loc'\ stk'$
where $ins: pc' < length\ (compE2\ e2)\ compE2\ e2\ !\ pc' = Invoke\ M'\ (length\ vs)$
and $exec: \tau\text{Exec-mover-a}\ P\ t\ e2\ h\ (stk, loc, pc, None)\ (rev\ vs\ @\ Addr\ a\ \#\ stk', loc', pc', None)$
and $bisim': P, e2, h \vdash (e2', xs) \leftrightarrow (rev\ vs\ @\ Addr\ a\ \#\ stk', loc', pc', None)$ **by** $fastforce$
from $bisim'$ **have** $P, e1\cdot\text{compareAndSwap}(D\cdot F, e2, e3), h \vdash (Val\ v\cdot\text{compareAndSwap}(D\cdot F, e2', e3),$
 $xs) \leftrightarrow ((rev\ vs\ @\ Addr\ a\ \#\ stk')\ @\ [v], loc', length\ (compE2\ e1) + pc', None)$
by $(rule\ bisim1\text{-}bisims1.bisim1CAS2)$
with $CAS\text{-}\tau\text{ExecrI2}[OF\ exec, of\ e1\ D\ F\ e3\ v]\ ins\ False$ **show** $?thesis$ **by** $(auto\ intro!: exI)$
qed
next
case $(bisim1CAS3\ e3\ n\ e3'\ xs\ stk\ loc\ pc\ e1\ e2\ D\ F\ v\ v')$
then obtain $pc'\ loc'\ stk'$ **where** $pc': pc' < length\ (compE2\ e3)\ compE2\ e3\ !\ pc' = Invoke\ M'\ (length\ vs)$
and $exec: \tau\text{Exec-mover-a}\ P\ t\ e3\ h\ (stk, loc, pc, None)\ (rev\ vs\ @\ Addr\ a\ \#\ stk', loc', pc', None)$
and $bisim': P, e3, h \vdash (e3', xs) \leftrightarrow (rev\ vs\ @\ Addr\ a\ \#\ stk', loc', pc', None)$ **by** $fastforce$
from $exec$ **have** $\tau\text{Exec-mover-a}\ P\ t\ (e1\cdot\text{compareAndSwap}(D\cdot F, e2, e3))\ h\ (stk\ @\ [v', v], loc, length\ (compE2\ e1)$
 $+ length\ (compE2\ e2) + pc, None)$
 $((rev\ vs\ @\ Addr\ a\ \#\ stk')\ @\ [v', v], loc', length\ (compE2\ e1) + length\ (compE2\ e2) + pc', None)$
by $(rule\ CAS\text{-}\tau\text{ExecrI3})$
moreover from $bisim'$
have $P, e1\cdot\text{compareAndSwap}(D\cdot F, e2, e3), h \vdash (Val\ v\cdot\text{compareAndSwap}(D\cdot F, Val\ v', e3'), xs) \leftrightarrow$
 $((rev\ vs\ @\ Addr\ a\ \#\ stk')\ @\ [v', v], loc', length\ (compE2\ e1) + length\ (compE2\ e2) + pc', None)$
by $(rule\ bisim1\text{-}bisims1.bisim1CAS3)$
ultimately show $?case$ **using** pc' **by** $(fastforce\ intro!: exI)$
next
case $(bisim1Call1\ obj\ n\ obj'\ xs\ stk\ loc\ pc\ ps\ M)$
note $IH1 = \langle \llbracket call1\ obj' = \lfloor (a, M', vs) \rfloor; n + max\text{-}vars\ obj' \leq length\ xs; \neg\ contains\text{-}insync\ obj \rrbracket \implies$
 $?concl\ obj\ n\ obj'\ xs\ pc\ stk\ loc \rangle$
note $IH2 = \langle \bigwedge xs. \llbracket calls1\ ps = \lfloor (a, M', vs) \rfloor; n + max\text{-}varss\ ps \leq length\ xs; \neg\ contains\text{-}insyncs\ ps \rrbracket$
 $\implies ?concls\ ps\ n\ ps\ xs\ 0\ []\ xs \rangle$
note $len = \langle n + max\text{-}vars\ (obj'\cdot M(ps)) \leq length\ xs \rangle$
note $bisim1 = \langle P, obj, h \vdash (obj', xs) \leftrightarrow (stk, loc, pc, None) \rangle$
note $call = \langle call1\ (obj'\cdot M(ps)) = \lfloor (a, M', vs) \rfloor \rangle$

```

note  $cs = \langle \neg \text{contains-insync } (obj \cdot M(ps)) \rangle$ 
from call show ?case
proof(cases rule: call1-callE)
  case CallObj
  hence  $\neg \text{is-val } obj'$  by auto
  with CallObj IH1 len cs show ?thesis
    by(clarsimp)(fastforce intro: bisim1-bisims1.bisim1Call1 elim!: Call- $\tau$ ExecrI1 intro!: exI)
next
  case (CallParams v)
  with bisim1 have  $\tau$ Exec-mover-a P t obj h (stk, loc, pc, None) ([v], loc, length (compE2 obj), None)
    and [simp]:  $xs = loc$  by(auto dest!: bisim1Val2D1)
  hence  $\tau$ Exec-mover-a P t (obj  $\cdot$  M(ps)) h (stk, loc, pc, None) ([v], loc, length (compE2 obj), None)
    by-(rule Call- $\tau$ ExecrI1)
  also from IH2[of loc] CallParams len cs obtain pc' stk' loc'
    where exec:  $\tau$ Exec-movesr-a P t ps h ([], loc, 0, None) (rev vs @ Addr a # stk', loc', pc', None)
    and ins: compEs2 ps ! pc' = Invoke M' (length vs) pc' < length (compEs2 ps)
    and bisim': P,ps,h  $\vdash$  (ps, xs) [ $\leftrightarrow$ ] (rev vs @ Addr a # stk',loc',pc',None) by auto
  from Call- $\tau$ ExecrI2[OF exec, of obj M v]
  have  $\tau$ Exec-mover-a P t (obj  $\cdot$  M(ps)) h ([v], loc, length (compE2 obj), None) (rev vs @ Addr a # (stk' @ [v]), loc', length (compE2 obj) + pc', None) by simp
  also (rtranclp-trans)
  have P,obj  $\cdot$  M(ps),h  $\vdash$  (Val v  $\cdot$  M(ps), xs)  $\leftrightarrow$  ((rev vs @ Addr a # stk') @ [v], loc', length (compE2 obj) + pc', None)
    using bisim' by(rule bisim1CallParams)
  ultimately show ?thesis using ins CallParams by fastforce
next
  case [simp]: Call
  from bisim1 have  $\tau$ Exec-mover-a P t obj h (stk, loc, pc, None) ([Addr a], loc, length (compE2 obj), None)
    and [simp]:  $xs = loc$  by(auto dest!: bisim1Val2D1)
  hence  $\tau$ Exec-mover-a P t (obj  $\cdot$  M(ps)) h (stk, loc, pc, None) ([Addr a], loc, length (compE2 obj), None)
    by-(rule Call- $\tau$ ExecrI1)
  also have  $\tau$ Exec-movesr-a P t ps h ([], xs, 0, None) (rev vs, xs, length (compEs2 ps), None)
  proof(cases vs)
    case Nil with Call show ?thesis by(auto)
  next
    case Cons with Call bisims1-Val- $\tau$ Exec-moves[OF bisims1-refl[of P h map Val vs loc]]
    show ?thesis by(auto simp add: bsoks-def)
  qed
  from Call- $\tau$ ExecrI2[OF this, of obj M Addr a]
  have  $\tau$ Exec-mover-a P t (obj  $\cdot$  M(ps)) h ([Addr a], loc, length (compE2 obj), None) (rev vs @ [Addr a], xs, length (compE2 obj) + length (compEs2 ps), None) by simp
  also (rtranclp-trans)
  have P,ps,h  $\vdash$  (map Val vs,xs) [ $\leftrightarrow$ ] (rev vs,xs,length (compEs2 ps),None)
    by(rule bisims1-map-Val-append[OF bisims1Nil, simplified])(simp-all add: bsoks-def)
  hence P,obj  $\cdot$  M(ps),h  $\vdash$  (addr a  $\cdot$  M(map Val vs), xs)  $\leftrightarrow$  (rev vs @ [Addr a], xs, length (compE2 obj) + length (compEs2 ps), None)
    by(rule bisim1CallParams)
  ultimately show ?thesis by fastforce
  qed
next
  case (bisim1CallParams ps n ps' xs stk loc pc obj M v)

```

```

note IH2 = ⟨[[calls1 ps' = [(a, M', vs)]; n + max-varss ps' ≤ length xs; ¬ contains-insyncs ps]] ⇒
?concls ps n ps' xs pc stk loc⟩
note bisim2 = ⟨P,ps,h ⊢ (ps', xs) [↔] (stk, loc, pc, None)⟩
note call = ⟨call1 (Val v·M(ps')) = [(a, M', vs)]⟩
note len = ⟨n + max-vars (Val v·M(ps')) ≤ length xs⟩
note cs = ⟨¬ contains-insync (obj·M(ps))⟩
from call show ?case
proof(cases rule: call1-callE)
  case CallObj thus ?thesis by simp
next
  case (CallParams v')
  with IH2 len cs obtain pc' stk' loc'
    where exec: τExec-movesr-a P t ps h (stk, loc, pc, None) (rev vs @ Addr a # stk', loc', pc',
None)
    and ins: pc' < length (compEs2 ps) compEs2 ps ! pc' = Invoke M' (length vs)
    and bisim': P,ps,h ⊢ (ps', xs) [↔] (rev vs @ Addr a # stk',loc',pc',None) by auto
from exec have τExec-mover-a P t (obj·M(ps)) h (stk @ [v], loc, length (compE2 obj) + pc, None)
  ((rev vs @ Addr a # stk') @ [v], loc', length (compE2 obj) + pc', None)
  by(rule Call-τExecrI2)
moreover have P,obj·M(ps),h ⊢ (Val v·M(ps'), xs) ↔
  ((rev vs @ Addr a # stk') @ [v], loc', length (compE2 obj) + pc', None)
  using bisim' by(rule bisim1-bisims1.bisim1CallParams)
ultimately show ?thesis using ins by fastforce
next
  case Call
  hence [simp]: v = Addr a ps' = map Val vs M' = M by simp-all
  have xs = loc ∧ τExec-movesr-a P t ps h (stk, loc, pc, None) (rev vs, loc, length (compEs2 ps),
None)
proof(cases pc < length (compEs2 ps))
  case True with bisim2 show ?thesis by(auto dest: bisims1-Val-τExec-moves)
next
  case False
  from bisim2 have pc ≤ length (compEs2 ps) by(rule bisims1-pc-length-compEs2)
  with False have pc = length (compEs2 ps) by simp
  with bisim2 show ?thesis by(auto dest: bisims1-Val-length-compEs2D)
qed
then obtain [simp]: xs = loc
  and exec: τExec-movesr-a P t ps h (stk, loc, pc, None) (rev vs, loc, length (compEs2 ps), None)
..
from exec have τExec-mover-a P t (obj·M(ps)) h (stk @ [v], loc, length (compE2 obj) + pc, None)
  (rev vs @ [v], loc, length (compE2 obj) + length (compEs2 ps), None)
  by(rule Call-τExecrI2)
moreover from bisim2 have len: length ps = length ps' by(auto dest: bisims1-lengthD)
moreover have P,ps,h ⊢ (map Val vs,xs) [↔] (rev vs,xs,length (compEs2 ps),None) using len
  by-(rule bisims1-map-Val-append[OF bisims1Nil, simplified], simp-all)
hence P,obj·M(ps),h ⊢ (addr a·M(map Val vs), xs) ↔ (rev vs @ [Addr a], xs, length (compE2 obj)
+ length (compEs2 ps), None) by(rule bisim1-bisims1.bisim1CallParams)
ultimately show ?thesis by fastforce
qed
next
  case bisim1BlockSome1 thus ?case by simp
next
  case bisim1BlockSome2 thus ?case by simp
next

```

```

case (bisim1BlockSome4 e n e' xs stk loc pc V T v)
then obtain pc' loc' stk' where pc': pc' < length (compE2 e) compE2 e ! pc' = Invoke M' (length vs)
  and exec:  $\tau$ Exec-mover-a P t e h (stk, loc, pc, None) (rev vs @ Addr a # stk', loc', pc', None)
  and bisim':  $P, e, h \vdash (e', xs) \leftrightarrow (rev vs @ Addr a \# stk', loc', pc', None)$  by auto
  note Block- $\tau$ ExecrI-Some[OF exec, of V T v]
  moreover from bisim' have  $P, \{V:T=[v]; e\}, h \vdash (\{V:T=None; e'\}, xs) \leftrightarrow (rev vs @ Addr a \# stk', loc', Suc (Suc pc'), None)$ 
  by(rule bisim1-bisims1.bisim1BlockSome4)
  ultimately show ?case using pc' by fastforce
next
case (bisim1BlockNone e n e' xs stk loc pc V T)
then obtain pc' loc' stk' where pc': pc' < length (compE2 e) compE2 e ! pc' = Invoke M' (length vs)
  and exec:  $\tau$ Exec-mover-a P t e h (stk, loc, pc, None) (rev vs @ Addr a # stk', loc', pc', None)
  and bisim':  $P, e, h \vdash (e', xs) \leftrightarrow (rev vs @ Addr a \# stk', loc', pc', None)$  by auto
  note Block- $\tau$ ExecrI-None[OF exec, of V T]
  moreover from bisim' have  $P, \{V:T=None; e\}, h \vdash (\{V:T=None; e'\}, xs) \leftrightarrow (rev vs @ Addr a \# stk', loc', pc', None)$ 
  by(rule bisim1-bisims1.bisim1BlockNone)
  ultimately show ?case using pc' by fastforce
next
case bisim1Sync1 thus ?case
  by (auto)(fastforce intro: bisim1-bisims1.bisim1Sync1 elim!: Sync- $\tau$ ExecrI intro!: exI)
next
case bisim1Sync2 thus ?case by simp
next
case bisim1Sync3 thus ?case by simp
next
case bisim1Sync4 thus ?case
  by (auto)(fastforce intro: bisim1-bisims1.bisim1Sync4 elim!: Insync- $\tau$ ExecrI intro!: exI)
next
case bisim1Sync5 thus ?case by simp
next
case bisim1Sync6 thus ?case by simp
next
case bisim1Sync7 thus ?case by simp
next
case bisim1Sync8 thus ?case by simp
next
case bisim1Sync9 thus ?case by simp
next
case bisim1InSync thus ?case by simp
next
case bisim1Seq1 thus ?case
  by (auto)(fastforce intro: bisim1-bisims1.bisim1Seq1 elim!: Seq- $\tau$ ExecrI1 intro!: exI)
next
case (bisim1Seq2 e2 n e' xs stk loc pc e1)
then obtain pc' loc' stk' where pc': pc' < length (compE2 e2) compE2 e2 ! pc' = Invoke M' (length vs)
  and exec:  $\tau$ Exec-mover-a P t e2 h (stk, loc, pc, None) (rev vs @ Addr a # stk', loc', pc', None)
  and bisim':  $P, e2, h \vdash (e', xs) \leftrightarrow (rev vs @ Addr a \# stk', loc', pc', None)$  by auto
from Seq- $\tau$ ExecrI2[OF exec, of e1] pc' bisim'
show ?case by(fastforce intro: bisim1-bisims1.bisim1Seq2 intro!: exI)

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next
  case bisim1Cond1 thus ?case
    by (auto)(fastforce intro: bisim1-bisims1.bisim1Cond1 elim!: Cond-τExecrI1 intro!: exI)+
next
  case (bisim1CondThen e1 n e' xs stk loc pc e e2)
  then obtain pc' loc' stk' where pc': pc' < length (compE2 e1) compE2 e1 ! pc' = Invoke M' (length vs)
    and exec:  $\tau\text{Exec-mover-a } P \ t \ e1 \ h \ (stk, \ loc, \ pc, \ None) \ (rev \ vs \ @ \ Addr \ a \ \# \ stk', \ loc', \ pc', \ None)$ 
    and bisim':  $P, e1, h \vdash (e', xs) \leftrightarrow (rev \ vs \ @ \ Addr \ a \ \# \ stk', \ loc', \ pc', \ None)$  by auto
  from Cond-τExecrI2[OF exec] pc' bisim' show ?case
    by(fastforce intro: bisim1-bisims1.bisim1CondThen intro!: exI)
next
  case (bisim1CondElse e2 n e' xs stk loc pc e e1)
  then obtain pc' loc' stk' where pc': pc' < length (compE2 e2) compE2 e2 ! pc' = Invoke M' (length vs)
    and exec:  $\tau\text{Exec-mover-a } P \ t \ e2 \ h \ (stk, \ loc, \ pc, \ None) \ (rev \ vs \ @ \ Addr \ a \ \# \ stk', \ loc', \ pc', \ None)$ 
    and bisim':  $P, e2, h \vdash (e', xs) \leftrightarrow (rev \ vs \ @ \ Addr \ a \ \# \ stk', \ loc', \ pc', \ None)$  by auto
  from Cond-τExecrI3[OF exec] pc' bisim' show ?case
    by (fastforce intro: bisim1-bisims1.bisim1CondElse intro!: exI)
next
  case bisim1While1 thus ?case by simp
next
  case bisim1While3 thus ?case
    by (auto)(fastforce intro: bisim1-bisims1.bisim1While3 elim!: While-τExecrI1 intro!: exI)+
next
  case bisim1While4 thus ?case
    by (auto)(fastforce intro!: While-τExecrI2 bisim1-bisims1.bisim1While4 exI)+
next
  case bisim1While6 thus ?case by simp
next
  case bisim1While7 thus ?case by simp
next
  case bisim1Throw1 thus ?case
    by (auto)(fastforce intro!: exI bisim1-bisims1.bisim1Throw1 elim!: Throw-τExecrI)+
next
  case bisim1Try thus ?case
    by (auto)(fastforce intro: bisim1-bisims1.bisim1Try elim!: Try-τExecrI1 intro!: exI)+
next
  case (bisim1TryCatch1 e n a' xs stk loc pc C' C e2 V)
  note  $IH2 = \langle \bigwedge xs. \llbracket call1 \ e2 = \llbracket (a, M', vs) \rrbracket; Suc \ n + \max\text{-vars} \ e2 \leq \text{length} \ xs; \neg \text{contains-insync} \ e2 \rrbracket \implies ?concl \ e2 \ (Suc \ V) \ e2 \ xs \ 0 \ \llbracket \ \rrbracket \ xs \rangle$ 
  note  $bisim1 = \langle P, e, h \vdash (Throw \ a', \ xs) \leftrightarrow (stk, \ loc, \ pc, \ \llbracket a' \rrbracket) \rangle$ 
  note  $bisim2 = \langle \bigwedge xs. P, e2, h \vdash (e2, \ xs) \leftrightarrow (\llbracket \ \rrbracket, \ xs, \ 0, \ None) \rangle$ 
  note  $len = \langle n + \max\text{-vars} \ \{V:Class \ C=None; \ e2\} \leq \text{length} \ (xs[V := Addr \ a']) \rangle$ 
  note  $cs = \langle \neg \text{contains-insync} \ (try \ e \ catch(C \ V) \ e2) \rangle$ 
  from bisim1 have [simp]: xs = loc by(auto dest: bisim1-ThrowD)
  from len have  $\tau\text{Exec-mover-a } P \ t \ (try \ e \ catch(C \ V) \ e2) \ h \ (\llbracket Addr \ a' \rrbracket, \ loc, \ Suc \ (\text{length} \ (\text{compE2} \ e)), \ None) \ (\llbracket \ \rrbracket, \ loc[V := Addr \ a'], \ Suc \ (Suc \ (\text{length} \ (\text{compE2} \ e))), \ None)$ 
  by  $-(rule \ \tau\text{Execr1step}, auto \ simp \ add: \ exec\text{-move-def} \ intro: \ \tau\text{move2-}\tau\text{moves2.intros} \ exec\text{-instr})$ 
  also from  $IH2$ [of  $loc[V := Addr \ a']$ ] len  $\langle call1 \ \{V:Class \ C=None; \ e2\} = \llbracket (a, M', vs) \rrbracket \rangle \ cs$ 
  obtain pc' loc' stk'
    where exec:  $\tau\text{Exec-mover-a } P \ t \ e2 \ h \ (\llbracket \ \rrbracket, \ loc[V := Addr \ a'], \ 0, \ None) \ (rev \ vs \ @ \ Addr \ a \ \# \ stk', \ loc', \ pc', \ None)$ 
    and ins: pc' < length (compE2 e2) compE2 e2 ! pc' = Invoke M' (length vs)

```

```

and  $bisim'$ :  $P, e2, h \vdash (e2, loc[V := Addr a']) \leftrightarrow (rev\ vs \ @ \ Addr\ a \ \# \ stk', loc', pc', None)$  by auto
from  $Try\text{-}\tau\text{ExecrI2}[OF\ exec, \text{ of } e\ C\ V]$ 
have  $\tau\text{Exec-mover-a } P\ t\ (try\ e\ catch(C\ V)\ e2)\ h\ ([], loc[V := Addr a'], Suc\ (Suc\ (length\ (compE2\ e))), None)$   $(rev\ vs \ @ \ Addr\ a \ \# \ stk', loc', Suc\ (Suc\ (length\ (compE2\ e) + pc')))$ ,  $None$  by simp
also from  $bisim'$ 
have  $P, try\ e\ catch(C\ V)\ e2, h \vdash (\{V:Class\ C=None; e2\}, loc[V := Addr a']) \leftrightarrow (rev\ vs \ @ \ Addr\ a \ \# \ stk', loc', (Suc\ (Suc\ (length\ (compE2\ e) + pc'))), None)$ 
by(rule bisim1TryCatch2)
ultimately show  $?case$  using ins by fastforce
next
case  $bisim1TryCatch2$  thus  $?case$ 
by (auto)(fastforce intro!:  $Try\text{-}\tau\text{ExecrI2}\ bisim1\text{-}bisims1.bisim1TryCatch2\ exI$ )+
next
case  $bisims1Nil$  thus  $?case$  by simp
next
case ( $bisims1List1\ e\ n\ e'\ xs\ stk\ loc\ pc\ es$ )
note  $IH1 = \langle \llbracket call1\ e' = [(a, M', vs)]; n + max\text{-}vars\ e' \leq length\ xs; \neg\ contains\text{-}insync\ e \rrbracket \implies ?concl\ e\ n\ e'\ xs\ pc\ stk\ loc \rangle$ 
note  $IH2 = \langle \bigwedge xs. \llbracket calls1\ es = [(a, M', vs)]; n + max\text{-}varss\ es \leq length\ xs; \neg\ contains\text{-}insyncs\ es \rrbracket \implies ?concls\ es\ n\ es\ xs\ 0 \ \square\ xs \rangle$ 
note  $bisim1 = \langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, None) \rangle$ 
note  $call = \langle calls1\ (e' \ \# \ es) = [(a, M', vs)] \rangle$ 
note  $len = \langle n + max\text{-}varss\ (e' \ \# \ es) \leq length\ xs \rangle$ 
note  $cs = \langle \neg\ contains\text{-}insyncs\ (e \ \# \ es) \rangle$ 
show  $?case$ 
proof(cases is-val e')
case True
then obtain  $v$  where [simp]:  $e' = Val\ v$  by auto
with  $bisim1$  have  $\tau\text{Exec-mover-a } P\ t\ e\ h\ (stk, loc, pc, None)$   $([v], loc, length\ (compE2\ e), None)$ 
and [simp]:  $xs = loc$  by(auto dest!:  $bisim1Val2D1$ )
hence  $\tau\text{Exec-movesr-a } P\ t\ (e \ \# \ es)\ h\ (stk, loc, pc, None)$   $([v], loc, length\ (compE2\ e), None)$ 
by-(rule  $\tau\text{Exec-mover-}\tau\text{Exec-movesr}$ )
also from  $call\ IH2[of\ loc]\ len\ cs$  obtain  $pc'\ stk'\ loc'$ 
where  $exec: \tau\text{Exec-movesr-a } P\ t\ es\ h\ ([], xs, 0, None)$   $(rev\ vs \ @ \ Addr\ a \ \# \ stk', loc', pc', None)$ 
and  $ins: compEs2\ es \ ! \ pc' = Invoke\ M'\ (length\ vs)\ pc' < length\ (compEs2\ es)$ 
and  $bisim': P, es, h \vdash (es, xs) [\leftrightarrow] (rev\ vs \ @ \ Addr\ a \ \# \ stk', loc', pc', None)$  by auto
from  $append\text{-}\tau\text{Exec-movesr}[OF\ \text{-}\ exec, \text{ of } [v]\ [e]]$ 
have  $\tau\text{Exec-movesr-a } P\ t\ (e \ \# \ es)\ h\ ([v], loc, length\ (compE2\ e), None)$   $(rev\ vs \ @ \ Addr\ a \ \# \ (stk' \ @ \ [v]), loc', length\ (compE2\ e) + pc', None)$ 
by simp
also (rtranclp-trans) from  $bisim'$ 
have  $P, e \ \# \ es, h \vdash (Val\ v \ \# \ es, xs) [\leftrightarrow]$ 
 $((rev\ vs \ @ \ Addr\ a \ \# \ stk') \ @ \ [v], loc', length\ (compE2\ e) + pc', None)$ 
by(rule bisim1-bisims1.bisims1List2)
ultimately show  $?thesis$  using ins by fastforce
next
case False
with  $call\ IH1\ len\ cs$  show  $?thesis$ 
by (auto)(fastforce intro!:  $\tau\text{Exec-mover-}\tau\text{Exec-movesr}\ bisim1\text{-}bisims1.bisims1List1\ exI$ )+
qed
next
case ( $bisims1List2\ es\ n\ es'\ xs\ stk\ loc\ pc\ e\ v$ )
then obtain  $pc'\ stk'\ loc'$  where  $pc': pc' < length\ (compEs2\ es)$   $compEs2\ es \ ! \ pc' = Invoke\ M'\ (length\ vs)$ 

```


and $exec: \tau Exec\text{-}movesr\text{-}a P t es h (stk, loc, pc, None) (rev vs @ Addr a \# stk', loc', pc', None)$
and $bisim': P, es, h \vdash (es', xs) [\leftrightarrow] (rev vs @ Addr a \# stk', loc', pc', None)$ **by** *auto*
note $append\text{-}\tau Exec\text{-}movesr [OF - exec, of [v] [e]]$
moreover from $bisim'$
have $P, e \# es, h \vdash (Val v \# es', xs) [\leftrightarrow] ((rev vs @ Addr a \# stk') @ [v], loc', length (compE2 e) + pc', None)$
by $(rule\ bisim1\text{-}bisims1.bisims1List2)$
ultimately show $?case$ **using** pc' **by** *fastforce*
qed

lemma fixes $P :: 'addr J1\text{-}prog$

shows $bisim1\text{-}inline\text{-}call\text{-}Val:$

$\llbracket P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, None); call1 e' = [(a, M, vs)];$

$compE2 e ! pc = Invoke M n0 \rrbracket$

$\implies length\ stk \geq Suc\ (length\ vs) \wedge n0 = length\ vs \wedge$

$P, e, h \vdash (inline\text{-}call\ (Val\ v)\ e', xs) \leftrightarrow (v \# drop\ (Suc\ (length\ vs))\ stk, loc, Suc\ pc, None)$

(is $\llbracket -; -; - \rrbracket \implies ?concl\ e\ n\ e'\ xs\ pc\ stk\ loc$ **)**

and $bisims1\text{-}inline\text{-}calls\text{-}Val:$

$\llbracket P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, None); calls1 es' = [(a, M, vs)];$

$compEs2 es ! pc = Invoke M n0 \rrbracket$

$\implies length\ stk \geq Suc\ (length\ vs) \wedge n0 = length\ vs \wedge$

$P, es, h \vdash (inline\text{-}calls\ (Val\ v)\ es', xs) [\leftrightarrow] (v \# drop\ (Suc\ (length\ vs))\ stk, loc, Suc\ pc, None)$

(is $\llbracket -; -; - \rrbracket \implies ?concls\ es\ n\ es'\ xs\ pc\ stk\ loc$ **)**

proof $(induct\ (e', xs)\ (stk, loc, pc, None) :: 'addr\ option)$

and $(es', xs)\ (stk, loc, pc, None) :: 'addr\ option)$

arbitrary: $e' xs\ stk\ loc\ pc$ **and** $es' xs\ stk\ loc\ pc$ *rule:* $bisim1\text{-}bisims1.inducts$)

case $bisim1Val2$ **thus** $?case$ **by** *simp*

next

case $bisim1New$ **thus** $?case$ **by** *simp*

next

case $bisim1NewArray$ **thus** $?case$

by $(auto\ split: if\text{-}split\text{-}asm\ dest: bisim1\text{-}pc\text{-}length\text{-}compE2\ intro: bisim1\text{-}bisims1.bisim1NewArray)$

next

case $bisim1Cast$ **thus** $?case$

by $(auto\ split: if\text{-}split\text{-}asm\ dest: bisim1\text{-}pc\text{-}length\text{-}compE2\ intro: bisim1\text{-}bisims1.bisim1Cast)$

next

case $bisim1InstanceOf$ **thus** $?case$

by $(auto\ split: if\text{-}split\text{-}asm\ dest: bisim1\text{-}pc\text{-}length\text{-}compE2\ intro: bisim1\text{-}bisims1.bisim1InstanceOf)$

next

case $bisim1Val$ **thus** $?case$ **by** *simp*

next

case $bisim1Var$ **thus** $?case$ **by** *simp*

next

case $(bisim1BinOp1\ e1\ e'\ xs\ stk\ loc\ pc\ bop\ e2)$

note $IH1 = \langle \llbracket call1\ e' = [(a, M, vs)]; compE2\ e1 ! pc = Invoke\ M\ n0 \rrbracket \implies ?concl\ e1\ n\ e'\ xs\ pc\ stk\ loc \rangle$

note $bisim1 = \langle P, e1, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, None) \rangle$

note $call = \langle call1\ (e' \llbracket bop \rrbracket e2) = [(a, M, vs)] \rangle$

note $ins = \langle compE2\ (e1 \llbracket bop \rrbracket e2) ! pc = Invoke\ M\ n0 \rangle$

show $?case$

proof $(cases\ is\text{-}val\ e')$

case *False*

with $bisim1\ call$ **have** $pc < length\ (compE2\ e1)$ **by** $(auto\ intro: bisim1\text{-}call\text{-}pcD)$

```

with call ins False IH1 show ?thesis
  by(auto intro: bisim1-bisims1.bisim1BinOp1)
next
case True
then obtain v where [simp]: e' = Val v by auto
from bisim1 have pc ≤ length (compE2 e1) by(auto dest: bisim1-pc-length-compE2)
moreover {
  assume pc: pc < length (compE2 e1)
  with bisim1 ins have False by(auto dest: bisim-Val-pc-not-Invoke) }
ultimately have [simp]: pc = length (compE2 e1) by(cases pc < length (compE2 e1)) auto
with ins have False by(simp)
thus ?thesis ..
qed
next
case (bisim1BinOp2 e2 e' xs stk loc pc e1 bop v1)
note IH2 = ⟨[[call1 e' = [(a, M, vs)]; compE2 e2 ! pc = Invoke M n0]] ⇒ ?concl e2 n e' xs pc stk
loc⟩
note bisim2 = ⟨P,e2,h ⊢ (e', xs) ↔ (stk, loc, pc, None)⟩
note call = ⟨call1 (Val v1 «bop» e') = [(a, M, vs)]⟩
note ins = ⟨compE2 (e1 «bop» e2) ! (length (compE2 e1) + pc) = Invoke M n0⟩
from call bisim2 have pc: pc < length (compE2 e2) by(auto intro: bisim1-call-pcD)
with ins have ins': compE2 e2 ! pc = Invoke M n0 by(simp)
from IH2 ins' pc call show ?case by(auto dest: bisim1-bisims1.bisim1BinOp2)
next
case bisim1LAss1 thus ?case
  by(auto split: if-split-asm dest: bisim1-pc-length-compE2 intro: bisim1-bisims1.bisim1LAss1)
next
case bisim1LAss2 thus ?case by simp
next
case (bisim1AAcc1 A a' xs stk loc pc i)
note IH1 = ⟨[[call1 a' = [(a, M, vs)]; compE2 A ! pc = Invoke M n0]] ⇒ ?concl A n a' xs pc stk
loc⟩
note bisim1 = ⟨P,A,h ⊢ (a', xs) ↔ (stk, loc, pc, None)⟩
note call = ⟨call1 (a'[i]) = [(a, M, vs)]⟩
note ins = ⟨compE2 (A[i]) ! pc = Invoke M n0⟩
show ?case
proof(cases is-val a')
  case False
  with bisim1 call have pc < length (compE2 A) by(auto intro: bisim1-call-pcD)
  with call ins False IH1 show ?thesis
  by(auto intro: bisim1-bisims1.bisim1AAcc1)
next
case True
then obtain v where [simp]: a' = Val v by auto
from bisim1 have pc ≤ length (compE2 A) by(auto dest: bisim1-pc-length-compE2)
moreover {
  assume pc: pc < length (compE2 A)
  with bisim1 ins have False by(auto dest: bisim-Val-pc-not-Invoke) }
ultimately have [simp]: pc = length (compE2 A) by(cases pc < length (compE2 A)) auto
with ins have False by(simp)
thus ?thesis ..
qed
next
case (bisim1AAcc2 i i' xs stk loc pc A v)

```

```

note IH2 = ⟨[[call1 i' = [(a, M, vs)]; compE2 i ! pc = Invoke M n0]] ⇒ ?concl i n i' xs pc stk loc⟩
note bisim2 = ⟨P,i,h ⊢ (i', xs) ↔ (stk, loc, pc, None)⟩
note call = ⟨call1 (Val v[i∧]) = [(a, M, vs)]⟩
note ins = ⟨compE2 (A[i]) ! (length (compE2 A) + pc) = Invoke M n0⟩
from call bisim2 have pc: pc < length (compE2 i) by(auto intro: bisim1-call-pcD)
with ins have ins': compE2 i ! pc = Invoke M n0 by(simp)
from IH2 ins' pc call show ?case
  by(auto dest: bisim1-bisims1.bisim1AAcc2)
next
case (bisim1AAss1 A a' xs stk loc pc i e)
  note IH1 = ⟨[[call1 a' = [(a, M, vs)]; compE2 A ! pc = Invoke M n0]] ⇒ ?concl A n a' xs pc stk loc⟩
  note bisim1 = ⟨P,A,h ⊢ (a', xs) ↔ (stk, loc, pc, None)⟩
  note call = ⟨call1 (a'[i] := e) = [(a, M, vs)]⟩
  note ins = ⟨compE2 (A[i] := e) ! pc = Invoke M n0⟩
  show ?case
  proof(cases is-val a')
    case False
      with bisim1 call have pc < length (compE2 A) by(auto intro: bisim1-call-pcD)
      with call ins False IH1 show ?thesis by(auto intro: bisim1-bisims1.bisim1AAss1)
    next
    case True
      then obtain v where [simp]: a' = Val v by auto
      from bisim1 have pc ≤ length (compE2 A) by(auto dest: bisim1-pc-length-compE2)
      moreover {
        assume pc: pc < length (compE2 A)
        with bisim1 ins have False by(auto dest: bisim-Val-pc-not-Invoke) }
      ultimately have [simp]: pc = length (compE2 A) by(cases pc < length (compE2 A)) auto
      with ins have False by(simp)
      thus ?thesis ..
    qed
  next
  case (bisim1AAss2 i i' xs stk loc pc A e v)
  note IH2 = ⟨[[call1 i' = [(a, M, vs)]; compE2 i ! pc = Invoke M n0]] ⇒ ?concl i n i' xs pc stk loc⟩
  note bisim2 = ⟨P,i,h ⊢ (i', xs) ↔ (stk, loc, pc, None)⟩
  note call = ⟨call1 (Val v[i∧] := e) = [(a, M, vs)]⟩
  note ins = ⟨compE2 (A[i] := e) ! (length (compE2 A) + pc) = Invoke M n0⟩
  show ?case
  proof(cases is-val i')
    case False
      with bisim2 call have pc: pc < length (compE2 i) by(auto intro: bisim1-call-pcD)
      with ins have ins': compE2 i ! pc = Invoke M n0 by(simp)
      from IH2 ins' pc False call show ?thesis by(auto dest: bisim1-bisims1.bisim1AAss2)
    next
    case True
      then obtain v where [simp]: i' = Val v by auto
      from bisim2 have pc ≤ length (compE2 i) by(auto dest: bisim1-pc-length-compE2)
      moreover {
        assume pc: pc < length (compE2 i)
        with bisim2 ins have False by(auto dest: bisim-Val-pc-not-Invoke) }
      ultimately have [simp]: pc = length (compE2 i) by(cases pc < length (compE2 i)) auto
      with ins have False by(simp)
      thus ?thesis ..
    qed
  qed

```

```

next
  case (bisim1AAss3 e e' xs stk loc pc i A v v')
  note IH2 = ⟨[[call1 e' = [(a, M, vs)]; compE2 e ! pc = Invoke M n0]] ⟹ ?concl e n e' xs pc stk
loc⟩
  note bisim3 = ⟨P,e,h ⊢ (e', xs) ↔ (stk, loc, pc, None)⟩
  note call = ⟨call1 (Val v [Val v'] := e') = [(a, M, vs)]⟩
  note ins = ⟨compE2 (i[A] := e) ! (length (compE2 i) + length (compE2 A) + pc) = Invoke M n0⟩
  from call bisim3 have pc: pc < length (compE2 e) by(auto intro: bisim1-call-pcD)
  with ins have ins': compE2 e ! pc = Invoke M n0 by(simp)
  from IH2 ins' pc call show ?case by(auto dest: bisim1-bisims1.bisim1AAss3)
next
  case bisim1AAss4 thus ?case by simp
next
  case bisim1ALength thus ?case
  by(auto split: if-split-asm dest: bisim1-pc-length-compE2 intro: bisim1-bisims1.bisim1ALength)
next
  case bisim1FAcc thus ?case
  by(auto split: if-split-asm dest: bisim1-pc-length-compE2 intro: bisim1-bisims1.bisim1FAcc)
next
  case (bisim1FAss1 e1 e' xs stk loc pc F D e2)
  note IH1 = ⟨[[call1 e' = [(a, M, vs)]; compE2 e1 ! pc = Invoke M n0]] ⟹ ?concl e1 n e' xs pc stk
loc⟩
  note bisim1 = ⟨P,e1,h ⊢ (e', xs) ↔ (stk, loc, pc, None)⟩
  note call = ⟨call1 (e'·F{D} := e2) = [(a, M, vs)]⟩
  note ins = ⟨compE2 (e1·F{D} := e2) ! pc = Invoke M n0⟩
  show ?case
  proof(cases is-val e')
    case False
    with bisim1 call have pc < length (compE2 e1) by(auto intro: bisim1-call-pcD)
    with call ins False IH1 show ?thesis
    by(auto intro: bisim1-bisims1.bisim1FAss1)
  next
  case True
  then obtain v where [simp]: e' = Val v by auto
  from bisim1 have pc ≤ length (compE2 e1) by(auto dest: bisim1-pc-length-compE2)
  moreover {
    assume pc: pc < length (compE2 e1)
    with bisim1 ins have False by(auto dest: bisim-Val-pc-not-Invoke) }
  ultimately have [simp]: pc = length (compE2 e1) by(cases pc < length (compE2 e1)) auto
  with ins have False by(simp)
  thus ?thesis ..
  qed
next
  case (bisim1FAss2 e2 e' xs stk loc pc e1 F D v1)
  note IH2 = ⟨[[call1 e' = [(a, M, vs)]; compE2 e2 ! pc = Invoke M n0]] ⟹ ?concl e2 n e' xs pc stk
loc⟩
  note bisim2 = ⟨P,e2,h ⊢ (e', xs) ↔ (stk, loc, pc, None)⟩
  note call = ⟨call1 (Val v1·F{D} := e') = [(a, M, vs)]⟩
  note ins = ⟨compE2 (e1·F{D} := e2) ! (length (compE2 e1) + pc) = Invoke M n0⟩
  from call bisim2 have pc: pc < length (compE2 e2) by(auto intro: bisim1-call-pcD)
  with ins have ins': compE2 e2 ! pc = Invoke M n0 by(simp)
  from IH2 ins' pc call show ?case by(auto dest: bisim1-bisims1.bisim1FAss2)
next
  case bisim1FAss3 thus ?case by simp

```

```

next
  case (bisim1CAS1 e1 e' xs stk loc pc D F e2 E3)
  note IH1 = ⟨[call1 e' = [(a, M, vs)]; compE2 e1 ! pc = Invoke M n0] ⟹ ?concl e1 n e' xs pc stk
loc⟩
  note bisim1 = ⟨P, e1, h ⊢ (e', xs) ↔ (stk, loc, pc, None)⟩
  note call = ⟨call1 - = [(a, M, vs)]⟩
  note ins = ⟨compE2 - ! pc = Invoke M n0⟩
  show ?case
  proof(cases is-val e')
    case False
    with bisim1 call have pc < length (compE2 e1) by(auto intro: bisim1-call-pcD)
    with call ins False IH1 show ?thesis by(auto intro: bisim1-bisims1.bisim1CAS1)
  next
  case True
  then obtain v where [simp]: e' = Val v by auto
  from bisim1 have pc ≤ length (compE2 e1) by(auto dest: bisim1-pc-length-compE2)
  moreover {
    assume pc: pc < length (compE2 e1)
    with bisim1 ins have False by(auto dest: bisim-Val-pc-not-Invoke) }
  ultimately have [simp]: pc = length (compE2 e1) by(cases pc < length (compE2 e1)) auto
  with ins have False by(simp)
  thus ?thesis ..
qed
next
  case (bisim1CAS2 e2 e2' xs stk loc pc e1 D F e3 v)
  note IH2 = ⟨[call1 e2' = [(a, M, vs)]; compE2 e2 ! pc = Invoke M n0] ⟹ ?concl e2 n e2' xs pc
stk loc⟩
  note bisim2 = ⟨P, e2, h ⊢ (e2', xs) ↔ (stk, loc, pc, None)⟩
  note call = ⟨call1 - = [(a, M, vs)]⟩
  note ins = ⟨compE2 - ! (length (compE2 e1) + pc) = Invoke M n0⟩
  show ?case
  proof(cases is-val e2')
    case False
    with bisim2 call have pc: pc < length (compE2 e2) by(auto intro: bisim1-call-pcD)
    with ins have ins': compE2 e2 ! pc = Invoke M n0 by(simp)
    from IH2 ins' pc False call show ?thesis by(auto dest: bisim1-bisims1.bisim1CAS2)
  next
  case True
  then obtain v where [simp]: e2' = Val v by auto
  from bisim2 have pc ≤ length (compE2 e2) by(auto dest: bisim1-pc-length-compE2)
  moreover {
    assume pc: pc < length (compE2 e2)
    with bisim2 ins have False by(auto dest: bisim-Val-pc-not-Invoke) }
  ultimately have [simp]: pc = length (compE2 e2) by(cases pc < length (compE2 e2)) auto
  with ins have False by(simp)
  thus ?thesis ..
qed
next
  case (bisim1CAS3 e3 e3' xs stk loc pc e1 D F e2 v v')
  note IH2 = ⟨[call1 e3' = [(a, M, vs)]; compE2 e3 ! pc = Invoke M n0] ⟹ ?concl e3 n e3' xs pc
stk loc⟩
  note bisim3 = ⟨P, e3, h ⊢ (e3', xs) ↔ (stk, loc, pc, None)⟩
  note call = ⟨call1 - = [(a, M, vs)]⟩
  note ins = ⟨compE2 - ! (length (compE2 e1) + length (compE2 e2) + pc) = Invoke M n0⟩

```

```

from call bisim3 have pc: pc < length (compE2 e3) by(auto intro: bisim1-call-pcD)
with ins have ins': compE2 e3 ! pc = Invoke M n0 by(simp)
from IH2 ins' pc call show ?case by(auto dest: bisim1-bisims1.bisim1CAS3)
next
  case (bisim1Call1 obj obj' xs stk loc pc M' ps)
  note IH1 = ⟨[call1 obj' = [(a, M, vs)]; compE2 obj ! pc = Invoke M n0] ⟹ ?concl obj n obj' xs
pc stk loc⟩
  note bisim1 = ⟨P, obj, h ⊢ (obj', xs) ↔ (stk, loc, pc, None)⟩
  note call = ⟨call1 (obj'·M'(ps)) = [(a, M, vs)]⟩
  note ins = ⟨compE2 (obj·M'(ps)) ! pc = Invoke M n0⟩
  show ?case
  proof(cases is-val obj')
    case False
    with call bisim1 have pc < length (compE2 obj) by(auto intro: bisim1-call-pcD)
    with call False ins IH1 False show ?thesis
    by(auto intro: bisim1-bisims1.bisim1Call1)
  next
  case True
  then obtain v' where [simp]: obj' = Val v' by auto
  from bisim1 have pc ≤ length (compE2 obj) by(auto dest: bisim1-pc-length-compE2)
  moreover {
    assume pc: pc < length (compE2 obj)
    with bisim1 ins have False by(auto dest: bisim-Val-pc-not-Invoke) }
  ultimately have [simp]: pc = length (compE2 obj) by(cases pc < length (compE2 obj)) auto
  with ins have [simp]: ps = [] M' = M
  by(auto split: if-split-asm)(auto simp add: neq-Nil-conv)
  from ins call have [simp]: vs = [] by(auto split: if-split-asm)
  with bisim1 have [simp]: stk = [v'] xs = loc by(auto dest: bisim1-pc-length-compE2D)
  from bisim1 Val2[of length (compE2 (obj·M([])))] obj·M([]) P h v loc] call ins
  show ?thesis by(auto simp add: is-val-iff)
  qed
next
  case (bisim1CallParams ps ps' xs stk loc pc obj M' v')
  note IH2 = ⟨[calls1 ps' = [(a, M, vs)]; compEs2 ps ! pc = Invoke M n0] ⟹ ?concl ps n ps' xs
pc stk loc⟩
  note bisim = ⟨P, ps, h ⊢ (ps', xs) [↔] (stk, loc, pc, None)⟩
  note call = ⟨call1 (Val v'·M'(ps')) = [(a, M, vs)]⟩
  note ins = ⟨compE2 (obj·M'(ps)) ! (length (compE2 obj) + pc) = Invoke M n0⟩
  from call show ?case
  proof(cases rule: call1-callE)
    case CallObj thus ?thesis by simp
  next
  case (CallParams v')
  hence [simp]: v'' = v' and call': calls1 ps' = [(a, M, vs)] by simp-all
  from bisim call' have pc: pc < length (compEs2 ps) by(rule bisims1-calls-pcD)
  with ins have ins': compEs2 ps ! pc = Invoke M n0 by(simp)
  with IH2 call' ins pc
  have P, ps, h ⊢ (inline-calls (Val v) ps', xs)
    [↔] (v # drop (Suc (length vs)) stk, loc, Suc pc, None)
    and len: Suc (length vs) ≤ length stk and n0: n0 = length vs by auto
  hence P, obj·M'(ps), h ⊢ (Val v'·M'(inline-calls (Val v) ps'), xs)
    ↔ ((v # drop (Suc (length vs)) stk) @ [v'], loc, length (compE2 obj) + Suc pc,
None)
  by-(rule bisim1-bisims1.bisim1CallParams)

```

```

thus ?thesis using call' len n0 by(auto simp add: is-vals-conv)
next
  case Call
  hence [simp]: v' = Addr a M' = M ps' = map Val vs by auto
  from bisim have pc ≤ length (compEs2 ps) by(auto dest: bisims1-pc-length-compEs2)
  moreover {
    assume pc: pc < length (compEs2 ps)
    with bisim ins have False by(auto dest: bisims-Val-pc-not-Invoke) }
  ultimately have [simp]: pc = length (compEs2 ps) by(cases pc < length (compEs2 ps)) auto
  from bisim have [simp]: stk = rev vs xs = loc by(auto dest: bisims1-Val-length-compEs2D)
  hence P,obj·M(ps),h ⊢ (Val v, loc) ↔ ([v], loc, length (compE2 (obj·M(ps))), None) by-(rule
bisim1Val2, simp)
  moreover from bisim have length ps = length ps' by(rule bisims1-lengthD)
  ultimately show ?thesis using ins by(auto)
  qed
next
  case bisim1BlockSome1 thus ?case by simp
next
  case bisim1BlockSome2 thus ?case by simp
next
  case bisim1BlockSome4 thus ?case
  by(auto intro: bisim1-bisims1.bisim1BlockSome4)
next
  case bisim1BlockNone thus ?case
  by(auto intro: bisim1-bisims1.bisim1BlockNone)
next
  case bisim1Sync1 thus ?case
  by(auto split: if-split-asm dest: bisim1-pc-length-compE2 intro: bisim1-bisims1.bisim1Sync1)
next
  case bisim1Sync2 thus ?case by simp
next
  case bisim1Sync3 thus ?case by simp
next
  case bisim1Sync4 thus ?case
  by(auto split: if-split-asm dest: bisim1-pc-length-compE2 bisim1-bisims1.bisim1Sync4)
next
  case bisim1Sync5 thus ?case by simp
next
  case bisim1Sync6 thus ?case by simp
next
  case bisim1Sync7 thus ?case by simp
next
  case bisim1Sync8 thus ?case by simp
next
  case bisim1Sync9 thus ?case by simp
next
  case bisim1InSync thus ?case by(simp)
next
  case bisim1Seq1 thus ?case
  by(auto split: if-split-asm dest: bisim1-pc-length-compE2 intro: bisim1-bisims1.bisim1Seq1)
next
  case bisim1Seq2 thus ?case
  by(auto split: if-split-asm dest: bisim1-pc-length-compE2)(fastforce dest: bisim1-bisims1.bisim1Seq2)
next

```

```

case bisim1Cond1 thus ?case
  by(auto split: if-split-asm dest: bisim1-pc-length-compE2 intro: bisim1-bisims1.bisim1Cond1)
next
case (bisim1CondThen e1 stk loc pc e e2 e' xs) thus ?case
  by(auto split: if-split-asm dest: bisim1-pc-length-compE2)
  (fastforce dest: bisim1-bisims1.bisim1CondThen[where e=e and ?e2.0=e2])
next
case (bisim1CondElse e2 stk loc pc e e1 e' xs) thus ?case
  by(auto split: if-split-asm dest: bisim1-pc-length-compE2)
  (fastforce dest: bisim1-bisims1.bisim1CondElse[where e=e and ?e1.0=e1])
next
case bisim1While1 thus ?case by simp
next
case bisim1While3 thus ?case
  by(auto split: if-split-asm dest: bisim1-pc-length-compE2 intro: bisim1-bisims1.bisim1While3)
next
case bisim1While4 thus ?case
  by(auto split: if-split-asm dest: bisim1-pc-length-compE2)(fastforce dest: bisim1-bisims1.bisim1While4)
next
case bisim1While6 thus ?case by simp
next
case bisim1While7 thus ?case by simp
next
case bisim1Throw1 thus ?case
  by(auto split: if-split-asm dest: bisim1-pc-length-compE2 intro: bisim1-bisims1.bisim1Throw1)
next
case bisim1Try thus ?case
  by(auto split: if-split-asm dest: bisim1-pc-length-compE2 intro: bisim1-bisims1.bisim1Try)
next
case bisim1TryCatch1 thus ?case by simp
next
case bisim1TryCatch2 thus ?case
  by(fastforce dest: bisim1-bisims1.bisim1TryCatch2)
next
case bisims1Nil thus ?case by simp
next
case (bisims1List1 e e' xs stk loc pc es)
  note IH1 =  $\langle \llbracket \text{call1 } e' = [(a, M, vs)]; \text{compE2 } e ! pc = \text{Invoke } M \ n0 \rrbracket \implies ?\text{concl } e \ n \ e' \ xs \ pc \ stk \ loc \rangle$ 
  note bisim1 =  $\langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, \text{None}) \rangle$ 
  note call =  $\langle \text{calls1 } (e' \# es) = [(a, M, vs)] \rangle$ 
  note ins =  $\langle \text{compEs2 } (e \# es) ! pc = \text{Invoke } M \ n0 \rangle$ 
  show ?case
  proof(cases is-val e')
    case False
      with bisim1 call have  $pc < \text{length } (\text{compE2 } e)$  by(auto intro: bisim1-call-pcD)
      with call ins False IH1 show ?thesis
      by(auto intro: bisim1-bisims1.bisims1List1)
    next
      case True
        then obtain v where [simp]:  $e' = \text{Val } v$  by auto
        from bisim1 have  $pc \leq \text{length } (\text{compE2 } e)$  by(auto dest: bisim1-pc-length-compE2)
        moreover {
          assume pc:  $pc < \text{length } (\text{compE2 } e)$ 

```



```

    with bisim1 ins have False by(auto dest: bisim-Val-pc-not-Invoke) }
  ultimately have [simp]: pc = length (compE2 e) by(cases pc < length (compE2 e)) auto
  with ins call have False by(cases es)(auto)
  thus ?thesis ..
qed
next
case (bisims1List2 es es' xs stk loc pc e v')
  note IH = ⟨[calls1 es' = [(a, M, vs)]; compEs2 es ! pc = Invoke M n0] ⟹ ?concls es n es' xs pc
stk loc⟩
  note call = ⟨calls1 (Val v' # es') = [(a, M, vs)]⟩
  note bisim = ⟨P, es, h ⊢ (es', xs) [↔] (stk, loc, pc, None)⟩
  note ins = ⟨compEs2 (e # es) ! (length (compE2 e) + pc) = Invoke M n0⟩
  from call have call': calls1 es' = [(a, M, vs)] by simp
  with bisim have pc: pc < length (compEs2 es) by(rule bisims1-calls-pcD)
  with ins have ins': compEs2 es ! pc = Invoke M n0 by(simp)
  from IH call ins pc show ?case
    by(auto split: if-split-asm dest: bisim1-bisims1.bisims1List2)
qed

lemma bisim1-fv: P, e, h ⊢ (e', xs) ↔ s ⟹ fv e' ⊆ fv e
  and bisims1-fvs: P, es, h ⊢ (es', xs) [↔] s ⟹ fvs es' ⊆ fvs es
apply(induct (e', xs) s and (es', xs) s arbitrary: e' xs and es' xs rule: bisim1-bisims1.inducts)
apply(auto)
done

lemma bisim1-syncvars:  $\llbracket P, e, h \vdash (e', xs) \leftrightarrow s; \text{syncvars } e \rrbracket \Longrightarrow \text{syncvars } e'$ 
  and bisims1-syncvarss:  $\llbracket P, es, h \vdash (es', xs) [\leftrightarrow] s; \text{syncvarss } es \rrbracket \Longrightarrow \text{syncvarss } es'$ 
apply(induct (e', xs) s and (es', xs) s arbitrary: e' xs and es' xs rule: bisim1-bisims1.inducts)
apply(auto dest: bisim1-fv)
done

declare pcs-stack-xlift [simp]

lemma bisim1-Val-τred1r:
 $\llbracket P, E, h \vdash (e, xs) \leftrightarrow ([v], \text{loc}, \text{length (compE2 } E), \text{None}); n + \text{max-vars } e \leq \text{length } xs; \mathcal{B} E n \rrbracket$ 
 $\Longrightarrow \tau\text{red1r } P \text{ t h } (e, xs) (\text{Val } v, \text{loc})$ 

and bisims1-Val-τReds1r:
 $\llbracket P, Es, h \vdash (es, xs) [\leftrightarrow] (\text{rev } vs, \text{loc}, \text{length (compEs2 } Es), \text{None}); n + \text{max-varss } es \leq \text{length } xs; \mathcal{B} s Es n \rrbracket$ 
 $\Longrightarrow \tau\text{reds1r } P \text{ t h } (es, xs) (\text{map Val } vs, \text{loc})$ 
proof(induct E n e xs stk ≡ [v] loc pc ≡ length (compE2 E) xcp ≡ None::'addr option
  and Es n es xs stk ≡ rev vs loc pc ≡ length (compEs2 Es) xcp ≡ None::'addr option
  arbitrary: v and vs rule: bisim1-bisims1-inducts-split)
  case bisim1BlockSome2 thus ?case by(simp (no-asm-use))
next
case (bisim1BlockSome4 e n e' xs loc pc V T val)
  from  $\langle \mathcal{B} \{V:T=[val]; e\} n \rangle$  have [simp]: n = V and  $\mathcal{B} e (\text{Suc } n)$  by auto
  note len = ⟨n + max-vars {V:T=None; e'} ≤ length xs⟩
  hence V: V < length xs by simp
  from  $\langle P, e, h \vdash (e', xs) \leftrightarrow ([v], \text{loc}, \text{pc}, \text{None}) \rangle$ 
  have len.xs: length xs = length loc by(auto dest: bisim1-length-xs)
  note IH = ⟨[pc = length (compE2 e); Suc n + max-vars e' ≤ length xs; B e (Suc n)]

```

```

       $\impl \tau\text{red1r } P t h (e', xs) (Val v, loc)\rangle$ 
with  $\langle Suc (Suc pc) = length (compE2 \{V:T=[val]; e\}) \rangle \langle \mathcal{B} e (Suc n) \rangle$ 
have  $\tau\text{red1r } P t h (e', xs) (Val v, loc)$  by (simp)
hence  $\tau\text{red1r } P t h (\{V:T=None; e'\}, xs) (\{V:T=None; Val v\}, loc)$ 
  by (rule Block-None- $\tau\text{red1r-xt}$ )
thus ?case using  $V len xs$  by (auto elim!: rtranclp.rtrancl-into-rtrancl intro: Red1Block  $\tau\text{move1BlockRed}$ )
next
  case (bisim1BlockNone  $e n e' xs loc V T$ )
from  $\langle \mathcal{B} \{V:T=None; e\} n \rangle$  have [simp]:  $n = V$  and  $\mathcal{B} e (Suc n)$  by auto
note  $len = \langle n + max\text{-vars} \{V:T=None; e'\} \leq length xs \rangle$ 
hence  $V: V < length xs$  by simp
from  $\langle P, e, h \vdash (e', xs) \leftrightarrow ([v], loc, length (compE2 \{V:T=None; e\}), None) \rangle$ 
have  $len xs: length xs = length loc$  by (auto dest: bisim1-length-xs)
note  $IH = \langle [length (compE2 \{V:T=None; e\}) = length (compE2 e); Suc n + max\text{-vars} e' \leq length xs; \mathcal{B} e (Suc n)] \rangle$ 
       $\impl \tau\text{red1r } P t h (e', xs) (Val v, loc)\rangle$ 
with  $\langle \mathcal{B} e (Suc n) \rangle$  have  $\tau\text{red1r } P t h (e', xs) (Val v, loc)$  by (simp)
hence  $\tau\text{red1r } P t h (\{V:T=None; e'\}, xs) (\{V:T=None; Val v\}, loc)$ 
  by (rule Block-None- $\tau\text{red1r-xt}$ )
thus ?case using  $V len xs$  by (auto elim!: rtranclp.rtrancl-into-rtrancl intro: Red1Block  $\tau\text{move1BlockRed}$ )
next
  case (bisim1TryCatch2  $e2 n e' xs loc pc e C V$ )
from  $\langle \mathcal{B} (try e catch (C V) e2) n \rangle$  have [simp]:  $n = V$  and  $\mathcal{B} e2 (Suc n)$  by auto
note  $len = \langle n + max\text{-vars} \{V:Class C=None; e'\} \leq length xs \rangle$ 
hence  $V: V < length xs$  by simp
from  $\langle P, e2, h \vdash (e', xs) \leftrightarrow ([v], loc, pc, None) \rangle$ 
have  $len xs: length xs = length loc$  by (auto dest: bisim1-length-xs)
note  $IH = \langle [pc = length (compE2 e2); Suc n + max\text{-vars} e' \leq length xs; \mathcal{B} e2 (Suc n)] \rangle$ 
       $\impl \tau\text{red1r } P t h (e', xs) (Val v, loc)\rangle$ 
with  $\langle Suc (Suc (length (compE2 e) + pc)) = length (compE2 (try e catch (C V) e2)) \rangle \langle \mathcal{B} e2 (Suc n) \rangle$ 
have  $\tau\text{red1r } P t h (e', xs) (Val v, loc)$  by (simp)
hence  $\tau\text{red1r } P t h (\{V:Class C=None; e'\}, xs) (\{V:Class C=None; Val v\}, loc)$ 
  by (rule Block-None- $\tau\text{red1r-xt}$ )
thus ?case using  $V len xs$  by (auto elim!: rtranclp.rtrancl-into-rtrancl intro: Red1Block  $\tau\text{move1BlockRed}$ )
next
  case (bisims1List1  $e n e' xs loc es$ )
note  $bisim = \langle P, e, h \vdash (e', xs) \leftrightarrow (rev vs, loc, length (compEs2 (e \# es)), None) \rangle$ 
then have  $es: es = []$  and  $pc: length (compEs2 (e \# es)) = length (compE2 e)$ 
  by (auto dest: bisim1-pc-length-compE2)
with bisim obtain  $val$  where  $stk: rev vs = [val]$  and  $e': is\text{-val } e' \impl e' = Val val$ 
  by (auto dest: bisim1-pc-length-compE2D)
with  $es pc$  bisims1List1 have  $\tau\text{red1r } P t h (e', xs) (Val val, loc)$  by simp
with  $stk es$  show ?case by (auto intro:  $\tau\text{red1r-inj-}\tau\text{reds1r}$ )
next
  case (bisims1List2  $es n es' xs stk loc pc e v$ )
from  $\langle stk @ [v] = rev vs \rangle$  obtain  $vs'$  where  $vs: vs = v \# vs'$  by (cases vs) auto
with bisims1List2 show ?case by (auto intro:  $\tau\text{reds1r-cons-}\tau\text{reds1r}$ )
qed (fastforce dest: bisim1-pc-length-compE2 bisims1-pc-length-compEs2) +

```

lemma *exec-meth-stk-split*:

```

   $[ [ P, E, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp);$ 
     $exec\text{-meth-d} (compP2 P) (compE2 E) (stack\text{-xlift} (length STK) (compE2 E 0 0)) t$ 
     $h (stk @ STK, loc, pc, xcp) ta h' (stk', loc', pc', xcp') ] ]$ 

```

$\implies \exists stk''. stk' = stk'' @ STK \wedge \text{exec-meth-d } (compP2 P) (compE2 E) (compxE2 E 0 0) t$
 $h (stk, loc, pc, xcp) \text{ ta } h' (stk'', loc', pc', xcp')$
 (is $\llbracket -; ?exec E stk STK loc pc xcp stk' loc' pc' xcp' \rrbracket \implies ?concl E stk STK loc pc xcp stk' loc' pc' xcp'$)

and *exec-meth-stk-splits*:

$\llbracket P, Es, h \vdash (es, xs) [\leftrightarrow] (stk, loc, pc, xcp);$

$\text{exec-meth-d } (compP2 P) (compEs2 Es) (\text{stack-xlift } (\text{length } STK) (compxEs2 Es 0 0)) t$
 $h (stk @ STK, loc, pc, xcp) \text{ ta } h' (stk', loc', pc', xcp') \rrbracket$

$\implies \exists stk''. stk' = stk'' @ STK \wedge \text{exec-meth-d } (compP2 P) (compEs2 Es) (compxEs2 Es 0 0) t$
 $h (stk, loc, pc, xcp) \text{ ta } h' (stk'', loc', pc', xcp')$

(is $\llbracket -; ?execs Es stk STK loc pc xcp stk' loc' pc' xcp' \rrbracket \implies ?concls Es stk STK loc pc xcp stk' loc' pc' xcp'$)

proof(*induct* $E n :: \text{nat } e \text{ xs } stk \text{ loc } pc \text{ xcp}$ **and** $Es n :: \text{nat } es \text{ xs } stk \text{ loc } pc \text{ xcp}$)

arbitrary: $stk' \text{ loc}' pc' xcp' STK$ **and** $stk' \text{ loc}' pc' xcp' STK$ *rule*: *bisim1-bisims1-inducts-split*)

case *bisim1InSync* **thus** $?case$ **by**(*auto elim!*: *exec-meth.cases intro!*: *exec-meth.intros*)

next

case *bisim1Val2* **thus** $?case$ **by**(*auto dest*: *exec-meth-length-compE2-stack-xliftD*)

next

case *bisim1New* **thus** $?case$

by (*fastforce elim*: *exec-meth.cases intro*: *exec-meth.intros split*: *if-split-asm cong del*: *image-cong-simp*)

next

case *bisim1NewThrow* **thus** $?case$ **by**(*fastforce elim*: *exec-meth.cases intro*: *exec-meth.intros*)

next

case (*bisim1NewArray* $e n e' \text{ xs } stk \text{ loc } pc \text{ xcp } T$)

note *bisim* = $\langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$

note *IH* = $\langle \bigwedge stk' \text{ loc}' pc' xcp' STK. ?exec e \text{ stk } STK \text{ loc } pc \text{ xcp } stk' \text{ loc}' pc' xcp' \implies ?concl e \text{ stk } STK \text{ loc } pc \text{ xcp } stk' \text{ loc}' pc' xcp' \rangle$

note *exec* = $\langle ?exec (\text{newA } T[e]) \text{ stk } STK \text{ loc } pc \text{ xcp } stk' \text{ loc}' pc' xcp' \rangle$

from *bisim* **have** $pc: pc \leq \text{length } (compE2 e)$ **by**(*rule bisim1-pc-length-compE2*)

show $?case$

proof(*cases* $pc < \text{length } (compE2 e)$)

case *True*

with *exec* **have** $?exec e \text{ stk } STK \text{ loc } pc \text{ xcp } stk' \text{ loc}' pc' xcp'$

by(*simp add*: *compxE2-size-convs*)(*erule exec-meth-take*)

from *IH*[*OF this*] **show** $?thesis$ **by** *auto*

next

case *False*

with pc **have** [*simp*]: $pc = \text{length } (compE2 e)$ **by** *simp*

with *bisim* **obtain** v **where** [*simp*]: $stk = [v] \text{ xcp} = \text{None}$

by(*auto dest*: *dest*: *bisim1-pc-length-compE2D*)

with *exec* **show** $?thesis$

apply *simp*

apply (*erule exec-meth.cases*)

apply (*auto 4 4 intro*: *exec-meth.intros split*: *if-split-asm cong del*: *image-cong-simp*)

done

qed

next

case (*bisim1NewArrayThrow* $e n a \text{ xs } stk \text{ loc } pc T$)

note *bisim* = $\langle P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$

note *IH* = $\langle \bigwedge stk' \text{ loc}' pc' xcp' STK. ?exec e \text{ stk } STK \text{ loc } pc [a] \text{ stk}' \text{ loc}' pc' xcp' \implies ?concl e \text{ stk } STK \text{ loc } pc [a] \text{ stk}' \text{ loc}' pc' xcp' \rangle$

note *exec* = $\langle ?exec (\text{newA } T[e]) \text{ stk } STK \text{ loc } pc [a] \text{ stk}' \text{ loc}' pc' xcp' \rangle$

from *bisim* **have** $pc: pc < \text{length } (compE2 e)$ **and** [*simp*]: $xs = loc$

```

  by(auto dest: bisim1-ThrowD)
  from exec have ?exec e stk STK loc pc [a] stk' loc' pc' xcp'
    by(simp)(erule exec-meth-take[OF - pc])
  from IH[OF this] show ?case by(auto)
next
  case bisim1NewArrayFail thus ?case
  by(auto elim!: exec-meth.cases dest: match-ex-table-pcsD simp add: stack-xlift-compxEs2 stack-xlift-compxE2)
next
  case (bisim1Cast e n e' xs stk loc pc xcp T)
  note bisim = ⟨P,e,h ⊢ (e', xs) ↔ (stk, loc, pc, xcp)⟩
  note IH = ⟨∧stk' loc' pc' xcp' STK. ?exec e stk STK loc pc xcp stk' loc' pc' xcp'
    ⇒ ?concl e stk STK loc pc xcp stk' loc' pc' xcp'⟩
  note exec = ⟨?exec (Cast T e) stk STK loc pc xcp stk' loc' pc' xcp'⟩
  from bisim have pc: pc ≤ length (compE2 e) by(rule bisim1-pc-length-compE2)
  show ?case
  proof(cases pc < length (compE2 e))
    case True
    with exec have ?exec e stk STK loc pc xcp stk' loc' pc' xcp'
      by(simp add: compxE2-size-convs)(erule exec-meth-take)
    from IH[OF this] show ?thesis by auto
  next
    case False
    with pc have [simp]: pc = length (compE2 e) by simp
    with bisim obtain v where [simp]: stk = [v] xcp = None
      by(auto dest: dest: bisim1-pc-length-compE2D)
    with exec show ?thesis apply(simp)
      by(erule exec-meth.cases)(auto intro!: exec-meth.intros split: if-split-asm)
  qed
next
  case (bisim1CastThrow e n a xs stk loc pc T)
  note bisim = ⟨P,e,h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
  note IH = ⟨∧stk' loc' pc' xcp' STK. ?exec e stk STK loc pc [a] stk' loc' pc' xcp'
    ⇒ ?concl e stk STK loc pc [a] stk' loc' pc' xcp'⟩
  note exec = ⟨?exec (Cast T e) stk STK loc pc [a] stk' loc' pc' xcp'⟩
  from bisim have pc: pc < length (compE2 e) and [simp]: xs = loc
    by(auto dest: bisim1-ThrowD)
  from exec have ?exec e stk STK loc pc [a] stk' loc' pc' xcp'
    by(simp)(erule exec-meth-take[OF - pc])
  from IH[OF this] show ?case by(auto)
next
  case bisim1CastFail thus ?case
  by(auto elim!: exec-meth.cases dest: match-ex-table-pcsD simp add: stack-xlift-compxEs2 stack-xlift-compxE2)
next
  case (bisim1InstanceOf e n e' xs stk loc pc xcp T)
  note bisim = ⟨P,e,h ⊢ (e', xs) ↔ (stk, loc, pc, xcp)⟩
  note IH = ⟨∧stk' loc' pc' xcp' STK. ?exec e stk STK loc pc xcp stk' loc' pc' xcp'
    ⇒ ?concl e stk STK loc pc xcp stk' loc' pc' xcp'⟩
  note exec = ⟨?exec (e instanceof T) stk STK loc pc xcp stk' loc' pc' xcp'⟩
  from bisim have pc: pc ≤ length (compE2 e) by(rule bisim1-pc-length-compE2)
  show ?case
  proof(cases pc < length (compE2 e))
    case True
    with exec have ?exec e stk STK loc pc xcp stk' loc' pc' xcp'
      by(simp add: compxE2-size-convs)(erule exec-meth-take)

```

```

from IH[OF this] show ?thesis by auto
next
case False
with pc have [simp]: pc = length (compE2 e) by simp
with bisim obtain v where [simp]: stk = [v] xcp = None
  by(auto dest: dest: bisim1-pc-length-compE2D)
with exec show ?thesis apply(simp)
  by(erule exec-meth.cases)(auto intro!: exec-meth.intros split: if-split-asm)
qed
next
case (bisim1InstanceOfThrow e n a xs stk loc pc T)
note bisim = ⟨P,e,h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
note IH = ⟨∧stk' loc' pc' xcp' STK. ?exec e stk STK loc pc [a] stk' loc' pc' xcp'
  ⇒ ?concl e stk STK loc pc [a] stk' loc' pc' xcp'⟩
note exec = ⟨?exec (e instanceof T) stk STK loc pc [a] stk' loc' pc' xcp'⟩
from bisim have pc: pc < length (compE2 e) and [simp]: xs = loc
  by(auto dest: bisim1-ThrowD)
from exec have ?exec e stk STK loc pc [a] stk' loc' pc' xcp'
  by(simp)(erule exec-meth-take[OF - pc])
from IH[OF this] show ?case by(auto)
next
case bisim1Val thus ?case by(fastforce elim: exec-meth.cases intro: exec-meth.intros)
next
case bisim1Var thus ?case by(fastforce elim: exec-meth.cases intro: exec-meth.intros)
next
case (bisim1BinOp1 e1 n e1' xs stk loc pc xcp e2 bop)
note IH1 = ⟨∧stk' loc' pc' xcp' STK. ?exec e1 stk STK loc pc xcp stk' loc' pc' xcp'
  ⇒ ?concl e1 stk STK loc pc xcp stk' loc' pc' xcp'⟩
note IH2 = ⟨∧xs stk' loc' pc' xcp' STK. ?exec e2 [] STK xs 0 None stk' loc' pc' xcp'
  ⇒ ?concl e2 [] STK xs 0 None stk' loc' pc' xcp'⟩
note bisim1 = ⟨P,e1,h ⊢ (e1', xs) ↔ (stk, loc, pc, xcp)⟩
note bisim2 = ⟨P,e2,h ⊢ (e2, loc) ↔ ([], loc, 0, None)⟩
note exec = ⟨?exec (e1 «bop» e2) stk STK loc pc xcp stk' loc' pc' xcp'⟩
from bisim1 have pc: pc ≤ length (compE2 e1) by(rule bisim1-pc-length-compE2)
show ?case
proof(cases pc < length (compE2 e1))
  case True
  with exec have ?exec e1 stk STK loc pc xcp stk' loc' pc' xcp'
    by(simp add: compxE2-size-convs)(erule exec-meth-take-xt)
  from IH1[OF this] show ?thesis by auto
next
  case False
  with pc have pc: pc = length (compE2 e1) by simp
  with exec have pc' ≥ length (compE2 e1)
    by(simp add: compxE2-size-convs stack-xlift-compxE2)(auto split: bop.splits elim!: exec-meth-drop-xt-pc)
  then obtain PC where PC: pc' = PC + length (compE2 e1)
    by -(rule-tac PC34=pc' - length (compE2 e1) in that, simp)
  from pc bisim1 obtain v where stk = [v] xcp = None by(auto dest: bisim1-pc-length-compE2D)
  with exec pc have exec-meth-d (compP2 P) (compE2 e1 @ compE2 e2)
    (stack-xlift (length STK) (compxE2 e1 0 0 @ compxE2 e2 (length (compE2 e1)) (Suc 0))) t h (stk
  @ STK, loc, length (compE2 e1) + 0, xcp) ta h' (stk', loc', pc', xcp')
    by-(rule exec-meth-take, auto)
  hence ?exec e2 [] (v # STK) loc 0 None stk' loc' (pc' - length (compE2 e1)) xcp'
    using ⟨stk = [v]⟩ ⟨xcp = None⟩

```

by $-(\text{rule } \text{exec-meth-drop-xt}, \text{ auto simp add: stack-xlift-compxE2 shift-compxE2})$
from $\text{IH2}[\text{OF this}] \text{ PC}$ **obtain** stk'' **where** $\text{stk}': \text{stk}' = \text{stk}'' @ v \# \text{STK}$
and $\text{exec-meth-d} (\text{compP2 } P) (\text{compE2 } e2) (\text{compxE2 } e2 \ 0 \ 0) \ t \ h \ (\ [], \ \text{loc}, \ 0, \ \text{None}) \ \text{ta} \ h' (\text{stk}'', \ \text{loc}', \ \text{PC}, \ \text{xcp}')$ **by** auto
hence $\text{exec-meth-d} (\text{compP2 } P) ((\text{compE2 } e1 @ \text{compE2 } e2) @ [\text{BinOpInstr } \text{bop}])$
 $(\text{compxE2 } e1 \ 0 \ 0 @ \text{shift} (\text{length } (\text{compE2 } e1)) (\text{stack-xlift} (\text{length } [v]) (\text{compxE2 } e2 \ 0 \ 0))) \ t \ h$
 $([] @ [v], \ \text{loc}, \ \text{length } (\text{compE2 } e1) + 0, \ \text{None}) \ \text{ta} \ h' (\text{stk}'' @ [v], \ \text{loc}', \ \text{length } (\text{compE2 } e1) + \text{PC}, \ \text{xcp}')$
apply $-$
apply $(\text{rule } \text{exec-meth-append})$
apply $(\text{rule } \text{append-exec-meth-xt})$
apply $(\text{erule } \text{exec-meth-stk-offer})$
by (auto)
thus $?\text{thesis}$ **using** $\langle \text{stk} = [v] \rangle \langle \text{xcp} = \text{None} \rangle \text{stk}' \ \text{pc} \ \text{PC}$
by $(\text{clarsimp simp add: shift-compxE2 stack-xlift-compxE2 ac-simps})$
qed
next
case $(\text{bisim1BinOp2 } e2 \ n \ e2' \ \text{xs} \ \text{stk} \ \text{loc} \ \text{pc} \ \text{xcp} \ e1 \ \text{bop} \ v1)$
note $\text{IH2} = \langle \bigwedge \text{stk}' \ \text{loc}' \ \text{pc}' \ \text{xcp}' \ \text{STK}. \ ?\text{exec } e2 \ \text{stk} \ \text{STK} \ \text{loc} \ \text{pc} \ \text{xcp} \ \text{stk}' \ \text{loc}' \ \text{pc}' \ \text{xcp}'$
 $\implies ?\text{concl } e2 \ \text{stk} \ \text{STK} \ \text{loc} \ \text{pc} \ \text{xcp} \ \text{stk}' \ \text{loc}' \ \text{pc}' \ \text{xcp}' \rangle$
note $\text{bisim1} = \langle P, e1, h \vdash (e1, \ \text{xs}) \leftrightarrow ([], \ \text{xs}, \ 0, \ \text{None}) \rangle$
note $\text{bisim2} = \langle P, e2, h \vdash (e2', \ \text{xs}) \leftrightarrow (\text{stk}, \ \text{loc}, \ \text{pc}, \ \text{xcp}) \rangle$
note $\text{exec} = \langle ?\text{exec } (e1 \ \llbracket \text{bop} \rrbracket e2) (\text{stk} @ [v1]) \ \text{STK} \ \text{loc} \ (\text{length } (\text{compE2 } e1) + \text{pc}) \ \text{xcp} \ \text{stk}' \ \text{loc}' \ \text{pc}' \ \text{xcp}' \rangle$
from bisim2 **have** $\text{pc}: \text{pc} \leq \text{length } (\text{compE2 } e2)$ **by** $(\text{rule } \text{bisim1-pc-length-compE2})$
show $?\text{case}$
proof $(\text{cases } \text{pc} < \text{length } (\text{compE2 } e2))$
case True
from exec **have** $\text{exec-meth-d} (\text{compP2 } P) ((\text{compE2 } e1 @ \text{compE2 } e2) @ [\text{BinOpInstr } \text{bop}])$
 $(\text{stack-xlift} (\text{length } \text{STK}) (\text{compxE2 } e1 \ 0 \ 0) @ \text{shift} (\text{length } (\text{compE2 } e1)) (\text{stack-xlift} (\text{length } \text{STK}) (\text{compxE2 } e2 \ 0 \ (\text{Suc } 0)))) \ t$
 $h (\text{stk} @ v1 \ # \ \text{STK}, \ \text{loc}, \ \text{length } (\text{compE2 } e1) + \text{pc}, \ \text{xcp}) \ \text{ta} \ h' (\text{stk}', \ \text{loc}', \ \text{pc}', \ \text{xcp}')$ **by** $(\text{simp add: compxE2-size-convs})$
hence $\text{exec}' : \text{exec-meth-d} (\text{compP2 } P) (\text{compE2 } e1 @ \text{compE2 } e2) (\text{stack-xlift} (\text{length } \text{STK}) (\text{compxE2 } e1 \ 0 \ 0) @$
 $\text{shift} (\text{length } (\text{compE2 } e1)) (\text{stack-xlift} (\text{length } \text{STK}) (\text{compxE2 } e2 \ 0 \ (\text{Suc } 0)))) \ t$
 $h (\text{stk} @ v1 \ # \ \text{STK}, \ \text{loc}, \ \text{length } (\text{compE2 } e1) + \text{pc}, \ \text{xcp}) \ \text{ta} \ h' (\text{stk}', \ \text{loc}', \ \text{pc}', \ \text{xcp}')$
by $(\text{rule } \text{exec-meth-take})(\text{simp add: True})$
hence $\text{exec-meth-d} (\text{compP2 } P) (\text{compE2 } e2) (\text{stack-xlift} (\text{length } \text{STK}) (\text{compxE2 } e2 \ 0 \ (\text{Suc } 0))) \ t$
 $h (\text{stk} @ v1 \ # \ \text{STK}, \ \text{loc}, \ \text{pc}, \ \text{xcp}) \ \text{ta} \ h' (\text{stk}', \ \text{loc}', \ \text{pc}' - \text{length } (\text{compE2 } e1), \ \text{xcp}')$
by $(\text{rule } \text{exec-meth-drop-xt})(\text{auto simp add: stack-xlift-compxE2})$
hence $?\text{exec } e2 \ \text{stk} \ (v1 \ # \ \text{STK}) \ \text{loc} \ \text{pc} \ \text{xcp} \ \text{stk}' \ \text{loc}' \ (\text{pc}' - \text{length } (\text{compE2 } e1)) \ \text{xcp}'$
by $(\text{simp add: compxE2-stack-xlift-convs})$
from $\text{IH2}[\text{OF this}]$ **obtain** stk'' **where** $\text{stk}': \text{stk}' = \text{stk}'' @ v1 \ # \ \text{STK}$
and $\text{exec}'' : \text{exec-meth-d} (\text{compP2 } P) (\text{compE2 } e2) (\text{compxE2 } e2 \ 0 \ 0) \ t \ h (\text{stk}, \ \text{loc}, \ \text{pc}, \ \text{xcp}) \ \text{ta} \ h'$
 $(\text{stk}'', \ \text{loc}', \ \text{pc}' - \text{length } (\text{compE2 } e1), \ \text{xcp}')$ **by** blast
from exec'' **have** $\text{exec-meth-d} (\text{compP2 } P) (\text{compE2 } e2) (\text{stack-xlift} (\text{length } [v1]) (\text{compxE2 } e2 \ 0 \ 0)) \ t \ h$
 $(\text{stk} @ [v1], \ \text{loc}, \ \text{pc}, \ \text{xcp})$
 $\text{ta} \ h' (\text{stk}'' @ [v1], \ \text{loc}', \ \text{pc}' - \text{length } (\text{compE2 } e1), \ \text{xcp}')$
by $(\text{rule } \text{exec-meth-stk-offer})$
hence $\text{exec-meth-d} (\text{compP2 } P) (\text{compE2 } e1 @ \text{compE2 } e2) (\text{compxE2 } e1 \ 0 \ 0 @ \text{shift} (\text{length } (\text{compE2 } e1)) (\text{stack-xlift} (\text{length } [v1]) (\text{compxE2 } e2 \ 0 \ 0))) \ t \ h$
 $(\text{stk} @ [v1], \ \text{loc}, \ \text{length } (\text{compE2 } e1) + \text{pc}, \ \text{xcp})$
 $\text{ta} \ h' (\text{stk}'' @ [v1], \ \text{loc}', \ \text{length } (\text{compE2 } e1) + (\text{pc}' - \text{length } (\text{compE2 } e1)), \ \text{xcp}')$

by(rule *append-exec-meth-xt*) *auto*
hence *exec-meth-d* (*compP2 P*) ((*compE2 e1* @ *compE2 e2*) @ [*BinOpInstr bop*]) (*compxE2 e1 0 0*
@ *shift* (*length* (*compE2 e1*)) (*stack-xlift* (*length* [*v1*] (*compxE2 e2 0 0*))) *t h* (*stk* @ [*v1*], *loc*, *length*
(*compE2 e1*) + *pc*, *xcp*)
ta h' (*stk''* @ [*v1*], *loc'*, *length* (*compE2 e1*) + (*pc' - length* (*compE2 e1*)), *xcp'*)
by(rule *exec-meth-append*)
moreover from *exec' have* *pc' ≥ length* (*compE2 e1*)
by(rule *exec-meth-drop-xt-pc*)(*auto simp add: stack-xlift-compxE2*)
ultimately show *?thesis using* *stk'* **by**(*simp add: stack-xlift-compxE2 shift-compxE2*)
next
case *False*
with *pc have* *pc: pc = length* (*compE2 e2*) **by** *simp*
with *bisim2 obtain* *v2 where* [*simp*]: *stk = [v2]* *xcp = None*
by(*auto dest: dest: bisim1-pc-length-compE2D*)
with *exec pc show* *?thesis*
by(*fastforce elim: exec-meth.cases split: sum.split-asm intro!: exec-meth.intros*)
qed
next
case (*bisim1BinOpThrow1 e1 n a xs stk loc pc e2 bop*)
note *bisim1 = ⟨P, e1, h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩*
note *IH1 = ⟨∧stk' loc' pc' xcp' STK. ?exec e1 stk STK loc pc [a] stk' loc' pc' xcp'⟩*
 $\implies ?concl e1 stk STK loc pc [a] stk' loc' pc' xcp'$
note *exec = ⟨?exec (e1 «bop» e2) stk STK loc pc [a] stk' loc' pc' xcp'⟩*
from *bisim1 have* *pc: pc < length* (*compE2 e1*) **and** [*simp*]: *xs = loc*
by(*auto dest: bisim1-ThrowD*)
from *exec have* *exec-meth-d* (*compP2 P*) (*compE2 e1* @ (*compE2 e2* @ [*BinOpInstr bop*]))
(*stack-xlift* (*length* *STK*) (*compxE2 e1 0 0*) @ *shift* (*length* (*compE2 e1*)) (*stack-xlift* (*length* *STK*)
(*compxE2 e2 0 (Suc 0)*))) *t*
h (*stk* @ *STK*, *loc*, *pc*, [*a*]) *ta h'* (*stk'*, *loc'*, *pc'*, *xcp'*) **by**(*simp add: compxE2-size-convs*)
hence *?exec e1 stk STK loc pc [a] stk' loc' pc' xcp'*
by(rule *exec-meth-take-xt*)(rule *pc*)
from *IH1[OF this] show* *?case by*(*auto*)
next
case (*bisim1BinOpThrow2 e2 n a xs stk loc pc e1 bop v1*)
note *bisim2 = ⟨P, e2, h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩*
note *IH2 = ⟨∧stk' loc' pc' xcp' STK. ?exec e2 stk STK loc pc [a] stk' loc' pc' xcp'⟩*
 $\implies ?concl e2 stk STK loc pc [a] stk' loc' pc' xcp'$
note *exec = ⟨?exec (e1 «bop» e2) (stk @ [v1]) STK loc (length* (*compE2 e1*) + *pc*) [*a*] stk' loc' pc'
xcp'⟩
from *bisim2 have* *pc: pc < length* (*compE2 e2*) **and** [*simp*]: *xs = loc*
by(*auto dest: bisim1-ThrowD*)
from *exec have* *exec-meth-d* (*compP2 P*) ((*compE2 e1* @ *compE2 e2*) @ [*BinOpInstr bop*])
(*stack-xlift* (*length* *STK*) (*compxE2 e1 0 0*) @ *shift* (*length* (*compE2 e1*)) (*stack-xlift* (*length* *STK*)
(*compxE2 e2 0 (Suc 0)*)))
t h (*stk* @ *v1 # STK*, *loc*, *length* (*compE2 e1*) + *pc*, [*a*]) *ta h'* (*stk'*, *loc'*, *pc'*, *xcp'*)
by(*simp add: compxE2-size-convs*)
hence *exec': exec-meth-d* (*compP2 P*) (*compE2 e1* @ *compE2 e2*)
(*stack-xlift* (*length* *STK*) (*compxE2 e1 0 0*) @ *shift* (*length* (*compE2 e1*)) (*stack-xlift* (*length* *STK*)
(*compxE2 e2 0 (Suc 0)*))) *t*
h (*stk* @ *v1 # STK*, *loc*, *length* (*compE2 e1*) + *pc*, [*a*]) *ta h'* (*stk'*, *loc'*, *pc'*, *xcp'*)
by(rule *exec-meth-take*)(*simp add: pc*)
hence *exec-meth-d* (*compP2 P*) (*compE2 e2*) (*stack-xlift* (*length* *STK*) (*compxE2 e2 0 (Suc 0)*)) *t*
h (*stk* @ *v1 # STK*, *loc*, *pc*, [*a*]) *ta h'* (*stk'*, *loc'*, *pc' - length* (*compE2 e1*), *xcp'*)
by(rule *exec-meth-drop-xt*)(*auto simp add: stack-xlift-compxE2*)

hence $?exec\ e2\ stk\ (v1\ \# \text{STK})\ loc\ pc\ [a]\ stk'\ loc'\ (pc' - \text{length}\ (compE2\ e1))\ xcp'$
by(*simp add: compxE2-stack-xlift-convs*)
from *IH2[OF this]* **obtain** stk'' **where** $stk' = stk''\ @\ v1\ \# \text{STK}$ **and**
 $exec'' : exec\ meth\ d\ (compP2\ P)\ (compE2\ e2)\ (compxE2\ e2\ 0\ 0)\ t\ h\ (stk,\ loc,\ pc,\ [a])\ ta\ h'\ (stk'',$
 $loc',\ pc' - \text{length}\ (compE2\ e1),\ xcp')$ **by** *blast*
from $exec''$ **have** $exec\ meth\ d\ (compP2\ P)\ (compE2\ e2)\ (stack\ xlift\ (\text{length}\ [v1])\ (compxE2\ e2\ 0\ 0))$
 $t\ h\ (stk\ @\ [v1],\ loc,\ pc,\ [a])$
 $ta\ h'\ (stk''\ @\ [v1],\ loc',\ pc' - \text{length}\ (compE2\ e1),\ xcp')$
by(*rule exec-meth-stk-offer*)
hence $exec\ meth\ d\ (compP2\ P)\ (compE2\ e1\ @\ compE2\ e2)\ (compxE2\ e1\ 0\ 0\ @\ shift\ (\text{length}\ (compE2$
 $e1))\ (stack\ xlift\ (\text{length}\ [v1])\ (compxE2\ e2\ 0\ 0)))\ t\ h\ (stk\ @\ [v1],\ loc,\ \text{length}\ (compE2\ e1) + pc,\ [a])$
 $ta\ h'\ (stk''\ @\ [v1],\ loc',\ \text{length}\ (compE2\ e1) + (pc' - \text{length}\ (compE2\ e1)),\ xcp')$
by(*rule append-exec-meth-xt*)(*auto*)
hence $exec\ meth\ d\ (compP2\ P)\ ((compE2\ e1\ @\ compE2\ e2)\ @\ [BinOpInstr\ bop])\ (compxE2\ e1\ 0\ 0$
 $@\ shift\ (\text{length}\ (compE2\ e1))\ (stack\ xlift\ (\text{length}\ [v1])\ (compxE2\ e2\ 0\ 0)))\ t\ h\ (stk\ @\ [v1],\ loc,\ \text{length}$
 $(compE2\ e1) + pc,\ [a])$
 $ta\ h'\ (stk''\ @\ [v1],\ loc',\ \text{length}\ (compE2\ e1) + (pc' - \text{length}\ (compE2\ e1)),\ xcp')$
by(*rule exec-meth-append*)
moreover from $exec'$ **have** $pc' : pc' \geq \text{length}\ (compE2\ e1)$
by(*rule exec-meth-drop-xt-pc*)(*auto simp add: stack-xlift-compxE2*)
ultimately show $?case$ **using** stk' **by**(*auto simp add: stack-xlift-compxE2 shift-compxE2*)
next
case *bisim1BinOpThrow* **thus** $?case$
by(*auto elim!: exec-meth.cases dest: match-ex-table-pcsD simp add: stack-xlift-compxEs2 stack-xlift-compxE2*)
next
case (*bisim1LAss1* $e\ n\ e'\ xs\ stk\ loc\ pc\ xcp\ V$)
note $bisim = \langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
note $IH = \langle \bigwedge stk'\ loc'\ pc'\ xcp'\ STK. ?exec\ e\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \Rightarrow ?concl\ e\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$
note $exec = \langle ?exec\ (V := e)\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$
from *bisim* **have** $pc : pc \leq \text{length}\ (compE2\ e)$ **by**(*rule bisim1-pc-length-compE2*)
show $?case$
proof(*cases pc < length (compE2 e)*)
case *True*
with $exec$ **have** $?exec\ e\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp'$
by(*simp add: compxE2-size-convs*)(*erule exec-meth-take*)
from *IH[OF this]* **show** $?thesis$ **by** *auto*
next
case *False*
with pc **have** [*simp*]: $pc = \text{length}\ (compE2\ e)$ **by** *simp*
with *bisim* **obtain** v **where** [*simp*]: $stk = [v]\ xcp = None$
by(*auto dest: dest: bisim1-pc-length-compE2D*)
with $exec$ **show** $?thesis$ **apply**(*simp*)
by(*erule exec-meth.cases*)(*auto intro!: exec-meth.intros*)
qed
next
case (*bisim1LAss2* $e\ n\ xs\ V$)
thus $?case$ **by**(*fastforce elim: exec-meth.cases intro: exec-meth.intros*)
next
case (*bisim1LAssThrow* $e\ n\ a\ xs\ stk\ loc\ pc\ V$)
note $bisim = \langle P, e, h \vdash (Throw\ a,\ xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$
note $IH = \langle \bigwedge stk'\ loc'\ pc'\ xcp'\ STK. ?exec\ e\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \Rightarrow ?concl\ e\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$
note $exec = \langle ?exec\ (V := e)\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$


```

from bisim have pc: pc < length (compE2 e) and [simp]: xs = loc
  by(auto dest: bisim1-ThrowD)
from exec have ?exec e stk STK loc pc [a] stk' loc' pc' xcp'
  by(simp)(erule exec-meth-take[OF - pc])
from IH[OF this] show ?case by(auto)
next
case (bisim1AAcc1 a n a' xs stk loc pc xcp i)
note IH1 = ⟨ $\bigwedge$  stk' loc' pc' xcp' STK. ?exec a stk STK loc pc xcp stk' loc' pc' xcp'
  ⇒ ?concl a stk STK loc pc xcp stk' loc' pc' xcp'⟩
note IH2 = ⟨ $\bigwedge$  xs stk' loc' pc' xcp' STK. ?exec i [] STK xs 0 None stk' loc' pc' xcp'
  ⇒ ?concl i [] STK xs 0 None stk' loc' pc' xcp'⟩
note bisim1 = ⟨P, a, h ⊢ (a', xs) ↔ (stk, loc, pc, xcp)⟩
note bisim2 = ⟨P, i, h ⊢ (i, loc) ↔ ([], loc, 0, None)⟩
note exec = ⟨?exec (a[i]) stk STK loc pc xcp stk' loc' pc' xcp'⟩
from bisim1 have pc: pc ≤ length (compE2 a) by(rule bisim1-pc-length-compE2)
show ?case
proof(cases pc < length (compE2 a))
  case True
    with exec have ?exec a stk STK loc pc xcp stk' loc' pc' xcp'
      by(simp add: compxE2-size-convs)(erule exec-meth-take-xt)
    from IH1[OF this] show ?thesis by auto
  next
    case False
      with pc have pc: pc = length (compE2 a) by simp
      with exec have pc' ≥ length (compE2 a)
        by(simp add: compxE2-size-convs stack-xlift-compxE2)(auto elim!: exec-meth-drop-xt-pc)
      then obtain PC where PC: pc' = PC + length (compE2 a)
        by −(rule-tac PC34=pc' - length (compE2 a)) in that, simp)
      from pc bisim1 obtain v where stk = [v] xcp = None by(auto dest: bisim1-pc-length-compE2D)
      with exec pc have exec-meth-d (compP2 P) (compE2 a @ compE2 i)
        (stack-xlift (length STK) (compxE2 a 0 0 @ compxE2 i (length (compE2 a)) (Suc 0))) t h (stk @
        STK, loc, length (compE2 a) + 0, xcp) ta h' (stk', loc', pc', xcp')
        by−(rule exec-meth-take, auto)
      hence ?exec i [] (v # STK) loc 0 None stk' loc' (pc' − length (compE2 a)) xcp'
        using ⟨stk = [v]⟩ ⟨xcp = None⟩
        by −(rule exec-meth-drop-xt, auto simp add: stack-xlift-compxE2 shift-compxE2)
      from IH2[OF this] PC obtain stk'' where stk': stk' = stk'' @ v # STK
        and exec-meth-d (compP2 P) (compE2 i) (compxE2 i 0 0) t h ([], loc, 0, None) ta h' (stk'', loc',
        PC, xcp') by auto
      hence exec-meth-d (compP2 P) ((compE2 a @ compE2 i) @ [ALoad])
        (compxE2 a 0 0 @ shift (length (compE2 a)) (stack-xlift (length [v] (compxE2 i 0 0))) t h
        ([] @ [v], loc, length (compE2 a) + 0, None) ta h' (stk'' @ [v], loc', length (compE2 a) + PC,
        xcp')
        apply −
        apply(rule exec-meth-append)
        apply(rule append-exec-meth-xt)
        apply(erule exec-meth-stk-offer)
        by(auto)
      thus ?thesis using ⟨stk = [v]⟩ ⟨xcp = None⟩ stk' pc PC
        by(clarsimp simp add: shift-compxE2 stack-xlift-compxE2 ac-simps)
    qed
  next
    case (bisim1AAcc2 i n i' xs stk loc pc xcp a v1)
    note IH2 = ⟨ $\bigwedge$  stk' loc' pc' xcp' STK. ?exec i stk STK loc pc xcp stk' loc' pc' xcp'⟩

```

$\implies ?concl\ i\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp'$
note $bisim2 = \langle P, i, h \vdash (i', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
note $exec = \langle ?exec\ (a[i])\ (stk\ @\ [v1])\ STK\ loc\ (length\ (compE2\ a) + pc)\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$
from $bisim2$ **have** $pc: pc \leq length\ (compE2\ i)$ **by**(rule $bisim1-pc-length-compE2$)
show $?case$
proof(cases $pc < length\ (compE2\ i)$)
case $True$
from $exec$ **have** $exec-meth-d\ (compP2\ P)\ ((compE2\ a\ @\ compE2\ i)\ @\ [ALoad])$
 $(stack-xlift\ (length\ STK)\ (compxE2\ a\ 0\ 0)\ @\ shift\ (length\ (compE2\ a))\ (stack-xlift\ (length\ STK)$
 $(compxE2\ i\ 0\ (Suc\ 0))))\ t$
 $h\ (stk\ @\ v1\ \# \ STK, loc, length\ (compE2\ a) + pc, xcp)\ ta\ h'\ (stk', loc', pc', xcp')$ **by**(simp $add: compxE2-size-convs$)
hence $exec': exec-meth-d\ (compP2\ P)\ (compE2\ a\ @\ compE2\ i)\ (stack-xlift\ (length\ STK)\ (compxE2\ a\ 0\ 0)\ @$
 $shift\ (length\ (compE2\ a))\ (stack-xlift\ (length\ STK)\ (compxE2\ i\ 0\ (Suc\ 0))))\ t$
 $h\ (stk\ @\ v1\ \# \ STK, loc, length\ (compE2\ a) + pc, xcp)\ ta\ h'\ (stk', loc', pc', xcp')$
by(rule $exec-meth-take$)(simp $add: True$)
hence $exec-meth-d\ (compP2\ P)\ (compE2\ i)\ (stack-xlift\ (length\ STK)\ (compxE2\ i\ 0\ (Suc\ 0))))\ t$
 $h\ (stk\ @\ v1\ \# \ STK, loc, pc, xcp)\ ta\ h'\ (stk', loc', pc' - length\ (compE2\ a), xcp')$
by(rule $exec-meth-drop-xt$)(auto simp $add: stack-xlift-compxE2$)
hence $?exec\ i\ stk\ (v1\ \# \ STK)\ loc\ pc\ xcp\ stk'\ loc'\ (pc' - length\ (compE2\ a))\ xcp'$
by(simp $add: compxE2-stack-xlift-convs$)
from $IH2[OF\ this]$ **obtain** stk'' **where** $stk': stk' = stk''\ @\ v1\ \# \ STK$
and $exec'': exec-meth-d\ (compP2\ P)\ (compE2\ i)\ (compxE2\ i\ 0\ 0)\ t\ h\ (stk, loc, pc, xcp)\ ta\ h'$
 $(stk'', loc', pc' - length\ (compE2\ a), xcp')$ **by** $blast$
from $exec''$ **have** $exec-meth-d\ (compP2\ P)\ (compE2\ i)\ (stack-xlift\ (length\ [v1])\ (compxE2\ i\ 0\ 0))$
 $t\ h\ (stk\ @\ [v1], loc, pc, xcp)$
 $ta\ h'\ (stk''\ @\ [v1], loc', pc' - length\ (compE2\ a), xcp')$
by(rule $exec-meth-stk-offer$)
hence $exec-meth-d\ (compP2\ P)\ (compE2\ a\ @\ compE2\ i)\ (compxE2\ a\ 0\ 0\ @\ shift\ (length\ (compE2$
 $a))\ (stack-xlift\ (length\ [v1])\ (compxE2\ i\ 0\ 0)))\ t\ h\ (stk\ @\ [v1], loc, length\ (compE2\ a) + pc, xcp)$
 $ta\ h'\ (stk''\ @\ [v1], loc', length\ (compE2\ a) + (pc' - length\ (compE2\ a)), xcp')$
by(rule $append-exec-meth-xt$) $auto$
hence $exec-meth-d\ (compP2\ P)\ ((compE2\ a\ @\ compE2\ i)\ @\ [ALoad])\ (compxE2\ a\ 0\ 0\ @\ shift$
 $(length\ (compE2\ a))\ (stack-xlift\ (length\ [v1])\ (compxE2\ i\ 0\ 0)))\ t\ h\ (stk\ @\ [v1], loc, length\ (compE2$
 $a) + pc, xcp)$
 $ta\ h'\ (stk''\ @\ [v1], loc', length\ (compE2\ a) + (pc' - length\ (compE2\ a)), xcp')$
by(rule $exec-meth-append$)
moreover from $exec'$ **have** $pc' \geq length\ (compE2\ a)$
by(rule $exec-meth-drop-xt-pc$)(auto simp $add: stack-xlift-compxE2$)
ultimately show $?thesis$ **using** stk' **by**(simp $add: stack-xlift-compxE2\ shift-compxE2$)
next
case $False$
with pc **have** $pc: pc = length\ (compE2\ i)$ **by** $simp$
with $bisim2$ **obtain** $v2$ **where** $[simp]: stk = [v2]\ xcp = None$
by(auto $dest: dest: bisim1-pc-length-compE2D$)
with $exec\ pc$ **show** $?thesis$
by($clarsimp$)(erule $exec-meth.cases$, auto $intro!: exec-meth.intros\ split: if-split-asm$)
qed
next
case $(bisim1AAccThrow1\ A\ n\ a\ xs\ stk\ loc\ pc\ i)$
note $bisim1 = \langle P, A, h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$
note $IH1 = \langle \bigwedge stk'\ loc'\ pc'\ xcp'\ STK. ?exec\ A\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \implies ?concl\ A\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$

note $exec = \langle ?exec (A[i]) \text{ stk STK loc pc } [a] \text{ stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$
from $bisim1$ **have** $pc: pc < length (compE2 A)$ **and** $[simp]: xs = loc$
by($auto \text{ dest: } bisim1\text{-ThrowD}$)
from $exec$ **have** $exec\text{-meth-d } (compP2 P) (compE2 A @ (compE2 i @ [ALoad]))$
 $(stack\text{-xlift } (length STK) (compxE2 A 0 0) @ shift (length (compE2 A)) (stack\text{-xlift } (length STK)$
 $(compxE2 i 0 (Suc 0)))) t$
 $h (stk @ STK, loc, pc, [a]) \text{ ta } h' (stk', loc', pc', xcp')$ **by**($simp \text{ add: } compxE2\text{-size-convs}$)
hence $?exec A \text{ stk STK loc pc } [a] \text{ stk}' \text{ loc}' \text{ pc}' \text{ xcp}'$ **by**($rule \text{ exec-meth-take-xt}$)($rule pc$)
from $IH1[OF \text{ this}]$ **show** $?case$ **by**($auto$)
next
case ($bisim1AAccThrow2 i n a xs \text{ stk loc pc } A v1$)
note $bisim2 = \langle P, i, h \vdash (Throw a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$
note $IH2 = \langle \bigwedge \text{stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \text{ STK. } ?exec i \text{ stk STK loc pc } [a] \text{ stk}' \text{ loc}' \text{ pc}' \text{ xcp}'$
 $\implies ?concl i \text{ stk STK loc pc } [a] \text{ stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$
note $exec = \langle ?exec (A[i]) (stk @ [v1]) \text{ STK loc } (length (compE2 A) + pc) [a] \text{ stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$
from $bisim2$ **have** $pc: pc < length (compE2 i)$ **and** $[simp]: xs = loc$
by($auto \text{ dest: } bisim1\text{-ThrowD}$)
from $exec$ **have** $exec\text{-meth-d } (compP2 P) ((compE2 A @ compE2 i) @ [ALoad])$
 $(stack\text{-xlift } (length STK) (compxE2 A 0 0) @ shift (length (compE2 A)) (stack\text{-xlift } (length STK)$
 $(compxE2 i 0 (Suc 0)))) t$
 $h (stk @ v1 \# STK, loc, length (compE2 A) + pc, [a]) \text{ ta } h' (stk', loc', pc', xcp')$
by($simp \text{ add: } compxE2\text{-size-convs}$)
hence $exec': exec\text{-meth-d } (compP2 P) (compE2 A @ compE2 i)$
 $(stack\text{-xlift } (length STK) (compxE2 A 0 0) @ shift (length (compE2 A)) (stack\text{-xlift } (length STK)$
 $(compxE2 i 0 (Suc 0)))) t$
 $h (stk @ v1 \# STK, loc, length (compE2 A) + pc, [a]) \text{ ta } h' (stk', loc', pc', xcp')$
by($rule \text{ exec-meth-take}$)($simp \text{ add: } pc$)
hence $exec\text{-meth-d } (compP2 P) (compE2 i) (stack\text{-xlift } (length STK) (compxE2 i 0 (Suc 0))) t$
 $h (stk @ v1 \# STK, loc, pc, [a]) \text{ ta } h' (stk', loc', pc' - length (compE2 A), xcp')$
by($rule \text{ exec-meth-drop-xt}$)($auto \text{ simp add: } stack\text{-xlift-compxE2}$)
hence $?exec i \text{ stk } (v1 \# STK) \text{ loc pc } [a] \text{ stk}' \text{ loc}' (pc' - length (compE2 A)) \text{ xcp}'$
by($simp \text{ add: } compxE2\text{-stack-xlift-convs}$)
from $IH2[OF \text{ this}]$ **obtain** stk'' **where** $stk': stk' = stk'' @ v1 \# STK$ **and**
 $exec'': exec\text{-meth-d } (compP2 P) (compE2 i) (compxE2 i 0 0) t h (stk, loc, pc, [a]) \text{ ta } h' (stk'', loc',$
 $pc' - length (compE2 A), xcp')$ **by** $blast$
from $exec''$ **have** $exec\text{-meth-d } (compP2 P) (compE2 i) (stack\text{-xlift } (length [v1]) (compxE2 i 0 0)) t$
 $h (stk @ [v1], loc, pc, [a])$
 $\text{ ta } h' (stk'' @ [v1], loc', pc' - length (compE2 A), xcp')$
by($rule \text{ exec-meth-stk-offer}$)
hence $exec\text{-meth-d } (compP2 P) (compE2 A @ compE2 i) (compxE2 A 0 0 @ shift (length (compE2$
 $A)) (stack\text{-xlift } (length [v1]) (compxE2 i 0 0))) t h (stk @ [v1], loc, length (compE2 A) + pc, [a])$
 $\text{ ta } h' (stk'' @ [v1], loc', length (compE2 A) + (pc' - length (compE2 A)), xcp')$
by($rule \text{ append-exec-meth-xt}$)($auto$)
hence $exec\text{-meth-d } (compP2 P) ((compE2 A @ compE2 i) @ [ALoad]) (compxE2 A 0 0 @ shift$
 $(length (compE2 A)) (stack\text{-xlift } (length [v1]) (compxE2 i 0 0))) t h (stk @ [v1], loc, length (compE2$
 $A) + pc, [a])$
 $\text{ ta } h' (stk'' @ [v1], loc', length (compE2 A) + (pc' - length (compE2 A)), xcp')$
by($rule \text{ exec-meth-append}$)
moreover from $exec'$ **have** $pc': pc' \geq length (compE2 A)$
by($rule \text{ exec-meth-drop-xt-pc}$)($auto \text{ simp add: } stack\text{-xlift-compxE2}$)
ultimately show $?case$ **using** stk' **by**($auto \text{ simp add: } stack\text{-xlift-compxE2 shift-compxE2}$)
next
case $bisim1AAccFail$ **thus** $?case$
by($auto \text{ elim!: } exec\text{-meth.cases dest: match-ex-table-pcsD simp add: stack\text{-xlift-compxEs2 stack\text{-xlift-compxE2}$)

next

case (*bisim1Ass1* *a n a' xs stk loc pc xcp i e*)
note *IH1* = $\langle \bigwedge \text{stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \text{ STK}. ?\text{exec } a \text{ stk STK loc pc xcp stk}' \text{ loc}' \text{ pc}' \text{ xcp}'$
 $\implies ?\text{concl } a \text{ stk STK loc pc xcp stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$
note *IH2* = $\langle \bigwedge \text{xs stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \text{ STK}. ?\text{exec } i \ [] \text{ STK xs } 0 \text{ None stk}' \text{ loc}' \text{ pc}' \text{ xcp}'$
 $\implies ?\text{concl } i \ [] \text{ STK xs } 0 \text{ None stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$
note *bisim1* = $\langle P, a, h \vdash (a', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \rangle$
note *bisim2* = $\langle P, i, h \vdash (i, \text{loc}) \leftrightarrow ([], \text{loc}, 0, \text{None}) \rangle$
note *exec* = $\langle ?\text{exec } (a[i] := e) \text{ stk STK loc pc xcp stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$
from *bisim1* **have** *pc*: $pc \leq \text{length } (\text{compE2 } a)$ **by** (*rule bisim1-pc-length-compE2*)
from *exec* **have** *exec'*: *exec-meth-d* (*compP2* *P*) (*compE2* *a* @ *compE2* *i* @ *compE2* *e* @ [*AStore*, *Push Unit*]) (*stack-xlift* (*length* *STK*) (*compxE2* *a* 0 0) @ *shift* (*length* (*compE2* *a*)) (*stack-xlift* (*length* *STK*) (*compxE2* *i* 0 (*Suc* 0) @ *compxE2* *e* (*length* (*compE2* *i*)) (*Suc* (*Suc* 0)))))) *t*
 $h (\text{stk} @ \text{STK}, \text{loc}, \text{pc}, \text{xcp}) \text{ ta } h' (\text{stk}', \text{loc}', \text{pc}', \text{xcp}')$
by (*simp add: compxE2-size-convs*)
show *?case*
proof (*cases* $pc < \text{length } (\text{compE2 } a)$)
case *True*
with *exec'* **have** *?exec* *a stk STK loc pc xcp stk'* *loc'* *pc'* *xcp'* **by** (*rule exec-meth-take-xt*)
from *IH1* [*OF this*] **show** *?thesis* **by** *auto*
next
case *False*
with *pc* **have** *pc*: $pc = \text{length } (\text{compE2 } a)$ **by** *simp*
with *exec'* **have** $pc' \geq \text{length } (\text{compE2 } a)$ **by** $-(\text{erule } \text{exec-meth-drop-xt-pc}, \text{ auto})$
then obtain *PC* **where** *PC*: $pc' = PC + \text{length } (\text{compE2 } a)$
by $-(\text{rule-tac } PC34=pc' - \text{length } (\text{compE2 } a) \text{ in } \text{that}, \text{ simp})$
from *pc bisim1* **obtain** *v* **where** $\text{stk} = [v] \text{ xcp} = \text{None}$ **by** (*auto dest: bisim1-pc-length-compE2D*)
with *exec* *PC* *pc*
have *exec-meth-d* (*compP2* *P*) ((*compE2* *a* @ *compE2* *i*) @ *compE2* *e* @ [*AStore*, *Push Unit*]) (*stack-xlift* (*length* *STK*) (*compxE2* *a* 0 0 @ *shift* (*length* (*compE2* *a*)) (*compxE2* *i* 0 (*Suc* 0))) @ *shift* (*length* (*compE2* *a* @ *compE2* *i*)) (*compxE2* *e* 0 (*length* *STK* + *Suc* (*Suc* 0)))))) *t*
 $h (v \# \text{STK}, \text{loc}, \text{length } (\text{compE2 } a) + 0, \text{None}) \text{ ta } h' (\text{stk}', \text{loc}', \text{length } (\text{compE2 } a) + PC, \text{xcp}')$
by (*simp add: shift-compxE2 stack-xlift-compxE2 ac-simps*)
hence *exec-meth-d* (*compP2* *P*) (*compE2* *a* @ *compE2* *i*)
(*stack-xlift* (*length* *STK*) (*compxE2* *a* 0 0 @ *shift* (*length* (*compE2* *a*)) (*compxE2* *i* 0 (*Suc* 0)))))) *t* $h (v \# \text{STK}, \text{loc}, \text{length } (\text{compE2 } a) + 0, \text{None}) \text{ ta } h' (\text{stk}', \text{loc}', \text{length } (\text{compE2 } a) + PC, \text{xcp}')$
by (*rule exec-meth-take-xt*) *simp*
hence *?exec* *i* [] ($v \# \text{STK}$) *loc* 0 *None* *stk'* *loc'* ((*length* (*compE2* *a*) + *PC*) - *length* (*compE2* *a*)) *xcp'*
by $-(\text{rule } \text{exec-meth-drop-xt}, \text{ auto } \text{simp add: } \text{stack-xlift-compxE2 } \text{shift-compxE2})$
from *IH2* [*OF this*] *PC* **obtain** *stk''* **where** *stk'*: $\text{stk}' = \text{stk}'' @ v \# \text{STK}$
and *exec-meth-d* (*compP2* *P*) (*compE2* *i*) (*compxE2* *i* 0 0) *t* $h ([], \text{loc}, 0, \text{None}) \text{ ta } h' (\text{stk}'', \text{loc}', PC, \text{xcp}')$ **by** *auto*
hence *exec-meth-d* (*compP2* *P*) ((*compE2* *a* @ *compE2* *i*) @ (*compE2* *e* @ [*AStore*, *Push Unit*]))
((*compxE2* *a* 0 0 @ *shift* (*length* (*compE2* *a*)) (*stack-xlift* (*length* [*v*]) (*compxE2* *i* 0 0))) @
shift (*length* (*compE2* *a* @ *compE2* *i*)) (*compxE2* *e* 0 (*Suc* (*Suc* 0)))))) *t* h
($[] @ [v], \text{loc}, \text{length } (\text{compE2 } a) + 0, \text{None}) \text{ ta } h' (\text{stk}'' @ [v], \text{loc}', \text{length } (\text{compE2 } a) + PC,$
xcp')
apply $-$
apply (*rule exec-meth-append-xt*)
apply (*rule append-exec-meth-xt*)
apply (*erule exec-meth-stk-offer*)
by (*auto*)
thus *?thesis* **using** $\langle \text{stk} = [v] \rangle \langle \text{xcp} = \text{None} \rangle \text{stk}' \text{ pc } PC$

by(*clarsimp simp add: shift-compxE2 stack-xlift-compxE2 ac-simps*)
qed
next
case (*bisim1AAss2 i n i' xs stk loc pc xcp a e v*)
note $IH2 = \langle \bigwedge \text{stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \text{ STK}. ?\text{exec } i \text{ stk STK loc pc xcp stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$
 $\implies ?\text{concl } i \text{ stk STK loc pc xcp stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$
note $IH3 = \langle \bigwedge \text{xs} \text{ stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \text{ STK}. ?\text{exec } e \ [] \text{ STK xs } 0 \text{ None stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$
 $\implies ?\text{concl } e \ [] \text{ STK xs } 0 \text{ None stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$
note $\text{bisim2} = \langle P, i, h \vdash (i', \text{xs}) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \rangle$
note $\text{bisim3} = \langle P, e, h \vdash (e, \text{loc}) \leftrightarrow ([], \text{loc}, 0, \text{None}) \rangle$
note $\text{exec} = \langle ?\text{exec } (a[i] := e) (\text{stk} @ [v]) \text{ STK loc } (\text{length } (\text{compE2 } a) + \text{pc}) \text{ xcp stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$
from *bisim2* **have** $\text{pc}: \text{pc} \leq \text{length } (\text{compE2 } i)$ **by**(*rule bisim1-pc-length-compE2*)
from *exec* **have** $\text{exec}': \bigwedge v'. \text{exec-meth-d } (\text{compP2 } P) ((\text{compE2 } a @ \text{compE2 } i) @ \text{compE2 } e @$
 $[AStore, Push Unit]) ((\text{compxE2 } a \ 0 \ (\text{length } \text{STK}) @ \text{shift } (\text{length } (\text{compE2 } a)) (\text{stack-xlift } (\text{length } (v \# \text{STK}))) (\text{compxE2 } i \ 0 \ 0))) @ \text{shift } (\text{length } (\text{compE2 } a @ \text{compE2 } i)) (\text{stack-xlift } (\text{length } (v' \# v \# \text{STK})) (\text{compxE2 } e \ 0 \ 0))) t$
 $h (\text{stk} @ v \# \text{STK}, \text{loc}, \text{length } (\text{compE2 } a) + \text{pc}, \text{xcp}) \text{ ta } h' (\text{stk}', \text{loc}', \text{pc}', \text{xcp}')$
by(*simp add: shift-compxE2 stack-xlift-compxE2*)
show *?case*
proof(*cases pc < length (compE2 i)*)
case *True* **with** *exec'*[*of arbitrary*]
have $\text{exec}'': \text{exec-meth-d } (\text{compP2 } P) (\text{compE2 } a @ \text{compE2 } i) (\text{compxE2 } a \ 0 \ (\text{length } \text{STK}) @$
 $\text{shift } (\text{length } (\text{compE2 } a)) (\text{stack-xlift } (\text{length } (v \# \text{STK})) (\text{compxE2 } i \ 0 \ 0))) t h (\text{stk} @ v \# \text{STK}, \text{loc},$
 $\text{length } (\text{compE2 } a) + \text{pc}, \text{xcp}) \text{ ta } h' (\text{stk}', \text{loc}', \text{pc}', \text{xcp}')$
by-(*erule exec-meth-take-xt, simp*)
hence $?\text{exec } i \text{ stk } (v \# \text{STK}) \text{ loc pc xcp stk}' \text{ loc}' (\text{pc}' - \text{length } (\text{compE2 } a)) \text{ xcp}'$
by(*rule exec-meth-drop-xt*) *auto*
from $IH2[OF \text{ this}]$ **obtain** stk'' **where** $\text{stk}': \text{stk}' = \text{stk}'' @ v \# \text{STK}$
and $\text{exec}''': \text{exec-meth-d } (\text{compP2 } P) (\text{compE2 } i) (\text{compxE2 } i \ 0 \ 0) t h (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \text{ ta } h'$
 $(\text{stk}'', \text{loc}', \text{pc}' - \text{length } (\text{compE2 } a), \text{xcp}')$ **by** *blast*
from exec''' **have** $\text{exec-meth-d } (\text{compP2 } P) ((\text{compE2 } a @ \text{compE2 } i) @ \text{compE2 } e @ [AStore, Push$
 $Unit]) ((\text{compxE2 } a \ 0 \ 0 @ \text{shift } (\text{length } (\text{compE2 } a)) (\text{stack-xlift } (\text{length } [v]) (\text{compxE2 } i \ 0 \ 0))) @ \text{shift}$
 $(\text{length } (\text{compE2 } a @ \text{compE2 } i)) (\text{compxE2 } e \ 0 \ (\text{Suc } (\text{Suc } 0)))) t h (\text{stk} @ [v], \text{loc}, \text{length } (\text{compE2 } a)$
 $+ \text{pc}, \text{xcp}) \text{ ta } h' (\text{stk}'' @ [v], \text{loc}', \text{length } (\text{compE2 } a) + (\text{pc}' - \text{length } (\text{compE2 } a)), \text{xcp}')$
apply -
apply(*rule exec-meth-append-xt*)
apply(*rule append-exec-meth-xt*)
apply(*erule exec-meth-stk-offer*)
by *auto*
moreover **from** exec'' **have** $\text{pc}' \geq \text{length } (\text{compE2 } a)$
by(*rule exec-meth-drop-xt-pc*) *auto*
ultimately show *?thesis* **using** stk' **by**(*auto simp add: shift-compxE2 stack-xlift-compxE2*)
next
case *False*
with pc **have** $\text{pc}: \text{pc} = \text{length } (\text{compE2 } i)$ **by** *simp*
with *exec'*[*of arbitrary*] **have** $\text{pc}' \geq \text{length } (\text{compE2 } a @ \text{compE2 } i)$
by-(*erule exec-meth-drop-xt-pc, auto simp add: shift-compxE2 stack-xlift-compxE2*)
then **obtain** PC **where** $PC: \text{pc}' = PC + \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i)$
by -(*rule-tac PC34=pc' - length (compE2 a @ compE2 i) in that, simp*)
from pc *bisim2* **obtain** v' **where** $\text{stk}: \text{stk} = [v']$ **and** $\text{xcp}: \text{xcp} = \text{None}$ **by**(*auto dest: bisim1-pc-length-compE2D*)
from *exec'*[*of v'*]
have $\text{exec-meth-d } (\text{compP2 } P) (\text{compE2 } e @ [AStore, Push Unit]) (\text{stack-xlift } (\text{length } (v' \# v \# \text{STK})) (\text{compxE2 } e \ 0 \ 0)) t$
 $h (v' \# v \# \text{STK}, \text{loc}, 0, \text{xcp}) \text{ ta } h' (\text{stk}', \text{loc}', \text{pc}' - \text{length } (\text{compE2 } a @ \text{compE2 } i),$

xcp'
unfolding $stk\ pc\ append\text{-}Cons\ append\text{-}Nil$
by $-(rule\ exec\text{-}meth\text{-}drop\text{-}xt,\ simp\ only:\ add\text{-}0\text{-}right\ length\text{-}append,\ auto\ simp\ add:\ shift\text{-}compxE2\ stack\text{-}xlift\text{-}compxE2)$
with $PC\ xcp\ have\ ?exec\ e\ []\ (v'\ \#\ v\ \#\ STK)\ loc\ 0\ None\ stk'\ loc'\ PC\ xcp'$
by $-(rule\ exec\text{-}meth\text{-}take,\ auto)$
from $IH3[OF\ this]\ obtain\ stk''\ where\ stk':\ stk' = stk''\ @\ v'\ \#\ v\ \#\ STK$
and $exec\text{-}meth\text{-}d\ (compP2\ P)\ (compE2\ e)\ (compxE2\ e\ 0\ 0)\ t\ h\ ([],\ loc,\ 0,\ None)\ ta\ h'\ (stk'',\ loc',\ PC,\ xcp')$ **by** $auto$
hence $exec\text{-}meth\text{-}d\ (compP2\ P)\ (((compE2\ a\ @\ compE2\ i)\ @\ compE2\ e)\ @\ [AStore,\ Push\ Unit])\ ((compxE2\ a\ 0\ 0\ @\ compxE2\ i\ (length\ (compE2\ a))\ (Suc\ 0))\ @\ shift\ (length\ (compE2\ a\ @\ compE2\ i))\ (stack\text{-}xlift\ (length\ [v',\ v])\ (compxE2\ e\ 0\ 0)))\ t\ h\ ([]\ @\ [v',\ v],\ loc,\ length\ (compE2\ a\ @\ compE2\ i)\ +\ 0,\ None)\ ta\ h'\ (stk''\ @\ [v',\ v],\ loc',\ length\ (compE2\ a\ @\ compE2\ i)\ +\ PC,\ xcp')$
apply $-$
apply $(rule\ exec\text{-}meth\text{-}append)$
apply $(rule\ append\text{-}exec\text{-}meth\text{-}xt)$
apply $(erule\ exec\text{-}meth\text{-}stk\text{-}offer)$
by $auto$
thus $?thesis\ using\ stk\ xcp\ stk'\ pc\ PC$
by $(clarsimp\ simp\ add:\ shift\text{-}compxE2\ stack\text{-}xlift\text{-}compxE2\ ac\text{-}simps)$
qed
next
case $(bisim1AAss3\ e\ n\ e'\ xs\ stk\ loc\ pc\ xcp\ a\ i\ v1\ v2)$
note $IH3 = \langle \bigwedge stk'\ loc'\ pc'\ xcp'\ STK.\ ?exec\ e\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$
 $\implies ?concl\ e\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp'$
note $bisim3 = \langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
note $exec = \langle ?exec\ (a[i] := e)\ (stk\ @\ [v2,\ v1])\ STK\ loc\ (length\ (compE2\ a)\ +\ length\ (compE2\ i)\ +\ pc)\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$
from $bisim3\ have\ pc:\ pc \leq length\ (compE2\ e)$ **by** $(rule\ bisim1\text{-}pc\text{-}length\text{-}compE2)$
show $?case$
proof $(cases\ pc < length\ (compE2\ e))$
case $True$
from $exec\ have\ exec\text{-}meth\text{-}d\ (compP2\ P)\ (((compE2\ a\ @\ compE2\ i)\ @\ compE2\ e)\ @\ [AStore,\ Push\ Unit])\ ((compxE2\ a\ 0\ (length\ STK)\ @\ compxE2\ i\ (length\ (compE2\ a))\ (Suc\ (length\ STK)))\ @\ shift\ (length\ (compE2\ a\ @\ compE2\ i))\ (stack\text{-}xlift\ (length\ (v2\ \#\ v1\ \#\ STK))\ (compxE2\ e\ 0\ 0)))\ t\ h\ (stk\ @\ v2\ \#\ v1\ \#\ STK,\ loc,\ length\ (compE2\ a\ @\ compE2\ i)\ +\ pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$
by $(simp\ add:\ shift\text{-}compxE2\ stack\text{-}xlift\text{-}compxE2)$
hence $exec':\ exec\text{-}meth\text{-}d\ (compP2\ P)\ ((compE2\ a\ @\ compE2\ i)\ @\ compE2\ e)\ ((compxE2\ a\ 0\ (length\ STK)\ @\ compxE2\ i\ (length\ (compE2\ a))\ (Suc\ (length\ STK)))\ @\ shift\ (length\ (compE2\ a\ @\ compE2\ i))\ (stack\text{-}xlift\ (length\ (v2\ \#\ v1\ \#\ STK))\ (compxE2\ e\ 0\ 0)))\ t\ h\ (stk\ @\ v2\ \#\ v1\ \#\ STK,\ loc,\ length\ (compE2\ a\ @\ compE2\ i)\ +\ pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$
by $(rule\ exec\text{-}meth\text{-}take)(simp\ add:\ True)$
hence $?exec\ e\ stk\ (v2\ \#\ v1\ \#\ STK)\ loc\ pc\ xcp\ stk'\ loc'\ (pc' - length\ (compE2\ a\ @\ compE2\ i))\ xcp'$
by $(rule\ exec\text{-}meth\text{-}drop\text{-}xt)\ auto$
from $IH3[OF\ this]\ obtain\ stk''\ where\ stk':\ stk' = stk''\ @\ v2\ \#\ v1\ \#\ STK$
and $exec'':\ exec\text{-}meth\text{-}d\ (compP2\ P)\ (compE2\ e)\ (compxE2\ e\ 0\ 0)\ t\ h\ (stk,\ loc,\ pc,\ xcp)\ ta\ h'\ (stk'',\ loc',\ pc' - length\ (compE2\ a\ @\ compE2\ i),\ xcp')$ **by** $blast$
from $exec''\ have\ exec\text{-}meth\text{-}d\ (compP2\ P)\ (compE2\ e)\ (stack\text{-}xlift\ (length\ [v2,\ v1])\ (compxE2\ e\ 0\ 0))\ t\ h\ (stk\ @\ [v2,\ v1],\ loc,\ pc,\ xcp)$
 $ta\ h'\ (stk''\ @\ [v2,\ v1],\ loc',\ pc' - length\ (compE2\ a\ @\ compE2\ i),\ xcp')$

by(rule *exec-meth-stk-offer*)
hence *exec-meth-d* (*compP2 P*) ((*compE2 a @ compE2 i*) @ *compE2 e*) ((*compxE2 a 0 0 @ compxE2 i* (*length (compE2 a)*) (*Suc 0*)) @ *shift (length (compE2 a @ compE2 i)) (stack-xlift (length [v2, v1]) (compxE2 e 0 0))*) *t*
h (stk @ [v2, v1], loc, length (compE2 a @ compE2 i) + pc, xcp)
ta h' (stk'' @ [v2, v1], loc', length (compE2 a @ compE2 i) + (pc' - length (compE2 a @ compE2 i)), xcp')
by(rule *append-exec-meth-xt*) *auto*
hence *exec-meth-d* (*compP2 P*) (((*compE2 a @ compE2 i*) @ *compE2 e*) @ [*AStore, Push Unit*]) ((*compxE2 a 0 0 @ compxE2 i* (*length (compE2 a)*) (*Suc 0*)) @ *shift (length (compE2 a @ compE2 i)) (stack-xlift (length [v2, v1]) (compxE2 e 0 0))*) *t*
h (stk @ [v2, v1], loc, length (compE2 a @ compE2 i) + pc, xcp)
ta h' (stk'' @ [v2, v1], loc', length (compE2 a @ compE2 i) + (pc' - length (compE2 a @ compE2 i)), xcp')
by(rule *exec-meth-append*)
moreover from *exec'* **have** $pc' \geq \text{length } (compE2 a @ compE2 i)$
by(rule *exec-meth-drop-xt-pc*)(*auto simp add: stack-xlift-compxE2*)
ultimately show *?thesis* **using** *stk'* **by**(*simp add: stack-xlift-compxE2 shift-compxE2*)
next
case *False*
with *pc* **have** $pc = \text{length } (compE2 e)$ **by** *simp*
with *bisim3* **obtain** *v3* **where** [*simp*]: $stk = [v3]$ $xcp = \text{None}$
by(*auto dest: dest: bisim1-pc-length-compE2D*)
with *exec pc* **show** *?thesis* **apply**(*simp*)
by(*erule exec-meth.cases*)(*auto intro!: exec-meth.intros split: if-split-asm*)
qed
next
case (*bisim1AAssThrow1 A n a xs stk loc pc i e*)
note $bisim1 = \langle P, A, h \vdash (Throw a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$
note $IH1 = \langle \bigwedge stk' loc' pc' xcp' STK. ?exec A stk STK loc pc [a] stk' loc' pc' xcp' \Rightarrow ?concl A stk STK loc pc [a] stk' loc' pc' xcp' \rangle$
note $exec = \langle ?exec (A[i] := e) stk STK loc pc [a] stk' loc' pc' xcp' \rangle$
from *bisim1* **have** $pc < \text{length } (compE2 A)$ **and** [*simp*]: $xs = loc$
by(*auto dest: bisim1-ThrowD*)
from *exec* **have** *exec-meth-d* (*compP2 P*) (*compE2 A @ (compE2 i @ compE2 e @ [AStore, Push Unit])*)
(*stack-xlift (length STK) (compxE2 A 0 0) @ shift (length (compE2 A)) (stack-xlift (length STK) (compxE2 i 0 (Suc 0) @ compxE2 e (length (compE2 i)) (Suc (Suc 0))))*) *t*
h (stk @ STK, loc, pc, [a]) ta h' (stk', loc', pc', xcp') **by**(*simp add: compxE2-size-convs*)
hence $?exec A stk STK loc pc [a] stk' loc' pc' xcp'$ **by**(rule *exec-meth-take-xt*)(rule *pc*)
from $IH1[OF \text{this}]$ **show** *?case* **by**(*auto*)
next
case (*bisim1AAssThrow2 i n a xs stk loc pc A e v1*)
note $bisim2 = \langle P, i, h \vdash (Throw a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$
note $IH2 = \langle \bigwedge stk' loc' pc' xcp' STK. ?exec i stk STK loc pc [a] stk' loc' pc' xcp' \Rightarrow ?concl i stk STK loc pc [a] stk' loc' pc' xcp' \rangle$
note $exec = \langle ?exec (A[i] := e) (stk @ [v1]) STK loc (length (compE2 A) + pc) [a] stk' loc' pc' xcp' \rangle$
from *bisim2* **have** $pc < \text{length } (compE2 i)$ **and** [*simp*]: $xs = loc$
by(*auto dest: bisim1-ThrowD*)
from *exec* **have** *exec-meth-d* (*compP2 P*) ((*compE2 A @ compE2 i*) @ *compE2 e @ [AStore, Push Unit]*)
(*stack-xlift (length STK) (compxE2 A 0 0) @ shift (length (compE2 A)) (stack-xlift (length STK) (compxE2 i 0 (Suc 0)))*) @ (*shift (length (compE2 A @ compE2 i)) (compxE2 e 0 (Suc (Suc (length*

$STK)))))) t$
 $h (stk @ v1 \# STK, loc, length (compE2 A) + pc, [a]) ta h' (stk', loc', pc', xcp')$
by(simp add: shift-compxE2 stack-xlift-compxE2 ac-simps)
hence $exec'$: $exec\text{-meth-d} (compP2 P) (compE2 A @ compE2 i)$
 $(stack\text{-xlift} (length STK) (compxE2 A 0 0) @ shift (length (compE2 A)) (stack\text{-xlift} (length STK)$
 $(compxE2 i 0 (Suc 0)))) t$
 $h (stk @ v1 \# STK, loc, length (compE2 A) + pc, [a]) ta h' (stk', loc', pc', xcp')$
by(rule $exec\text{-meth-take-xt}$)(simp add: pc)
hence $exec\text{-meth-d} (compP2 P) (compE2 i) (stack\text{-xlift} (length STK) (compxE2 i 0 (Suc 0))) t$
 $h (stk @ v1 \# STK, loc, pc, [a]) ta h' (stk', loc', pc' - length (compE2 A), xcp')$
by(rule $exec\text{-meth-drop-xt}$)(auto simp add: stack-xlift-compxE2)
hence $?exec i stk (v1 \# STK) loc pc [a] stk' loc' (pc' - length (compE2 A)) xcp'$
by(simp add: compxE2-stack-xlift-convs)
from $IH2[OF this]$ **obtain** stk'' **where** stk' : $stk' = stk'' @ v1 \# STK$ **and**
 $exec''$: $exec\text{-meth-d} (compP2 P) (compE2 i) (compxE2 i 0 0) t h (stk, loc, pc, [a]) ta h' (stk'', loc',$
 $pc' - length (compE2 A), xcp')$ **by** blast
from $exec''$ **have** $exec\text{-meth-d} (compP2 P) (compE2 i) (stack\text{-xlift} (length [v1]) (compxE2 i 0 0)) t$
 $h (stk @ [v1], loc, pc, [a])$
 $ta h' (stk'' @ [v1], loc', pc' - length (compE2 A), xcp')$
by(rule $exec\text{-meth-stk-offer}$)
hence $exec\text{-meth-d} (compP2 P) (compE2 A @ compE2 i) (compxE2 A 0 0 @ shift (length (compE2$
 $A)) (stack\text{-xlift} (length [v1]) (compxE2 i 0 0))) t$
 $h (stk @ [v1], loc, length (compE2 A) + pc, [a])$
 $ta h' (stk'' @ [v1], loc', length (compE2 A) + (pc' - length (compE2 A)), xcp')$
by(rule $append\text{-exec-meth-xt}$)(auto)
hence $exec\text{-meth-d} (compP2 P) ((compE2 A @ compE2 i) @ compE2 e @ [AStore, Push Unit])$
 $((compxE2 A 0 0 @ shift (length (compE2 A)) (stack\text{-xlift} (length [v1]) (compxE2 i 0 0))) @ (shift$
 $(length (compE2 A @ compE2 i)) (compxE2 e 0 (Suc (Suc 0)))))) t$
 $h (stk @ [v1], loc, length (compE2 A) + pc, [a])$
 $ta h' (stk'' @ [v1], loc', length (compE2 A) + (pc' - length (compE2 A)), xcp')$
by(rule $exec\text{-meth-append-xt}$)
moreover from $exec'$ **have** pc' : $pc' \geq length (compE2 A)$
by(rule $exec\text{-meth-drop-xt-pc}$)(auto simp add: stack-xlift-compxE2)
ultimately show $?case$ **using** stk' **by**(auto simp add: stack-xlift-compxE2 shift-compxE2)
next
case ($bisim1AAssThrow3 e n a xs stk loc pc A i v2 v1$)
note $bisim3 = \langle P, e, h \vdash (Throw a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$
note $IH3 = \langle \bigwedge stk' loc' pc' xcp' STK. ?exec e stk STK loc pc [a] stk' loc' pc' xcp'$
 $\implies ?concl e stk STK loc pc [a] stk' loc' pc' xcp' \rangle$
note $exec = \langle ?exec (A[i] := e) (stk @ [v2, v1]) STK loc (length (compE2 A) + length (compE2 i)$
 $+ pc) [a] stk' loc' pc' xcp' \rangle$
from $bisim3$ **have** pc : $pc < length (compE2 e)$ **and** $[simp]$: $xs = loc$
by(auto dest: $bisim1\text{-ThrowD}$)
from $exec$ **have** $exec\text{-meth-d} (compP2 P) (((compE2 A @ compE2 i) @ compE2 e) @ [AStore, Push$
 $Unit])$
 $((stack\text{-xlift} (length STK) (compxE2 A 0 0 @ compxE2 i (length (compE2 A)) (Suc 0))) @ shift$
 $(length (compE2 A @ compE2 i)) (stack\text{-xlift} (length (v2 \# v1 \# STK)) (compxE2 e 0 0))) t$
 $h (stk @ v2 \# v1 \# STK, loc, length (compE2 A @ compE2 i) + pc, [a]) ta h' (stk', loc', pc',$
 $xcp')$
by(simp add: shift-compxE2 stack-xlift-compxE2 ac-simps)
hence $exec'$: $exec\text{-meth-d} (compP2 P) ((compE2 A @ compE2 i) @ compE2 e)$
 $((stack\text{-xlift} (length STK) (compxE2 A 0 0 @ compxE2 i (length (compE2 A)) (Suc 0))) @ shift$
 $(length (compE2 A @ compE2 i)) (stack\text{-xlift} (length (v2 \# v1 \# STK)) (compxE2 e 0 0))) t$
 $h (stk @ v2 \# v1 \# STK, loc, length (compE2 A @ compE2 i) + pc, [a]) ta h' (stk', loc', pc',$

xcp'
by(rule *exec-meth-take*)(simp add: *pc*)
hence $?exec\ e\ stk\ (v2\ \#\ v1\ \#\ STK)\ loc\ pc\ [a]\ stk'\ loc'\ (pc' - length\ (compE2\ A\ @\ compE2\ i))$
 xcp'
by(rule *exec-meth-drop-xt*) *auto*
from *IH3[OF this]* **obtain** stk'' **where** $stk':\ stk' = stk''\ @\ v2\ \#\ v1\ \#\ STK$ **and**
 $exec'':\ exec\ meth\ d\ (compP2\ P)\ (compE2\ e)\ (compxE2\ e\ 0\ 0)\ t\ h\ (stk,\ loc,\ pc,\ [a])\ ta\ h'\ (stk'',$
 $loc',\ pc' - length\ (compE2\ A\ @\ compE2\ i),\ xcp')$ **by** *blast*
from $exec''$ **have** $exec\ meth\ d\ (compP2\ P)\ (compE2\ e)\ (stack\ xlift\ (length\ [v2,\ v1])\ (compxE2\ e\ 0\ 0))\ t\ h\ (stk\ @\ [v2,\ v1],\ loc,\ pc,\ [a])$
 $ta\ h'\ (stk''\ @\ [v2,\ v1],\ loc',\ pc' - length\ (compE2\ A\ @\ compE2\ i),\ xcp')$
by(rule *exec-meth-stk-offer*)
hence $exec\ meth\ d\ (compP2\ P)\ (((compE2\ A\ @\ compE2\ i)\ @\ compE2\ e)\ ((compxE2\ A\ 0\ 0\ @\ compxE2\ i\ (length\ (compE2\ A))\ (Suc\ 0))\ @\ shift\ (length\ (compE2\ A\ @\ compE2\ i))\ (stack\ xlift\ (length\ [v2,\ v1])\ (compxE2\ e\ 0\ 0))))\ t\ h\ (stk\ @\ [v2,\ v1],\ loc,\ length\ (compE2\ A\ @\ compE2\ i) + pc,\ [a])$
 $ta\ h'\ (stk''\ @\ [v2,\ v1],\ loc',\ length\ (compE2\ A\ @\ compE2\ i) + (pc' - length\ (compE2\ A\ @\ compE2\ i)),\ xcp')$
by(rule *append-exec-meth-xt*)(*auto*)
hence $exec\ meth\ d\ (compP2\ P)\ (((compE2\ A\ @\ compE2\ i)\ @\ compE2\ e)\ @\ [AStore,\ Push\ Unit])\ ((compxE2\ A\ 0\ 0\ @\ compxE2\ i\ (length\ (compE2\ A))\ (Suc\ 0))\ @\ shift\ (length\ (compE2\ A\ @\ compE2\ i))\ (stack\ xlift\ (length\ [v2,\ v1])\ (compxE2\ e\ 0\ 0))))\ t\ h\ (stk\ @\ [v2,\ v1],\ loc,\ length\ (compE2\ A\ @\ compE2\ i) + pc,\ [a])$
 $ta\ h'\ (stk''\ @\ [v2,\ v1],\ loc',\ length\ (compE2\ A\ @\ compE2\ i) + (pc' - length\ (compE2\ A\ @\ compE2\ i)),\ xcp')$
by(rule *exec-meth-append*)
moreover from $exec'$ **have** $pc':\ pc' \geq length\ (compE2\ A\ @\ compE2\ i)$
by(rule *exec-meth-drop-xt-pc*)(*auto* simp add: *stack-xlift-compxE2*)
ultimately show $?case$ **using** stk' **by**(*auto* simp add: *stack-xlift-compxE2* *shift-compxE2*)
next
case *bisim1AAssFail* **thus** $?case$
by(*auto* elim!: *exec-meth.cases* dest: *match-ex-table-pcsD* simp add: *stack-xlift-compxEs2* *stack-xlift-compxE2*)
next
case *bisim1AAss4* **thus** $?case$
by $-(erule\ exec\ meth.\ cases,\ auto\ intro!:\ exec\ meth.\ exec\ instr)$
next
case (*bisim1ALength* $a\ n\ a'\ xs\ stk\ loc\ pc\ xcp$)
note $bisim = \langle P, a, h \vdash (a', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
note $IH = \langle \bigwedge stk'\ loc'\ pc'\ xcp'\ STK.\ ?exec\ a\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \implies ?concl\ a\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$
note $exec = \langle ?exec\ (a.\ length)\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$
from *bisim* **have** $pc:\ pc \leq length\ (compE2\ a)$ **by**(rule *bisim1-pc-length-compE2*)
show $?case$
proof(*cases* $pc < length\ (compE2\ a)$)
case *True*
with $exec$ **have** $?exec\ a\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp'$
by(simp add: *compxE2-size-convs*)(*erule* *exec-meth-take*)
from *IH[OF this]* **show** $?thesis$ **by** *auto*
next
case *False*
with pc **have** [*simp*]: $pc = length\ (compE2\ a)$ **by** *simp*
with *bisim* **obtain** v **where** [*simp*]: $stk = [v]\ xcp = None$
by(*auto* dest: *dest: bisim1-pc-length-compE2D*)
with $exec$ **show** $?thesis$ **apply**(*simp*)
by(*erule* *exec-meth.cases*)(*auto* intro!: *exec-meth.intros* *split: if-split-asm*)

qed
next
case (*bisim1ALengthThrow* $e\ n\ a\ xs\ stk\ loc\ pc$)
note $bisim = \langle P, e, h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$
note $IH = \langle \bigwedge stk' loc' pc' xcp' STK. ?exec\ e\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \Rightarrow ?concl\ e\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$
note $exec = \langle ?exec\ (e \cdot length)\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$
from *bisim* **have** $pc: pc < length\ (compE2\ e)$ **and** $[simp]: xs = loc$
by(*auto dest: bisim1-ThrowD*)
from *exec* **have** $?exec\ e\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp'$
by(*simp*)(*erule exec-meth-take[OF - pc]*)
from *IH*[*OF this*] **show** $?case$ **by**(*auto*)
next
case *bisim1ALengthNull* **thus** $?case$
by(*auto elim!: exec-meth.cases dest: match-ex-table-pcsD simp add: stack-xlift-compxEs2 stack-xlift-compxE2*)
next
case (*bisim1FAcc* $e\ n\ e'\ xs\ stk\ loc\ pc\ xcp\ F\ D$)
note $bisim = \langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
note $IH = \langle \bigwedge stk' loc' pc' xcp' STK. ?exec\ e\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \Rightarrow ?concl\ e\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$
note $exec = \langle ?exec\ (e \cdot F\{D\})\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$
from *bisim* **have** $pc: pc \leq length\ (compE2\ e)$ **by**(*rule bisim1-pc-length-compE2*)
show $?case$
proof(*cases pc < length (compE2 e)*)
case *True*
with *exec* **have** $?exec\ e\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp'$
by(*simp add: compxE2-size-convs*)(*erule exec-meth-take*)
from *IH*[*OF this*] **show** $?thesis$ **by** *auto*
next
case *False*
with *pc* **have** $[simp]: pc = length\ (compE2\ e)$ **by** *simp*
with *bisim* **obtain** v **where** $[simp]: stk = [v]\ xcp = None$
by(*auto dest: dest: bisim1-pc-length-compE2D*)
with *exec* **show** $?thesis$ **apply**(*simp*)
by(*erule exec-meth.cases*)(*fastforce intro!: exec-meth.intros simp add: is-Ref-def split: if-split-asm*)
qed
next
case (*bisim1FAccThrow* $e\ n\ a\ xs\ stk\ loc\ pc\ F\ D$)
note $bisim = \langle P, e, h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$
note $IH = \langle \bigwedge stk' loc' pc' xcp' STK. ?exec\ e\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \Rightarrow ?concl\ e\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$
note $exec = \langle ?exec\ (e \cdot F\{D\})\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$
from *bisim* **have** $pc: pc < length\ (compE2\ e)$ **and** $[simp]: xs = loc$
by(*auto dest: bisim1-ThrowD*)
from *exec* **have** $?exec\ e\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp'$
by(*simp*)(*erule exec-meth-take[OF - pc]*)
from *IH*[*OF this*] **show** $?case$ **by**(*auto*)
next
case *bisim1FAccNull* **thus** $?case$
by(*auto elim!: exec-meth.cases dest: match-ex-table-pcsD simp add: stack-xlift-compxEs2 stack-xlift-compxE2*)
next
case (*bisim1FAss1* $e\ n\ e'\ xs\ stk\ loc\ pc\ xcp\ e2\ F\ D$)
note $IH1 = \langle \bigwedge stk' loc' pc' xcp' STK. ?exec\ e\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \Rightarrow ?concl\ e\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$

note $IH2 = \langle \bigwedge xs \text{ stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \text{ STK}. ?exec \ e2 \ [] \ \text{STK} \ xs \ 0 \ \text{None} \ \text{stk}' \ \text{loc}' \ \text{pc}' \ \text{xcp}' \Rightarrow ?concl \ e2 \ [] \ \text{STK} \ xs \ 0 \ \text{None} \ \text{stk}' \ \text{loc}' \ \text{pc}' \ \text{xcp}' \rangle$
note $bisim1 = \langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
note $bisim2 = \langle P, e2, h \vdash (e2, loc) \leftrightarrow ([], loc, 0, \text{None}) \rangle$
note $exec = \langle ?exec \ (e \cdot F\{D\} := e2) \ \text{stk} \ \text{STK} \ \text{loc} \ \text{pc} \ \text{xcp} \ \text{stk}' \ \text{loc}' \ \text{pc}' \ \text{xcp}' \rangle$
from $bisim1$ **have** $pc: pc \leq \text{length} \ (\text{compE2} \ e)$ **by** $(\text{rule} \ \text{bisim1-pc-length-compE2})$
show $?case$
proof $(\text{cases} \ pc < \text{length} \ (\text{compE2} \ e))$
 case True
 with $exec$ **have** $?exec \ e \ \text{stk} \ \text{STK} \ \text{loc} \ \text{pc} \ \text{xcp} \ \text{stk}' \ \text{loc}' \ \text{pc}' \ \text{xcp}'$
 by $(\text{simp} \ \text{add:} \ \text{compxE2-size-convs})(\text{erule} \ \text{exec-meth-take-xt})$
 from $IH1$ $[OF \ \text{this}]$ **show** $?thesis$ **by** auto
 next
 case False
 with pc **have** $pc: pc = \text{length} \ (\text{compE2} \ e)$ **by** simp
 with $exec$ **have** $pc' \geq \text{length} \ (\text{compE2} \ e)$
 by $(\text{simp} \ \text{add:} \ \text{compxE2-size-convs} \ \text{stack-xlift-compxE2})(\text{auto} \ \text{elim!}: \ \text{exec-meth-drop-xt-pc})$
 then obtain PC **where** $PC: pc' = PC + \text{length} \ (\text{compE2} \ e)$
 by $-(\text{rule-tac} \ PC34 = pc' - \text{length} \ (\text{compE2} \ e) \ \text{in} \ \text{that}, \ \text{simp})$
 from pc $bisim1$ **obtain** v **where** $stk = [v] \ \text{xcp} = \text{None}$ **by** $(\text{auto} \ \text{dest:} \ \text{bisim1-pc-length-compE2D})$
 with $exec \ pc$ **have** $\text{exec-meth-d} \ (\text{compP2} \ P) \ (\text{compE2} \ e \ @ \ \text{compE2} \ e2)$
 $(\text{stack-xlift} \ (\text{length} \ \text{STK}) \ (\text{compxE2} \ e \ 0 \ 0 \ @ \ \text{compxE2} \ e2 \ (\text{length} \ (\text{compE2} \ e)) \ (\text{Suc} \ 0))) \ t$
 $h \ (stk \ @ \ \text{STK}, \ \text{loc}, \ \text{length} \ (\text{compE2} \ e) + 0, \ \text{xcp}) \ \text{ta} \ h' \ (stk', \ \text{loc}', \ \text{pc}', \ \text{xcp}')$
 by $-(\text{rule} \ \text{exec-meth-take}, \ \text{auto})$
 hence $?exec \ e2 \ [] \ (v \ # \ \text{STK}) \ \text{loc} \ 0 \ \text{None} \ \text{stk}' \ \text{loc}' \ (pc' - \text{length} \ (\text{compE2} \ e)) \ \text{xcp}'$
 using $\langle \text{stk} = [v] \rangle \langle \text{xcp} = \text{None} \rangle$
 by $-(\text{rule} \ \text{exec-meth-drop-xt}, \ \text{auto} \ \text{simp} \ \text{add:} \ \text{stack-xlift-compxE2} \ \text{shift-compxE2})$
 from $IH2$ $[OF \ \text{this}]$ PC **obtain** stk'' **where** $stk': \text{stk}' = \text{stk}'' \ @ \ v \ # \ \text{STK}$
 and $\text{exec-meth-d} \ (\text{compP2} \ P) \ (\text{compE2} \ e2) \ (\text{compxE2} \ e2 \ 0 \ 0) \ t \ h \ ([], \ \text{loc}, \ 0, \ \text{None}) \ \text{ta} \ h' \ (stk'',$
 $\text{loc}', \ PC, \ \text{xcp}')$ **by** auto
 hence $\text{exec-meth-d} \ (\text{compP2} \ P) \ ((\text{compE2} \ e \ @ \ \text{compE2} \ e2) \ @ \ [\text{Putfield} \ F \ D, \ \text{Push} \ \text{Unit}])$
 $(\text{compxE2} \ e \ 0 \ 0 \ @ \ \text{shift} \ (\text{length} \ (\text{compE2} \ e)) \ (\text{stack-xlift} \ (\text{length} \ [v]) \ (\text{compxE2} \ e2 \ 0 \ 0))) \ t \ h$
 $([] \ @ \ [v], \ \text{loc}, \ \text{length} \ (\text{compE2} \ e) + 0, \ \text{None}) \ \text{ta} \ h' \ (stk'' \ @ \ [v], \ \text{loc}', \ \text{length} \ (\text{compE2} \ e) + PC,$
 $\text{xcp}')$
 apply $-$
 apply $(\text{rule} \ \text{exec-meth-append})$
 apply $(\text{rule} \ \text{append-exec-meth-xt})$
 apply $(\text{erule} \ \text{exec-meth-stk-offer})$
 by (auto)
 thus $?thesis$ **using** $\langle \text{stk} = [v] \rangle \langle \text{xcp} = \text{None} \rangle \ \text{stk}' \ \text{pc} \ PC$
 by $(\text{clarsimp} \ \text{simp} \ \text{add:} \ \text{shift-compxE2} \ \text{stack-xlift-compxE2} \ \text{ac-simps})$
 qed
next
 case $(bisim1FAss2 \ e2 \ n \ e' \ xs \ \text{stk} \ \text{loc} \ \text{pc} \ \text{xcp} \ e \ F \ D \ v1)$
 note $IH2 = \langle \bigwedge \text{stk}' \ \text{loc}' \ \text{pc}' \ \text{xcp}' \ \text{STK}. ?exec \ e2 \ \text{stk} \ \text{STK} \ \text{loc} \ \text{pc} \ \text{xcp} \ \text{stk}' \ \text{loc}' \ \text{pc}' \ \text{xcp}' \Rightarrow ?concl \ e2 \ \text{stk} \ \text{STK} \ \text{loc} \ \text{pc} \ \text{xcp} \ \text{stk}' \ \text{loc}' \ \text{pc}' \ \text{xcp}' \rangle$
 by $(\text{rule} \ \text{bisim1-pc-length-compE2})$
 note $bisim2 = \langle P, e2, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
 note $exec = \langle ?exec \ (e \cdot F\{D\} := e2) \ (\text{stk} \ @ \ [v1]) \ \text{STK} \ \text{loc} \ (\text{length} \ (\text{compE2} \ e) + \text{pc}) \ \text{xcp} \ \text{stk}' \ \text{loc}' \ \text{pc}' \ \text{xcp}' \rangle$
 from $bisim2$ **have** $pc: pc \leq \text{length} \ (\text{compE2} \ e2)$ **by** $(\text{rule} \ \text{bisim1-pc-length-compE2})$
 show $?case$
 proof $(\text{cases} \ pc < \text{length} \ (\text{compE2} \ e2))$
 case True
 from $exec$ **have** $\text{exec-meth-d} \ (\text{compP2} \ P) \ ((\text{compE2} \ e \ @ \ \text{compE2} \ e2) \ @ \ [\text{Putfield} \ F \ D, \ \text{Push} \ \text{Unit}])$

$(\text{stack-xlift } (\text{length } STK) (\text{compxE2 } e \ 0 \ 0) \ @ \ \text{shift } (\text{length } (\text{compE2 } e)) (\text{stack-xlift } (\text{length } STK) (\text{compxE2 } e2 \ 0 \ (\text{Suc } 0)))) \ t$
 $h \ (stk \ @ \ v1 \ \# \ STK, \ loc, \ \text{length } (\text{compE2 } e) + pc, \ xcp) \ ta \ h' \ (stk', \ loc', \ pc', \ xcp') \ \text{by} \ (\text{simp add: compxE2-size-convs})$
hence exec' : $\text{exec-meth-d } (\text{compP2 } P) (\text{compE2 } e \ @ \ \text{compE2 } e2) (\text{stack-xlift } (\text{length } STK) (\text{compxE2 } e \ 0 \ 0) \ @ \ \text{shift } (\text{length } (\text{compE2 } e)) (\text{stack-xlift } (\text{length } STK) (\text{compxE2 } e2 \ 0 \ (\text{Suc } 0)))) \ t$
 $h \ (stk \ @ \ v1 \ \# \ STK, \ loc, \ \text{length } (\text{compE2 } e) + pc, \ xcp) \ ta \ h' \ (stk', \ loc', \ pc', \ xcp')$
by(rule exec-meth-take)(simp add: True)
hence $\text{exec-meth-d } (\text{compP2 } P) (\text{compE2 } e2) (\text{stack-xlift } (\text{length } STK) (\text{compxE2 } e2 \ 0 \ (\text{Suc } 0))) \ t$
 $h \ (stk \ @ \ v1 \ \# \ STK, \ loc, \ pc, \ xcp) \ ta \ h' \ (stk', \ loc', \ pc' - \text{length } (\text{compE2 } e), \ xcp')$
by(rule exec-meth-drop-xt)(auto simp add: $\text{stack-xlift-compxE2}$)
hence $?exec \ e2 \ stk \ (v1 \ \# \ STK) \ loc \ pc \ xcp \ stk' \ loc' \ (pc' - \text{length } (\text{compE2 } e)) \ xcp'$
by(simp add: $\text{compxE2-stack-xlift-convs}$)
from $IH2[OF \ \text{this}]$ **obtain** stk'' **where** stk' : $stk' = stk'' \ @ \ v1 \ \# \ STK$
and exec'' : $\text{exec-meth-d } (\text{compP2 } P) (\text{compE2 } e2) (\text{compxE2 } e2 \ 0 \ 0) \ t \ h \ (stk, \ loc, \ pc, \ xcp) \ ta \ h' \ (stk'', \ loc', \ pc' - \text{length } (\text{compE2 } e), \ xcp')$ **by** blast
from exec'' **have** $\text{exec-meth-d } (\text{compP2 } P) (\text{compE2 } e2) (\text{stack-xlift } (\text{length } [v1]) (\text{compxE2 } e2 \ 0 \ 0)) \ t \ h \ (stk \ @ \ [v1], \ loc, \ pc, \ xcp)$
 $ta \ h' \ (stk'' \ @ \ [v1], \ loc', \ pc' - \text{length } (\text{compE2 } e), \ xcp')$
by(rule $\text{exec-meth-stk-offer}$)
hence $\text{exec-meth-d } (\text{compP2 } P) (\text{compE2 } e \ @ \ \text{compE2 } e2) (\text{compxE2 } e \ 0 \ 0 \ @ \ \text{shift } (\text{length } (\text{compE2 } e)) (\text{stack-xlift } (\text{length } [v1]) (\text{compxE2 } e2 \ 0 \ 0))) \ t \ h \ (stk \ @ \ [v1], \ loc, \ \text{length } (\text{compE2 } e) + pc, \ xcp)$
 $ta \ h' \ (stk'' \ @ \ [v1], \ loc', \ \text{length } (\text{compE2 } e) + (pc' - \text{length } (\text{compE2 } e)), \ xcp')$
by(rule $\text{append-exec-meth-xt}$) auto
hence $\text{exec-meth-d } (\text{compP2 } P) ((\text{compE2 } e \ @ \ \text{compE2 } e2) \ @ \ [\text{Putfield } F \ D, \ \text{Push } \text{Unit}]) (\text{compxE2 } e \ 0 \ 0 \ @ \ \text{shift } (\text{length } (\text{compE2 } e)) (\text{stack-xlift } (\text{length } [v1]) (\text{compxE2 } e2 \ 0 \ 0))) \ t \ h \ (stk \ @ \ [v1], \ loc, \ \text{length } (\text{compE2 } e) + pc, \ xcp)$
 $ta \ h' \ (stk'' \ @ \ [v1], \ loc', \ \text{length } (\text{compE2 } e) + (pc' - \text{length } (\text{compE2 } e)), \ xcp')$
by(rule exec-meth-append)
moreover from exec' **have** $pc' \geq \text{length } (\text{compE2 } e)$
by(rule $\text{exec-meth-drop-xt-pc}$)(auto simp add: $\text{stack-xlift-compxE2}$)
ultimately show $?thesis$ **using** stk' **by**(simp add: $\text{stack-xlift-compxE2}$ shift-compxE2)
next
case False
with pc **have** pc : $pc = \text{length } (\text{compE2 } e2)$ **by** simp
with bisim2 **obtain** $v2$ **where** $[\text{simp}]$: $stk = [v2] \ xcp = \text{None}$
by(auto dest: $\text{dest: bisim1-pc-length-compE2D}$)
with $\text{exec } pc$ **show** $?thesis$ **apply**(simp)
by(erule exec-meth.cases)(fastforce intro!: $\text{exec-meth.intros split: if-split-asm}$)
qed
next
case ($\text{bisim1FAssThrow1 } e \ n \ a \ xs \ stk \ loc \ pc \ e2 \ F \ D$)
note $\text{bisim1} = \langle P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$
note $IH1 = \langle \bigwedge stk' \ loc' \ pc' \ xcp' \ STK. \ ?exec \ e \ stk \ STK \ loc \ pc \ [a] \ stk' \ loc' \ pc' \ xcp' \ \Longrightarrow \ ?concl \ e \ stk \ STK \ loc \ pc \ [a] \ stk' \ loc' \ pc' \ xcp' \rangle$
note $\text{exec} = \langle ?exec \ (e \cdot F \{D\}) := e2 \rangle \ stk \ STK \ loc \ pc \ [a] \ stk' \ loc' \ pc' \ xcp'$
from bisim1 **have** pc : $pc < \text{length } (\text{compE2 } e)$ **and** $[\text{simp}]$: $xs = loc$
by(auto dest: bisim1-ThrowD)
from exec **have** $\text{exec-meth-d } (\text{compP2 } P) (\text{compE2 } e \ @ \ (\text{compE2 } e2 \ @ \ [\text{Putfield } F \ D, \ \text{Push } \text{Unit}])) (\text{stack-xlift } (\text{length } STK) (\text{compxE2 } e \ 0 \ 0) \ @ \ \text{shift } (\text{length } (\text{compE2 } e)) (\text{stack-xlift } (\text{length } STK) (\text{compxE2 } e2 \ 0 \ (\text{Suc } 0)))) \ t$
 $h \ (stk \ @ \ STK, \ loc, \ pc, \ [a]) \ ta \ h' \ (stk', \ loc', \ pc', \ xcp')$ **by**(simp add: $\text{compxE2-size-convs}$)
hence $?exec \ e \ stk \ STK \ loc \ pc \ [a] \ stk' \ loc' \ pc' \ xcp'$ **by**(rule exec-meth-take-xt)(rule pc)

from $IH1$ [OF this] **show** $?case$ **by**(*auto*)
next
case (*bisim1FAssThrow2* $e2$ n a xs stk loc pc e F D $v1$)
note $bisim2 = \langle P, e2, h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$
note $IH2 = \langle \bigwedge stk' loc' pc' xcp' STK. ?exec\ e2\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$
 $\implies ?concl\ e2\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp'$
note $exec = \langle ?exec\ (e \cdot F\{D\} := e2)\ (stk\ @\ [v1])\ STK\ loc\ (length\ (compE2\ e) + pc)\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$
from *bisim2* **have** $pc: pc < length\ (compE2\ e2)$ **and** [*simp*]: $xs = loc$
by(*auto* *dest: bisim1-ThrowD*)
from *exec* **have** *exec-meth-d* (*compP2* P) ((*compE2* e @ *compE2* $e2$) @ [*Putfield* F D , *Push* *Unit*])
(*stack-xlift* (*length* STK) (*compxE2* e 0 0) @ *shift* (*length* (*compE2* e)) (*stack-xlift* (*length* STK)
(*compxE2* $e2$ 0 (*Suc* 0)))) t
 $h\ (stk\ @\ v1\ \# STK, loc, length\ (compE2\ e) + pc, [a])$ ta $h'\ (stk', loc', pc', xcp')$
by(*simp* *add: compxE2-size-convs*)
hence *exec'*: *exec-meth-d* (*compP2* P) (*compE2* e @ *compE2* $e2$)
(*stack-xlift* (*length* STK) (*compxE2* e 0 0) @ *shift* (*length* (*compE2* e)) (*stack-xlift* (*length* STK)
(*compxE2* $e2$ 0 (*Suc* 0)))) t
 $h\ (stk\ @\ v1\ \# STK, loc, length\ (compE2\ e) + pc, [a])$ ta $h'\ (stk', loc', pc', xcp')$
by(*rule* *exec-meth-take*)(*simp* *add: pc*)
hence *exec-meth-d* (*compP2* P) (*compE2* $e2$) (*stack-xlift* (*length* STK) (*compxE2* $e2$ 0 (*Suc* 0))) t
 $h\ (stk\ @\ v1\ \# STK, loc, pc, [a])$ ta $h'\ (stk', loc', pc' - length\ (compE2\ e), xcp')$
by(*rule* *exec-meth-drop-xt*)(*auto* *simp* *add: stack-xlift-compxE2*)
hence $?exec\ e2\ stk\ (v1\ \# STK)\ loc\ pc\ [a]\ stk'\ loc'\ (pc' - length\ (compE2\ e))\ xcp'$
by(*simp* *add: compxE2-stack-xlift-convs*)
from $IH2$ [OF this] **obtain** stk'' **where** $stk': stk' = stk'' @ v1 \# STK$ **and**
 $exec'': exec-meth-d\ (compP2\ P)\ (compE2\ e2)\ (compxE2\ e2\ 0\ 0)\ t\ h\ (stk, loc, pc, [a])\ ta\ h'\ (stk'',$
 $loc', pc' - length\ (compE2\ e), xcp')$ **by** *blast*
from $exec''$ **have** *exec-meth-d* (*compP2* P) (*compE2* $e2$) (*stack-xlift* (*length* [$v1$]) (*compxE2* $e2$ 0 0))
 $t\ h\ (stk\ @\ [v1], loc, pc, [a])$
 $ta\ h'\ (stk''\ @\ [v1], loc', pc' - length\ (compE2\ e), xcp')$
by(*rule* *exec-meth-stk-offer*)
hence *exec-meth-d* (*compP2* P) (*compE2* e @ *compE2* $e2$) (*compxE2* e 0 0 @ *shift* (*length* (*compE2*
 e)) (*stack-xlift* (*length* [$v1$]) (*compxE2* $e2$ 0 0))) $t\ h\ (stk\ @\ [v1], loc, length\ (compE2\ e) + pc, [a])$
 $ta\ h'\ (stk''\ @\ [v1], loc', length\ (compE2\ e) + (pc' - length\ (compE2\ e)), xcp')$
by(*rule* *append-exec-meth-xt*)(*auto*)
hence *exec-meth-d* (*compP2* P) ((*compE2* e @ *compE2* $e2$) @ [*Putfield* F D , *Push* *Unit*]) (*compxE2*
 e 0 0 @ *shift* (*length* (*compE2* e)) (*stack-xlift* (*length* [$v1$]) (*compxE2* $e2$ 0 0))) $t\ h\ (stk\ @\ [v1], loc,$
 $length\ (compE2\ e) + pc, [a])$
 $ta\ h'\ (stk''\ @\ [v1], loc', length\ (compE2\ e) + (pc' - length\ (compE2\ e)), xcp')$
by(*rule* *exec-meth-append*)
moreover **from** $exec'$ **have** $pc': pc' \geq length\ (compE2\ e)$
by(*rule* *exec-meth-drop-xt-pc*)(*auto* *simp* *add: stack-xlift-compxE2*)
ultimately **show** $?case$ **using** stk' **by**(*auto* *simp* *add: stack-xlift-compxE2* *shift-compxE2*)
next
case *bisim1FAssNull* **thus** $?case$
by(*auto* *elim!*: *exec-meth.cases* *dest: match-ex-table-pcsD* *simp* *add: stack-xlift-compxEs2* *stack-xlift-compxE2*)
next
case *bisim1FAss3* **thus** $?case$
by $-(erule\ exec-meth.cases, auto\ intro!:\ exec-meth.exec-instr)$
next
case (*bisim1CAS1* $e1$ n $e1'$ xs stk loc pc xcp $e2$ $e3$ D F)
note $IH1 = \langle \bigwedge stk' loc' pc' xcp' STK. ?exec\ e1\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$
 $\implies ?concl\ e1\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp'$

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note IH2 = ⟨ $\bigwedge xs\ stk'\ loc'\ pc'\ xcp'\ STK.\ ?exec\ e2\ []\ STK\ xs\ 0\ None\ stk'\ loc'\ pc'\ xcp'$ 
   $\implies\ ?concl\ e2\ []\ STK\ xs\ 0\ None\ stk'\ loc'\ pc'\ xcp'$ ⟩
note bisim1 = ⟨ $P, e1, h \vdash (e1', xs) \leftrightarrow (stk, loc, pc, xcp)$ ⟩
note bisim2 = ⟨ $P, e2, h \vdash (e2, loc) \leftrightarrow ([], loc, 0, None)$ ⟩
note exec = ⟨ $?exec - stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp'$ ⟩
from bisim1 have pc:  $pc \leq length\ (compE2\ e1)$  by(rule bisim1-pc-length-compE2)
from exec have exec':  $exec\text{-meth-d}\ (compP2\ P)\ (compE2\ e1\ @\ compE2\ e2\ @\ compE2\ e3\ @\ [CAS\ F\ D])\ (stack\text{-xlift}\ (length\ STK)\ (compxE2\ e1\ 0\ 0)\ @\ shift\ (length\ (compE2\ e1))\ (stack\text{-xlift}\ (length\ STK)\ (compxE2\ e2\ 0\ (Suc\ 0)\ @\ compxE2\ e3\ (length\ (compE2\ e2))\ (Suc\ (Suc\ 0))))\ t\ h\ (stk\ @\ STK,\ loc,\ pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$ 
  by(simp add: compxE2-size-convs)
show ?case
proof(cases  $pc < length\ (compE2\ e1)$ )
  case True
    with exec' have ?exec e1  $stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp'$  by(rule exec-meth-take-xt)
    from IH1[OF this] show ?thesis by auto
  next
    case False
      with pc have pc:  $pc = length\ (compE2\ e1)$  by simp
      with exec' have pc'  $\geq length\ (compE2\ e1)$  by  $-(erule\ exec\text{-meth-drop-xt-pc},\ auto)$ 
      then obtain PC where PC:  $pc' = PC + length\ (compE2\ e1)$ 
        by  $-(rule\ tac\ PC34=pc' - length\ (compE2\ e1)\ in\ that,\ simp)$ 
      from pc bisim1 obtain v where  $stk = [v]\ xcp = None$  by(auto dest: bisim1-pc-length-compE2D)
      with exec PC pc
        have  $exec\text{-meth-d}\ (compP2\ P)\ ((compE2\ e1\ @\ compE2\ e2)\ @\ compE2\ e3\ @\ [CAS\ F\ D])\ (stack\text{-xlift}\ (length\ STK)\ (compxE2\ e1\ 0\ 0\ @\ shift\ (length\ (compE2\ e1))\ (compxE2\ e2\ 0\ (Suc\ 0)))\ @\ shift\ (length\ (compE2\ e1\ @\ compE2\ e2))\ (compxE2\ e3\ 0\ (length\ STK + Suc\ (Suc\ 0))))\ t\ h\ (v\ \# \ STK,\ loc,\ length\ (compE2\ e1) + 0,\ None)\ ta\ h'\ (stk',\ loc',\ length\ (compE2\ e1) + PC,\ xcp')$ 
          by(simp add: shift-compxE2 stack-xlift-compxE2 ac-simps)
        hence  $exec\text{-meth-d}\ (compP2\ P)\ (compE2\ e1\ @\ compE2\ e2)\ (stack\text{-xlift}\ (length\ STK)\ (compxE2\ e1\ 0\ 0\ @\ shift\ (length\ (compE2\ e1))\ (compxE2\ e2\ 0\ (Suc\ 0))))\ t\ h\ (v\ \# \ STK,\ loc,\ length\ (compE2\ e1) + 0,\ None)\ ta\ h'\ (stk',\ loc',\ length\ (compE2\ e1) + PC,\ xcp')$ 
          by(rule exec-meth-take-xt) simp
        hence  $?exec\ e2\ []\ (v\ \# \ STK)\ loc\ 0\ None\ stk'\ loc'\ ((length\ (compE2\ e1) + PC) - length\ (compE2\ e1))\ xcp'$ 
          by  $-(rule\ exec\text{-meth-drop-xt},\ auto\ simp\ add:\ stack\text{-xlift-compxE2}\ shift\text{-compxE2})$ 
        from IH2[OF this] PC obtain stk'' where  $stk':\ stk' = stk''\ @\ v\ \# \ STK$ 
          and  $exec\text{-meth-d}\ (compP2\ P)\ (compE2\ e2)\ (compxE2\ e2\ 0\ 0)\ t\ h\ ([],\ loc,\ 0,\ None)\ ta\ h'\ (stk'',\ loc',\ PC,\ xcp')$ 
          by auto
        hence  $exec\text{-meth-d}\ (compP2\ P)\ ((compE2\ e1\ @\ compE2\ e2)\ @\ (compE2\ e3\ @\ [CAS\ F\ D]))\ ((compxE2\ e1\ 0\ 0\ @\ shift\ (length\ (compE2\ e1))\ (stack\text{-xlift}\ (length\ [v])\ (compxE2\ e2\ 0\ 0)))\ @\ shift\ (length\ (compE2\ e1\ @\ compE2\ e2))\ (compxE2\ e3\ 0\ (Suc\ (Suc\ 0))))\ t\ h\ ([]\ @\ [v],\ loc,\ length\ (compE2\ e1) + 0,\ None)\ ta\ h'\ (stk''\ @\ [v],\ loc',\ length\ (compE2\ e1) + PC,\ xcp')$ 
          by auto
      apply  $-(rule\ exec\text{-meth-append-xt})$ 
      apply(rule append-exec-meth-xt)
      apply(erule exec-meth-stk-offer)
      by(auto)
      thus ?thesis using  $\langle stk = [v] \rangle\ \langle xcp = None \rangle\ stk'\ pc\ PC$ 
        by(clarsimp simp add: shift-compxE2 stack-xlift-compxE2 ac-simps)
    qed
  next

```

case (*bisim1CAS2* *e2 n e2' xs stk loc pc xcp e1 e3 D F v*)
note *IH2* = $\langle \bigwedge \text{stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \text{ STK}. ?\text{exec } e2 \text{ stk STK loc pc xcp stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$
 $\implies ?\text{concl } e2 \text{ stk STK loc pc xcp stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$
note *IH3* = $\langle \bigwedge \text{xs stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \text{ STK}. ?\text{exec } e3 \ [] \text{ STK xs } 0 \text{ None stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$
 $\implies ?\text{concl } e3 \ [] \text{ STK xs } 0 \text{ None stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$
note *bisim2* = $\langle P, e2, h \vdash (e2', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
note *bisim3* = $\langle P, e3, h \vdash (e3, loc) \leftrightarrow ([], loc, 0, \text{None}) \rangle$
note *exec* = $\langle ?\text{exec} - (\text{stk} @ [v]) \text{ STK loc } (\text{length } (\text{compE2 } e1) + \text{pc}) \text{ xcp stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$
from *bisim2* **have** *pc*: $\text{pc} \leq \text{length } (\text{compE2 } e2)$ **by** (*rule bisim1-pc-length-compE2*)
from *exec* **have** *exec'*: $\bigwedge v'. \text{exec-meth-d } (\text{compP2 } P) ((\text{compE2 } e1 @ \text{compE2 } e2) @ \text{compE2 } e3 @ [\text{CAS } F \ D]) ((\text{compxE2 } e1 \ 0 \ (\text{length } \text{STK}) @ \text{shift } (\text{length } (\text{compE2 } e1)) (\text{stack-xlift } (\text{length } (v \# \text{STK})) (\text{compxE2 } e2 \ 0 \ 0))) @ \text{shift } (\text{length } (\text{compE2 } e1 @ \text{compE2 } e2)) (\text{stack-xlift } (\text{length } (v' \# v \# \text{STK})) (\text{compxE2 } e3 \ 0 \ 0))) t$
 $h (\text{stk} @ v \# \text{STK}, \text{loc}, \text{length } (\text{compE2 } e1) + \text{pc}, \text{xcp}) \text{ ta } h' (\text{stk}', \text{loc}', \text{pc}', \text{xcp}')$
by (*simp add: shift-compxE2 stack-xlift-compxE2*)
show *?case*
proof (*cases pc < length (compE2 e2)*)
case *True* **with** *exec'* [*of undefined*]
have *exec''*: $\text{exec-meth-d } (\text{compP2 } P) (\text{compE2 } e1 @ \text{compE2 } e2) (\text{compxE2 } e1 \ 0 \ (\text{length } \text{STK}) @ \text{shift } (\text{length } (\text{compE2 } e1)) (\text{stack-xlift } (\text{length } (v \# \text{STK})) (\text{compxE2 } e2 \ 0 \ 0))) t h (\text{stk} @ v \# \text{STK}, \text{loc}, \text{length } (\text{compE2 } e1) + \text{pc}, \text{xcp}) \text{ ta } h' (\text{stk}', \text{loc}', \text{pc}', \text{xcp}')$
by $-(\text{erule } \text{exec-meth-take-xt}, \text{simp})$
hence *?exec e2 stk (v # STK) loc pc xcp stk' loc' (pc' - length (compE2 e1)) xcp'*
by (*rule exec-meth-drop-xt*) *auto*
from *IH2* [*OF this*] **obtain** *stk''* **where** *stk'*: $\text{stk}' = \text{stk}'' @ v \# \text{STK}$
and *exec'''*: $\text{exec-meth-d } (\text{compP2 } P) (\text{compE2 } e2) (\text{compxE2 } e2 \ 0 \ 0) t h (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \text{ ta } h' (\text{stk}'', \text{loc}', \text{pc}' - \text{length } (\text{compE2 } e1), \text{xcp}')$ **by** *blast*
from *exec'''* **have** $\text{exec-meth-d } (\text{compP2 } P) ((\text{compE2 } e1 @ \text{compE2 } e2) @ \text{compE2 } e3 @ [\text{CAS } F \ D]) ((\text{compxE2 } e1 \ 0 \ 0 @ \text{shift } (\text{length } (\text{compE2 } e1)) (\text{stack-xlift } (\text{length } [v]) (\text{compxE2 } e2 \ 0 \ 0))) @ \text{shift } (\text{length } (\text{compE2 } e1 @ \text{compE2 } e2)) (\text{compxE2 } e3 \ 0 \ (\text{Suc } (\text{Suc } 0)))) t h (\text{stk} @ [v], \text{loc}, \text{length } (\text{compE2 } e1) + \text{pc}, \text{xcp}) \text{ ta } h' (\text{stk}'' @ [v], \text{loc}', \text{length } (\text{compE2 } e1) + (\text{pc}' - \text{length } (\text{compE2 } e1)), \text{xcp}')$
apply $-$
apply (*rule exec-meth-append-xt*)
apply (*rule append-exec-meth-xt*)
apply (*erule exec-meth-stk-offer*)
by *auto*
moreover **from** *exec''* **have** $\text{pc}' \geq \text{length } (\text{compE2 } e1)$
by (*rule exec-meth-drop-xt-pc*) *auto*
ultimately show *?thesis* **using** *stk'* **by** (*auto simp add: shift-compxE2 stack-xlift-compxE2*)
next
case *False*
with *pc* **have** *pc*: $\text{pc} = \text{length } (\text{compE2 } e2)$ **by** *simp*
with *exec'* [*of undefined*] **have** $\text{pc}' \geq \text{length } (\text{compE2 } e1 @ \text{compE2 } e2)$
by $-(\text{erule } \text{exec-meth-drop-xt-pc}, \text{auto } \text{simp } \text{add: } \text{shift-compxE2 } \text{stack-xlift-compxE2})$
then obtain *PC* **where** *PC*: $\text{pc}' = \text{PC} + \text{length } (\text{compE2 } e1) + \text{length } (\text{compE2 } e2)$
by $-(\text{rule-tac } \text{PC34} = \text{pc}' - \text{length } (\text{compE2 } e1 @ \text{compE2 } e2) \text{ in } \text{that}, \text{simp})$
from *pc bisim2* **obtain** *v'* **where** *stk*: $\text{stk} = [v']$ **and** *xcp*: $\text{xcp} = \text{None}$ **by** (*auto dest: bisim1-pc-length-compE2D*)
from *exec'* [*of v'*]
have $\text{exec-meth-d } (\text{compP2 } P) (\text{compE2 } e3 @ [\text{CAS } F \ D]) (\text{stack-xlift } (\text{length } (v' \# v \# \text{STK})) (\text{compxE2 } e3 \ 0 \ 0)) t$
 $h (v' \# v \# \text{STK}, \text{loc}, 0, \text{xcp}) \text{ ta } h' (\text{stk}', \text{loc}', \text{pc}' - \text{length } (\text{compE2 } e1 @ \text{compE2 } e2), \text{xcp}')$
unfolding *stk pc append-Cons append-Nil*

by $-(\text{rule } \text{exec-meth-drop-xt}, \text{ simp only: add-0-right length-append, auto simp add: shift-compxE2 stack-xlift-compxE2})$
with $PC \text{ have } ?\text{exec } e3 \ [] \ (v' \# v \# STK) \ \text{loc } 0 \ \text{None } \text{stk}' \ \text{loc}' \ PC \ \text{xcp}'$
by $-(\text{rule } \text{exec-meth-take, auto})$
from $IH3[OF \ \text{this}] \ \text{obtain } \text{stk}'' \ \text{where } \text{stk}': \text{stk}' = \text{stk}'' \ @ \ v' \# v \# STK$
and $\text{exec-meth-d } (\text{compP2 } P) \ (\text{compE2 } e3) \ (\text{compxE2 } e3 \ 0 \ 0) \ t \ h \ (\ [], \ \text{loc}, \ 0, \ \text{None}) \ \text{ta } h' \ (\text{stk}'', \ \text{loc}', \ PC, \ \text{xcp}')$ **by** auto
hence $\text{exec-meth-d } (\text{compP2 } P) \ (((\text{compE2 } e1 \ @ \ \text{compE2 } e2) \ @ \ \text{compE2 } e3) \ @ \ [CAS \ F \ D])$
 $((\text{compxE2 } e1 \ 0 \ 0 \ @ \ \text{compxE2 } e2 \ (\text{length } (\text{compE2 } e1)) \ (\text{Suc } 0)) \ @ \ \text{shift } (\text{length } (\text{compE2 } e1 \ @ \ \text{compE2 } e2)) \ (\text{stack-xlift } (\text{length } [v', \ v]) \ (\text{compxE2 } e3 \ 0 \ 0))) \ t \ h \ (\ [] \ @ \ [v', \ v], \ \text{loc}, \ \text{length } (\text{compE2 } e1 \ @ \ \text{compE2 } e2) \ + \ 0, \ \text{None}) \ \text{ta } h' \ (\text{stk}'' \ @ \ [v', \ v], \ \text{loc}', \ \text{length } (\text{compE2 } e1 \ @ \ \text{compE2 } e2) \ + \ PC, \ \text{xcp}')$
apply $-$
apply $(\text{rule } \text{exec-meth-append})$
apply $(\text{rule } \text{append-exec-meth-xt})$
apply $(\text{erule } \text{exec-meth-stk-offer})$
by auto
thus $?\text{thesis}$ **using** $\text{stk } \text{xcp } \text{stk}' \ \text{pc } PC$
by $(\text{clarsimp simp add: shift-compxE2 stack-xlift-compxE2 ac-simps})$
qed
next
case $(\text{bisim1CAS3 } e3 \ n \ e3' \ \text{xs } \text{stk } \ \text{loc } \ \text{pc } \ \text{xcp } \ e1 \ e2 \ D \ F \ v1 \ v2)$
note $IH3 = \langle \bigwedge \text{stk}' \ \text{loc}' \ \text{pc}' \ \text{xcp}' \ STK. \ ?\text{exec } e3 \ \text{stk } STK \ \text{loc } \ \text{pc } \ \text{xcp } \ \text{stk}' \ \text{loc}' \ \text{pc}' \ \text{xcp}' \rangle$
 $\implies ?\text{concl } e3 \ \text{stk } STK \ \text{loc } \ \text{pc } \ \text{xcp } \ \text{stk}' \ \text{loc}' \ \text{pc}' \ \text{xcp}' \rangle$
note $\text{bisim3} = \langle P, e3, h \vdash (e3', \ \text{xs}) \leftrightarrow (\text{stk}, \ \text{loc}, \ \text{pc}, \ \text{xcp}) \rangle$
note $\text{exec} = \langle ?\text{exec} - (\text{stk} \ @ \ [v2, \ v1]) \ STK \ \text{loc} \ (\text{length } (\text{compE2 } e1) \ + \ \text{length } (\text{compE2 } e2) \ + \ \text{pc}) \ \text{xcp } \ \text{stk}' \ \text{loc}' \ \text{pc}' \ \text{xcp}' \rangle$
from bisim3 **have** $\text{pc}: \text{pc} \leq \text{length } (\text{compE2 } e3)$ **by** $(\text{rule } \text{bisim1-pc-length-compE2})$
show $?\text{case}$
proof $(\text{cases } \text{pc} < \text{length } (\text{compE2 } e3))$
case True
from exec **have** $\text{exec-meth-d } (\text{compP2 } P) \ (((\text{compE2 } e1 \ @ \ \text{compE2 } e2) \ @ \ \text{compE2 } e3) \ @ \ [CAS \ F \ D])$
 $((\text{compxE2 } e1 \ 0 \ (\text{length } STK) \ @ \ \text{compxE2 } e2 \ (\text{length } (\text{compE2 } e1)) \ (\text{Suc } (\text{length } STK))) \ @ \ \text{shift}$
 $(\text{length } (\text{compE2 } e1 \ @ \ \text{compE2 } e2)) \ (\text{stack-xlift } (\text{length } (v2 \# v1 \# STK)) \ (\text{compxE2 } e3 \ 0 \ 0))) \ t$
 $h \ (\text{stk} \ @ \ v2 \# v1 \# STK, \ \text{loc}, \ \text{length } (\text{compE2 } e1 \ @ \ \text{compE2 } e2) \ + \ \text{pc}, \ \text{xcp}) \ \text{ta } h' \ (\text{stk}', \ \text{loc}', \ \text{pc}', \ \text{xcp}')$
by $(\text{simp add: shift-compxE2 stack-xlift-compxE2})$
hence $\text{exec}': \text{exec-meth-d } (\text{compP2 } P) \ ((\text{compE2 } e1 \ @ \ \text{compE2 } e2) \ @ \ \text{compE2 } e3)$
 $((\text{compxE2 } e1 \ 0 \ (\text{length } STK) \ @ \ \text{compxE2 } e2 \ (\text{length } (\text{compE2 } e1)) \ (\text{Suc } (\text{length } STK))) \ @ \ \text{shift}$
 $(\text{length } (\text{compE2 } e1 \ @ \ \text{compE2 } e2)) \ (\text{stack-xlift } (\text{length } (v2 \# v1 \# STK)) \ (\text{compxE2 } e3 \ 0 \ 0))) \ t$
 $h \ (\text{stk} \ @ \ v2 \# v1 \# STK, \ \text{loc}, \ \text{length } (\text{compE2 } e1 \ @ \ \text{compE2 } e2) \ + \ \text{pc}, \ \text{xcp}) \ \text{ta } h' \ (\text{stk}', \ \text{loc}', \ \text{pc}', \ \text{xcp}')$
by $(\text{rule } \text{exec-meth-take})(\text{simp add: True})$
hence $?\text{exec } e3 \ \text{stk} \ (v2 \# v1 \# STK) \ \text{loc } \ \text{pc } \ \text{xcp} \ \text{stk}' \ \text{loc}' \ (\text{pc}' - \text{length } (\text{compE2 } e1 \ @ \ \text{compE2 } e2)) \ \text{xcp}'$
by $(\text{rule } \text{exec-meth-drop-xt}) \ \text{auto}$
from $IH3[OF \ \text{this}] \ \text{obtain } \text{stk}'' \ \text{where } \text{stk}': \text{stk}' = \text{stk}'' \ @ \ v2 \# v1 \# STK$
and $\text{exec}'': \text{exec-meth-d } (\text{compP2 } P) \ (\text{compE2 } e3) \ (\text{compxE2 } e3 \ 0 \ 0) \ t \ h \ (\text{stk}, \ \text{loc}, \ \text{pc}, \ \text{xcp}) \ \text{ta } h' \ (\text{stk}'', \ \text{loc}', \ \text{pc}' - \text{length } (\text{compE2 } e1 \ @ \ \text{compE2 } e2), \ \text{xcp}')$ **by** blast
from exec'' **have** $\text{exec-meth-d } (\text{compP2 } P) \ (\text{compE2 } e3) \ (\text{stack-xlift } (\text{length } [v2, \ v1]) \ (\text{compxE2 } e3 \ 0 \ 0)) \ t \ h \ (\text{stk} \ @ \ [v2, \ v1], \ \text{loc}, \ \text{pc}, \ \text{xcp})$
 $\ \text{ta } h' \ (\text{stk}'' \ @ \ [v2, \ v1], \ \text{loc}', \ \text{pc}' - \text{length } (\text{compE2 } e1 \ @ \ \text{compE2 } e2), \ \text{xcp}')$
by $(\text{rule } \text{exec-meth-stk-offer})$
hence $\text{exec-meth-d } (\text{compP2 } P) \ ((\text{compE2 } e1 \ @ \ \text{compE2 } e2) \ @ \ \text{compE2 } e3) \ ((\text{compxE2 } e1 \ 0 \ 0 \ @$

$\text{compxE2 } e2 \text{ (length (compE2 } e1)) \text{ (Suc 0)) @ shift (length (compE2 } e1 \text{ @ compE2 } e2)) \text{ (stack-xlift (length [v2, v1]) (compxE2 } e3 \text{ 0 0))} t$
 $h \text{ (stk @ [v2, v1], loc, length (compE2 } e1 \text{ @ compE2 } e2) + pc, xcp)$
 $ta \text{ h' (stk'' @ [v2, v1], loc', length (compE2 } e1 \text{ @ compE2 } e2) + (pc' - \text{length (compE2 } e1 \text{ @ compE2 } e2)), xcp')$
by(rule *append-exec-meth-xt*) *auto*
hence *exec-meth-d* (compP2 P) (((compE2 e1 @ compE2 e2) @ compE2 e3) @ [CAS F D])
 $((\text{compxE2 } e1 \text{ 0 0 @ compxE2 } e2 \text{ (length (compE2 } e1)) \text{ (Suc 0)) @ shift (length (compE2 } e1 \text{ @ compE2 } e2)) \text{ (stack-xlift (length [v2, v1]) (compxE2 } e3 \text{ 0 0))} t$
 $h \text{ (stk @ [v2, v1], loc, length (compE2 } e1 \text{ @ compE2 } e2) + pc, xcp)$
 $ta \text{ h' (stk'' @ [v2, v1], loc', length (compE2 } e1 \text{ @ compE2 } e2) + (pc' - \text{length (compE2 } e1 \text{ @ compE2 } e2)), xcp')$
by(rule *exec-meth-append*)
moreover from *exec' have* $pc' \geq \text{length (compE2 } e1 \text{ @ compE2 } e2)$
by(rule *exec-meth-drop-xt-pc*)(*auto simp add: stack-xlift-compxE2*)
ultimately show *?thesis using stk' by*(*simp add: stack-xlift-compxE2 shift-compxE2*)
next
case *False*
with *pc have* $pc = \text{length (compE2 } e3)$ **by** *simp*
with *bisim3 obtain* *v3 where* [*simp*]: $stk = [v3]$ $xcp = \text{None}$
by(*auto dest: dest: bisim1-pc-length-compE2D*)
with *exec pc show* *?thesis apply*(*simp*)
by(*erule exec-meth.cases*)(*fastforce intro!: exec-meth.intros split: if-split-asm*)+
qed
next
case (*bisim1CASThrow1* *e1 n a xs stk loc pc e2 e3 D F*)
note *bisim1* = $\langle P, e1, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$
note *IH1* = $\langle \bigwedge stk' loc' pc' xcp' STK. ?exec \text{ } e1 \text{ } stk \text{ } STK \text{ } loc \text{ } pc \text{ } [a] \text{ } stk' \text{ } loc' \text{ } pc' \text{ } xcp' \rangle$
 $\implies ?concl \text{ } e1 \text{ } stk \text{ } STK \text{ } loc \text{ } pc \text{ } [a] \text{ } stk' \text{ } loc' \text{ } pc' \text{ } xcp'$
note *exec* = $\langle ?exec - stk \text{ } STK \text{ } loc \text{ } pc \text{ } [a] \text{ } stk' \text{ } loc' \text{ } pc' \text{ } xcp' \rangle$
from *bisim1 have* $pc < \text{length (compE2 } e1)$ **and** [*simp*]: $xs = loc$
by(*auto dest: bisim1-ThrowD*)
from *exec have* *exec-meth-d* (compP2 P) (compE2 e1 @ (compE2 e2 @ compE2 e3 @ [CAS F D]))
 $(\text{stack-xlift (length STK) (compxE2 } e1 \text{ 0 0) @ shift (length (compE2 } e1)) \text{ (stack-xlift (length STK) (compxE2 } e2 \text{ 0 (Suc 0) @ compxE2 } e3 \text{ (length (compE2 } e2)) \text{ (Suc (Suc 0))))} t$
 $h \text{ (stk @ STK, loc, pc, [a]) } ta \text{ h' (stk', loc', pc', xcp') } \mathbf{by}(\text{simp add: compxE2-size-convs})$
hence *?exec e1 stk STK loc pc [a] stk' loc' pc' xcp'* **by**(rule *exec-meth-take-xt*)(rule *pc*)
from *IH1[OF this] show* *?case by*(*auto*)
next
case (*bisim1CASThrow2* *e2 n a xs stk loc pc e1 e3 D F v1*)
note *bisim2* = $\langle P, e2, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$
note *IH2* = $\langle \bigwedge stk' loc' pc' xcp' STK. ?exec \text{ } e2 \text{ } stk \text{ } STK \text{ } loc \text{ } pc \text{ } [a] \text{ } stk' \text{ } loc' \text{ } pc' \text{ } xcp' \rangle$
 $\implies ?concl \text{ } e2 \text{ } stk \text{ } STK \text{ } loc \text{ } pc \text{ } [a] \text{ } stk' \text{ } loc' \text{ } pc' \text{ } xcp'$
note *exec* = $\langle ?exec - (stk @ [v1]) \text{ } STK \text{ } loc \text{ } (\text{length (compE2 } e1) + pc) \text{ } [a] \text{ } stk' \text{ } loc' \text{ } pc' \text{ } xcp' \rangle$
from *bisim2 have* $pc < \text{length (compE2 } e2)$ **and** [*simp*]: $xs = loc$
by(*auto dest: bisim1-ThrowD*)
from *exec have* *exec-meth-d* (compP2 P) ((compE2 e1 @ compE2 e2) @ compE2 e3 @ [CAS F D])
 $((\text{stack-xlift (length STK) (compxE2 } e1 \text{ 0 0) @ shift (length (compE2 } e1)) \text{ (stack-xlift (length STK) (compxE2 } e2 \text{ 0 (Suc 0))} @ (\text{shift (length (compE2 } e1 \text{ @ compE2 } e2)) \text{ (compxE2 } e3 \text{ 0 (Suc (length STK))))} t$
 $h \text{ (stk @ v1 \# STK, loc, length (compE2 } e1) + pc, [a]) } ta \text{ h' (stk', loc', pc', xcp')$
by(*simp add: shift-compxE2 stack-xlift-compxE2 ac-simps*)
hence *exec': exec-meth-d* (compP2 P) (compE2 e1 @ compE2 e2)
 $(\text{stack-xlift (length STK) (compxE2 } e1 \text{ 0 0) @ shift (length (compE2 } e1)) \text{ (stack-xlift (length STK)$

$(\text{compxE2 } e2 \ 0 \ (\text{Suc } 0))) \ t$
 $h \ (\text{stk} \ @ \ v1 \ \# \ \text{STK}, \ \text{loc}, \ \text{length} \ (\text{compE2 } e1) \ + \ \text{pc}, \ [a]) \ \text{ta} \ h' \ (\text{stk}', \ \text{loc}', \ \text{pc}', \ \text{xcp}')$
by(rule exec-meth-take-xt)(simp add: pc)
hence exec-meth-d (compP2 P) (compE2 e2) (stack-xlift (length STK) (compxE2 e2 0 (Suc 0))) t
 $h \ (\text{stk} \ @ \ v1 \ \# \ \text{STK}, \ \text{loc}, \ \text{pc}, \ [a]) \ \text{ta} \ h' \ (\text{stk}', \ \text{loc}', \ \text{pc}' - \ \text{length} \ (\text{compE2 } e1), \ \text{xcp}')$
by(rule exec-meth-drop-xt)(auto simp add: stack-xlift-compxE2)
hence ?exec e2 stk (v1 # STK) loc pc [a] stk' loc' (pc' - length (compE2 e1)) xcp'
by(simp add: compxE2-stack-xlift-convs)
from IH2[OF this] **obtain** stk'' **where** stk': stk' = stk'' @ v1 # STK **and**
 $\text{exec}'': \ \text{exec-meth-d} \ (\text{compP2 } P) \ (\text{compE2 } e2) \ (\text{compxE2 } e2 \ 0 \ 0) \ t \ h \ (\text{stk}, \ \text{loc}, \ \text{pc}, \ [a]) \ \text{ta} \ h' \ (\text{stk}'', \ \text{loc}', \ \text{pc}' - \ \text{length} \ (\text{compE2 } e1), \ \text{xcp}')$ **by** blast
from exec'' **have** exec-meth-d (compP2 P) (compE2 e2) (stack-xlift (length [v1]) (compxE2 e2 0 0))
t
 $h \ (\text{stk} \ @ \ [v1], \ \text{loc}, \ \text{pc}, \ [a])$
 $\text{ta} \ h' \ (\text{stk}'' \ @ \ [v1], \ \text{loc}', \ \text{pc}' - \ \text{length} \ (\text{compE2 } e1), \ \text{xcp}')$
by(rule exec-meth-stk-offer)
hence exec-meth-d (compP2 P) (compE2 e1 @ compE2 e2) (compxE2 e1 0 0 @ shift (length (compE2 e1)) (stack-xlift (length [v1]) (compxE2 e2 0 0))) t
 $h \ (\text{stk} \ @ \ [v1], \ \text{loc}, \ \text{length} \ (\text{compE2 } e1) \ + \ \text{pc}, \ [a])$
 $\text{ta} \ h' \ (\text{stk}'' \ @ \ [v1], \ \text{loc}', \ \text{length} \ (\text{compE2 } e1) \ + \ (\text{pc}' - \ \text{length} \ (\text{compE2 } e1)), \ \text{xcp}')$
by(rule append-exec-meth-xt)(auto)
hence exec-meth-d (compP2 P) ((compE2 e1 @ compE2 e2) @ compE2 e3 @ [CAS F D]) ((compxE2 e1 0 0 @ shift (length (compE2 e1)) (stack-xlift (length [v1]) (compxE2 e2 0 0))) @ (shift (length (compE2 e1 @ compE2 e2)) (compxE2 e3 0 (Suc (Suc 0))))) t
 $h \ (\text{stk} \ @ \ [v1], \ \text{loc}, \ \text{length} \ (\text{compE2 } e1) \ + \ \text{pc}, \ [a])$
 $\text{ta} \ h' \ (\text{stk}'' \ @ \ [v1], \ \text{loc}', \ \text{length} \ (\text{compE2 } e1) \ + \ (\text{pc}' - \ \text{length} \ (\text{compE2 } e1)), \ \text{xcp}')$
by(rule exec-meth-append-xt)
moreover from exec' **have** pc': pc' ≥ length (compE2 e1)
by(rule exec-meth-drop-xt-pc)(auto simp add: stack-xlift-compxE2)
ultimately show ?case **using** stk' **by**(auto simp add: stack-xlift-compxE2 shift-compxE2)
next
case (bisim1CASThrow3 e3 n a xs stk loc pc e1 e2 D F v2 v1)
note bisim3 = ⟨P, e3, h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
note IH3 = ⟨∧stk' loc' pc' xcp' STK. ?exec e3 stk STK loc pc [a] stk' loc' pc' xcp' ⇒ ?concl e3 stk STK loc pc [a] stk' loc' pc' xcp'⟩
note exec = ⟨?exec - (stk @ [v2, v1]) STK loc (length (compE2 e1) + length (compE2 e2) + pc) [a] stk' loc' pc' xcp'⟩
from bisim3 **have** pc: pc < length (compE2 e3) **and** [simp]: xs = loc
by(auto dest: bisim1-ThrowD)
from exec **have** exec-meth-d (compP2 P) (((compE2 e1 @ compE2 e2) @ compE2 e3) @ [CAS F D])
((stack-xlift (length STK) (compxE2 e1 0 0 @ compxE2 e2 (length (compE2 e1)) (Suc 0))) @ shift (length (compE2 e1 @ compE2 e2)) (stack-xlift (length (v2 # v1 # STK)) (compxE2 e3 0 0))) t
 $h \ (\text{stk} \ @ \ v2 \ \# \ v1 \ \# \ \text{STK}, \ \text{loc}, \ \text{length} \ (\text{compE2 } e1 \ @ \ \text{compE2 } e2) \ + \ \text{pc}, \ [a]) \ \text{ta} \ h' \ (\text{stk}', \ \text{loc}', \ \text{pc}', \ \text{xcp}')$
by(simp add: shift-compxE2 stack-xlift-compxE2 ac-simps)
hence exec': exec-meth-d (compP2 P) ((compE2 e1 @ compE2 e2) @ compE2 e3)
((stack-xlift (length STK) (compxE2 e1 0 0 @ compxE2 e2 (length (compE2 e1)) (Suc 0))) @ shift (length (compE2 e1 @ compE2 e2)) (stack-xlift (length (v2 # v1 # STK)) (compxE2 e3 0 0))) t
 $h \ (\text{stk} \ @ \ v2 \ \# \ v1 \ \# \ \text{STK}, \ \text{loc}, \ \text{length} \ (\text{compE2 } e1 \ @ \ \text{compE2 } e2) \ + \ \text{pc}, \ [a]) \ \text{ta} \ h' \ (\text{stk}', \ \text{loc}', \ \text{pc}', \ \text{xcp}')$
by(rule exec-meth-take)(simp add: pc)
hence ?exec e3 stk (v2 # v1 # STK) loc pc [a] stk' loc' (pc' - length (compE2 e1 @ compE2 e2)) xcp'

by(rule *exec-meth-drop-xt*) *auto*
from *IH3[OF this]* **obtain** *stk''* **where** *stk'*: *stk' = stk'' @ v2 # v1 # STK* **and**
exec'': *exec-meth-d (compP2 P) (compE2 e3) (compxE2 e3 0 0) t h (stk, loc, pc, [a]) ta h' (stk'',*
loc', pc' - length (compE2 e1 @ compE2 e2), xcp') **by** *blast*
from *exec''* **have** *exec-meth-d (compP2 P) (compE2 e3) (stack-xlift (length [v2, v1]) (compxE2 e3*
0 0)) t h (stk @ [v2, v1], loc, pc, [a])
ta h' (stk'' @ [v2, v1], loc', pc' - length (compE2 e1 @ compE2 e2), xcp')
by(rule *exec-meth-stk-offer*)
hence *exec-meth-d (compP2 P) ((compE2 e1 @ compE2 e2) @ compE2 e3) ((compxE2 e1 0 0 @*
compxE2 e2 (length (compE2 e1)) (Suc 0)) @ shift (length (compE2 e1 @ compE2 e2)) (stack-xlift
(length [v2, v1]) (compxE2 e3 0 0))) t h (stk @ [v2, v1], loc, length (compE2 e1 @ compE2 e2) + pc,
[a])
ta h' (stk'' @ [v2, v1], loc', length (compE2 e1 @ compE2 e2) + (pc' - length (compE2 e1 @
compE2 e2)), xcp')
by(rule *append-exec-meth-xt*)(*auto*)
hence *exec-meth-d (compP2 P) (((compE2 e1 @ compE2 e2) @ compE2 e3) @ [CAS F D])*
((compxE2 e1 0 0 @ compxE2 e2 (length (compE2 e1)) (Suc 0)) @ shift (length (compE2 e1 @
compE2 e2)) (stack-xlift (length [v2, v1]) (compxE2 e3 0 0))) t h (stk @ [v2, v1], loc, length (compE2
e1 @ compE2 e2) + pc, [a])
ta h' (stk'' @ [v2, v1], loc', length (compE2 e1 @ compE2 e2) + (pc' - length (compE2 e1 @
compE2 e2)), xcp')
by(rule *exec-meth-append*)
moreover from *exec'* **have** *pc'*: *pc' ≥ length (compE2 e1 @ compE2 e2)*
by(rule *exec-meth-drop-xt-pc*)(*auto simp add: stack-xlift-compxE2*)
ultimately show *?case* **using** *stk'* **by**(*auto simp add: stack-xlift-compxE2 shift-compxE2*)
next
case *bisim1CASFail* **thus** *?case*
by(*auto elim!: exec-meth.cases dest: match-ex-table-pcsD simp add: stack-xlift-compxEs2 stack-xlift-compxE2*)
next
case (*bisim1Call1 obj n obj' xs stk loc pc xcp ps M'*)
note *bisimObj = ⟨P, obj, h ⊢ (obj', xs) ↔ (stk, loc, pc, xcp)⟩*
note *IHobj = ⟨∧stk' loc' pc' xcp' STK. ?exec obj stk STK loc pc xcp stk' loc' pc' xcp'⟩*
 \implies *?concl obj stk STK loc pc xcp stk' loc' pc' xcp'*
note *IHparams = ⟨∧xs stk' loc' pc' xcp' STK. ?execs ps [] STK xs 0 None stk' loc' pc' xcp'⟩*
 \implies *?concls ps [] STK xs 0 None stk' loc' pc' xcp'*
note *exec = ⟨?exec (obj.M'(ps)) stk STK loc pc xcp stk' loc' pc' xcp'⟩*
from *bisimObj* **have** *pc*: *pc ≤ length (compE2 obj)* **by**(rule *bisim1-pc-length-compE2*)
show *?case*
proof(*cases pc < length (compE2 obj)*)
case *True*
from *exec* **have** *?exec obj stk STK loc pc xcp stk' loc' pc' xcp'*
by(*simp add: compxEs2-size-convs*)(*erule exec-meth-take-xt[OF - True]*)
from *IHobj[OF this]* **show** *?thesis* **by** *auto*
next
case *False*
with *pc* **have** [*simp*]: *pc = length (compE2 obj)* **by** *simp*
with *exec* **have** *pc' ≥ length (compE2 obj)*
by(*simp add: compxEs2-size-convs stack-xlift-compxE2*)(*auto elim!: exec-meth-drop-xt-pc*)
then obtain *PC* **where** *PC*: *pc' = PC + length (compE2 obj)*
by $-($ *rule-tac PC34=pc' - length (compE2 obj)* $)$ **in** *that, simp*
from *pc bisimObj* **obtain** *v* **where** [*simp*]: *stk = [v] xcp = None* **by**(*auto dest: bisim1-pc-length-compE2D*)
show *?thesis*
proof(*cases ps*)
case *Cons*

```

with exec pc have exec-meth-d (compP2 P) (compE2 obj @ compEs2 ps)
  (stack-xlift (length STK) (compxE2 obj 0 0 @ compxEs2 ps (length (compE2 obj)) (Suc 0))) t
  h (stk @ STK, loc, length (compE2 obj) + 0, xcp) ta h' (stk', loc', pc', xcp')
  by -(rule exec-meth-take, auto)
hence ?execs ps [] (v # STK) loc 0 None stk' loc' (pc' - length (compE2 obj)) xcp'
  apply -
  apply(rule exec-meth-drop-xt)
  apply(simp add: compxEs2-size-convs compxEs2-stack-xlift-convs)
  apply(auto simp add: stack-xlift-compxE2)
  done
from IHparams[OF this] PC obtain stk'' where stk': stk' = stk'' @ v # STK
  and exec': exec-meth-d (compP2 P) (compEs2 ps) (compxEs2 ps 0 0) t h ([] , loc, 0, None) ta
h' (stk'', loc', PC, xcp')
  by auto
  from exec' have exec-meth-d (compP2 P) ((compE2 obj @ compEs2 ps) @ [Invoke M' (length
ps)]) (compxE2 obj 0 0 @ shift (length (compE2 obj)) (stack-xlift (length [v]) (compxEs2 ps 0 0))) t
h ([] @ [v], loc, length (compE2 obj) + 0, None) ta h' (stk'' @ [v], loc', length (compE2 obj) + PC,
xcp')
  apply -
  apply(rule exec-meth-append)
  apply(rule append-exec-meth-xt)
  apply(erule exec-meth-stk-offer)
  by(auto)
thus ?thesis using stk' PC by(clarsimp simp add: shift-compxEs2 stack-xlift-compxEs2 ac-simps)
next
case Nil
with exec pc show ?thesis
  apply(auto elim!: exec-meth.cases intro!: exec-meth.intros simp add: split-beta split: if-split-asm)
  apply(auto split: extCallRet.split-asm intro!: exec-meth.intros)
  apply(force intro!: exI)
  apply(force intro!: exI)
  apply(force intro!: exI)
  done
qed
qed
next
case (bisim1CallParams ps n ps' xs stk loc pc xcp obj M' v)
note bisimParam = ⟨P, ps, h ⊢ (ps', xs) [↔] (stk, loc, pc, xcp)⟩
note IHparam = ⟨∧stk' loc' pc' xcp' STK. ?execs ps stk STK loc pc xcp stk' loc' pc' xcp'
  ⇒ ?concls ps stk STK loc pc xcp stk' loc' pc' xcp'⟩
note exec = ⟨?exec (obj·M'(ps)) (stk @ [v]) STK loc (length (compE2 obj) + pc) xcp stk' loc' pc'
xcp'⟩
show ?case
proof(cases ps)
  case Nil
  with bisimParam have pc = 0 xcp = None by(auto elim: bisims1.cases)
  with exec Nil show ?thesis
    apply(auto elim!: exec-meth.cases intro!: exec-meth.intros simp add: split-beta extRet2JVM-def
split: if-split-asm)
    apply(auto split: extCallRet.split-asm simp add: neq-Nil-conv)
    apply(force intro!: exec-meth.intros)+
    done
  next
  case Cons

```

```

from bisimParam have pc:  $pc \leq \text{length}(\text{compEs2 } ps)$  by(rule bisims1-pc-length-compEs2)
show ?thesis
proof(cases pc < length(compEs2 ps))
  case True
    from exec have exec-meth-d(compP2 P)((compE2 obj @ compEs2 ps) @ [Invoke M'(length ps)])
      (stack-xlift(length STK)(compxE2 obj 0 0) @ shift(length(compE2 obj))(stack-xlift(length(v # STK)(compxEs2 ps 0 0))) t
        h(stk @ v # STK, loc, length(compE2 obj) + pc, xcp) ta h'(stk', loc', pc', xcp'))
      by(simp add: compxEs2-size-convs compxEs2-stack-xlift-convs)
      hence exec': exec-meth-d(compP2 P)(compE2 obj @ compEs2 ps)(stack-xlift(length STK)(compxE2 obj 0 0) @
        shift(length(compE2 obj))(stack-xlift(length(v # STK)(compxEs2 ps 0 0))) t
        h(stk @ v # STK, loc, length(compE2 obj) + pc, xcp) ta h'(stk', loc', pc', xcp'))
      by(rule exec-meth-take(simp add: True))
      hence ?execs ps stk(v # STK) loc pc xcp stk' loc'(pc' - length(compE2 obj)) xcp'
        by(rule exec-meth-drop-xt(auto simp add: stack-xlift-compxE2))
      from IHparam[OF this] obtain stk'' where stk':  $stk' = stk'' @ v \# STK$ 
        and exec'': exec-meth-d(compP2 P)(compEs2 ps)(compxEs2 ps 0 0) t h(stk, loc, pc, xcp) ta
        h'(stk'', loc', pc' - length(compE2 obj), xcp') by blast
      from exec'' have exec-meth-d(compP2 P)(compEs2 ps)(stack-xlift(length[v])(compxEs2 ps 0 0)) t h(stk @ [v], loc, pc, xcp) ta
        h'(stk'' @ [v], loc', pc' - length(compE2 obj), xcp')
      by(rule exec-meth-stk-offer)
      hence exec-meth-d(compP2 P)(compE2 obj @ compEs2 ps)(compxE2 obj 0 0 @ shift(length(compE2 obj)(stack-xlift(length[v])(compxEs2 ps 0 0))) t
        h(stk @ [v], loc, length(compE2 obj) + pc, xcp) ta h'(stk'' @ [v], loc', length(compE2 obj) + (pc' - length(compE2 obj)), xcp')
      by(rule append-exec-meth-xt) auto
      hence exec-meth-d(compP2 P)((compE2 obj @ compEs2 ps) @ [Invoke M'(length ps)](compxE2 obj 0 0 @ shift(length(compE2 obj)(stack-xlift(length[v])(compxEs2 ps 0 0))) t
        h(stk @ [v], loc, length(compE2 obj) + pc, xcp) ta h'(stk'' @ [v], loc', length(compE2 obj) + (pc' - length(compE2 obj)), xcp')
      by(rule exec-meth-append)
      moreover from exec' have  $pc' \geq \text{length}(\text{compE2 obj})$ 
        by(rule exec-meth-drop-xt-pc(auto simp add: stack-xlift-compxE2))
      ultimately show ?thesis using stk'
        by(auto simp add: shift-compxEs2 stack-xlift-compxEs2)
    next
      case False
      with pc have  $pc = \text{length}(\text{compEs2 } ps)$  by simp
      with bisimParam obtain vs where  $stk = vs \text{ length } vs = \text{length } ps \ xcp = \text{None}$ 
        by(auto dest: bisims1-pc-length-compEs2D)
      with exec pc Cons show ?thesis
        apply(auto elim!: exec-meth.cases intro!: exec-meth.intros simp add: split-beta extRet2JVM-def split: if-split-asm)
        apply(auto simp add: neq-Nil-conv split: extCallRet.split-asm)
        apply(force intro!: exec-meth.intros)+
        done
      qed
    qed
  next
    case (bisim1CallThrowObj obj n a xs stk loc pc ps M')
    note bisim = ⟨P, obj, h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
    note IH = ⟨∧stk' loc' pc' xcp' STK. ?exec obj stk STK loc pc [a] stk' loc' pc' xcp'⟩

```

$\implies ?concl\ obj\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp'$
note $exec = \langle ?exec\ (obj \cdot M'(ps))\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$
from $bisim\ have\ pc < length\ (compE2\ obj)\ and\ [simp]:\ xs = loc\ by(auto\ dest:\ bisim1-ThrowD)$
with $exec\ have\ ?exec\ obj\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp'$
by $(simp\ add:\ compxEs2-size-convs\ compxEs2-stack-xlift-convs)(erule\ exec-meth-take-xt)$
from $IH[OF\ this]\ show\ ?case\ by\ auto$
next
case $(bisim1CallThrowParams\ ps\ n\ vs\ a\ ps'\ xs\ stk\ loc\ pc\ obj\ M'\ v)$
note $bisim = \langle P, ps, h \vdash (map\ Val\ vs\ @\ Throw\ a\ \# \ ps', xs)\ [\leftrightarrow]\ (stk, loc, pc, [a]) \rangle$
note $IH = \langle \bigwedge stk'\ loc'\ pc'\ xcp'\ STK. ?execs\ ps\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$
 $\implies ?concls\ ps\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp'$
note $exec = \langle ?exec\ (obj \cdot M'(ps))\ (stk\ @\ [v])\ STK\ loc\ (length\ (compE2\ obj) + pc)\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$
from $bisim\ have\ pc:\ pc < length\ (compEs2\ ps)\ loc = xs\ by(auto\ dest:\ bisims1-ThrowD)$
from $exec\ have\ exec-meth-d\ (compP2\ P)\ ((compE2\ obj\ @\ compEs2\ ps)\ @\ [Invoke\ M'\ (length\ ps)])$
 $(stack-xlift\ (length\ STK)\ (compxE2\ obj\ 0\ 0)\ @\ shift\ (length\ (compE2\ obj))\ (stack-xlift\ (length\ (v\ \# \ STK))\ (compxEs2\ ps\ 0\ 0)))\ t$
 $h\ (stk\ @\ v\ \# \ STK,\ loc,\ length\ (compE2\ obj) + pc,\ [a])\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$
by $(simp\ add:\ compxEs2-size-convs\ compxEs2-stack-xlift-convs)$
hence $exec': exec-meth-d\ (compP2\ P)\ (compE2\ obj\ @\ compEs2\ ps)\ (stack-xlift\ (length\ STK)\ (compxE2\ obj\ 0\ 0)\ @\ shift\ (length\ (compE2\ obj))\ (stack-xlift\ (length\ (v\ \# \ STK))\ (compxEs2\ ps\ 0\ 0)))\ t$
 $h\ (stk\ @\ v\ \# \ STK,\ loc,\ length\ (compE2\ obj) + pc,\ [a])\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$
by $(rule\ exec-meth-take)(simp\ add:\ pc)$
hence $?execs\ ps\ stk\ (v\ \# \ STK)\ loc\ pc\ [a]\ stk'\ loc'\ (pc' - length\ (compE2\ obj))\ xcp'$
by $(rule\ exec-meth-drop-xt)(auto\ simp\ add:\ stack-xlift-compxE2)$
from $IH[OF\ this]\ obtain\ stk''\ where\ stk':\ stk' = stk''\ @\ v\ \# \ STK$
and $exec'': exec-meth-d\ (compP2\ P)\ (compEs2\ ps)\ (compxEs2\ ps\ 0\ 0)\ t\ h\ (stk,\ loc,\ pc,\ [a])\ ta\ h'\ (stk'',\ loc',\ pc' - length\ (compE2\ obj),\ xcp')\ by\ auto$
from $exec''\ have\ exec-meth-d\ (compP2\ P)\ ((compE2\ obj\ @\ compEs2\ ps)\ @\ [Invoke\ M'\ (length\ ps)])$
 $(compxE2\ obj\ 0\ 0\ @\ shift\ (length\ (compE2\ obj))\ (stack-xlift\ (length\ [v])\ (compxEs2\ ps\ 0\ 0)))\ t$
 $h\ (stk\ @\ [v],\ loc,\ length\ (compE2\ obj) + pc,\ [a])\ ta\ h'\ (stk''\ @\ [v],\ loc',\ length\ (compE2\ obj) + (pc' - length\ (compE2\ obj)),\ xcp')$
apply –
apply $(rule\ exec-meth-append)$
apply $(rule\ append-exec-meth-xt)$
apply $(erule\ exec-meth-stk-offer)$
apply $auto$
done
moreover **from** $exec'\ have\ pc' \geq length\ (compE2\ obj)$
by $(rule\ exec-meth-drop-xt-pc)(auto\ simp\ add:\ stack-xlift-compxE2)$
ultimately **show** $?case\ using\ stk'\ by(auto\ simp\ add:\ compxEs2-size-convs\ compxEs2-stack-xlift-convs)$
next
case $bisim1CallThrow\ thus\ ?case$
by $(auto\ elim!:\ exec-meth.cases\ dest:\ match-ex-table-pcsD\ simp\ add:\ stack-xlift-compxEs2\ stack-xlift-compxE2)$
next
case $bisim1BlockSome1\ thus\ ?case$
by $(fastforce\ elim:\ exec-meth.cases\ intro:\ exec-meth.intros)$
next
case $bisim1BlockSome2\ thus\ ?case$
by $(fastforce\ elim:\ exec-meth.cases\ intro:\ exec-meth.intros)$
next
case $(bisim1BlockSome4\ e\ n\ e'\ xs\ stk\ loc\ pc\ xcp\ V\ T\ v)$
note $IH = \langle \bigwedge stk'\ loc'\ pc'\ xcp'\ STK. ?exec\ e\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$

$\implies ?concl\ e\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp'$
note $bisim = \langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
note $exec = \langle ?exec\ \{V:T=[v];\ e\}\ stk\ STK\ loc\ (Suc\ (Suc\ pc))\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$
hence $exec': exec\text{-meth-d}\ (compP2\ P)\ ([Push\ v,\ Store\ V]\ @\ compE2\ e)$
 $(shift\ (length\ [Push\ v,\ Store\ V])\ (stack\text{-xlift}\ (length\ STK)\ (compxE2\ e\ 0\ 0)))\ t\ h\ (stk\ @\ STK,\ loc,$
 $length\ [Push\ v,\ Store\ V]\ +\ pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$
by $(simp\ add:\ compxE2\text{-size-convs})$
hence $?exec\ e\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ (pc' - length\ [Push\ v,\ Store\ V])\ xcp'$
by $(rule\ exec\text{-meth-drop})\ auto$
from $IH[OF\ this]$ **obtain** stk'' **where** $stk': stk' = stk''\ @\ STK$
and $exec'': exec\text{-meth-d}\ (compP2\ P)\ (compE2\ e)\ (compxE2\ e\ 0\ 0)\ t\ h\ (stk,\ loc,\ pc,\ xcp)\ ta$
 $h'\ (stk'',\ loc',\ pc' - length\ [Push\ v,\ Store\ V],\ xcp')$ **by** $auto$
from $exec''$ **have** $exec\text{-meth-d}\ (compP2\ P)\ ([Push\ v,\ Store\ V]\ @\ compE2\ e)$
 $(shift\ (length\ [Push\ v,\ Store\ V])\ (compxE2\ e\ 0\ 0))\ t\ h\ (stk,\ loc,\ length\ [Push\ v,\ Store\ V]\ +\ pc,$
 $xcp)\ ta\ h'\ (stk'',\ loc',\ length\ [Push\ v,\ Store\ V]\ +\ (pc' - length\ [Push\ v,\ Store\ V]),\ xcp')$
by $(rule\ append\text{-exec-meth})\ auto$
moreover from $exec'$ **have** $pc' \geq length\ [Push\ v,\ Store\ V]$
by $(rule\ exec\text{-meth-drop-pc})(auto\ simp\ add:\ stack\text{-xlift-compxE2})$
hence $Suc\ (Suc\ (pc' - Suc\ (Suc\ 0))) = pc'$ **by** $(simp)$
ultimately show $?case\ using\ stk'$ **by** $(auto\ simp\ add:\ compxE2\text{-size-convs})$

next

case $(bisim1BlockThrowSome\ e\ n\ a\ xs\ stk\ loc\ pc\ V\ T\ v)$
note $IH = \langle \bigwedge\ stk'\ loc'\ pc'\ xcp'\ STK.\ ?exec\ e\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$
 $\implies ?concl\ e\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp'$
note $exec = \langle ?exec\ \{V:T=[v];\ e\}\ stk\ STK\ loc\ (Suc\ (Suc\ pc))\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$
hence $exec': exec\text{-meth-d}\ (compP2\ P)\ ([Push\ v,\ Store\ V]\ @\ compE2\ e)$
 $(shift\ (length\ [Push\ v,\ Store\ V])\ (stack\text{-xlift}\ (length\ STK)\ (compxE2\ e\ 0\ 0)))\ t\ h\ (stk\ @\ STK,\ loc,$
 $length\ [Push\ v,\ Store\ V]\ +\ pc,\ [a])\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$
by $(simp\ add:\ compxE2\text{-size-convs})$
hence $?exec\ e\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ (pc' - length\ [Push\ v,\ Store\ V])\ xcp'$
by $(rule\ exec\text{-meth-drop})\ auto$
from $IH[OF\ this]$ **obtain** stk'' **where** $stk': stk' = stk''\ @\ STK$
and $exec'': exec\text{-meth-d}\ (compP2\ P)\ (compE2\ e)\ (compxE2\ e\ 0\ 0)\ t\ h\ (stk,\ loc,\ pc,\ [a])\ ta$
 $h'\ (stk'',\ loc',\ pc' - length\ [Push\ v,\ Store\ V],\ xcp')$ **by** $auto$
from $exec''$ **have** $exec\text{-meth-d}\ (compP2\ P)\ ([Push\ v,\ Store\ V]\ @\ compE2\ e)$
 $(shift\ (length\ [Push\ v,\ Store\ V])\ (compxE2\ e\ 0\ 0))\ t\ h\ (stk,\ loc,\ length\ [Push\ v,\ Store\ V]\ +\ pc,$
 $[a])\ ta\ h'\ (stk'',\ loc',\ length\ [Push\ v,\ Store\ V]\ +\ (pc' - length\ [Push\ v,\ Store\ V]),\ xcp')$
by $(rule\ append\text{-exec-meth})\ auto$
moreover from $exec'$ **have** $pc' \geq length\ [Push\ v,\ Store\ V]$
by $(rule\ exec\text{-meth-drop-pc})(auto\ simp\ add:\ stack\text{-xlift-compxE2})$
hence $Suc\ (Suc\ (pc' - Suc\ (Suc\ 0))) = pc'$ **by** $(simp)$
ultimately show $?case\ using\ stk'$ **by** $(auto\ simp\ add:\ compxE2\text{-size-convs})$

next

case $bisim1BlockNone$ **thus** $?case$
by $(fastforce\ elim:\ exec\text{-meth.cases\ intro:\ exec\text{-meth.intros})$

next

case $bisim1BlockThrowNone$ **thus** $?case$
by $(fastforce\ elim:\ exec\text{-meth.cases\ intro:\ exec\text{-meth.intros})$

next

case $(bisim1Sync1\ e1\ n\ e1'\ xs\ stk\ loc\ pc\ xcp\ e2\ V)$
note $bisim = \langle P, e1, h \vdash (e1', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
note $IH = \langle \bigwedge\ stk'\ loc'\ pc'\ xcp'\ STK.\ ?exec\ e1\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$
 $\implies ?concl\ e1\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp'$
note $exec = \langle ?exec\ (sync_V\ (e1)\ e2)\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$

```

from bisim have pc:  $pc \leq \text{length}(\text{compE2 } e1)$  by(rule bisim1-pc-length-compE2)
show ?case
proof(cases pc < length (compE2 e1))
  case True
    from exec have exec-meth-d (compP2 P)
      (compE2 e1 @ (Dup # Store V # MEnter # compE2 e2 @ [Load V, MExit, Goto 4, Load V, MExit, ThrowExc]))
      (stack-xlift (length STK) (compxE2 e1 0 0) @ shift (length (compE2 e1)) (stack-xlift (length STK) (compxE2 e2 3 0) @
        stack-xlift (length STK) [(3, 3 + length (compE2 e2), None, 6 + length (compE2 e2), 0)])) t
      h (stk @ STK, loc, pc, xcp) ta h' (stk', loc', pc', xcp')
    by(simp add: shift-compxE2 stack-xlift-compxE2 ac-simps)
    hence ?exec e1 stk STK loc pc xcp stk' loc' pc' xcp'
    by(rule exec-meth-take-xt)(rule True)
    from IH[OF this] obtain stk'' where stk':  $stk' = stk'' @ STK$ 
      and exec': exec-meth-d (compP2 P) (compE2 e1) (compxE2 e1 0 0) t h (stk, loc, pc, xcp) ta h' (stk'', loc', pc', xcp')
    by blast
    from exec' have exec-meth-d (compP2 P) (compE2 e1 @ (Dup # Store V # MEnter # compE2 e2 @ [Load V, MExit, Goto 4, Load V, MExit, ThrowExc]))
      (compxE2 e1 0 0 @ shift (length (compE2 e1)) (compxE2 e2 3 0 @ [(3, 3 + length (compE2 e2), None, 6 + length (compE2 e2), 0)])) t
      h (stk, loc, pc, xcp) ta h' (stk'', loc', pc', xcp')
    by(rule exec-meth-append-xt)
    thus ?thesis using stk' by(simp add: shift-compxE2 stack-xlift-compxE2 ac-simps)
  next
    case False
      with pc have [simp]:  $pc = \text{length}(\text{compE2 } e1)$  by simp
      with bisim obtain v where stk = [v] xcp = None by(auto dest: bisim1-pc-length-compE2D)
      thus ?thesis using exec by(auto elim!: exec-meth.cases intro!: exec-meth.intros)
    qed
  next
    case bisim1Sync2 thus ?case
      by(fastforce elim!: exec-meth.cases intro!: exec-meth.intros)
  next
    case bisim1Sync3 thus ?case
      by(fastforce elim!: exec-meth.cases intro!: exec-meth.intros split: if-split-asm)
  next
    case (bisim1Sync4 e2 n e2' xs stk loc pc xcp e1 V a)
      note IH = ⟨∧stk' loc' pc' xcp' STK. ?exec e2 stk STK loc pc xcp stk' loc' pc' xcp' ⇒ ?concl e2 stk STK loc pc xcp stk' loc' pc' xcp'⟩
      note bisim = ⟨P, e2, h ⊢ (e2', xs) ↔ (stk, loc, pc, xcp)⟩
      note exec = ⟨?exec (syncV (e1) e2) stk STK loc (Suc (Suc (Suc (length (compE2 e1) + pc)))) xcp stk' loc' pc' xcp'⟩
      from bisim have pc:  $pc \leq \text{length}(\text{compE2 } e2)$  by(rule bisim1-pc-length-compE2)
      show ?case
      proof(cases pc < length (compE2 e2))
        case True
          let ?pre = compE2 e1 @ [Dup, Store V, MEnter]
          from exec have exec': exec-meth-d (compP2 P) (?pre @ compE2 e2 @ [Load V, MExit, Goto 4, Load V, MExit, ThrowExc])
            (stack-xlift (length STK) (compxE2 e1 0 0) @ shift (length ?pre) (stack-xlift (length STK) (compxE2 e2 0 0) @
              ((0, length (compE2 e2), None, 3 + length (compE2 e2), length STK)))) t

```



```

  h (stk @ STK, loc, length ?pre + pc, xcp) ta h' (stk', loc', pc', xcp')
  by(simp add: stack-xlift-compxE2 shift-compxE2 eval-nat-numeral ac-simps)
  hence exec'': exec-meth-d (compP2 P) (compE2 e2 @ [Load V, MExit, Goto 4, Load V, MExit,
ThrowExc])
  (stack-xlift (length STK) (compxE2 e2 0 0) @ [(0, length (compE2 e2), None, 3 + length (compE2
e2), length STK)]) t
  h (stk @ STK, loc, pc, xcp) ta h' (stk', loc', pc' - length ?pre, xcp')
  by(rule exec-meth-drop-xt[where n=1])(auto simp add: stack-xlift-compxE2)
  from exec' have pc': pc' ≥ length ?pre
  by(rule exec-meth-drop-xt-pc[where n'=1])(auto simp add: stack-xlift-compxE2)
  hence pc'': (Suc (Suc (Suc (pc' - Suc (Suc (Suc 0))))) = pc' by simp
  show ?thesis
  proof(cases xcp)
  case None
  from exec'' None True
  have ?exec e2 stk STK loc pc xcp stk' loc' (pc' - length ?pre) xcp'
  apply -
  apply (erule exec-meth.cases)
  apply (cases compE2 e2 ! pc)
  apply (fastforce simp add: is-Ref-def intro: exec-meth.intros split: if-split-asm
cong del: image-cong-simp)+
  done
  from IH[OF this] obtain stk'' where stk: stk' = stk'' @ STK
  and exec''': exec-meth-d (compP2 P) (compE2 e2) (compxE2 e2 0 0) t h (stk, loc, pc, xcp)
  ta h' (stk'', loc', pc' - length ?pre, xcp') by blast
  from exec''' have exec-meth-d (compP2 P) (compE2 e2 @ [Load V, MExit, Goto 4, Load V,
MExit, ThrowExc])
  (compxE2 e2 0 0 @ [(0, length (compE2 e2), None, 3 + length (compE2 e2), 0)]) t
  h (stk, loc, pc, xcp) ta h' (stk'', loc', pc' - length ?pre, xcp')
  by(rule exec-meth-append-xt)
  hence exec-meth-d (compP2 P) (?pre @ compE2 e2 @ [Load V, MExit, Goto 4, Load V, MExit,
ThrowExc])
  (compxE2 e1 0 0 @ shift (length ?pre) (compxE2 e2 0 0 @ [(0, length (compE2 e2), None, 3 +
length (compE2 e2), 0)])) t
  h (stk, loc, length ?pre + pc, xcp) ta h' (stk'', loc', length ?pre + (pc' - length ?pre), xcp')
  by(rule append-exec-meth-xt[where n=1]) auto
  thus ?thesis using stk pc' pc'' by(simp add: eval-nat-numeral shift-compxE2 ac-simps)
  next
  case (Some a)
  with exec'' have [simp]: h' = h xcp' = None loc' = loc ta = ε
  by(auto elim!: exec-meth.cases simp add: match-ex-table-append
split: if-split-asm dest!: match-ex-table-stack-xliftD)
  show ?thesis
  proof(cases match-ex-table (compP2 P) (cname-of h a) pc (compxE2 e2 0 0))
  case None
  with Some exec'' True have [simp]: stk' = Addr a # STK
  and pc': pc' = length (compE2 e1) + length (compE2 e2) + 6
  by(auto elim!: exec-meth.cases simp add: match-ex-table-append
split: if-split-asm dest!: match-ex-table-stack-xliftD)
  with exec'' Some None
  have exec-meth-d (compP2 P) (compE2 e2 @ [Load V, MExit, Goto 4, Load V, MExit,
ThrowExc])
  (compxE2 e2 0 0 @ [(0, length (compE2 e2), None, 3 + length (compE2 e2), 0)]) t
  h (stk, loc, pc, [a]) ε h (Addr a # drop (length stk - 0) stk, loc, pc' - length ?pre, None)

```

```

by  $-(rule\ exec\ catch, auto\ elim!: exec\ meth.\ cases\ simp\ add: match\ ex\ table\ append\ matches\ ex\ entry\ def\ split: if\ split\ asm\ dest!: match\ ex\ table\ stack\ xliftD)$ 
hence  $exec\ meth\ d\ (compP2\ P)\ (?pre\ @\ compE2\ e2\ @\ [Load\ V, MExit, Goto\ 4, Load\ V, MExit, ThrowExc])$ 
 $(compxE2\ e1\ 0\ 0\ @\ shift\ (length\ ?pre)\ (compxE2\ e2\ 0\ 0\ @\ [(0, length\ (compE2\ e2), None, 3 + length\ (compE2\ e2), 0)]))\ t$ 
 $h\ (stk, loc, length\ ?pre + pc, [a]) \varepsilon h\ (Addr\ a\ \# \ drop\ (length\ stk - 0)\ stk, loc, length\ ?pre + (pc' - length\ ?pre), None)$ 
by  $(rule\ append\ exec\ meth\ xt[where\ n=1])\ auto$ 
with  $pc'$  show  $?thesis$  by  $(simp\ add: eval\ nat\ numeral\ shift\ compxE2\ ac\ simps)$ 
next
case  $(Some\ pcd)$ 
with  $\langle xcp = [a] \rangle\ exec''\ True$ 
have  $exec\ meth\ d\ (compP2\ P)\ (compE2\ e2)\ (compxE2\ e2\ 0\ 0)\ t$ 
 $h\ (stk, loc, pc, [a]) \varepsilon h\ (Addr\ a\ \# \ drop\ (length\ stk - snd\ pcd)\ stk, loc, pc' - length\ ?pre, None)$ 
apply  $-$ 
apply  $(rule\ exec\ catch)$ 
apply  $(auto\ elim!: exec\ meth.\ cases\ simp\ add: match\ ex\ table\ append\ split: if\ split\ asm\ dest!: match\ ex\ table\ stack\ xliftD)$ 
done
hence  $exec\ meth\ d\ (compP2\ P)\ (compE2\ e2\ @\ [Load\ V, MExit, Goto\ 4, Load\ V, MExit, ThrowExc])\ (compxE2\ e2\ 0\ 0\ @\ [(0, length\ (compE2\ e2), None, 3 + length\ (compE2\ e2), 0)])\ t$ 
 $h\ (stk, loc, pc, [a]) \varepsilon h\ (Addr\ a\ \# \ drop\ (length\ stk - snd\ pcd)\ stk, loc, pc' - length\ ?pre, None)$ 
by  $(rule\ exec\ meth\ append\ xt)$ 
hence  $exec\ meth\ d\ (compP2\ P)\ (?pre\ @\ compE2\ e2\ @\ [Load\ V, MExit, Goto\ 4, Load\ V, MExit, ThrowExc])$ 
 $(compxE2\ e1\ 0\ 0\ @\ shift\ (length\ ?pre)\ (compxE2\ e2\ 0\ 0\ @\ [(0, length\ (compE2\ e2), None, 3 + length\ (compE2\ e2), 0)]))\ t$ 
 $h\ (stk, loc, length\ ?pre + pc, [a]) \varepsilon h\ (Addr\ a\ \# \ drop\ (length\ stk - snd\ pcd)\ stk, loc, length\ ?pre + (pc' - length\ ?pre), None)$ 
by  $(rule\ append\ exec\ meth\ xt[where\ n=1])(auto)$ 
moreover from  $Some\ \langle xcp = [a] \rangle\ exec''\ True\ pc'$ 
have  $pc' = length\ (compE2\ e1) + 3 + fst\ pcd\ stk' = Addr\ a\ \# \ drop\ (length\ stk - snd\ pcd)\ stk$ 
 $@\ STK$ 
by  $(auto\ elim!: exec\ meth.\ cases\ dest!: match\ ex\ table\ stack\ xliftD\ simp: match\ ex\ table\ append\ split: if\ split\ asm)$ 
ultimately show  $?thesis$  using  $\langle xcp = [a] \rangle$  by  $(auto\ simp\ add: eval\ nat\ numeral\ shift\ compxE2\ ac\ simps)$ 
qed
qed
next
case  $False$ 
with  $pc$  have  $[simp]: pc = length\ (compE2\ e2)$  by  $simp$ 
with  $exec$  show  $?thesis$ 
by  $(auto\ elim!: exec\ meth.\ cases\ intro!: exec\ meth.\ intros\ split: if\ split\ asm\ simp\ add: match\ ex\ table\ append\ not\ p\ eval\ nat\ numeral)(simp\ all\ add: matches\ ex\ entry\ def)$ 
qed
next
case  $bisim1Sync5$  thus  $?case$ 
by  $(fastforce\ elim: exec\ meth.\ cases\ intro: exec\ meth.\ intros\ split: if\ split\ asm)$ 
next
case  $bisim1Sync6$  thus  $?case$ 
by  $(fastforce\ elim: exec\ meth.\ cases\ intro: exec\ meth.\ intros\ split: if\ split\ asm)$ 

```

```

next
  case bisim1Sync7 thus ?case
    by(fastforce elim: exec-meth.cases intro: exec-meth.intros split: if-split-asm)
next
  case bisim1Sync8 thus ?case
    by(fastforce elim: exec-meth.cases intro: exec-meth.intros split: if-split-asm)
next
  case (bisim1Sync9 e1 n e2 V a xs)
    note exec = ⟨?exec (syncV (e1) e2) [Addr a] STK xs (8 + length (compE2 e1) + length (compE2 e2)) None stk' loc' pc' xcp'⟩
    let ?pre = compE2 e1 @ Dup # Store V # MEnter # compE2 e2 @ [Load V, MExit, Goto 4, Load V, MExit]
    from exec have exec': exec-meth-d (compP2 P) (?pre @ [ThrowExc]) (stack-xlift (length STK) (compxE2 (syncV (e1) e2) 0 0) @ shift (length ?pre) []) t h (Addr a # STK, xs, length ?pre + 0, None) ta h' (stk', loc', pc', xcp')
    by(simp add: eval-nat-numeral)
    hence exec-meth-d (compP2 P) [ThrowExc] [] t h (Addr a # STK, xs, 0, None) ta h' (stk', loc', pc' - length ?pre, xcp')
    by(rule exec-meth-drop-xt)(auto simp add: stack-xlift-compxE2)
    moreover from exec' have pc' = 8 + length (compE2 e1) + length (compE2 e2) stk' = Addr a # STK
    by(auto elim!: exec-meth.cases)
    ultimately show ?case by(fastforce elim!: exec-meth.cases intro: exec-meth.intros)
next
  case (bisim1Sync10 e1 n e2 V a xs)
    note exec = ⟨?exec (syncV (e1) e2) [Addr a] STK xs (8 + length (compE2 e1) + length (compE2 e2)) [a] stk' loc' pc' xcp'⟩
    hence match-ex-table (compP2 P) (cname-of h a) (8 + length (compE2 e1) + length (compE2 e2)) (stack-xlift (length STK) (compxE2 (syncV (e1) e2) 0 0)) ≠ None
    by(rule exec-meth.cases) auto
    hence False by(auto split: if-split-asm simp add: match-ex-table-append-not-pcs)(simp add: matches-ex-entry-def)
    thus ?case ..
next
  case (bisim1Sync11 e1 n e2 V xs)
    note exec = ⟨?exec (syncV (e1) e2) [Null] STK xs (Suc (Suc (length (compE2 e1)))) [addr-of-sys-xcpt NullPointer] stk' loc' pc' xcp'⟩
    hence match-ex-table (compP2 P) (cname-of h (addr-of-sys-xcpt NullPointer)) (2 + length (compE2 e1)) (stack-xlift (length STK) (compxE2 (syncV (e1) e2) 0 0)) ≠ None
    by(rule exec-meth.cases)(auto split: if-split-asm)
    hence False by(auto split: if-split-asm simp add: match-ex-table-append-not-pcs)(simp add: matches-ex-entry-def)
    thus ?case ..
next
  case (bisim1SyncThrow e1 n a xs stk loc pc e2 V)
    note exec = ⟨?exec (syncV (e1) e2) stk STK loc pc [a] stk' loc' pc' xcp'⟩
    note IH = ⟨∧stk' loc' pc' xcp' STK. ?exec e1 stk STK loc pc [a] stk' loc' pc' xcp'
      ⇒ ?concl e1 stk STK loc pc [a] stk' loc' pc' xcp'⟩
    note bisim = ⟨P, e1, h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
    from bisim have pc: pc < length (compE2 e1)
    and [simp]: loc = xs by(auto dest: bisim1-ThrowD)
    from exec have exec-meth-d (compP2 P) (compE2 e1 @ Dup # Store V # MEnter # compE2 e2 @ [Load V, MExit, Goto 4, Load V, MExit, ThrowExc])
      (stack-xlift (length STK) (compxE2 e1 0 0) @ shift (length (compE2 e1)) (stack-xlift (length STK) (compxE2 e2 3 0) @
        [(3, 3 + length (compE2 e2), None, 6 + length (compE2 e2), length STK)])) t

```

$h (stk @ STK, loc, pc, [a]) \text{ ta } h' (stk', loc', pc', xcp')$
by(simp add: shift-compxE2 stack-xlift-compxE2 ac-simps)
hence $?exec\ e1\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp'$
by(rule exec-meth-take-xt)(rule pc)
from IH[OF this] **show** $?case$ **by** auto
next
case (bisim1Seq1 e1 n e1' xs stk loc pc xcp e2)
note bisim1 = $\langle P, e1, h \vdash (e1', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
note IH = $\langle \bigwedge stk' loc' pc' xcp' STK. ?exec\ e1\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \Rightarrow ?concl\ e1\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$
note exec = $\langle ?exec\ (e1;;e2)\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$
from bisim1 **have** pc: $pc \leq length\ (compE2\ e1)$ **by**(rule bisim1-pc-length-compE2)
show $?case$
proof(cases pc < length (compE2 e1))
case True
from exec **have** exec-meth-d (compP2 P) (compE2 e1 @ Pop # compE2 e2) (stack-xlift (length STK) (compxE2 e1 0 0) @
shift (length (compE2 e1)) (stack-xlift (length STK) (compxE2 e2 (Suc 0) 0))) t
 $h (stk @ STK, loc, pc, xcp) \text{ ta } h' (stk', loc', pc', xcp')$
by(simp add: shift-compxE2 stack-xlift-compxE2)
hence $?exec\ e1\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp'$
by(rule exec-meth-take-xt)(rule True)
from IH[OF this] **show** $?thesis$ **by** auto
next
case False
with pc **have** [simp]: $pc = length\ (compE2\ e1)$ **by** simp
with bisim1 **obtain** v **where** xcp = None stk = [v] **by**(auto dest: bisim1-pc-length-compE2D)
with exec **show** $?thesis$ **by**(fastforce elim: exec-meth.cases intro: exec-meth.intros)
qed
next
case (bisim1SeqThrow1 e1 n a xs stk loc pc e2)
note bisim1 = $\langle P, e1, h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$
note IH = $\langle \bigwedge stk' loc' pc' xcp' STK. ?exec\ e1\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \Rightarrow ?concl\ e1\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$
note exec = $\langle ?exec\ (e1;;e2)\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$
from bisim1 **have** pc: $pc < length\ (compE2\ e1)$ **by**(auto dest: bisim1-ThrowD)
from exec **have** exec-meth-d (compP2 P) (compE2 e1 @ Pop # compE2 e2) (stack-xlift (length STK) (compxE2 e1 0 0) @
shift (length (compE2 e1)) (stack-xlift (length STK) (compxE2 e2 (Suc 0) 0))) t
 $h (stk @ STK, loc, pc, [a]) \text{ ta } h' (stk', loc', pc', xcp')$
by(simp add: shift-compxE2 stack-xlift-compxE2)
hence $?exec\ e1\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp'$
by(rule exec-meth-take-xt)(rule pc)
from IH[OF this] **show** $?case$ **by**(fastforce elim: exec-meth.cases intro: exec-meth.intros)
next
case (bisim1Seq2 e2 n e2' xs stk loc pc xcp e1)
note bisim2 = $\langle P, e2, h \vdash (e2', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
note IH = $\langle \bigwedge stk' loc' pc' xcp' STK. ?exec\ e2\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \Rightarrow ?concl\ e2\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$
note exec = $\langle ?exec\ (e1;;e2)\ stk\ STK\ loc\ (Suc\ (length\ (compE2\ e1) + pc))\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$
from bisim2 **have** pc: $pc \leq length\ (compE2\ e2)$ **by**(rule bisim1-pc-length-compE2)
show $?case$
proof(cases pc < length (compE2 e2))
case False

```

with pc have [simp]: pc = length (compE2 e2) by simp
from bisim2 have xcp = None by(auto dest: bisim1-pc-length-compE2D)
with exec have False by(auto elim: exec-meth.cases)
thus ?thesis ..
next
case True
from exec have exec':
  exec-meth-d (compP2 P) ((compE2 e1 @ [Pop]) @ compE2 e2) (stack-xlift (length STK) (compxE2
e1 0 0) @
  shift (length (compE2 e1 @ [Pop])) (stack-xlift (length STK) (compxE2 e2 0 0))) t
  h (stk @ STK, loc, length ((compE2 e1) @ [Pop]) + pc, xcp) ta h' (stk', loc', pc', xcp')
  by(simp add: compxE2-size-convs)
hence ?exec e2 stk STK loc pc xcp stk' loc' (pc' - length ((compE2 e1) @ [Pop])) xcp'
  by(rule exec-meth-drop-xt)(auto simp add: stack-xlift-compxE2)
from IH[OF this] obtain stk'' where stk': stk' = stk'' @ STK
  and exec'': exec-meth-d (compP2 P) (compE2 e2) (compxE2 e2 0 0) t h (stk, loc, pc, xcp)
  ta h' (stk'', loc', pc' - length (compE2 e1 @ [Pop]), xcp') by auto
from exec'' have exec-meth-d (compP2 P) ((compE2 e1 @ [Pop]) @ compE2 e2) (compxE2 e1 0
0 @ shift (length (compE2 e1 @ [Pop])) (compxE2 e2 0 0)) t
  h (stk, loc, length ((compE2 e1) @ [Pop]) + pc, xcp) ta h' (stk'', loc', length ((compE2 e1) @
[Pop]) + (pc' - length ((compE2 e1) @ [Pop])), xcp')
  by(rule append-exec-meth-xt) auto
moreover from exec' have pc' ≥ length ((compE2 e1) @ [Pop])
  by(rule exec-meth-drop-xt-pc)(auto simp add: stack-xlift-compxE2)
ultimately show ?thesis using stk' by(auto simp add: shift-compxE2 stack-xlift-compxE2)
qed
next
case (bisim1Cond1 e n e' xs stk loc pc xcp e1 e2)
note bisim = ⟨P, e, h ⊢ (e', xs) ↔ (stk, loc, pc, xcp)⟩
note IH = ⟨∧stk' loc' pc' xcp' STK. ?exec e stk STK loc pc xcp stk' loc' pc' xcp'
  ⇒ ?concl e stk STK loc pc xcp stk' loc' pc' xcp'⟩
note exec = ⟨?exec (if (e) e1 else e2) stk STK loc pc xcp stk' loc' pc' xcp'⟩
from bisim have pc: pc ≤ length (compE2 e) by(rule bisim1-pc-length-compE2)
show ?case
proof(cases pc < length (compE2 e))
  case True
  from exec have exec-meth-d (compP2 P) (compE2 e @ IfFalse (2 + int (length (compE2 e1))) #
compE2 e1 @ Goto (1 + int (length (compE2 e2))) # compE2 e2)
    (stack-xlift (length STK) (compxE2 e 0 0) @ shift (length (compE2 e)) (stack-xlift (length STK)
(compxE2 e1 (Suc 0) 0) @
    stack-xlift (length STK) (compxE2 e2 (Suc (Suc (length (compE2 e1)))) 0))) t
    h (stk @ STK, loc, pc, xcp) ta h' (stk', loc', pc', xcp')
    by(simp add: stack-xlift-compxE2 shift-compxE2 ac-simps)
  hence ?exec e stk STK loc pc xcp stk' loc' pc' xcp'
    by(rule exec-meth-take-xt)(rule True)
  from IH[OF this] show ?thesis by auto
  next
  case False
  with pc have [simp]: pc = length (compE2 e) by simp
  from bisim obtain v where stk = [v] xcp = None
    by(auto dest: bisim1-pc-length-compE2D)
  with exec show ?thesis by(auto elim!: exec-meth.cases intro!: exec-meth.intros)
qed
next

```

```

case (bisim1CondThen e1 n e1' xs stk loc pc xcp e e2)
note bisim = ⟨P, e1, h ⊢ (e1', xs) ↔ (stk, loc, pc, xcp)⟩
note IH = ⟨∧stk' loc' pc' xcp' STK. ?exec e1 stk STK loc pc xcp stk' loc' pc' xcp'
  ⇒ ?concl e1 stk STK loc pc xcp stk' loc' pc' xcp'⟩
note exec = ⟨?exec (if (e) e1 else e2) stk STK loc (Suc (length (compE2 e) + pc)) xcp stk' loc' pc'
  xcp'⟩
from bisim have pc: pc ≤ length (compE2 e1) by(rule bisim1-pc-length-compE2)
show ?case
proof(cases pc < length (compE2 e1))
  case True
    let ?pre = compE2 e @ [IfFalse (2 + int (length (compE2 e1)))]
    from exec have exec': exec-meth-d (compP2 P) (?pre @ compE2 e1 @ Goto (1 + int (length
  (compE2 e2))) # compE2 e2)
    (stack-xlift (length STK) (compxE2 e 0 0) @ shift (length ?pre) (stack-xlift (length STK) (compxE2
  e1 0 0) @
      shift (length (compE2 e1)) (stack-xlift (length STK) (compxE2 e2 (Suc 0) 0)))) t
    h (stk @ STK, loc, length ?pre + pc, xcp) ta h' (stk', loc', pc', xcp')
    by(simp add: stack-xlift-compxE2 shift-compxE2 ac-simps)
    hence exec-meth-d (compP2 P) (compE2 e1 @ Goto (1 + int (length (compE2 e2))) # compE2
  e2)
    (stack-xlift (length STK) (compxE2 e1 0 0) @ shift (length (compE2 e1)) (stack-xlift (length
  STK) (compxE2 e2 (Suc 0) 0))) t
    h (stk @ STK, loc, pc, xcp) ta h' (stk', loc', pc' - length ?pre, xcp')
    by(rule exec-meth-drop-xt)(auto simp add: stack-xlift-compxE2)
    hence ?exec e1 stk STK loc pc xcp stk' loc' (pc' - length ?pre) xcp'
    by(rule exec-meth-take-xt)(rule True)
    from IH[OF this] obtain stk'' where stk': stk' = stk'' @ STK
    and exec'': exec-meth-d (compP2 P) (compE2 e1) (compxE2 e1 0 0) t h (stk, loc, pc, xcp)
    ta h' (stk'', loc', pc' - length ?pre, xcp') by blast
    from exec'' have exec-meth-d (compP2 P) (compE2 e1 @ Goto (1 + int (length (compE2 e2)))
  # compE2 e2)
    (compxE2 e1 0 0 @ shift (length (compE2 e1)) (compxE2 e2 (Suc 0) 0)) t
    h (stk, loc, pc, xcp) ta h' (stk'', loc', pc' - length ?pre, xcp')
    by(rule exec-meth-append-xt)
    hence exec-meth-d (compP2 P) (?pre @ compE2 e1 @ Goto (1 + int (length (compE2 e2))) #
  compE2 e2)
    (compxE2 e 0 0 @ shift (length ?pre) (compxE2 e1 0 0 @ shift (length (compE2 e1)) (compxE2
  e2 (Suc 0) 0))) t
    h (stk, loc, length ?pre + pc, xcp) ta h' (stk'', loc', length ?pre + (pc' - length ?pre), xcp')
    by(rule append-exec-meth-xt)(auto)
    moreover from exec' have pc' ≥ length ?pre
    by(rule exec-meth-drop-xt-pc)(auto simp add: stack-xlift-compxE2)
    ultimately show ?thesis using stk'
    by(auto simp add: shift-compxE2 stack-xlift-compxE2 ac-simps)
  next
    case False
    with pc have [simp]: pc = length (compE2 e1) by simp
    from bisim obtain v where stk = [v] xcp = None
    by(auto dest: bisim1-pc-length-compE2D)
    with exec show ?thesis by(auto elim!: exec-meth.cases intro!: exec-meth.intros)
  qed
next
  case (bisim1CondElse e2 n e2' xs stk loc pc xcp e e1)
  note IH = ⟨∧stk' loc' pc' xcp' STK. ?exec e2 stk STK loc pc xcp stk' loc' pc' xcp'

```

$\implies ?concl\ e2\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp'$
note $exec = \langle ?exec\ (if\ (e)\ e1\ else\ e2)\ stk\ STK\ loc\ (Suc\ (Suc\ (length\ (compE2\ e) + length\ (compE2\ e1) + pc)))\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$
note $bisim = \langle P, e2, h \vdash (e2', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
from $bisim\ have\ pc: pc \leq length\ (compE2\ e2)\ by\ (rule\ bisim1-pc-length-compE2)$

let $?pre = compE2\ e\ @\ IfFalse\ (2 + int\ (length\ (compE2\ e1)))\ \#\ compE2\ e1\ @\ [Goto\ (1 + int\ (length\ (compE2\ e2)))]$
from $exec\ have\ exec': exec-meth-d\ (compP2\ P)\ (?pre\ @\ compE2\ e2)$
 $(stack-xlift\ (length\ STK)\ (compxE2\ e\ 0\ 0\ @\ compxE2\ e1\ (Suc\ (length\ (compE2\ e))))\ 0)\ @$
 $shift\ (length\ ?pre)\ (stack-xlift\ (length\ STK)\ (compxE2\ e2\ 0\ 0))\ t$
 $h\ (stk\ @\ STK, loc, length\ ?pre + pc, xcp)\ ta\ h'\ (stk', loc', pc', xcp')$
by $(simp\ add: stack-xlift-compxE2\ shift-compxE2\ ac-simps)$
hence $?exec\ e2\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ (pc' - length\ ?pre)\ xcp'$
by $(rule\ exec-meth-drop-xt)(auto\ simp\ add: stack-xlift-compxE2\ shift-compxE2\ ac-simps)$
from $IH[OF\ this]\ obtain\ stk''\ where\ stk': stk' = stk''\ @\ STK$
and $exec'': exec-meth-d\ (compP2\ P)\ (compE2\ e2)\ (compxE2\ e2\ 0\ 0)\ t\ h\ (stk, loc, pc, xcp)$
 $ta\ h'\ (stk'', loc', pc' - length\ ?pre, xcp')\ by\ blast$
from $exec''\ have\ exec-meth-d\ (compP2\ P)\ (?pre\ @\ compE2\ e2)$
 $((compxE2\ e\ 0\ 0\ @\ compxE2\ e1\ (Suc\ (length\ (compE2\ e))))\ 0)\ @\ shift\ (length\ ?pre)\ (compxE2\ e2\ 0\ 0)\ t$
 $h\ (stk, loc, length\ ?pre + pc, xcp)\ ta\ h'\ (stk'', loc', length\ ?pre + (pc' - length\ ?pre), xcp')$
by $(rule\ append-exec-meth-xt)(auto)$
moreover **from** $exec'\ have\ pc' \geq length\ ?pre$
by $(rule\ exec-meth-drop-xt-pc)(auto\ simp\ add: stack-xlift-compxE2)$
moreover **hence** $(Suc\ (Suc\ (pc' - Suc\ (Suc\ 0)))) = pc'\ by\ simp$
ultimately **show** $?case\ using\ stk'$
by $(auto\ simp\ add: shift-compxE2\ stack-xlift-compxE2\ ac-simps\ eval-nat-numeral)$

next
case $(bisim1CondThrow\ e\ n\ a\ xs\ stk\ loc\ pc\ e1\ e2)$
note $bisim = \langle P, e, h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$
note $IH = \langle \bigwedge\ stk'\ loc'\ pc'\ xcp'\ STK. ?exec\ e\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \implies ?concl\ e\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$
note $exec = \langle ?exec\ (if\ (e)\ e1\ else\ e2)\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$
from $bisim\ have\ pc: pc < length\ (compE2\ e)\ by\ (auto\ dest: bisim1-ThrowD)$
from $exec\ have\ exec-meth-d\ (compP2\ P)\ (compE2\ e\ @\ IfFalse\ (2 + int\ (length\ (compE2\ e1)))\ \#\ compE2\ e1\ @\ Goto\ (1 + int\ (length\ (compE2\ e2)))\ \#\ compE2\ e2)$
 $(stack-xlift\ (length\ STK)\ (compxE2\ e\ 0\ 0)\ @\ shift\ (length\ (compE2\ e))\ (stack-xlift\ (length\ STK)\ (compxE2\ e1\ (Suc\ 0)\ 0)\ @$
 $stack-xlift\ (length\ STK)\ (compxE2\ e2\ (Suc\ (Suc\ (length\ (compE2\ e1))))\ 0)))\ t$
 $h\ (stk\ @\ STK, loc, pc, [a])\ ta\ h'\ (stk', loc', pc', xcp')$
by $(simp\ add: stack-xlift-compxE2\ shift-compxE2\ ac-simps)$
hence $?exec\ e\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp'$
by $(rule\ exec-meth-take-xt)(rule\ pc)$
from $IH[OF\ this]\ show\ ?case\ by\ auto$

next
case $(bisim1While1\ c\ n\ e\ xs)$
note $IH = \langle \bigwedge\ stk'\ loc'\ pc'\ xcp'\ STK. ?exec\ c\ []\ STK\ xs\ 0\ None\ stk'\ loc'\ pc'\ xcp' \implies ?concl\ c\ []\ STK\ xs\ 0\ None\ stk'\ loc'\ pc'\ xcp' \rangle$
note $exec = \langle ?exec\ (while\ (c)\ e)\ []\ STK\ xs\ 0\ None\ stk'\ loc'\ pc'\ xcp' \rangle$
hence $exec-meth-d\ (compP2\ P)\ (compE2\ c\ @\ IfFalse\ (int\ (length\ (compE2\ e)) + 3)\ \#\ compE2\ e\ @\ [Pop, Goto\ (-2 + (-int\ (length\ (compE2\ e)) - int\ (length\ (compE2\ c))))], Push\ Unit)$
 $(stack-xlift\ (length\ STK)\ (compxE2\ c\ 0\ 0)\ @\ shift\ (length\ (compE2\ c))\ (stack-xlift\ (length\ STK)\ (compxE2\ e\ (Suc\ 0)\ 0)))\ t$

$h (\ [] @ STK, xs, 0, None) ta h' (stk', loc', pc', xcp')$
by(simp add: compxE2-size-convs)
hence $?exec\ c\ []\ STK\ xs\ 0\ None\ stk'\ loc'\ pc'\ xcp'$
by(rule exec-meth-take-xt) simp
from IH[OF this] **show** $?case$ **by** auto
next
case (bisim1While3 c n c' xs stk loc pc xcp e)
note bisim = $\langle P, c, h \vdash (c', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
note IH = $\langle \bigwedge stk' loc' pc' xcp' STK. ?exec\ c\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \Rightarrow ?concl\ c\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$
note exec = $\langle ?exec\ (while\ (c)\ e)\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$
from bisim **have** pc: $pc \leq length\ (compE2\ c)$ **by**(rule bisim1-pc-length-compE2)
show $?case$
proof(cases pc < length (compE2 c))
case True
from exec **have** exec-meth-d (compP2 P) (compE2 c @ IfFalse (int (length (compE2 e)) + 3) # compE2 e @ [Pop, Goto (-2 + (- int (length (compE2 e)) - int (length (compE2 c))), Push Unit]) (stack-xlift (length STK) (compxE2 c 0 0) @ shift (length (compE2 c)) (stack-xlift (length STK) (compxE2 e (Suc 0) 0))) t
 $h (stk @ STK, loc, pc, xcp) ta h' (stk', loc', pc', xcp')$
by(simp add: compxE2-size-convs)
hence $?exec\ c\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp'$
by(rule exec-meth-take-xt)(rule True)
from IH[OF this] **show** $?thesis$ **by** auto
next
case False
with pc **have** [simp]: $pc = length\ (compE2\ c)$ **by** simp
from bisim **obtain** v **where** $stk = [v]\ xcp = None$ **by**(auto dest: bisim1-pc-length-compE2D)
with exec **show** $?thesis$ **by**(auto elim!: exec-meth.cases intro!: exec-meth.intros)
qed
next
case (bisim1While4 e n e' xs stk loc pc xcp c)
note bisim = $\langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
note IH = $\langle \bigwedge stk' loc' pc' xcp' STK. ?exec\ e\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \Rightarrow ?concl\ e\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$
note exec = $\langle ?exec\ (while\ (c)\ e)\ stk\ STK\ loc\ (Suc\ (length\ (compE2\ c) + pc))\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$
from bisim **have** pc: $pc \leq length\ (compE2\ e)$ **by**(rule bisim1-pc-length-compE2)
show $?case$
proof(cases pc < length (compE2 e))
case True
let $?pre = compE2\ c\ @\ [IfFalse\ (int\ (length\ (compE2\ e)) + 3)]$
from exec **have** exec-meth-d (compP2 P) ((?pre @ compE2 e) @ [Pop, Goto (-2 + (- int (length (compE2 e)) - int (length (compE2 c))), Push Unit]) (stack-xlift (length STK) (compxE2 c 0 0) @ shift (length ?pre) (stack-xlift (length STK) (compxE2 e 0 0))) t
 $h (stk @ STK, loc, length ?pre + pc, xcp) ta h' (stk', loc', pc', xcp')$
by(simp add: compxE2-size-convs)
hence $exec': exec-meth-d\ (compP2\ P)\ (?pre\ @\ compE2\ e)\ (stack-xlift\ (length\ STK)\ (compxE2\ c\ 0)\ @\ shift\ (length\ ?pre)\ (stack-xlift\ (length\ STK)\ (compxE2\ e\ 0\ 0)))\ t$
 $h (stk @ STK, loc, length ?pre + pc, xcp) ta h' (stk', loc', pc', xcp')$
by(rule exec-meth-take)(auto intro: True)
hence $?exec\ e\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ (pc' - length\ ?pre)\ xcp'$
by(rule exec-meth-drop-xt)(auto simp add: stack-xlift-compxE2)
from IH[OF this] **obtain** stk'' **where** $stk': stk' = stk'' @ STK$

and $exec''$: $exec\text{-meth-d}$ ($compP2$ P) ($compE2$ e) ($compxE2$ e 0 0) t h (stk , loc , pc , xcp) ta h' (stk'' , loc' , $pc' - length$ $?pre$, xcp') **by** *auto*
from $exec''$ **have** $exec\text{-meth-d}$ ($compP2$ P) ($?pre$ @ $compE2$ e) ($compxE2$ c 0 0 @ $shift$ ($length$ $?pre$) ($compxE2$ e 0 0)) t
 h (stk , loc , $length$ $?pre + pc$, xcp) ta h' (stk'' , loc' , $length$ $?pre + (pc' - length$ $?pre)$, xcp')
by(*rule* $append\text{-exec-meth-xt}$) *auto*
hence $exec\text{-meth-d}$ ($compP2$ P) (($?pre$ @ $compE2$ e) @ [Pop , $Goto$ ($-2 + (- int$ ($length$ ($compE2$ e)) - int ($length$ ($compE2$ c)))), $Push$ $Unit$])
($compxE2$ c 0 0 @ $shift$ ($length$ $?pre$) ($compxE2$ e 0 0)) t
 h (stk , loc , $length$ $?pre + pc$, xcp) ta h' (stk'' , loc' , $length$ $?pre + (pc' - length$ $?pre)$, xcp')
by(*rule* $exec\text{-meth-append}$)
moreover from $exec''$ **have** $pc' \geq length$ $?pre$
by(*rule* $exec\text{-meth-drop-xt-pc}$)(*auto simp add: stack-xlift-compxE2*)
moreover have $-2 + (- int$ ($length$ ($compE2$ e)) - int ($length$ ($compE2$ c))) = $- int$ ($length$ ($compE2$ c)) + ($-2 - int$ ($length$ ($compE2$ e))) **by** *simp*
ultimately show $?thesis$ **using** stk'
by(*auto simp add: shift-compxE2 stack-xlift-compxE2 algebra-simps uminus-minus-left-commute*)
next
case *False*
with pc **have** [$simp$]: $pc = length$ ($compE2$ e) **by** *simp*
from *bisim* **obtain** v **where** $stk = [v]$ $xcp = None$ **by**(*auto dest: bisim1-pc-length-compE2D*)
with $exec$ **show** $?thesis$ **by**(*auto elim!: exec-meth.cases intro!: exec-meth.intros*)
qed
next
case ($bisim1While6$ c n e xs)
note $exec = \langle ?exec$ ($while$ (c) e) [] STK xs (Suc (Suc ($length$ ($compE2$ c) + $length$ ($compE2$ e)))) $None$ stk' loc' pc' xcp' \rangle
thus $?case$ **by**(*rule* $exec\text{-meth.cases}$)(*simp-all, auto intro!: exec-meth.intros*)
next
case ($bisim1While7$ c n e xs)
note $exec = \langle ?exec$ ($while$ (c) e) [] STK xs (Suc (Suc (Suc ($length$ ($compE2$ c) + $length$ ($compE2$ e)))) $None$ stk' loc' pc' xcp' \rangle
thus $?case$ **by**(*rule* $exec\text{-meth.cases}$)(*simp-all, auto intro!: exec-meth.intros*)
next
case ($bisim1WhileThrow1$ c n a xs stk loc pc e)
note $bisim = \langle P, c, h \vdash (Throw$ a , xs) \leftrightarrow (stk , loc , pc , $[a]$) \rangle
note $IH = \langle \bigwedge stk' loc' pc' xcp' STK. ?exec$ c stk STK loc pc $[a]$ $stk' loc' pc' xcp'$ $\implies ?concl$ c stk STK loc pc $[a]$ $stk' loc' pc' xcp'$ \rangle
note $exec = \langle ?exec$ ($while$ (c) e) stk STK loc pc $[a]$ $stk' loc' pc' xcp'$ \rangle
from *bisim* **have** pc : $pc < length$ ($compE2$ c) **by**(*auto dest: bisim1-ThrowD*)
from $exec$ **have** $exec\text{-meth-d}$ ($compP2$ P) ($compE2$ c @ $IfFalse$ (int ($length$ ($compE2$ e)) + 3) # $compE2$ e @ [Pop , $Goto$ ($-2 + (- int$ ($length$ ($compE2$ e)) - int ($length$ ($compE2$ c)))), $Push$ $Unit$])
($stack\text{-xlift}$ ($length$ STK) ($compxE2$ c 0 0) @ $shift$ ($length$ ($compE2$ c)) ($stack\text{-xlift}$ ($length$ STK) ($compxE2$ e (Suc 0) 0))) t
 h (stk @ STK , loc , pc , $[a]$) ta h' (stk' , loc' , pc' , xcp')
by(*simp add: compxE2-size-convs*)
hence $?exec$ c stk STK loc pc $[a]$ $stk' loc' pc' xcp'$
by(*rule* $exec\text{-meth-take-xt}$)(*rule* pc)
from IH [*OF this*] **show** $?case$ **by** *auto*
next
case ($bisim1WhileThrow2$ e n a xs stk loc pc c)
note $bisim = \langle P, e, h \vdash (Throw$ a , xs) \leftrightarrow (stk , loc , pc , $[a]$) \rangle
note $IH = \langle \bigwedge stk' loc' pc' xcp' STK. ?exec$ e stk STK loc pc $[a]$ $stk' loc' pc' xcp'$ \rangle

```

    ⇒ ?concl e stk STK loc pc [a] stk' loc' pc' xcp'
  note exec = ⟨?exec (while (c) e) stk STK loc (Suc (length (compE2 c) + pc)) [a] stk' loc' pc' xcp'⟩
  from bisim have pc: pc < length (compE2 e) by(auto dest: bisim1-ThrowD)
  let ?pre = compE2 c @ [IfFalse (int (length (compE2 e)) + 3)]
  from exec have exec-meth-d (compP2 P) ((?pre @ compE2 e) @ [Pop, Goto (-2 + (- int (length
  (compE2 e)) - int (length (compE2 c))), Push Unit])
    (stack-xlift (length STK) (compxE2 c 0 0) @ shift (length ?pre) (stack-xlift (length STK) (compxE2
  e 0 0))) t
    h (stk @ STK, loc, length ?pre + pc, [a]) ta h' (stk', loc', pc', xcp')
    by(simp add: compxE2-size-convs)
  hence exec': exec-meth-d (compP2 P) (?pre @ compE2 e)
    (stack-xlift (length STK) (compxE2 c 0 0) @ shift (length ?pre) (stack-xlift (length STK) (compxE2
  e 0 0))) t
    h (stk @ STK, loc, (length ?pre + pc), [a]) ta h' (stk', loc', pc', xcp')
    by(rule exec-meth-take)(auto intro: pc)
  hence ?exec e stk STK loc pc [a] stk' loc' (pc' - length ?pre) xcp'
    by(rule exec-meth-drop-xt)(auto simp add: stack-xlift-compxE2)
  from IH[OF this] obtain stk'' where stk': stk' = stk'' @ STK
    and exec'': exec-meth-d (compP2 P) (compE2 e) (compxE2 e 0 0) t h (stk, loc, pc, [a]) ta h' (stk'',
  loc', pc' - length ?pre, xcp') by auto
  from exec'' have exec-meth-d (compP2 P) (?pre @ compE2 e) (compxE2 c 0 0 @ shift (length ?pre)
  (compxE2 e 0 0)) t
    h (stk, loc, length ?pre + pc, [a]) ta h' (stk'', loc', length ?pre + (pc' - length ?pre), xcp')
    by(rule append-exec-meth-xt) auto
  hence exec-meth-d (compP2 P) ((?pre @ compE2 e) @ [Pop, Goto (-2 + (- int (length (compE2
  e)) - int (length (compE2 c))), Push Unit])
    (compxE2 c 0 0 @ shift (length ?pre) (compxE2 e 0 0))) t
    h (stk, loc, length ?pre + pc, [a]) ta h' (stk'', loc', length ?pre + (pc' - length ?pre), xcp')
    by(rule exec-meth-append)
  moreover from exec' have pc' ≥ length ?pre
    by(rule exec-meth-drop-xt-pc)(auto simp add: stack-xlift-compxE2)
  moreover have -2 + (- int (length (compE2 e)) - int (length (compE2 c))) = - int (length
  (compE2 c)) + (-2 - int (length (compE2 e))) by simp
  ultimately show ?case using stk'
    by(auto simp add: shift-compxE2 stack-xlift-compxE2 algebra-simps uminus-minus-left-commute)
next
  case (bisim1Throw1 e n e' xs stk loc pc xcp)
  note bisim = ⟨P, e, h ⊢ (e', xs) ↔ (stk, loc, pc, xcp)⟩
  note IH = ⟨∧stk' loc' pc' xcp' STK. ?exec e stk STK loc pc xcp stk' loc' pc' xcp'
    ⇒ ?concl e stk STK loc pc xcp stk' loc' pc' xcp'⟩
  note exec = ⟨?exec (throw e) stk STK loc pc xcp stk' loc' pc' xcp'⟩
  from bisim have pc: pc ≤ length (compE2 e) by(rule bisim1-pc-length-compE2)
  show ?case
  proof(cases pc < length (compE2 e))
    case True
    with exec have ?exec e stk STK loc pc xcp stk' loc' pc' xcp' by(auto elim: exec-meth-take)
    from IH[OF this] show ?thesis by auto
  next
    case False
    with pc have [simp]: pc = length (compE2 e) by simp
    from bisim obtain v where stk = [v] xcp = None by(auto dest: bisim1-pc-length-compE2D)
    with exec show ?thesis by(auto elim!: exec-meth.cases intro!: exec-meth.intros split: if-split-asm)
  qed
next

```

```

case bisim1Throw2 thus ?case
  apply(auto elim!:exec-meth.cases intro: exec-meth.intros dest!: match-ex-table-stack-xliftD)
  apply(auto intro: exec-meth.intros dest!: match-ex-table-stack-xliftD intro!: exI)
  apply(auto simp add: le-Suc-eq)
  done
next
case bisim1ThrowNull thus ?case
  apply(auto elim!:exec-meth.cases intro: exec-meth.intros dest!: match-ex-table-stack-xliftD)
  apply(auto intro: exec-meth.intros dest!: match-ex-table-stack-xliftD intro!: exI)
  apply(auto simp add: le-Suc-eq)
  done
next
case (bisim1ThrowThrow e n a xs stk loc pc)
note bisim =  $\langle P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor) \rangle$ 
note IH =  $\langle \bigwedge stk' loc' pc' xcp' STK. ?exec\ e\ stk\ STK\ loc\ pc\ \lfloor a \rfloor\ stk'\ loc'\ pc'\ xcp' \Rightarrow ?concl\ e\ stk\ STK\ loc\ pc\ \lfloor a \rfloor\ stk'\ loc'\ pc'\ xcp' \rangle$ 
note exec =  $\langle ?exec\ (throw\ e)\ stk\ STK\ loc\ pc\ \lfloor a \rfloor\ stk'\ loc'\ pc'\ xcp' \rangle$ 
from bisim have pc: pc < length (compE2 e) by(auto dest: bisim1-ThrowD)
with exec have ?exec e stk STK loc pc  $\lfloor a \rfloor$  stk' loc' pc' xcp'
  by(auto elim: exec-meth-take simp add: compE2-size-convs)
from IH[OF this] show ?case by auto
next
case (bisim1Try e n e' xs stk loc pc xcp e2 C' V)
note bisim =  $\langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
note IH =  $\langle \bigwedge stk' loc' pc' xcp' STK. ?exec\ e\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \Rightarrow ?concl\ e\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$ 
note exec =  $\langle ?exec\ (try\ e\ catch(C'\ V)\ e2)\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$ 
from bisim have pc: pc ≤ length (compE2 e) by(rule bisim1-pc-length-compE2)
show ?case
proof(cases pc < length (compE2 e))
  case True
    from exec have exec': exec-meth-d (compP2 P) (compE2 e @ Goto (int (length (compE2 e2)) +
    2) # Store V # compE2 e2)
      (stack-xlift (length STK) (compE2 e 0 0) @ shift (length (compE2 e)) (stack-xlift (length STK)
      (compE2 e2 (Suc (Suc 0)) 0)) @ [(0, length (compE2 e),  $\lfloor C' \rfloor$ , Suc (length (compE2 e)), length
      STK)]]) t
      h (stk @ STK, loc, pc, xcp) ta h' (stk', loc', pc', xcp')
      by(simp add: compE2-size-convs)
    show ?thesis
    proof(cases xcp)
      case None
        with exec' True have ?exec e stk STK loc pc xcp stk' loc' pc' xcp'
          apply –
          apply (erule exec-meth.cases)
          apply (cases compE2 e ! pc)
          apply (fastforce simp add: is-Ref-def intro: exec-meth.intros split: if-split-asm cong del: image-cong-simp)
          done
        from IH[OF this] show ?thesis by auto
      next
      case (Some a)
      with exec' have [simp]: h' = h loc' = loc xcp' = None ta =  $\varepsilon$ 
      by(auto elim: exec-meth.cases)
      show ?thesis

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proof(cases match-ex-table (compP2 P) (cname-of h a) pc (compxE2 e 0 0))
  case (Some pcd)
    from exec ⟨xcp = [a]⟩ Some pc
    have stk': stk' = Addr a # (drop (length stk - snd pcd) stk) @ STK
      by(auto elim!: exec-meth.cases simp add: match-ex-table-append split: if-split-asm dest!:
match-ex-table-stack-xliftD)
    from exec' ⟨xcp = [a]⟩ Some pc have exec-meth-d (compP2 P)
      (compE2 e) (stack-xlift (length STK) (compxE2 e 0 0)) t h (stk @ STK, loc, pc, [a]) ∈ h
(Addr a # (drop (length (stk @ STK) - (snd pcd + length STK)) (stk @ STK)), loc, pc', None)
    apply -
    apply(rule exec-meth.intros)
    apply(auto elim!: exec-meth.cases simp add: match-ex-table-append split: if-split-asm dest!:
match-ex-table-shift-pcD match-ex-table-stack-xliftD)
    done
    from IH[unfolded ⟨ta = ε⟩ ⟨xcp = [a]⟩ ⟨h' = h⟩, OF this]
    have stk: Addr a # drop (length stk - snd pcd) (stk @ STK) = Addr a # drop (length stk -
snd pcd) stk @ STK
      and exec'': exec-meth-d (compP2 P) (compE2 e) (compxE2 e 0 0) t h (stk, loc, pc, [a]) ∈ h
(Addr a # drop (length stk - snd pcd) stk, loc, pc', None) by auto
    thus ?thesis using Some stk' ⟨xcp = [a]⟩ by(auto)
  next
    case None
    with Some exec pc have stk': stk' = Addr a # STK
      and pc': pc' = Suc (length (compE2 e))
      and subcls: compP2 P ⊢ cname-of h a ≼* C'
    by(auto elim!: exec-meth.cases split: if-split-asm simp add: match-ex-table-append-not-pcs)(simp
add: matches-ex-entry-def)
    moreover from Some True None pc' subcls
    have exec-meth-d (compP2 P) (compE2 (try e catch(C' V) e2)) (compxE2 (try e catch(C' V)
e2) 0 0) t h
      (stk, loc, pc, [a]) ∈ h (Addr a # drop (length stk - 0) stk, loc, pc', None)
      by -(rule exec-catch,auto simp add: match-ex-table-append-not-pcs matches-ex-entry-def)
    ultimately show ?thesis using Some by auto
  qed
qed
next
  case False
  with pc have [simp]: pc = length (compE2 e) by simp
  from bisim obtain v where stk = [v] xcp = None by(auto dest: bisim1-pc-length-compE2D)
  with exec show ?thesis by(auto elim!: exec-meth.cases intro!: exec-meth.intros split: if-split-asm)
qed
next
  case bisim1TryCatch1 thus ?case
  by(auto elim!: exec-meth.cases intro!: exec-meth.intros split: if-split-asm)
next
  case (bisim1TryCatch2 e2 n e' xs stk loc pc xcp e C' V)
  note bisim = ⟨P,e2,h ⊢ (e', xs) ↔ (stk, loc, pc, xcp)⟩
  note IH = ⟨∧stk' loc' pc' xcp' STK. ?exec e2 stk STK loc pc xcp stk' loc' pc' xcp'
⇒ ?concl e2 stk STK loc pc xcp stk' loc' pc' xcp'⟩
  note exec = ⟨?exec (try e catch(C' V) e2) stk STK loc (Suc (Suc (length (compE2 e) + pc))) xcp
stk' loc' pc' xcp'⟩
  let ?pre = compE2 e @ [Goto (int (length (compE2 e2)) + 2), Store V]
  from exec have exec': exec-meth-d (compP2 P) (?pre @ compE2 e2)
    (stack-xlift (length STK) (compxE2 e 0 0) @ shift (length ?pre) (stack-xlift (length STK) (compxE2

```

$e2\ 0\ 0)))\ t$
 $h\ (stk\ @\ STK,\ loc,\ length\ ?pre\ +\ pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$
proof(cases)
case (exec-catch xcp'' d)
let ?stk = stk @ STK **and** ?PC = Suc (Suc (length (compE2 e) + pc))
note s = ⟨stk' = Addr xcp'' # drop (length ?stk - d) ?stk⟩
⟨ta = ε⟩ ⟨h' = h⟩ ⟨xcp' = None⟩ ⟨xcp = [xcp'']⟩ ⟨loc' = loc⟩
from ⟨match-ex-table (compP2 P) (cname-of h xcp'') ?PC (stack-xlift (length STK) (compxE2 (try e catch (C' V) e2) 0 0)) = [(pc', d)]⟩ ⟨d ≤ length ?stk⟩
show ?thesis **unfolding** s
by -(rule exec-meth.exec-catch, simp-all add: shift-compxE2 stack-xlift-compxE2, simp add: match-ex-table-append add: matches-ex-entry-def)
qed(auto intro: exec-meth.intros simp add: shift-compxE2 stack-xlift-compxE2)
hence ?exec e2 stk STK loc pc xcp stk' loc' (pc' - length ?pre) xcp'
by(rule exec-meth-drop-xt)(auto simp add: stack-xlift-compxE2)
from IH[OF this] **obtain** stk'' **where** stk': stk' = stk'' @ STK
and exec'': exec-meth-d (compP2 P) (compE2 e2) (compxE2 e2 0 0) t h (stk, loc, pc, xcp) ta h' (stk'', loc', pc' - length ?pre, xcp') **by** auto
from exec'' **have** exec-meth-d (compP2 P) (?pre @ compE2 e2) (compxE2 e 0 0 @ shift (length ?pre) (compxE2 e2 0 0)) t h (stk, loc, length ?pre + pc, xcp) ta h' (stk'', loc', length ?pre + (pc' - length ?pre), xcp')
by(rule append-exec-meth-xt) auto
hence exec-meth-d (compP2 P) (?pre @ compE2 e2) (compxE2 e 0 0 @ shift (length ?pre) (compxE2 e2 0 0)) @ [(0, length (compE2 e), [C'], Suc (length (compE2 e)), 0)] t h (stk, loc, length ?pre + pc, xcp) ta h' (stk'', loc', length ?pre + (pc' - length ?pre), xcp')
by(rule exec-meth.cases)(auto intro: exec-meth.intros simp add: match-ex-table-append-not-pcs)
moreover from exec' **have** pc' ≥ length ?pre
by(rule exec-meth-drop-xt-pc)(auto simp add: stack-xlift-compxE2)
moreover hence (Suc (Suc (pc' - Suc (Suc 0)))) = pc' **by** simp
ultimately show ?case **using** stk' **by**(auto simp add: shift-compxE2 eval-nat-numeral)

next
case (bisim1TryFail e n a xs stk loc pc C' C'' e2 V)
note bisim = ⟨P, e, h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
note exec = ⟨?exec (try e catch (C'' V) e2) stk STK loc pc [a] stk' loc' pc' xcp'⟩
note a = ⟨type-of-addr h a = [Class-type C']⟩ ⟨¬ P ⊢ C' ≲* C''⟩
from bisim **have** match-ex-table (compP2 P) (cname-of h a) (0 + pc) (compxE2 e 0 0) = None
unfolding compP2-def **by**(rule bisim1-xcp-Some-not-caught)
moreover from bisim **have** pc < length (compE2 e) **by**(auto dest: bisim1-ThrowD)
ultimately have False **using** exec a
apply(auto elim!: exec-meth.cases simp add: outside-pcs-compxE2-not-matches-entry outside-pcs-not-matches-entry split: if-split-asm)
apply(auto simp add: compP2-def match-ex-entry match-ex-table-append-not-pcs cname-of-def split: if-split-asm)
done
thus ?case ..

next
case (bisim1TryCatchThrow e2 n a xs stk loc pc e C' V)
note bisim = ⟨P, e2, h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
note IH = ⟨∧ stk' loc' pc' xcp' STK. ?exec e2 stk STK loc pc [a] stk' loc' pc' xcp' ⇒ ?concl e2 stk STK loc pc [a] stk' loc' pc' xcp'⟩
note exec = ⟨?exec (try e catch (C' V) e2) stk STK loc (Suc (Suc (length (compE2 e) + pc))) [a] stk' loc' pc' xcp'⟩
from bisim **have** pc: pc < length (compE2 e2) **by**(auto dest: bisim1-ThrowD)
let ?pre = compE2 e @ [Goto (int (length (compE2 e2)) + 2), Store V]

from *exec* **have** *exec'*: *exec-meth-d* (*compP2* *P*) (?*pre* @ *compE2* *e2*) (*stack-xlift* (*length* *STK*) (*compxE2* *e* 0 0) @
shift (*length* ?*pre*) (*stack-xlift* (*length* *STK*) (*compxE2* *e2* 0 0))) *t*
h (*stk* @ *STK*, *loc*, *length* ?*pre* + *pc*, [*a*]) *ta* *h'* (*stk'*, *loc'*, *pc'*, *xcp'*)
proof (*cases*)
case (*exec-catch* *d*)
let ?*stk* = *stk* @ *STK* **and** ?*PC* = *Suc* (*Suc* (*length* (*compE2* *e*) + *pc*))
note *s* = ⟨*stk'* = *Addr* *a* # *drop* (*length* ?*stk* - *d*) ?*stk*⟩ ⟨*loc'* = *loc*⟩
⟨*ta* = ε ⟩ ⟨*h'* = *h*⟩ ⟨*xcp'* = *None*⟩
from ⟨*match-ex-table* (*compP2* *P*) (*cname-of* *h* *a*) ?*PC* (*stack-xlift* (*length* *STK*) (*compxE2* (*try* *e*
catch(*C'* *V*) *e2*) 0 0)) = [*pc'*, *d*])⟩ ⟨*d* ≤ *length* ?*stk*⟩
show ?*thesis* **unfolding** *s*
by -(*rule* *exec-meth.exec-catch*, *simp-all* *add: shift-compxE2 stack-xlift-compxE2*, *simp* *add:*
match-ex-table-append *add: matches-ex-entry-def*)
qed
hence ?*exec* *e2* *stk* *STK* *loc* *pc* [*a*] *stk'* *loc'* (*pc'* - *length* ?*pre*) *xcp'*
by(*rule* *exec-meth-drop-xt*)(*auto* *simp* *add: stack-xlift-compxE2*)
from *IH*[*OF this*] **obtain** *stk''* **where** *stk'*: *stk'* = *stk''* @ *STK*
and *exec''*: *exec-meth-d* (*compP2* *P*) (*compE2* *e2*) (*compxE2* *e2* 0 0) *t* *h* (*stk*, *loc*, *pc*, [*a*]) *ta* *h'*
(*stk''*, *loc'*, *pc'* - *length* ?*pre*, *xcp'*) **by** *auto*
from *exec''* **have** *exec-meth-d* (*compP2* *P*) (?*pre* @ *compE2* *e2*) (*compxE2* *e* 0 0 @ *shift* (*length*
?*pre*) (*compxE2* *e2* 0 0)) *t* *h* (*stk*, *loc*, *length* ?*pre* + *pc*, [*a*]) *ta* *h'* (*stk''*, *loc'*, *length* ?*pre* + (*pc'* -
length ?*pre*), *xcp'*)
by(*rule* *append-exec-meth-xt*) *auto*
hence *exec-meth-d* (*compP2* *P*) (?*pre* @ *compE2* *e2*) (*compxE2* *e* 0 0 @ *shift* (*length* ?*pre*) (*compxE2*
e2 0 0) @ [(0, *length* (*compE2* *e*), [*C'*], *Suc* (*length* (*compE2* *e*)), 0])]) *t* *h* (*stk*, *loc*, *length* ?*pre* + *pc*,
[*a*]) *ta* *h'* (*stk''*, *loc'*, *length* ?*pre* + (*pc'* - *length* ?*pre*), *xcp'*)
by(*rule* *exec-meth.cases*)(*auto* *intro!*: *exec-meth.intros* *simp* *add: match-ex-table-append-not-pcs*)
moreover **from** *exec'* **have** *pc'* ≥ *length* ?*pre*
by(*rule* *exec-meth-drop-xt-pc*)(*auto* *simp* *add: stack-xlift-compxE2*)
moreover **hence** (*Suc* (*Suc* (*pc'* - *Suc* (*Suc* 0)))) = *pc'* **by** *simp*
ultimately **show** ?*case* **using** *stk'* **by**(*auto* *simp* *add: shift-compxE2 eval-nat-numeral*)
next
case *bisims1Nil* **thus** ?*case* **by**(*auto* *elim: exec-meth.cases*)
next
case (*bisims1List1* *e* *n* *e'* *xs* *stk* *loc* *pc* *xcp* *es*)
note *bisim1* = ⟨*P*, *e*, *h* ⊢ (*e'*, *xs*) ↔ (*stk*, *loc*, *pc*, *xcp*)⟩
note *IH1* = ⟨ \bigwedge *stk'* *loc'* *pc'* *xcp'* *STK*. ?*exec* *e* *stk* *STK* *loc* *pc* *xcp* *stk'* *loc'* *pc'* *xcp'*
⇒ ?*concl* *e* *stk* *STK* *loc* *pc* *xcp* *stk'* *loc'* *pc'* *xcp'*⟩
note *IH2* = ⟨ \bigwedge *xs* *stk'* *loc'* *pc'* *xcp'* *STK*. ?*execs* *es* [] *STK* *xs* 0 *None* *stk'* *loc'* *pc'* *xcp'*
⇒ ?*concls* *es* [] *STK* *xs* 0 *None* *stk'* *loc'* *pc'* *xcp'*⟩
note *exec* = ⟨?*execs* (*e* # *es*) *stk* *STK* *loc* *pc* *xcp* *stk'* *loc'* *pc'* *xcp'*⟩
from *bisim1* **have** *pc*: *pc* ≤ *length* (*compE2* *e*) **by**(*rule* *bisim1-pc-length-compE2*)
show ?*case*
proof(*cases* *pc* < *length* (*compE2* *e*))
case *True*
with *exec* **have** ?*exec* *e* *stk* *STK* *loc* *pc* *xcp* *stk'* *loc'* *pc'* *xcp'*
by(*simp* *add: compxEs2-size-convs*)(*erule* *exec-meth-take-xt*)
from *IH1*[*OF this*] **show** ?*thesis* **by** *auto*
next
case *False*
with *pc* **have** *pc*: *pc* = *length* (*compE2* *e*) **by** *simp*
with *bisim1* **obtain** *v* **where** *s*: *stk* = [*v*] *xcp* = *None* **by**(*auto* *dest: bisim1-pc-length-compE2D*)
with *exec* *pc* **have** *exec'*: *exec-meth-d* (*compP2* *P*) (*compE2* *e* @ *compEs2* *es*)

$(\text{stack-xlift } (\text{length } STK) (\text{compxE2 } e \ 0 \ 0) \ @ \ \text{shift } (\text{length } (\text{compE2 } e)) (\text{stack-xlift } (\text{length } (v \ # \ STK)) (\text{compxEs2 } es \ 0 \ 0))) \ t$
 $h (\ [] \ @ \ v \ # \ STK, \ loc, \ \text{length } (\text{compE2 } e) + 0, \ \text{None}) \ \text{ta } h' (stk', \ loc', \ pc', \ xcp')$
by(*simp add: compxEs2-size-convs compxEs2-stack-xlift-convs*)
hence $?execs \ es \ [] \ (v \ # \ STK) \ loc \ 0 \ \text{None} \ stk' \ loc' \ (pc' - \text{length } (\text{compE2 } e)) \ xcp'$
by(*rule exec-meth-drop-xt*)(*auto simp add: stack-xlift-compxE2*)
from *IH2[OF this]* **obtain** stk'' **where** $stk': \ stk' = stk'' \ @ \ v \ # \ STK$
and $exec'': \ \text{exec-meth-d } (\text{compP2 } P) (\text{compEs2 } es) (\text{compxEs2 } es \ 0 \ 0) \ t \ h (\ [], \ loc, \ 0, \ \text{None}) \ \text{ta } h' (stk'', \ loc', \ pc' - \text{length } (\text{compE2 } e), \ xcp')$ **by** *auto*
from $exec''$ **have** $\text{exec-meth-d } (\text{compP2 } P) (\text{compEs2 } es) (\text{stack-xlift } (\text{length } [v]) (\text{compxEs2 } es \ 0 \ 0)) \ t \ h (\ [] \ @ \ [v], \ loc, \ 0, \ \text{None}) \ \text{ta } h' (stk'' \ @ \ [v], \ loc', \ pc' - \text{length } (\text{compE2 } e), \ xcp')$
by(*rule exec-meth-stk-offer*)
hence $\text{exec-meth-d } (\text{compP2 } P) (\text{compE2 } e \ @ \ \text{compEs2 } es) (\text{compxE2 } e \ 0 \ 0 \ @ \ \text{shift } (\text{length } (\text{compE2 } e)) (\text{stack-xlift } (\text{length } [v]) (\text{compxEs2 } es \ 0 \ 0))) \ t \ h (\ [] \ @ \ [v], \ loc, \ \text{length } (\text{compE2 } e) + 0, \ \text{None}) \ \text{ta } h' (stk'' \ @ \ [v], \ loc', \ \text{length } (\text{compE2 } e) + (pc' - \text{length } (\text{compE2 } e)), \ xcp')$
by(*rule append-exec-meth-xt*) *auto*
moreover from $exec'$ **have** $pc' \geq \text{length } (\text{compE2 } e)$
by(*rule exec-meth-drop-xt-pc*)(*auto simp add: stack-xlift-compxE2*)
ultimately show $?thesis$ **using** $s \ pc \ stk'$ **by**(*auto simp add: shift-compxEs2 stack-xlift-compxEs2*)
qed
next
case (*bisims1List2* $es \ n \ es' \ xs \ stk \ loc \ pc \ xcp \ e \ v$)
note $bisim = \langle P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, xcp) \rangle$
note $IH = \langle \bigwedge stk' \ loc' \ pc' \ xcp' \ STK. \ ?execs \ es \ stk \ STK \ loc \ pc \ xcp \ stk' \ loc' \ pc' \ xcp' \ \Rightarrow \ ?concls \ es \ stk \ STK \ loc \ pc \ xcp \ stk' \ loc' \ pc' \ xcp' \rangle$
note $exec = \langle ?execs \ (e \ # \ es) \ (stk \ @ \ [v]) \ STK \ loc \ (\text{length } (\text{compE2 } e) + pc) \ xcp \ stk' \ loc' \ pc' \ xcp' \rangle$
from $exec$ **have** $exec': \ \text{exec-meth-d } (\text{compP2 } P) (\text{compE2 } e \ @ \ \text{compEs2 } es) (\text{stack-xlift } (\text{length } STK) (\text{compxE2 } e \ 0 \ 0) \ @ \ \text{shift } (\text{length } (\text{compE2 } e)) (\text{stack-xlift } (\text{length } (v \ # \ STK)) (\text{compxEs2 } es \ 0 \ 0))) \ t$
 $h (stk \ @ \ v \ # \ STK, \ loc, \ \text{length } (\text{compE2 } e) + pc, \ xcp) \ \text{ta } h' (stk', \ loc', \ pc', \ xcp')$
by(*simp add: compxEs2-size-convs compxEs2-stack-xlift-convs*)
hence $?execs \ es \ stk \ (v \ # \ STK) \ loc \ pc \ xcp \ stk' \ loc' \ (pc' - \text{length } (\text{compE2 } e)) \ xcp'$
by(*rule exec-meth-drop-xt*)(*auto simp add: stack-xlift-compxE2*)
from *IH[OF this]* **obtain** stk'' **where** $stk': \ stk' = stk'' \ @ \ v \ # \ STK$
and $exec'': \ \text{exec-meth-d } (\text{compP2 } P) (\text{compEs2 } es) (\text{compxEs2 } es \ 0 \ 0) \ t \ h (stk, \ loc, \ pc, \ xcp) \ \text{ta } h' (stk'', \ loc', \ pc' - \text{length } (\text{compE2 } e), \ xcp')$ **by** *auto*
from $exec''$ **have** $\text{exec-meth-d } (\text{compP2 } P) (\text{compEs2 } es) (\text{stack-xlift } (\text{length } [v]) (\text{compxEs2 } es \ 0 \ 0)) \ t \ h (stk \ @ \ [v], \ loc, \ pc, \ xcp) \ \text{ta } h' (stk'' \ @ \ [v], \ loc', \ pc' - \text{length } (\text{compE2 } e), \ xcp')$
by(*rule exec-meth-stk-offer*)
hence $\text{exec-meth-d } (\text{compP2 } P) (\text{compE2 } e \ @ \ \text{compEs2 } es) (\text{compxE2 } e \ 0 \ 0 \ @ \ \text{shift } (\text{length } (\text{compE2 } e)) (\text{stack-xlift } (\text{length } [v]) (\text{compxEs2 } es \ 0 \ 0))) \ t \ h (stk \ @ \ [v], \ loc, \ \text{length } (\text{compE2 } e) + pc, \ xcp) \ \text{ta } h' (stk'' \ @ \ [v], \ loc', \ \text{length } (\text{compE2 } e) + (pc' - \text{length } (\text{compE2 } e)), \ xcp')$
by(*rule append-exec-meth-xt*) *auto*
moreover from $exec'$ **have** $pc' \geq \text{length } (\text{compE2 } e)$
by(*rule exec-meth-drop-xt-pc*)(*auto simp add: stack-xlift-compxE2*)
ultimately show $?case$ **using** stk' **by**(*auto simp add: shift-compxEs2 stack-xlift-compxEs2*)
next
case (*bisim1Sync12* $e1 \ n \ e2 \ V \ a \ xs \ v \ v'$)
note $exec = \langle ?exec \ (\text{sync } \vee \ (e1) \ e2) \ [v, \ v'] \ STK \ xs \ (4 + \text{length } (\text{compE2 } e1) + \text{length } (\text{compE2 } e2)) \ [a] \ stk' \ loc' \ pc' \ xcp' \rangle$
thus $?case$ **by**(*auto elim!: exec-meth.cases split: if-split-asm simp add: match-ex-table-append-not-pcs*)(*simp add: matches-ex-entry-def*)
next
case (*bisim1Sync14* $e1 \ n \ e2 \ V \ a \ xs \ v \ a'$)

note $exec = \langle ?exec (sync_V (e1) e2) [v, Addr a'] STK xs (7 + length (compE2 e1) + length (compE2 e2)) [a] stk' loc' pc' xcp' \rangle$
thus $?case \text{ by } (auto elim!: exec-meth.cases split: if-split-asm simp add: match-ex-table-append-not-pcs)(simp add: matches-ex-entry-def)$
qed

lemma shows $bisim1-callD$:

$\llbracket P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp); call1 e' = [(a, M, vs)];$
 $compE2 e ! pc = Invoke M' n0 \rrbracket$
 $\implies M = M'$

and $bisims1-callD$:

$\llbracket P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, xcp); calls1 es' = [(a, M, vs)];$
 $compEs2 es ! pc = Invoke M' n0 \rrbracket$
 $\implies M = M'$

proof($induct e n :: nat e' xs stk loc pc xcp$ **and** $es n :: nat es' xs stk loc pc xcp$
 $rule: bisim1-bisims1-inducts-split$)

case $bisim1AAss1$ **thus** $?case$

apply($simp (no-asm-use) split: if-split-asm add: is-val-iff$)
apply($fastforce dest: bisim-Val-pc-not-Invoke$)
apply($fastforce dest: bisim-Val-pc-not-Invoke$)
apply($fastforce dest: bisim-Val-pc-not-Invoke bisim1-pc-length-compE2$)
done

next

case $bisim1Call1$ **thus** $?case$

apply($clarsimp split: if-split-asm simp add: is-vals-conv$)
apply($drule bisim-Val-pc-not-Invoke, simp, fastforce$)
apply($drule bisim-Val-pc-not-Invoke, simp, fastforce$)
apply($drule bisim1-pc-length-compE2, clarsimp simp add: neq-Nil-conv$)
apply($drule bisim1-pc-length-compE2, simp$)
apply($drule bisim1-pc-length-compE2, simp$)
apply($drule bisim1-pc-length-compE2, simp$)
apply($drule bisim1-call-pcD, simp, simp$)
apply($drule bisim1-call-pcD, simp, simp$)
done

next

case $bisim1CallParams$ **thus** $?case$

apply($clarsimp split: if-split-asm simp add: is-vals-conv$)
apply($drule bisims-Val-pc-not-Invoke, simp, fastforce$)
apply($drule bisims1-pc-length-compEs2, simp$)
apply($drule bisims1-calls-pcD, simp, simp$)
done

qed($fastforce split: if-split-asm dest: bisim1-pc-length-compE2 bisims1-pc-length-compEs2 bisim1-call-pcD$
 $bisims1-calls-pcD bisim1-call-xcpNone bisims1-calls-xcpNone bisim-Val-pc-not-Invoke bisims-Val-pc-not-Invoke$)
 $+$

lemma $bisim1-xcpD$: $P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, [a]) \implies pc < length (compE2 e)$

and $bisims1-xcpD$: $P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, [a]) \implies pc < length (compEs2 es)$

by($induct (e', xs) (stk, loc, pc, [a :: 'addr])$ **and** $(es', xs) (stk, loc, pc, [a :: 'addr])$
 $arbitrary: e' xs stk loc pc$ **and** $es' xs stk loc pc$ $rule: bisim1-bisims1.inducts$)
 $simp-all$

lemma $bisim1-match-Some-stk-length$:

$\llbracket P, E, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, [a]);$
 $match-ex-table (compP2 P) (cname-of h a) pc (compxE2 E 0 0) = [(pc', d)] \rrbracket$


```

 $\implies d \leq \text{length } stk$ 

and bisims1-match-Some-stk-length:
[[  $P, Es, h \vdash (es, xs) [\leftrightarrow] (stk, loc, pc, [a])$ ;
    $\text{match-ex-table } (compP2 P) (cname-of h a) pc (compxEs2 Es 0 0) = [(pc', d)]$  ]]
 $\implies d \leq \text{length } stk$ 
proof(induct (e, xs) (stk, loc, pc, [a :: 'addr]) and (es, xs) (stk, loc, pc, [a :: 'addr])
arbitrary:  $pc' d e xs stk loc pc$  and  $pc' d es xs stk loc pc$  rule: bisim1-bisims1.inducts)
case bisim1Call1 thus ?case
  by(fastforce dest: bisim1-xcpD simp add: match-ex-table-append match-ex-table-not-pcs-None)
next
case bisim1CallThrowObj thus ?case
  by(fastforce dest: bisim1-xcpD simp add: match-ex-table-append match-ex-table-not-pcs-None)
next
case bisim1Sync4 thus ?case
  apply(clarsimp simp add: match-ex-table-not-pcs-None match-ex-table-append matches-ex-entry-def
split: if-split-asm)
  apply(fastforce simp add: match-ex-table-compxE2-shift-conv dest: bisim1-xcpD)
  done
next
case bisim1Try thus ?case
  by(fastforce simp add: match-ex-table-append matches-ex-entry-def match-ex-table-not-pcs-None
dest: bisim1-xcpD split: if-split-asm)
next
case bisim1TryCatch2 thus ?case
  apply(clarsimp simp add: match-ex-table-not-pcs-None match-ex-table-append matches-ex-entry-def
split: if-split-asm)
  apply(fastforce simp add: match-ex-table-compxE2-shift-conv dest: bisim1-xcpD)
  done
next
case bisim1TryFail thus ?case
  by(fastforce simp add: match-ex-table-append matches-ex-entry-def match-ex-table-not-pcs-None
dest: bisim1-xcpD split: if-split-asm)
next
case bisim1TryCatchThrow thus ?case
  apply(clarsimp simp add: match-ex-table-not-pcs-None match-ex-table-append matches-ex-entry-def
split: if-split-asm)
  apply(fastforce simp add: match-ex-table-compxE2-shift-conv dest: bisim1-xcpD)
  done
next
case bisims1List1 thus ?case
  by(fastforce simp add: match-ex-table-append split: if-split-asm dest: bisim1-xcpD match-ex-table-pcsD)
qed(fastforce simp add: match-ex-table-not-pcs-None match-ex-table-append match-ex-table-compxE2-shift-conv
match-ex-table-compxEs2-shift-conv match-ex-table-compxE2-stack-conv match-ex-table-compxEs2-stack-conv
matches-ex-entry-def dest: bisim1-xcpD)+

end

locale J1-JVM-heap-conf-base =
  J1-JVM-heap-base
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
+

```

```

J1-heap-conf-base
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  hconf P
+
JVM-heap-conf-base
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  hconf compP2 P
for addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool
and P :: 'addr J1-prog
begin

inductive bisim1-list1 ::
  'thread-id ⇒ 'heap ⇒ 'addr expr1 × 'addr locals1 ⇒ ('addr expr1 × 'addr locals1) list
  ⇒ 'addr option ⇒ 'addr frame list ⇒ bool
for t :: 'thread-id and h :: 'heap
where
  bl1-Normal:
  [ compTP P ⊢ t:(xcp, h, (stk, loc, C, M, pc) # frs) √;
    P ⊢ C sees M : Ts→T = [ body ] in D;
    P, blocks1 0 (Class D#Ts) body, h ⊢ (e, xs) ↔ (stk, loc, pc, xcp); max-vars e ≤ length xs;
    list-all2 (bisim1-fr P h) exs frs ]
  ⇒ bisim1-list1 t h (e, xs) exs xcp ((stk, loc, C, M, pc) # frs)

| bl1-finalVal:
  [ hconf h; preallocated h ] ⇒ bisim1-list1 t h (Val v, xs) [] None []

| bl1-finalThrow:
  [ hconf h; preallocated h ] ⇒ bisim1-list1 t h (Throw a, xs) [] [a] []

fun wbisim1 ::
  'thread-id
  ⇒ ((('addr expr1 × 'addr locals1) × ('addr expr1 × 'addr locals1) list) × 'heap,
    ('addr option × 'addr frame list) × 'heap) bisim
where wbisim1 t ((ex, exs), h) ((xcp, frs), h') ← h = h' ∧ bisim1-list1 t h ex exs xcp frs

lemma new-thread-conf-compTP:
  assumes hconf: hconf h preallocated h
  and ha: typeof-addr h a = [ Class-type C ]
  and sub: typeof-addr h (thread-id2addr t) = [ Class-type C' ] P ⊢ C' ≲* Thread
  and sees: P ⊢ C sees M: []→T = [ meth ] in D
  shows compTP P ⊢ t:(None, h, ([], Addr a # replicate (max-vars meth) undefined-value, D, M,
  0)]) √
```

proof –

from ha sees-method-decl-above[OF sees]
have $P, h \vdash \text{Addr } a : \leq \text{Class } D$ **by** ($\text{simp add: conf-def}$)
moreover
hence $\text{compP2 } P, h \vdash \text{Addr } a : \leq \text{Class } D$ **by** ($\text{simp add: compP2-def}$)
hence $\text{compP2 } P, h \vdash \text{Addr } a \# \text{replicate } (\text{max-vars meth}) \text{ undefined-value } [:\leq\top]$ $\text{map } (\lambda i. \text{if } i = 0 \text{ then OK } ([\text{Class } D] ! i) \text{ else Err}) [0..<\text{max-vars meth}] @ [\text{Err}]$
by –($\text{rule list-all2-all-nthI, simp-all}$)
hence $\text{conf-f } (\text{compP2 } P) h ([], \text{map } (\lambda i. \text{if } i = 0 \text{ then OK } ([\text{Class } D] ! i) \text{ else Err}) [0..<\text{max-vars meth}] @ [\text{Err}])$
 $(\text{compE2 meth } @ [\text{Return}]) ([], \text{Addr } a \# \text{replicate } (\text{max-vars meth}) \text{ undefined-value, } D, M, 0)$
unfolding conf-f-def2 **by** ($\text{simp add: compP2-def}$)
ultimately have $\text{conf-f } (\text{compP2 } P) h ([], \text{TC0.ty}_i (\text{Suc } (\text{max-vars meth})) [\text{Class } D] \{0\}) (\text{compE2 meth } @ [\text{Return}])$
 $([], \text{Addr } a \# \text{replicate } (\text{max-vars meth}) \text{ undefined-value, } D, M, 0)$
by ($\text{simp add: TC0.ty}_i\text{-def conf-f-def2 compP2-def}$)
with $h\text{conf } ha$ sub sees-method-compP[OF sees, **where** $f = \lambda C M Ts T. \text{compMb2}$] sees-method-idemp[OF sees]
show ?thesis
by ($\text{auto simp add: TC0.ty}_i'\text{-def correct-state-def compTP-def tconf-def}$) ($\text{fastforce simp add: compP2-def compMb2-def tconf-def intro: sees-method-idemp}$) +
qed

lemma $ta\text{-bisim12-extTA2J1-extTA2JVM}$:

assumes $nt: \bigwedge n T C M a h. \llbracket n < \text{length } \{\!|ta|\!\}_t; \{\!|ta|\!\}_t ! n = \text{NewThread } T (C, M, a) h \rrbracket$
 $\implies \text{typeof-addr } h a = \llbracket \text{Class-type } C \rrbracket \wedge (\exists C'. \text{typeof-addr } h (\text{thread-id2addr } T) = \llbracket \text{Class-type } C' \rrbracket \wedge P \vdash C' \preceq^* \text{Thread}) \wedge$
 $(\exists T \text{ meth } D. P \vdash C \text{ sees } M: [] \rightarrow T = \llbracket \text{meth} \rrbracket \text{ in } D) \wedge h\text{conf } h \wedge \text{preallocated } h$
shows $ta\text{-bisim } wbisim1 (\text{extTA2J1 } P \text{ ta}) (\text{extTA2JVM } (\text{compP2 } P) \text{ ta})$

proof –

{ **fix** $n t C M a m$
assume $n < \text{length } \{\!|ta|\!\}_t$ **and** $\{\!|ta|\!\}_t ! n = \text{NewThread } t (C, M, a) m$
from nt [OF this] **obtain** $T \text{ meth } D C'$
where $ma: \text{typeof-addr } m a = \llbracket \text{Class-type } C \rrbracket$
and $\text{sees}: P \vdash C \text{ sees } M: [] \rightarrow T = \llbracket \text{meth} \rrbracket \text{ in } D$
and $\text{sub}: \text{typeof-addr } m (\text{thread-id2addr } t) = \llbracket \text{Class-type } C' \rrbracket P \vdash C' \preceq^* \text{Thread}$
and $m\text{conf}: h\text{conf } m \text{ preallocated } m$ **by** fastforce
from sees-method-compP [OF sees, **where** $f = \lambda C M Ts T. \text{compMb2}$]
have $\text{sees}' : \text{compP2 } P \vdash C \text{ sees } M: [] \rightarrow T = \llbracket (\text{max-stack meth, max-vars meth, compE2 meth } @ [\text{Return}], \text{compxE2 meth } 0 \ 0) \rrbracket \text{ in } D$
by ($\text{simp add: compMb2-def compP2-def}$)
have $\text{bisim1-list1 } t m (\{0:\text{Class } D = \text{None}; \text{meth}\}, \text{Addr } a \# \text{replicate } (\text{max-vars meth}) \text{ undefined-value}) ([], \text{None } [([], \text{Addr } a \# \text{replicate } (\text{max-vars meth}) \text{ undefined-value, } D, M, 0)])$
proof
from $m\text{conf } ma$ sub sees
show $\text{compTP } P \vdash t:(\text{None}, m, [([], \text{Addr } a \# \text{replicate } (\text{max-vars meth}) \text{ undefined-value, } D, M, 0)]) \checkmark$
by ($\text{rule new-thread-conf-compTP}$)

from sees **show** $P \vdash D \text{ sees } M: [] \rightarrow T = \llbracket \text{meth} \rrbracket \text{ in } D$ **by** ($\text{rule sees-method-idemp}$)
show $\text{list-all2 } (\text{bisim1-fr } P m) [] []$ **by** simp
show $P, \text{blocks1 } 0 [\text{Class } D] \text{ meth, } m \vdash (\{0:\text{Class } D = \text{None}; \text{meth}\}, \text{Addr } a \# \text{replicate } (\text{max-vars meth}) \text{ undefined-value}) \leftrightarrow$

```

( $\square$ , Addr a # replicate (max-vars meth) undefined-value, 0, None)
  by simp(rule bisim1-refl)
qed simp
  with sees sees' have bisim1-list1 t m ({0:Class (fst (method P C M))=None; the (snd (snd (snd (method P C M))))})
  undefined-value  $\square$  None [( $\square$ , Addr a # replicate (fst (snd (the (snd (snd (snd (method (compP2 P) C M))))))
  undefined-value, fst (method (compP2 P) C M), M, 0)] by simp }
  thus ?thesis
  apply(auto simp add: ta-bisim-def intro!: list-all2-all-nthI)
  apply(case-tac  $\{ta\}_t ! n$ , auto simp add: extNTA2JVM-def)
done
qed

end

definition no-call2 :: 'addr expr1  $\Rightarrow$  pc  $\Rightarrow$  bool
where no-call2 e pc  $\longleftrightarrow$  (pc  $\leq$  length (compE2 e))  $\wedge$  (pc < length (compE2 e)  $\longrightarrow$  ( $\forall M n$ . compE2 e ! pc  $\neq$  Invoke M n))

definition no-calls2 :: 'addr expr1 list  $\Rightarrow$  pc  $\Rightarrow$  bool
where no-calls2 es pc  $\longleftrightarrow$  (pc  $\leq$  length (compEs2 es))  $\wedge$  (pc < length (compEs2 es)  $\longrightarrow$  ( $\forall M n$ . compEs2 es ! pc  $\neq$  Invoke M n))

locale J1-JVM-conf-read =
  J1-JVM-heap-conf-base
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  hconf P
+
  JVM-conf-read
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  hconf compP2 P
for addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype
and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
and hconf :: 'heap  $\Rightarrow$  bool
and P :: 'addr J1-prog

locale J1-JVM-heap-conf =
  J1-JVM-heap-conf-base
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  hconf P
+
  JVM-heap-conf

```

```

  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  hconf compP2 P
for addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool
and P :: 'addr J1-prog
begin

```

lemma *red-external-ta-bisim21*:

```

  [[ wf-prog wf-md P; P, t ⊢ ⟨a·M(vs), h⟩ -ta→ext ⟨va, h^⟩; hconf h^; preallocated h' ]
  ⇒ ta-bisim wbisim1 (extTA2J1 P ta) (extTA2JVM (compP2 P) ta)
apply(rule ta-bisim12-extTA2J1-extTA2JVM)
apply(frule (1) red-external-new-thread-sees)
  apply(fastforce simp add: in-set-conv-nth)
apply(frule red-ext-new-thread-heap)
  apply(fastforce simp add: in-set-conv-nth)
apply(frule red-external-new-thread-exists-thread-object[unfolded compP2-def, simplified])
  apply(fastforce simp add: in-set-conv-nth)
apply simp
done

```

lemma *ta-bisim-red-extTA2J1-extTA2JVM*:

```

  assumes wf: wf-prog wf-md P
  and red: wf, P, t' ⊢ 1 ⟨e, s⟩ -ta→ ⟨e', s'⟩
  and hconf: hconf (hp s') preallocated (hp s')
  shows ta-bisim wbisim1 (extTA2J1 P ta) (extTA2JVM (compP2 P) ta)

```

proof –

```

  { fix n t C M a H
    assume len: n < length {ta}_t and tan: {ta}_t ! n = NewThread t (C, M, a) H
    hence nt: NewThread t (C, M, a) H ∈ set {ta}_t unfolding set-conv-nth by(auto intro!: exI)
    from red1-new-threadD[OF red nt] obtain ad M' vs va T C' Ts' Tr' D'
      where rede: P, t' ⊢ ⟨ad·M'(vs), hp s⟩ -ta→ext ⟨va, hp s'⟩
      and ad: typeof-addr (hp s) ad = [T] by blast
    from red-ext-new-thread-heap[OF rede nt] have [simp]: hp s' = H by simp
    from red-external-new-thread-sees[OF wf rede nt]
    obtain T body D where Ha: typeof-addr H a = [Class-type C]
      and sees: P ⊢ C sees M:[]→T=[body] in D by auto
    have sees': compP2 P ⊢ C sees M:[]→T=[(max-stack body, max-vars body, compE2 body @ [Return],
    compxE2 body 0 0)] in D
      using sees unfolding compP2-def compMb2-def Let-def by(auto dest: sees-method-compP)
      from red-external-new-thread-exists-thread-object[unfolded compP2-def, simplified, OF rede nt]
    hconf Ha sees
    have compTP P ⊢ t:(None, H, [([]), Addr a # replicate (max-vars body) undefined-value, D, M,
    0])) √
      by(auto intro: new-thread-conf-compTP)
    hence bisim1-list1 t H ({0:Class D=None; body}, Addr a # replicate (max-vars body) unde-

```

```

fin-value) [] None [( [], Addr a # replicate (max-vars body) undefined-value, D, M, 0)]
proof
  from sees show  $P \vdash D$  sees  $M:[] \rightarrow T = [body]$  in  $D$  by(rule sees-method-idemp)

  show  $P, blocks1\ 0$  [Class  $D$ ] body,  $H \vdash (\{0:Class\ D=None; body\}, Addr\ a\ \# \text{replicate}\ (max\text{-vars}\ body)\ \text{undefined}\text{-value}) \leftrightarrow$ 
    ( [], Addr a # replicate (max-vars body) undefined-value, 0, None)
    by(auto intro: bisim1-refl)
  qed simp-all
  hence bisim1-list1  $t\ H$  ( $\{0:Class\ (fst\ (method\ P\ C\ M))=None; the\ (snd\ (snd\ (snd\ (method\ P\ C\ M))))\}$ ),
    Addr a # replicate (max-vars (the (snd (snd (snd (method P C M))))))
  undefined-value)
    []
    None [( [], Addr a # replicate (fst (snd (the (snd (snd (snd (method (compP2
  P) C M)))))) undefined-value,
    fst (method (compP2 P) C M), M, 0)]
    using sees sees' by simp }
  thus ?thesis
  apply(auto simp add: ta-bisim-def intro!: list-all2-all-nthI)
  apply(case-tac {ta}_t ! n)
  apply(auto simp add: extNTA2JVM-def extNTA2J1-def)
  done
qed
end

sublocale J1-JVM-conf-read < heap-conf?: J1-JVM-heap-conf
by(unfold-locales)

sublocale J1-JVM-conf-read < heap?: J1-heap
apply(rule J1-heap.intro)
apply(subst compP-heap[symmetric, where  $f=\lambda-$  - - -. compMb2, folded compP2-def])
apply(unfold-locales)
done
end

```

7.17 Correctness of Stage: From intermediate language to JVM

```

theory J1JVM imports J1JVMBisim begin

```

```

context J1-JVM-heap-base begin

```

```

declare  $\tau_{move1}.simps$  [simp del]  $\tau_{moves1}.simps$  [simp del]

```

```

lemma bisim1-insync-Throw-exec:

```

```

  assumes bisim2:  $P, e2, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, xcp)$ 
  shows  $\tau_{Exec}\text{-movet-a}\ P\ t\ (sync\ \vee\ (e1)\ e2)\ h\ (stk, loc, Suc\ (Suc\ (Suc\ (length\ (compE2\ e1)\ +\ pc))))$ ,
   $xcp$ ) ([Addr ad], loc, 6 + length (compE2 e1) + length (compE2 e2), None)
proof -
  from bisim2 have  $pc < length\ (compE2\ e2)$  and [simp]:  $xs = loc$  by(auto dest: bisim1-ThrowD)
  let ?pc = 6 + length (compE2 e1) + length (compE2 e2)

```

```

let ?stk = Addr ad # drop (size stk - 0) stk
from bisim2 have xcp = [ad] ∨ xcp = None by(auto dest: bisim1-ThrowD)
thus ?thesis
proof
  assume [simp]: xcp = [ad]
  have τExec-movet-a P t (sync_V (e1) e2) h (stk, loc, Suc (Suc (Suc (length (compE2 e1) + pc))),
  [ad]) (?stk, loc, ?pc, None)
  proof(rule τExec1step[unfolded exec-move-def, OF exec-catch])
    from bisim1-xcp-Some-not-caught[OF bisim2[simplified], of λC M Ts T. compMb2 Suc (Suc (Suc (length (compE2 e1)))) 0]
    have match-ex-table (compP2 P) (cname-of h ad) (Suc (Suc (Suc (length (compE2 e1) + pc))))
    (compxE2 e2 (Suc (Suc (Suc (length (compE2 e1)))))) 0 = None
    by(simp add: compP2-def)
    thus match-ex-table (compP2 P) (cname-of h ad) (Suc (Suc (Suc (length (compE2 e1) + pc))))
    (compxE2 (sync_V (e1) e2) 0 0) = [(6 + length (compE2 e1) + length (compE2 e2), 0)]
    using pc
    by(auto simp add: compP2-def match-ex-table-append matches-ex-entry-def eval-nat-numeral
    dest: match-ex-table-pc-length-compE2)
    qed(insert pc, auto intro: τmove2xcp)
  thus ?thesis by simp
next
  assume [simp]: xcp = None
  with bisim2 obtain pc'
    where τExec-movet-a P t e2 h (stk, loc, pc, None) ([Addr ad], loc, pc', [ad])
    and bisim': P, e2, h ⊢ (Throw ad, xs) ↔ ([Addr ad], loc, pc', [ad]) and [simp]: xs = loc
    by(auto dest: bisim1-Throw-τExec-movet)
  hence τExec-movet-a P t (sync_V (e1) e2) h (stk, loc, Suc (Suc (Suc (length (compE2 e1) + pc))),
  None) ([Addr ad], loc, Suc (Suc (Suc (length (compE2 e1) + pc'))), [ad])
  by-(rule Insync-τExecI)
  also let ?stk = Addr ad # drop (size [Addr ad] - 0) [Addr ad]
  from bisim' have pc': pc' < length (compE2 e2) by(auto dest: bisim1-ThrowD)
  have τExec-movet-a P t (sync_V (e1) e2) h ([Addr ad], loc, Suc (Suc (Suc (length (compE2 e1) +
  pc'))), [ad]) (?stk, loc, ?pc, None)
  proof(rule τExec1step[unfolded exec-move-def, OF exec-catch])
    from bisim1-xcp-Some-not-caught[OF bisim', of λC M Ts T. compMb2 Suc (Suc (Suc (length (compE2 e1)))) 0]
    have match-ex-table (compP2 P) (cname-of h ad) (Suc (Suc (Suc (length (compE2 e1) + pc'))))
    (compxE2 e2 (Suc (Suc (Suc (length (compE2 e1)))))) 0 = None
    by(simp add: compP2-def)
    thus match-ex-table (compP2 P) (cname-of h ad) (Suc (Suc (Suc (length (compE2 e1) + pc'))))
    (compxE2 (sync_V (e1) e2) 0 0) = [(6 + length (compE2 e1) + length (compE2 e2), 0)]
    using pc'
    by(auto simp add: compP2-def match-ex-table-append matches-ex-entry-def eval-nat-numeral
    dest: match-ex-table-pc-length-compE2)
    qed(insert pc', auto intro: τmove2xcp)
  finally (tranclp-trans) show ?thesis by simp
qed
qed
end

primrec sim12-size :: ('a, 'b, 'addr) exp ⇒ nat
  and sim12-sizes :: ('a, 'b, 'addr) exp list ⇒ nat
where

```

```

sim12-size (new C) = 0
| sim12-size (newA T[e]) = Suc (sim12-size e)
| sim12-size (Cast T e) = Suc (sim12-size e)
| sim12-size (e instanceof T) = Suc (sim12-size e)
| sim12-size (e «bop» e') = Suc (sim12-size e + sim12-size e')
| sim12-size (Val v) = 0
| sim12-size (Var V) = 0
| sim12-size (V := e) = Suc (sim12-size e)
| sim12-size (a[i]) = Suc (sim12-size a + sim12-size i)
| sim12-size (a[i] := e) = Suc (sim12-size a + sim12-size i + sim12-size e)
| sim12-size (a.length) = Suc (sim12-size a)
| sim12-size (e.F{D}) = Suc (sim12-size e)
| sim12-size (e.F{D} := e') = Suc (sim12-size e + sim12-size e')
| sim12-size (e.compareAndSwap(D.F, e', e'')) = Suc (sim12-size e + sim12-size e' + sim12-size e'')
| sim12-size (e.M(es)) = Suc (sim12-size e + sim12-sizes es)
| sim12-size ({V:T=vo; e}) = Suc (sim12-size e)
| sim12-size (syncV(e) e') = Suc (sim12-size e + sim12-size e')
| sim12-size (insyncV(a) e) = Suc (sim12-size e)
| sim12-size (e;; e') = Suc (sim12-size e + sim12-size e')
| sim12-size (if (e) e1 else e2) = Suc (sim12-size e)
| sim12-size (while(b) c) = Suc (Suc (sim12-size b))
| sim12-size (throw e) = Suc (sim12-size e)
| sim12-size (try e catch(C V) e') = Suc (sim12-size e)

| sim12-sizes [] = 0
| sim12-sizes (e # es) = sim12-size e + sim12-sizes es

```

lemma *sim12-sizes-map-Val* [simp]:

sim12-sizes (map Val vs) = 0

by(induct vs) simp-all

lemma *sim12-sizes-append* [simp]:

sim12-sizes (es @ es') = *sim12-sizes* es + *sim12-sizes* es'

by(induct es) simp-all

context *JVM-heap-base* **begin**

lemma *τExec-mover-length-compE2-conv* [simp]:

assumes *pc*: *pc* ≥ *length* (compE2 e)

shows *τExec-mover* *ci P t e h* (*stk, loc, pc, xcp*) *s* ↔ *s* = (*stk, loc, pc, xcp*)

proof

assume *τExec-mover* *ci P t e h* (*stk, loc, pc, xcp*) *s*

thus *s* = (*stk, loc, pc, xcp*) **using** *pc*

by *induct*(auto simp add: *τexec-move-def*)

qed *auto*

lemma *τExec-movesr-length-compE2-conv* [simp]:

assumes *pc*: *pc* ≥ *length* (compEs2 es)

shows *τExec-movesr* *ci P t es h* (*stk, loc, pc, xcp*) *s* ↔ *s* = (*stk, loc, pc, xcp*)

proof

assume *τExec-movesr* *ci P t es h* (*stk, loc, pc, xcp*) *s*

thus *s* = (*stk, loc, pc, xcp*) **using** *pc*

by *induct*(auto simp add: *τexec-moves-def*)

qed *auto*

end

context *J1-JVM-heap-base* begin

lemma assumes *wf*: *wf-J1-prog P*

defines [*simp*]: *sim-move* $\equiv \lambda e e'. \text{if } \text{sim12-size } e' < \text{sim12-size } e \text{ then } \tau \text{Exec-mover-a} \text{ else } \tau \text{Exec-movet-a}$
 and [*simp*]: *sim-moves* $\equiv \lambda es es'. \text{if } \text{sim12-sizes } es' < \text{sim12-sizes } es \text{ then } \tau \text{Exec-movesr-a} \text{ else } \tau \text{Exec-movest-a}$

shows *exec-instr-simulates-red1*:

$\llbracket P, E, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp); True, P, t \vdash 1 \langle e, (h, xs) \rangle \text{--}ta \rightarrow \langle e', (h', xs') \rangle; bsok E n \rrbracket$
 $\implies \exists pc'' stk'' loc'' xcp''. P, E, h' \vdash (e', xs') \leftrightarrow (stk'', loc'', pc'', xcp'') \wedge$
 (if $\tau \text{move1 } P h e$
 then $h = h' \wedge \text{sim-move } e e' P t E h (stk, loc, pc, xcp) (stk'', loc'', pc'', xcp'')$
 else $\exists pc' stk' loc' xcp'. \tau \text{Exec-mover-a } P t E h (stk, loc, pc, xcp) (stk', loc', pc', xcp') \wedge$
 $\text{exec-move-a } P t E h (stk', loc', pc', xcp') (\text{extTA2JVM } (compP2 P) ta) h'$
 $(stk'', loc'', pc'', xcp'') \wedge$
 $\neg \tau \text{move2 } (compP2 P) h stk' E pc' xcp' \wedge$
 $(call1 e = None \vee \text{no-call2 } E pc \vee pc' = pc \wedge stk' = stk \wedge loc' = loc \wedge$
 $xcp' = xcp))$
 (is $\llbracket -; -; - \rrbracket$
 $\implies \exists pc'' stk'' loc'' xcp''. - \wedge ?\text{exec } ta E e e' h stk loc pc xcp h' pc'' stk'' loc'' xcp''$)

and *exec-instr-simulates-reds1*:

$\llbracket P, Es, h \vdash (es, xs) [\leftrightarrow] (stk, loc, pc, xcp); True, P, t \vdash 1 \langle es, (h, xs) \rangle \text{--}ta \rightarrow \langle es', (h', xs') \rangle; bsoks Es n \rrbracket$
 $\implies \exists pc'' stk'' loc'' xcp''. P, Es, h' \vdash (es', xs') [\leftrightarrow] (stk'', loc'', pc'', xcp'') \wedge$
 (if $\tau \text{moves1 } P h es$
 then $h = h' \wedge \text{sim-moves } es es' P t Es h (stk, loc, pc, xcp) (stk'', loc'', pc'', xcp'')$
 else $\exists pc' stk' loc' xcp'. \tau \text{Exec-movesr-a } P t Es h (stk, loc, pc, xcp) (stk', loc', pc', xcp') \wedge$
 $\text{exec-moves-a } P t Es h (stk', loc', pc', xcp') (\text{extTA2JVM } (compP2 P) ta)$
 $h' (stk'', loc'', pc'', xcp'') \wedge$
 $\neg \tau \text{moves2 } (compP2 P) h stk' Es pc' xcp' \wedge$
 $(calls1 es = None \vee \text{no-calls2 } Es pc \vee pc' = pc \wedge stk' = stk \wedge loc' = loc \wedge$
 $xcp' = xcp))$
 (is $\llbracket -; -; - \rrbracket$
 $\implies \exists pc'' stk'' loc'' xcp''. - \wedge ?\text{execs } ta Es es es' h stk loc pc xcp h' pc'' stk'' loc'' xcp''$)

proof(induction *E n e xs stk loc pc xcp* and *Es n es xs stk loc pc xcp*)

arbitrary: $e' h' xs' Env T Env' T'$ and $es' h' xs' Env Ts Env' Ts'$ rule: *bisim1-bisims1-inducts-split*)

case (*bisim1Call1 obj n obj' xs stk loc pc xcp ps M'*)

note *IHobj* = *bisim1Call1.IH(2)*

note *IHparam* = *bisim1Call1.IH(4)*

note *bisim1* = $\langle P, obj, h \vdash (obj', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$

note *bisim2* = $\langle \bigwedge xs. P, ps, h \vdash (ps, xs) [\leftrightarrow] ([], xs, 0, None) \rangle$

note *bsok* = $\langle bsok (obj \cdot M'(ps)) n \rangle$

note *red* = $\langle True, P, t \vdash 1 \langle obj' \cdot M'(ps), (h, xs) \rangle \text{--}ta \rightarrow \langle e', (h', xs') \rangle \rangle$

from *red* show *?case*

proof(*cases*)

case (*Call1Obj E'*)

note [*simp*] = $\langle e' = E' \cdot M'(ps) \rangle$

and *red* = $\langle True, P, t \vdash 1 \langle obj', (h, xs) \rangle \text{--}ta \rightarrow \langle E', (h', xs') \rangle \rangle$

from *red* have τ : $\tau \text{move1 } P h obj' = \tau \text{move1 } P h (obj' \cdot M'(ps))$ by(*auto simp add: $\tau \text{move1.simps}$*

$\tau \text{moves1.simps}$)

moreover from *red* have *call1* ($obj' \cdot M'(ps)$) = *call1 obj'* by *auto*

moreover from $IHobj[OF\ red]\ bsok$
obtain $pc''\ stk''\ loc''\ xcp''$ **where** $bisim: P, obj, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$
and $redo: ?exec\ ta\ obj\ obj'\ E'\ h\ stk\ loc\ pc\ xcp\ h'\ pc''\ stk''\ loc''\ xcp''$ **by** *auto*
from $bisim$
have $P, obj \cdot M'(ps), h' \vdash (E' \cdot M'(ps), xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$
by(*rule bisim1-bisims1.bisim1Call1*)
moreover {
assume $no-call2\ obj\ pc$
hence $no-call2\ (obj \cdot M'(ps))\ pc \vee pc = length\ (compE2\ obj)$ **by**(*auto simp add: no-call2-def*) }
ultimately show $?thesis$ **using** $redo$
by(*auto simp del: call1.simps calls1.simps split: if-split-asm split del: if-split*)(*blast intro: Call- τ ExecrI1 Call- τ ExecI1 exec-move-CallI1*) +
next
case ($Call1Params\ es\ v$)
note $[simp] = \langle obj' = Val\ v \rangle \langle e' = Val\ v \cdot M'(es) \rangle$
and $red = \langle True, P, t \vdash 1 \ \langle ps, (h, xs) \rangle [-ta \rightarrow] \langle es, (h', xs') \rangle \rangle$
from red **have** $\tau: \tau move1\ P\ h\ (obj' \cdot M'(ps)) = \tau moves1\ P\ h\ ps$ **by**(*auto simp add: $\tau move1$.simps $\tau moves1$.simps*)
from $bisim1$ **have** $s: xcp = None\ xs = loc$
and $execo: \tau Exec-mover-a\ P\ t\ obj\ h\ (stk, loc, pc, xcp)\ ([v], loc, length\ (compE2\ obj), None)$
by(*auto dest: bisim1Val2D1*)
hence $\tau Exec-mover-a\ P\ t\ (obj \cdot M'(ps))\ h\ (stk, loc, pc, xcp)\ ([v], loc, length\ (compE2\ obj), None)$
by-(*rule Call- τ ExecrI1*)
moreover from $IHparam[OF\ red]\ bsok$ **obtain** $pc''\ stk''\ loc''\ xcp''$
where $bisim': P, ps, h' \vdash (es, xs') [\leftrightarrow] (stk'', loc'', pc'', xcp'')$
and $exec': ?execs\ ta\ ps\ ps\ es\ h\ []\ xs\ 0\ None\ h'\ pc''\ stk''\ loc''\ xcp''$ **by** *auto*
have $?exec\ ta\ (obj \cdot M'(ps))\ (obj' \cdot M'(ps))\ (obj' \cdot M(es))\ h\ [v]\ loc\ (length\ (compE2\ obj))\ None\ h'$
 $(length\ (compE2\ obj) + pc'')\ (stk''\ @\ [v])\ loc''\ xcp''$
proof(*cases $\tau move1\ P\ h\ (obj' \cdot M'(ps))$*)
case $True$
with $exec'\ \tau$ **have** $[simp]: h = h'$
and $e: sim-moves\ ps\ es\ P\ t\ ps\ h\ ([], xs, 0, None)\ (stk'', loc'', pc'', xcp'')$ **by** *auto*
from e **have** $sim-move\ (obj' \cdot M'(ps))\ (obj' \cdot M'(es))\ P\ t\ (obj \cdot M'(ps))\ h\ ([]\ @\ [v], xs, length\ (compE2\ obj) + 0, None)\ (stk''\ @\ [v], loc'', length\ (compE2\ obj) + pc'', xcp'')$
by(*fastforce dest: Call- τ ExecrI2 Call- τ ExecI2*)
with $s\ True$ **show** $?thesis$ **by** *auto*
next
case $False$
with $exec'\ \tau$ **obtain** $pc'\ stk'\ loc'\ xcp'$
where $e: \tau Exec-movesr-a\ P\ t\ ps\ h\ ([], xs, 0, None)\ (stk', loc', pc', xcp')$
and $e': exec-moves-a\ P\ t\ ps\ h\ (stk', loc', pc', xcp')\ (extTA2JVM\ (compP2\ P)\ ta)\ h'\ (stk'', loc'', pc'', xcp'')$
and $\tau': \neg\ \tau moves2\ (compP2\ P)\ h\ stk'\ ps\ pc'\ xcp'$
and $call: calls1\ ps = None \vee no-calls2\ ps\ 0 \vee pc' = 0 \wedge stk' = [] \wedge loc' = xs \wedge xcp' = None$
by *auto*
from e **have** $\tau Exec-mover-a\ P\ t\ (obj \cdot M'(ps))\ h\ ([]\ @\ [v], xs, length\ (compE2\ obj) + 0, None)\ (stk'\ @\ [v], loc', length\ (compE2\ obj) + pc', xcp')$ **by**(*rule Call- τ ExecrI2*)
moreover from e' **have** $exec-move-a\ P\ t\ (obj \cdot M'(ps))\ h\ (stk'\ @\ [v], loc', length\ (compE2\ obj) + pc', xcp')\ (extTA2JVM\ (compP2\ P)\ ta)\ h'\ (stk''\ @\ [v], loc'', length\ (compE2\ obj) + pc'', xcp'')$
by(*rule exec-move-CallI2*)
moreover from $\tau'\ e'$ **have** $\tau move2\ (compP2\ P)\ h\ (stk'\ @\ [v])\ (obj \cdot M'(ps))\ (length\ (compE2\ obj) + pc')\ xcp' \implies False$
by(*fastforce simp add: $\tau move2$ -iff $\tau moves2$ -iff $\tau instr$ -stk-drop-exec-moves split: if-split-asm*)
moreover from red **have** $call1\ (obj' \cdot M'(ps)) = calls1\ ps$ **by**(*auto simp add: is-vals-conv*)

moreover have $no\text{-calls2 } ps \ 0 \implies no\text{-call2 } (obj \cdot M'(ps)) \ (length \ (compE2 \ obj)) \ \vee \ ps = [] \ calls1$
 $[] = None$
by(*auto simp add: no-calls2-def no-call2-def*)
ultimately show *?thesis using False s call*
by(*auto simp del: split-paired-Ex call1.simps calls1.simps*) *blast*
qed
moreover from *bisim'*
have $P, obj \cdot M'(ps), h' \vdash (Val \ v \cdot M'(es), xs') \leftrightarrow ((stk'' \ @ \ [v]), loc'', length \ (compE2 \ obj) + pc'', xcp')$
by(*rule bisim1-bisims1.bisim1CallParams*)
moreover from *bisim1* **have** $pc \neq length \ (compE2 \ obj) \longrightarrow no\text{-call2 } (obj \cdot M'(ps)) \ pc$
by(*auto simp add: no-call2-def dest: bisim-Val-pc-not-Invoke bisim1-pc-length-compE2*)
ultimately show *?thesis using τ execo*
apply(*auto simp del: split-paired-Ex call1.simps calls1.simps split: if-split-asm split del: if-split*)
apply(*blast intro: τ Exec-mover-trans|fastforce elim!: τ Exec-mover-trans simp del: split-paired-Ex call1.simps calls1.simps*)
done
next
case (*Call1ThrowObj a*)
note $[simp] = \langle obj' = Throw \ a \rangle \langle ta = \varepsilon \rangle \langle e' = Throw \ a \rangle \langle h' = h \rangle \langle xs' = xs \rangle$
have $\tau: \tau move1 \ P \ h \ (Throw \ a \cdot M'(ps))$ **by**(*rule $\tau move1 CallThrowObj$*)
from *bisim1* **have** $xcp = [a] \ \vee \ xcp = None$ **by**(*auto dest: bisim1-ThrowD*)
thus *?thesis*
proof
assume $[simp]: xcp = [a]$
with *bisim1* **have** $P, obj \cdot M'(ps), h \vdash (Throw \ a, xs) \leftrightarrow (stk, loc, pc, [a])$
by(*auto intro: bisim1-bisims1.bisim1CallThrowObj*)
thus *?thesis using τ by fastforce*
next
assume $[simp]: xcp = None$
with *bisim1* **obtain** pc'
where $\tau Exec\text{-mover}\text{-}a \ P \ t \ obj \ h \ (stk, loc, pc, None) \ ([Addr \ a], loc, pc', [a])$
and *bisim'*: $P, obj, h \vdash (Throw \ a, xs) \leftrightarrow ([Addr \ a], loc, pc', [a])$ **and** $[simp]: xs = loc$
by(*auto dest: bisim1-Throw- τ Exec-mover*)
hence $\tau Exec\text{-mover}\text{-}a \ P \ t \ (obj \cdot M'(ps)) \ h \ (stk, loc, pc, None) \ ([Addr \ a], loc, pc', [a])$
by-(*rule Call- τ ExecrI1*)
moreover from *bisim'* **have** $P, obj \cdot M'(ps), h \vdash (Throw \ a, xs) \leftrightarrow ([Addr \ a], loc, pc', [a])$
by(*rule bisim1CallThrowObj*)
ultimately show *?thesis using τ by auto*
qed
next
case (*Call1ThrowParams vs a es' v*)
note $[simp] = \langle obj' = Val \ v \rangle \langle ta = \varepsilon \rangle \langle e' = Throw \ a \rangle \langle h' = h \rangle \langle xs' = xs \rangle$
and $ps = \langle ps = map \ Val \ vs \ @ \ Throw \ a \ \# \ es' \rangle$
from *bisim1* **have** $[simp]: xcp = None \ xs = loc$
and $\tau Exec\text{-mover}\text{-}a \ P \ t \ obj \ h \ (stk, loc, pc, xcp) \ ([v], loc, length \ (compE2 \ obj), None)$
by(*auto dest: bisim1Val2D1*)
hence $\tau Exec\text{-mover}\text{-}a \ P \ t \ (obj \cdot M'(ps)) \ h \ (stk, loc, pc, xcp) \ ([v], loc, length \ (compE2 \ obj), None)$
by-(*rule Call- τ ExecrI1*)
also from *bisims1-Throw- τ Exec-movest*[*OF bisim2*][*of xs, unfolded ps*]
obtain pc' **where** *exec'*: $\tau Exec\text{-mover}\text{-}a \ P \ t \ (map \ Val \ vs \ @ \ Throw \ a \ \# \ es') \ h \ ([], xs, 0, None)$
 $(Addr \ a \ \# \ rev \ vs, xs, pc', [a])$
and *bisim'*: $P, map \ Val \ vs \ @ \ Throw \ a \ \# \ es', h \vdash (map \ Val \ vs \ @ \ Throw \ a \ \# \ es', xs) \ [\leftrightarrow] \ (Addr \ a \ \# \ rev \ vs, xs, pc', [a])$

```

  by auto
  from Call- $\tau$ ExecI2[OF exec', of obj M' v] ps
  have  $\tau$ Exec-mover-a P t (obj.M'(ps)) h ([v], loc, length (compE2 obj), None) (Addr a # rev vs @
[v], xs, length (compE2 obj) + pc', [a]) by simp
  also from bisim1-bisims1.bisim1CallThrowParams[OF bisim', of obj M' v] ps
  have bisim'': P,obj.M'(ps),h  $\vdash$  (Throw a, xs)  $\leftrightarrow$  (Addr a # rev vs @ [v], xs, length (compE2 obj)
+ pc', [a]) by simp
  moreover have  $\tau$ move1 P h (obj'.M'(ps)) using ps by(auto intro:  $\tau$ move1CallThrowParams)
  ultimately show ?thesis by fastforce
next
case (Red1CallExternal a Ta Ts Tr D vs va H')
hence [simp]: obj' = addr a ps = map Val vs
  e' = extRet2J (addr a.M'(map Val vs)) va H' = h' xs' = xs
  and Ta: typeof-addr h a = [Ta]
  and iec: P  $\vdash$  class-type-of Ta sees M': Ts $\rightarrow$ Tr = Native in D
  and redex: P,t  $\vdash$   $\langle$ a.M'(vs),h $\rangle$  -ta $\rightarrow$ ext  $\langle$ va,h $\rangle$  by auto
from bisim1 have [simp]: xs = loc by(auto dest: bisim-Val-loc-eq-xcp-None)
  have  $\tau$ :  $\tau$ move1 P h (addr a.M'(map Val vs))  $\longleftrightarrow$   $\tau$ move2 (compP2 P) h (rev vs @ [Addr a])
(obj.M'(ps)) (length (compE2 obj) + length (compEs2 ps)) None using Ta iec
  by(auto simp add: map-eq-append-conv  $\tau$ move1.simps  $\tau$ moves1.simps  $\tau$ move2-iff compP2-def)
from bisim1 have s: xcp = None lcl (h, xs) = loc
  and  $\tau$ Exec-mover-a P t obj h (stk, loc, pc, xcp) ([Addr a], loc, length (compE2 obj), None)
  by(auto dest: bisim1Val2D1)
hence  $\tau$ Exec-mover-a P t (obj.M'(ps)) h (stk, loc, pc, xcp) ([Addr a], loc, length (compE2 obj),
None)
  by-(rule Call- $\tau$ ExecrI1)
  also have  $\tau$ Exec-movesr-a P t ps h ([], loc, 0, None) (rev vs, loc, length (compEs2 ps), None)
  unfolding  $\langle$ ps = map Val vs $\rangle$  by(rule  $\tau$ Exec-movesr-map-Val)
  from Call- $\tau$ ExecrI2[OF this, of obj M' Addr a]
  have  $\tau$ Exec-mover-a P t (obj.M'(ps)) h ([Addr a], loc, length (compE2 obj), None) (rev vs @ [Addr
a], loc, length (compE2 obj) + length (compEs2 ps), None) by simp
  also (rtranclp-trans) from bisim1 have pc  $\leq$  length (compE2 obj) by(rule bisim1-pc-length-compE2)
  hence no-call2 (obj.M'(ps)) pc  $\vee$  pc = length (compE2 obj) + length (compEs2 ps)
  using bisim1 by(fastforce simp add: no-call2-def neq-Nil-conv dest: bisim-Val-pc-not-Invoke)
  moreover {
    assume pc = length (compE2 obj) + length (compEs2 ps)
    with  $\langle$  $\tau$ Exec-mover-a P t obj h (stk, loc, pc, xcp) ([Addr a], loc, length (compE2 obj), None) $\rangle$ 
    have stk = rev vs @ [Addr a] xcp = None by auto }
  moreover
  let ?ret = extRet2JVM (length ps) h' (rev vs @ [Addr a]) loc undefined undefined (length (compE2
obj) + length (compEs2 ps)) [] va
  let ?stk' = fst (hd (snd (snd ?ret)))
  let ?xcp' = fst ?ret
  let ?pc' = snd (snd (snd (snd (hd (snd (snd ?ret)))))
  from redex have redex': (ta, va, h')  $\in$  red-external-aggr (compP2 P) t a M' vs h
  by -(rule red-external-imp-red-external-aggr, simp add: compP2-def)
  with Ta iec redex'
  have exec-move-a P t (obj.M'(ps)) h (rev vs @ [Addr a], loc, length (compE2 obj) + length (compEs2
ps), None) (extTA2JVM (compP2 P) ta) h' (?stk', loc, ?pc', ?xcp')
  unfolding exec-move-def
  by-(rule exec-instr,cases va,(force simp add: compP2-def simp del: split-paired-Ex)+)
  moreover have P,obj.M'(ps),h'  $\vdash$  (extRet2J1 (addr a.M'(map Val vs)) va, loc)  $\leftrightarrow$  (?stk', loc,
?pc', ?xcp')
  proof(cases va)

```

```

  case (RetVal v)
  have P,obj·M'(ps),h' ⊢ (Val v, loc) ↔ ([v], loc, length (compE2 (obj·M'(ps))), None)
    by(rule bisim1Val2) simp
  thus ?thesis unfolding RetVal by simp
next
  case (RetExc ad) thus ?thesis by(auto intro: bisim1CallThrow)
next
  case RetStaySame
  from bisims1-map-Val-append[OF bisims1Nil, of map Val vs vs P h' loc]
  have P,map Val vs,h' ⊢ (map Val vs, loc) [↔] (rev vs, loc, length (compEs2 (map Val vs)), None)
by simp
  hence P,obj·M'(map Val vs),h' ⊢ (addr a·M'(map Val vs), loc) ↔ (rev vs @ [Addr a], loc, length
(compE2 obj) + length (compEs2 (map Val vs)), None)
    by(rule bisim1CallParams)
  thus ?thesis using RetStaySame by simp
qed
moreover from redex Ta iec
  have τmove1 P h (addr a·M'(map Val vs)) ⇒ ta = ε ∧ h' = h
  by(fastforce simp add: τmove1.simps τmoves1.simps map-eq-append-conv τexternal'-def τexternal-def
dest: τexternal'-red-external-heap-unchanged τexternal'-red-external-TA-empty sees-method-fun)
  ultimately show ?thesis using τ
    apply(cases τmove1 P h (addr a·M'(map Val vs)) :: 'addr expr1)
    apply(auto simp del: split-paired-Ex simp add: compP2-def)
    apply(blast intro: rtranclp.rtrancl-into-rtrancl rtranclp-into-tranclp1 τexec-moveI)+
    done
next
  case (Red1CallNull vs)
  note [simp] = ⟨h' = h⟩ ⟨xs' = xs⟩ ⟨ta = ε⟩ ⟨obj' = null⟩ ⟨ps = map Val vs⟩ ⟨e' = THROW
NullPointer⟩
  from bisim1 have s: xcp = None xs = loc
    and τExec-mover-a P t obj h (stk, loc, pc, xcp) ([Null], loc, length (compE2 obj), None)
    by(auto dest: bisim1Val2D1)
  hence τExec-mover-a P t (obj·M'(map Val vs)) h (stk, loc, pc, xcp) ([Null], loc, length (compE2
obj), None)
    by-(rule Call-τExecrI1)
  also have τExec-movesr-a P t (map Val vs) h ([], loc, 0, None) (rev vs, loc, length (compEs2 (map
Val vs)), None)
  proof(cases vs)
    case Nil thus ?thesis by(auto)
  next
    case Cons
    with bisims1-refl[of P h map Val vs loc, simplified] show ?thesis
      by -(drule bisims1-Val-τExec-moves, auto)
  qed
  from Call-τExecrI2[OF this, of obj M' Null]
  have τExec-mover-a P t (obj·M'(map Val vs)) h ([Null], loc, length (compE2 obj), None) (rev vs
@ [Null], loc, length (compE2 obj) + length (compEs2 (map Val vs)), None) by simp
  also (rtranclp-trans) {
    have τmove2 (compP2 P) h (rev vs @ [Null]) (obj·M'(map Val vs)) (length (compE2 obj) +
length (compEs2 (map Val vs))) None
      by(simp add: τmove2-iff)
    moreover have exec-move-a P t (obj·M'(map Val vs)) h (rev vs @ [Null], loc, length (compE2
obj) + length (compEs2 (map Val vs)), None) ε h (rev vs @ [Null], loc, length (compE2 obj) + length
(compEs2 (map Val vs)), [addr-of-sys-xcpt NullPointer])

```

unfolding *exec-move-def* **by**(*cases vs*)(*auto intro: exec-instr*)
ultimately have τ *Exec-movet-a* P t (*obj*· M' (*map Val vs*)) h (*rev vs* @ [*Null*], *loc*, *length* (*compE2 obj*) + *length* (*compEs2* (*map Val vs*)), *None*) (*rev vs* @ [*Null*], *loc*, *length* (*compE2 obj*) + *length* (*compEs2* (*map Val vs*)), [*addr-of-sys-xcpt NullPointer*])
by(*auto intro: τ exec-moveI simp add: compP2-def*) }
also have τ *move1* P h (*null*· M' (*map Val vs*)) **by**(*auto simp add: τ move1.simps τ moves1.simps map-eq-append-conv*)
moreover have $P, \text{obj} \cdot M'(\text{map Val vs}), h \vdash (\text{THROW NullPointer}, \text{loc}) \leftrightarrow ((\text{rev vs} @ [\text{Null}]), \text{loc}, \text{length}(\text{compE2 obj}) + \text{length}(\text{compEs2}(\text{map Val vs})), [\text{addr-of-sys-xcpt NullPointer}])$
by(*rule bisim1CallThrow*) *simp*
ultimately show *?thesis using s* **by**(*auto simp del: split-paired-Ex*)
qed
next
case *bisim1Val2* **thus** *?case by fastforce*
next
case (*bisim1New C' n xs*)
have $\tau: \neg \tau$ *move1* P h (*new C'*) **by**(*auto simp add: τ move1.simps τ moves1.simps*)
from $\langle \text{True}, P, t \vdash 1 \langle \text{new } C', (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$ **show** *?case*
proof *cases*
case (*Red1New a*)
hence *exec-meth-a* (*compP2 P*) [*New C'*] [] t h ([], *xs*, 0, *None*) $\{\!|$ *NewHeapElem a* (*Class-type C'*) $\!\}$
 h' ([*Addr a*], *xs*, *Suc 0*, *None*)
and [*simp*]: $e' = \text{addr } a \text{ } xs' = xs \text{ } ta = \{\!|$ *NewHeapElem a* (*Class-type C'*) $\!\}$
by (*auto intro!: exec-instr simp add: compP2-def simp del: fun-upd-apply cong cong del: image-cong-simp*)
moreover have $P, \text{new } C', h' \vdash (\text{addr } a, xs) \leftrightarrow ([\text{Addr } a], xs, \text{length}(\text{compE2}(\text{new } C')), \text{None})$
by(*rule bisim1Val2*)(*simp*)
moreover have $\neg \tau$ *move2* (*compP2 P*) h [] (*new C'*) 0 *None* **by**(*simp add: τ move2-iff*)
ultimately show *?thesis using τ*
by(*fastforce simp add: exec-move-def ta-upd-simps*)
next
case *Red1NewFail*
hence *exec-meth-a* (*compP2 P*) [*New C'*] [] t h ([], *xs*, 0, *None*) ε h' ([], *xs*, 0, [*addr-of-sys-xcpt OutOfMemory*])
and [*simp*]: $ta = \varepsilon \text{ } xs' = xs \text{ } e' = \text{THROW OutOfMemory}$
by(*auto intro!: exec-instr simp add: compP2-def simp del: fun-upd-apply*)
moreover have $P, \text{new } C', h' \vdash (\text{THROW OutOfMemory}, xs) \leftrightarrow ([], xs, 0, [\text{addr-of-sys-xcpt OutOfMemory}])$
by(*rule bisim1NewThrow*)
moreover have $\neg \tau$ *move2* (*compP2 P*) h [] (*new C'*) 0 *None* **by**(*simp add: τ move2-iff*)
ultimately show *?thesis using τ* **by**(*fastforce simp add: exec-move-def*)
qed
next
case *bisim1NewThrow* **thus** *?case by fastforce*
next
case (*bisim1NewArray E n e xs stk loc pc xcp U*)
note $IH = \text{bisim1NewArray.IH}(2)$
note $\text{bisim} = \langle P, E, h \vdash (e, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \rangle$
note $\text{red} = \langle \text{True}, P, t \vdash 1 \langle \text{newA } U[e], (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$
note $\text{bsok} = \langle \text{bsok}(\text{newA } U[E]) \text{ } n \rangle$
from red **show** *?case*
proof *cases*
case (*New1ArrayRed ee'*)
note [*simp*] = $\langle e' = \text{newA } U[ee'] \rangle$

and $red = \langle True, P, t \vdash 1 \langle e, (h, xs) \rangle -ta \rightarrow \langle ee', (h', xs') \rangle \rangle$
from red **have** $\tau move1 P h (newA U[e]) = \tau move1 P h e$ **by** $(auto simp add: \tau move1.simps \tau moves1.simps)$
moreover from red **have** $call1 (newA U[e]) = call1 e$ **by** $auto$
moreover from $IH[OF red]$ $bsok$
obtain $pc'' stk'' loc'' xcp''$ **where** $bisim: P, E, h' \vdash (ee', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$
and $redo: ?exec ta E e ee' h stk loc pc xcp h' pc'' stk'' loc'' xcp''$ **by** $auto$
from $bisim$
have $P, newA U[E], h' \vdash (newA U[ee'], xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$
by $(rule bisim1-bisims1.bisim1NewArray)$
moreover {
assume $no-call2 E pc$
hence $no-call2 (newA U[E]) pc$ **by** $(auto simp add: no-call2-def)$ }
ultimately show $?thesis$ **using** $redo$
by $(auto simp del: call1.simps calls1.simps split: if-split-asm split del: if-split)(blast intro: NewArray-\tau ExecrI NewArray-\tau ExecrI exec-move-newArrayI)+$
next
case $(Red1NewArray i a)$
note $[simp] = \langle e = Val (Intg i) \rangle \langle ta = \{\!\{NewHeapElem a (Array-type U (nat (sint i)))\}\!\} \rangle \langle e' = addr a \rangle \langle xs' = xs \rangle$
and $new = \langle (h', a) \in allocate h (Array-type U (nat (sint i))) \rangle$
from $bisim$ **have** $s: xcp = None xs = loc$ **by** $(auto dest: bisim-Val-loc-eq-xcp-None)$
from $bisim$ **have** $\tau Exec-mover-a P t E h (stk, loc, pc, xcp) ([Intg i], loc, length (compE2 E), None)$
by $(auto dest: bisim1Val2D1)$
hence $\tau Exec-mover-a P t (newA U[E]) h (stk, loc, pc, xcp) ([Intg i], loc, length (compE2 E), None)$
by $(rule NewArray-\tau ExecrI)$
moreover from $new \langle 0 \leq s \ i \rangle$
have $exec-move-a P t (newA U[E]) h ([Intg i], loc, length (compE2 E), None) \{\!\{NewHeapElem a (Array-type U (nat (sint i)))\}\!\} h' ([Addr a], loc, Suc (length (compE2 E)), None)$
by $(auto intro!: exec-instr simp add: compP2-def exec-move-def cong del: image-cong-simp)$
moreover have $\tau move2 (compP2 P) h [Intg i] (newA U[E]) (length (compE2 E)) None \implies False$ **by** $(simp add: \tau move2-iff)$
moreover have $\neg \tau move1 P h (newA U[Val (Intg i)])$ **by** $(auto simp add: \tau move1.simps \tau moves1.simps)$
moreover have $P, newA U[E], h' \vdash (addr a, loc) \leftrightarrow ([Addr a], loc, length (compE2 (newA U[E])), None)$
by $(rule bisim1Val2) simp$
ultimately show $?thesis$ **using** s **by** $(auto simp del: fun-upd-apply simp add: ta-upd-simps) blast$
next
case $(Red1NewArrayNegative i)$
note $[simp] = \langle e = Val (Intg i) \rangle \langle e' = THROW NegativeArraySize \rangle \langle h' = h \rangle \langle xs' = xs \rangle \langle ta = \varepsilon \rangle$
have $\neg \tau move1 P h (newA U[Val (Intg i)])$ **by** $(auto simp add: \tau move1.simps \tau moves1.simps)$
moreover from $bisim$ **have** $s: xcp = None xs = loc$
and $\tau Exec-mover-a P t E h (stk, loc, pc, xcp) ([Intg i], loc, length (compE2 E), None)$
by $(auto dest: bisim1Val2D1)$
moreover from $\langle i < s \ 0 \rangle$
have $exec-meth-a (compP2 P) (compE2 (newA U[E])) (compxE2 (newA U[E]) 0 0) t h ([Intg i], loc, length (compE2 E), None) \varepsilon h ([Intg i], loc, length (compE2 E), [addr-of-sys-xcpt NegativeArraySize])$
by $-(rule exec-instr, auto simp add: compP2-def)$
moreover have $\tau move2 (compP2 P) h [Intg i] (newA U[E]) (length (compE2 E)) None \implies False$ **by** $(simp add: \tau move2-iff)$
moreover
have $P, newA U[E], h \vdash (THROW NegativeArraySize, loc) \leftrightarrow ([Intg i], loc, length (compE2 E),$

```

[addr-of-sys-xcpt NegativeArraySize]
  by(auto intro!: bisim1-bisims1.bisim1NewArrayFail)
  ultimately show ?thesis using s
  by(auto simp add: exec-move-def)(blast intro: NewArray-τExecrI)
next
case (Red1NewArrayFail i)
note [simp] = ⟨e = Val (Intg i)⟩ ⟨e' = THROW OutOfMemory⟩ ⟨xs' = xs⟩ ⟨ta = ε⟩ ⟨h' = h⟩
  and new = ⟨allocate h (Array-type U (nat (sint i))) = {}⟩
have ¬ τmove1 P h (newA U [Val (Intg i)]) by(auto simp add: τmove1.simps τmoves1.simps)
moreover from bisim have s: xcp = None xs = loc
  and τExec-mover-a P t E h (stk, loc, pc, xcp) ([Intg i], loc, length (compE2 E), None)
  by(auto dest: bisim1Val2D1)
moreover from ⟨0 ≤ s i⟩ new
have exec-meth-a (compP2 P) (compE2 (newA U [E])) (compxE2 (newA U [E]) 0 0) t h ([Intg i],
loc, length (compE2 E), None) ε h' ([Intg i], loc, length (compE2 E), [addr-of-sys-xcpt OutOfMemory])
  by -(rule exec-instr, auto simp add: compP2-def)
moreover have τmove2 (compP2 P) h [Intg i] (newA U [E]) (length (compE2 E)) None ⇒
False by(simp add: τmove2-iff)
moreover
  have P, newA U [E], h' ⊢ (THROW OutOfMemory, loc) ↔ ([Intg i], loc, length (compE2 E),
[addr-of-sys-xcpt OutOfMemory])
  by(auto intro!: bisim1-bisims1.bisim1NewArrayFail)
  ultimately show ?thesis using s by (auto simp add: exec-move-def)(blast intro: NewArray-τExecrI)
next
case (New1ArrayThrow a)
note [simp] = ⟨e = Throw a⟩ ⟨h' = h⟩ ⟨xs' = xs⟩ ⟨ta = ε⟩ ⟨e' = Throw a⟩
have τ: τmove1 P h (newA U [e]) by(auto intro: τmove1NewArrayThrow)
from bisim have xcp = [a] ∨ xcp = None by(auto dest: bisim1-ThrowD)
thus ?thesis
proof
  assume [simp]: xcp = [a]
  with bisim have P, newA U [E], h ⊢ (Throw a, xs) ↔ (stk, loc, pc, xcp)
  by(auto intro: bisim1-bisims1.bisim1NewArrayThrow)
  thus ?thesis using τ by(fastforce)
next
assume [simp]: xcp = None
with bisim obtain pc'
  where τExec-mover-a P t E h (stk, loc, pc, None) ([Addr a], loc, pc', [a])
  and bisim': P, E, h ⊢ (Throw a, xs) ↔ ([Addr a], loc, pc', [a]) and [simp]: xs = loc
  by(auto dest: bisim1-Throw-τExec-mover)
hence τExec-mover-a P t (newA U [E]) h (stk, loc, pc, None) ([Addr a], loc, pc', [a])
  by-(rule NewArray-τExecrI)
moreover from bisim' have P, newA U [E], h ⊢ (Throw a, xs) ↔ ([Addr a], loc, pc', [a])
  by(rule bisim1-bisims1.bisim1NewArrayThrow)
ultimately show ?thesis using τ by auto
qed
qed
next
case bisim1NewArrayThrow thus ?case by auto
next
case bisim1NewArrayFail thus ?case by auto
next
case (bisim1Cast E n e xs stk loc pc xcp U)
note IH = bisim1Cast.IH(2)

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note bisim =  $\langle P, E, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
note red =  $\langle True, P, t \vdash 1 \langle Cast\ U\ e, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$ 
note bsok =  $\langle bsok\ (Cast\ U\ E)\ n \rangle$ 
from red show ?case
proof cases
  case (Cast1Red ee')
    note [simp] =  $\langle e' = Cast\ U\ ee' \rangle$ 
    and red =  $\langle True, P, t \vdash 1 \langle e, (h, xs) \rangle -ta \rightarrow \langle ee', (h', xs') \rangle \rangle$ 
    from red have  $\tau move1\ P\ h\ (Cast\ U\ e) = \tau move1\ P\ h\ e$  by(auto simp add:  $\tau move1.simps$ 
 $\tau moves1.simps$ )
    moreover from red have call1 (Cast U e) = call1 e by auto
    moreover from IH[OF red] bsok
    obtain pc'' stk'' loc'' xcp'' where bisim:  $P, E, h' \vdash (ee', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$ 
    and redo: ?exec ta E e ee' h stk loc pc xcp h' pc'' stk'' loc'' xcp'' by auto
    from bisim
    have  $P, Cast\ U\ E, h' \vdash (Cast\ U\ ee', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$ 
    by(rule bisim1-bisims1.bisim1Cast)
    moreover {
      assume no-call2 E pc
      hence no-call2 (Cast U E) pc by(auto simp add: no-call2-def) }
    ultimately show ?thesis using redo
      by(auto simp del: call1.simps calls1.simps split: if-split-asm split del: if-split)(blast intro:
Cast- $\tau$ ExecrI Cast- $\tau$ ExecI exec-move-CastI)
    next
    case (Red1Cast c U')
    hence [simp]:  $e = Val\ c\ ta = \varepsilon\ e' = Val\ c\ h' = h\ xs' = xs$ 
    and type:  $typeof_h\ c = [U']\ P \vdash U' \leq U$  by auto
    from bisim have  $s: xcp = None\ xs = loc$  by(auto dest: bisim-Val-loc-eq-xcp-None)
    from bisim have  $\tau Exec-mover-a\ P\ t\ E\ h\ (stk, loc, pc, xcp)\ ([c], loc, length\ (compE2\ E), None)$ 
    by(auto dest: bisim1Val2D1)
    hence  $\tau Exec-mover-a\ P\ t\ (Cast\ U\ E)\ h\ (stk, loc, pc, xcp)\ ([c], loc, length\ (compE2\ E), None)$ 
    by(rule Cast- $\tau$ ExecrI)
    moreover from type
    have exec-meth-a (compP2 P) (compE2 (Cast U E)) (compxE2 (Cast U E) 0 0) t h ([c], loc,
length (compE2 E), None)  $\varepsilon\ h'$  ([c], loc, Suc (length (compE2 E)), None)
    by(auto intro!: exec-instr simp add: compP2-def)
    moreover have  $\tau move2\ (compP2\ P)\ h\ [c]\ (Cast\ U\ E)\ (length\ (compE2\ E))\ None$  by(simp add:
 $\tau move2-iff$ )
    ultimately have  $\tau Exec-mover-a\ P\ t\ (Cast\ U\ E)\ h\ (stk, loc, pc, xcp)\ ([c], loc, Suc\ (length\ (compE2$ 
E)), None)
    by(fastforce elim: rtrancl.rtrancl-into-rtrancl intro:  $\tau exec-moveI$  simp add: exec-move-def compP2-def)
    moreover have  $\tau move1\ P\ h\ (Cast\ U\ (Val\ c))$  by(rule  $\tau move1CastRed$ )
    moreover
    have  $P, Cast\ U\ E, h' \vdash (Val\ c, loc) \leftrightarrow ([c], loc, length\ (compE2\ (Cast\ U\ E)), None)$ 
    by(rule bisim1Val2) simp
    ultimately show ?thesis using s by(auto simp add: exec-move-def)
  next
  case (Red1CastFail v U')
  note [simp] =  $\langle e = Val\ v \rangle \langle e' = THROW\ ClassCast \rangle \langle h' = h \rangle \langle xs' = xs \rangle \langle ta = \varepsilon \rangle$ 
  moreover from bisim have  $s: xcp = None\ xs = loc$ 
  and  $\tau Exec-mover-a\ P\ t\ E\ h\ (stk, loc, pc, xcp)\ ([v], loc, length\ (compE2\ E), None)$ 
  by(auto dest: bisim1Val2D1)
  hence  $\tau Exec-mover-a\ P\ t\ (Cast\ U\ E)\ h\ (stk, loc, pc, xcp)\ ([v], loc, length\ (compE2\ E), None)$ 
  by(auto elim: Cast- $\tau$ ExecrI)

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moreover from $\langle \text{typeof}_{hp} (h, xs) \ v = \lfloor U' \rfloor \rangle \langle \neg P \vdash U' \leq U \rangle$
have $\text{exec-meth-a} (compP2\ P) (compE2\ (Cast\ U\ E)) (compxE2\ (Cast\ U\ E)\ 0\ 0) \ t\ h\ ([v],\ loc,$
 $length\ (compE2\ E),\ None) \ \varepsilon\ h\ ([v],\ loc,\ length\ (compE2\ E),\ \lfloor \text{addr-of-sys-xcpt}\ ClassCast \rfloor)$
by $\text{-(rule exec-instr, auto simp add: compP2-def)}$
moreover have $\tau\text{move2} (compP2\ P) \ h\ [v] (Cast\ U\ E) (length\ (compE2\ E))\ None$ **by** $(\text{simp add:}$
 $\tau\text{move2-iff})$
ultimately have $\tau\text{Exec-movet-a}\ P\ t\ (Cast\ U\ E) \ h\ (stk,\ loc,\ pc,\ xcp) ([v],\ loc,\ length\ (compE2\ E),$
 $\lfloor \text{addr-of-sys-xcpt}\ ClassCast \rfloor)$
by $(\text{fastforce simp add: exec-move-def compP2-def intro: rtranclp-into-tranclp1 } \tau\text{exec-moveI})$
moreover have $\tau\text{move1}\ P\ h\ (Cast\ U\ (Val\ v))$ **by** $(\text{rule } \tau\text{move1CastRed})$
moreover
have $P, Cast\ U\ E, h \vdash (THROW\ ClassCast, loc) \leftrightarrow ([v], loc, length\ (compE2\ E), \lfloor \text{addr-of-sys-xcpt}$
 $ClassCast \rfloor)$
by $(\text{auto intro!: bisim1-bisims1.bisim1CastFail})$
ultimately show $?thesis$ **using** s **by** $(\text{auto simp add: exec-move-def})$
next
case $[simp]: (Cast1Throw\ a)$
have $\tau: \tau\text{move1}\ P\ h\ (Cast\ U\ e)$ **by** $(\text{auto intro: } \tau\text{move1CastThrow})$
from $bisim$ **have** $xcp = \lfloor a \rfloor \vee xcp = None$ **by** $(\text{auto dest: bisim1-ThrowD})$
thus $?thesis$
proof
assume $[simp]: xcp = \lfloor a \rfloor$
with $bisim$ **have** $P, Cast\ U\ E, h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, pc, xcp)$
by $(\text{auto intro: bisim1-bisims1.bisim1CastThrow})$
thus $?thesis$ **using** τ **by** (fastforce)
next
assume $[simp]: xcp = None$
with $bisim$ **obtain** pc'
where $\tau\text{Exec-mover-a}\ P\ t\ E\ h\ (stk, loc, pc, None) ([Addr\ a], loc, pc', \lfloor a \rfloor)$
and $bisim': P, E, h \vdash (Throw\ a, xs) \leftrightarrow ([Addr\ a], loc, pc', \lfloor a \rfloor)$ **and** $[simp]: xs = loc$
by $(\text{auto dest: bisim1-Throw-}\tau\text{Exec-mover})$
hence $\tau\text{Exec-mover-a}\ P\ t\ (Cast\ U\ E) \ h\ (stk, loc, pc, None) ([Addr\ a], loc, pc', \lfloor a \rfloor)$
by $\text{-(rule Cast-}\tau\text{ExecrI)}$
moreover from $bisim'$ **have** $P, Cast\ U\ E, h \vdash (Throw\ a, xs) \leftrightarrow ([Addr\ a], loc, pc', \lfloor a \rfloor)$
by $\text{-(rule bisim1-bisims1.bisim1CastThrow, auto)}$
ultimately show $?thesis$ **using** τ **by** $auto$
qed
qed
next
case $bisim1CastThrow$ **thus** $?case$ **by** $auto$
next
case $bisim1CastFail$ **thus** $?case$ **by** $auto$
next
case $(bisim1InstanceOf\ E\ n\ e\ xs\ stk\ loc\ pc\ xcp\ U)$
note $IH = bisim1InstanceOf(\mathcal{Q})$
note $bisim = \langle P, E, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
note $red = \langle True, P, t \vdash 1 \langle e\ \text{instanceof}\ U, (h, xs) \rangle \text{-ta-} \rightarrow \langle e', (h', xs') \rangle \rangle$
note $bsok = \langle bsok\ (E\ \text{instanceof}\ U)\ n \rangle$
from red **show** $?case$
proof $cases$
case $(InstanceOf1Red\ ee')$
note $[simp] = \langle e' = ee'\ \text{instanceof}\ U \rangle$
and $red = \langle True, P, t \vdash 1 \langle e, (h, xs) \rangle \text{-ta-} \rightarrow \langle ee', (h', xs') \rangle \rangle$
from red **have** $\tau\text{move1}\ P\ h\ (e\ \text{instanceof}\ U) = \tau\text{move1}\ P\ h\ e$ **by** $(\text{auto simp add: } \tau\text{move1.simps})$

τ moves1.simps)

moreover from *red* **have** *call1* (*e* instanceof *U*) = *call1 e* **by** *auto*

moreover from *IH[OF red]* *bsok*

obtain *pc'' stk'' loc'' xcp''* **where** *bisim*: $P, E, h' \vdash (ee', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$

and *redo*: $?exec\ ta\ E\ e\ ee'\ h\ stk\ loc\ pc\ xcp\ h'\ pc''\ stk''\ loc''\ xcp''$ **by** *auto*

from *bisim*

have P, E instanceof *U*, $h' \vdash (e'$ instanceof *U*, $xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$

by(rule *bisim1-bisims1.bisim1InstanceOf*)

moreover {

assume *no-call2 E pc*

hence *no-call2* (*E* instanceof *U*) *pc* **by**(*auto simp add: no-call2-def*) }

ultimately show *?thesis* **using** *redo*

by(*auto simp del: call1.simps calls1.simps split: if-split-asm split del: if-split*)(*blast intro: InstanceOf-ExecrI InstanceOf-ExecrI exec-move-InstanceOfI*)+

next

case (*Red1InstanceOf c U' b*)

hence [*simp*]: $e = Val\ c\ ta = \varepsilon\ e' = Val\ (Bool\ (c \neq\ Null \wedge\ P \vdash\ U' \leq\ U))\ h' = h\ xs' = xs$

$b = (c \neq\ Null \wedge\ P \vdash\ U' \leq\ U)$

and *type*: $typeof_h\ c = \lfloor U' \rfloor$ **by** *auto*

from *bisim* **have** *s*: $xcp = None\ xs = loc$ **by**(*auto dest: bisim-Val-loc-eq-xcp-None*)

from *bisim* **have** $\tau Exec-mover-a\ P\ t\ E\ h\ (stk,\ loc,\ pc,\ xcp)\ ([c],\ loc,\ length\ (compE2\ E),\ None)$

by(*auto dest: bisim1Val2D1*)

hence $\tau Exec-mover-a\ P\ t\ (E\ instanceof\ U)\ h\ (stk,\ loc,\ pc,\ xcp)\ ([c],\ loc,\ length\ (compE2\ E),\ None)$

by(rule *InstanceOf-ExecrI*)

moreover from *type*

have *exec-meth-a* (*compP2 P*) (*compE2* (*E* instanceof *U*)) (*compxE2* (*E* instanceof *U*) 0 0) *t h*

($[c],\ loc,\ length\ (compE2\ E),\ None$) $\varepsilon\ h'$ ($[Bool\ b],\ loc,\ Suc\ (length\ (compE2\ E)),\ None$)

by(*auto intro!: exec-instr simp add: compP2-def*)

moreover have $\tau move2\ (compP2\ P)\ h\ [c]\ (E\ instanceof\ U)\ (length\ (compE2\ E))\ None$ **by**(*simp add: \tau move2-iff*)

ultimately have $\tau Exec-mover-a\ P\ t\ (E\ instanceof\ U)\ h\ (stk,\ loc,\ pc,\ xcp)\ ([Bool\ b],\ loc,\ Suc\ (length\ (compE2\ E)),\ None)$

by(*fastforce elim: rtranclp.rtrancl-into-rtrancl intro: \tau exec-moveI simp add: exec-move-def compP2-def*)

moreover have $\tau move1\ P\ h\ ((Val\ c)\ instanceof\ U)$ **by**(rule *\tau move1InstanceOfRed*)

moreover

have P, E instanceof *U*, $h' \vdash (Val\ (Bool\ b),\ loc) \leftrightarrow ([Bool\ b],\ loc,\ length\ (compE2\ (E\ instanceof\ U)),\ None)$

by(rule *bisim1Val2*) *simp*

ultimately show *?thesis* **using** *s* **by**(*auto simp add: exec-move-def*)

next

case (*InstanceOf1Throw a*)

note [*simp*] = $\langle e = Throw\ a \rangle \langle h' = h \rangle \langle xs' = xs \rangle \langle ta = \varepsilon \rangle \langle e' = Throw\ a \rangle$

have τ : $\tau move1\ P\ h\ (e\ instanceof\ U)$ **by**(*auto intro: \tau move1InstanceOfThrow*)

from *bisim* **have** $xcp = \lfloor a \rfloor \vee xcp = None$ **by**(*auto dest: bisim1-ThrowD*)

thus *?thesis*

proof

assume [*simp*]: $xcp = \lfloor a \rfloor$

with *bisim* **have** P, E instanceof *U*, $h \vdash (Throw\ a,\ xs) \leftrightarrow (stk,\ loc,\ pc,\ xcp)$

by(*auto intro: bisim1-bisims1.bisim1InstanceOfThrow*)

thus *?thesis* **using** τ **by**(*fastforce*)

next

assume [*simp*]: $xcp = None$

with *bisim* **obtain** *pc'*

where $\tau Exec-mover-a\ P\ t\ E\ h\ (stk,\ loc,\ pc,\ None)\ ([Addr\ a],\ loc,\ pc',\ \lfloor a \rfloor)$

and $\text{bisim}' : P, E, h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], \text{loc}, \text{pc}', [a])$ **and** $[\text{simp}] : xs = \text{loc}$
by (*auto dest: bisim1-Throw- τ Exec-mover*)
hence $\tau \text{Exec-mover-a } P \ t \ (E \ \text{instanceof } U) \ h \ (stk, \text{loc}, \text{pc}, \text{None}) \ ([\text{Addr } a], \text{loc}, \text{pc}', [a])$
by (*rule InstanceOf- τ Execr1*)
moreover from bisim' **have** $P, E \ \text{instanceof } U, h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], \text{loc}, \text{pc}', [a])$
by (*rule bisim1-bisims1.bisim1InstanceOfThrow, auto*)
ultimately show *?thesis using τ by auto*
qed
qed
next
case *bisim1InstanceOfThrow* **thus** *?case by auto*
next
case *bisim1Val* **thus** *?case by fastforce*
next
case (*bisim1Var* $V \ n \ xs$)
from $\langle \text{True}, P, t \vdash 1 \ \langle \text{Var } V, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$ **show** *?case*
proof cases
case (*Red1Var* v)
hence $\text{exec-meth-a} \ (compP2 \ P) \ [\text{Load } V] \ [] \ t \ h \ ([], xs, 0, \text{None}) \ \varepsilon \ h \ ([v], xs, 1, \text{None})$
and $[\text{simp}] : ta = \varepsilon \ h' = h \ xs' = xs \ e' = \text{Val } v$
by (*auto intro: exec-instr*)
moreover have $\tau \text{move2} \ (compP2 \ P) \ h \ [] \ (\text{Var } V) \ 0 \ \text{None}$ **by** (*simp add: τ move2-iff*)
ultimately have $\tau \text{Exec-movet-a } P \ t \ (\text{Var } V) \ h \ ([], xs, 0, \text{None}) \ ([v], xs, 1, \text{None})$
by (*auto intro: τ Exec1step simp add: exec-move-def compP2-def*)
moreover have $P, \text{Var } V, h \vdash (\text{Val } v, xs) \leftrightarrow ([v], xs, \text{length} \ (compE2 \ (\text{Var } V)), \text{None})$
by (*rule bisim1Val2*) *simp*
moreover have $\tau \text{move1 } P \ h \ (\text{Var } V)$ **by** (*rule τ move1Var*)
ultimately show *?thesis by fastforce*
qed
next
case (*bisim1BinOp1* $e1 \ n \ e1' \ xs \ stk \ \text{loc} \ \text{pc} \ \text{xcp} \ e2 \ \text{bop}$)
note $IH1 = \text{bisim1BinOp1.IH}(2)$
note $IH2 = \text{bisim1BinOp1.IH}(4)$
note $\text{bisim1} = \langle P, e1, h \vdash (e1', xs) \leftrightarrow (stk, \text{loc}, \text{pc}, \text{xcp}) \rangle$
note $\text{bisim2} = \langle \bigwedge xs. P, e2, h \vdash (e2, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$
note $\text{bsok} = \langle \text{bsok} \ (e1 \ \llbracket \text{bop} \rrbracket \ e2) \ n \rangle$
from $\langle \text{True}, P, t \vdash 1 \ \langle e1' \ \llbracket \text{bop} \rrbracket \ e2, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$ **show** *?case*
proof cases
case (*Bin1OpRed1* E')
note $[\text{simp}] = \langle e' = E' \ \llbracket \text{bop} \rrbracket \ e2 \rangle$
and $\text{red} = \langle \text{True}, P, t \vdash 1 \ \langle e1', (h, xs) \rangle -ta \rightarrow \langle E', (h', xs') \rangle \rangle$
from red **have** $\tau \text{move1 } P \ h \ (e1' \ \llbracket \text{bop} \rrbracket \ e2) = \tau \text{move1 } P \ h \ e1'$ **by** (*auto simp add: τ move1.simps τ moves1.simps*)
moreover from red **have** $\text{call1} \ (e1' \ \llbracket \text{bop} \rrbracket \ e2) = \text{call1} \ e1'$ **by** *auto*
moreover from $IH1[\text{OF } \text{red}] \ \text{bsok}$
obtain $\text{pc}'' \ \text{stk}'' \ \text{loc}'' \ \text{xcp}''$ **where** $\text{bisim} : P, e1, h' \vdash (E', xs') \leftrightarrow (stk'', \text{loc}'', \text{pc}'', \text{xcp}'')$
and $\text{redo} : ?\text{exec } ta \ e1 \ e1' \ E' \ h \ \text{stk} \ \text{loc} \ \text{pc} \ \text{xcp} \ h' \ \text{pc}'' \ \text{stk}'' \ \text{loc}'' \ \text{xcp}''$ **by** *auto*
from bisim
have $P, e1 \ \llbracket \text{bop} \rrbracket \ e2, h' \vdash (E' \ \llbracket \text{bop} \rrbracket \ e2, xs') \leftrightarrow (stk'', \text{loc}'', \text{pc}'', \text{xcp}'')$
by (*rule bisim1-bisims1.bisim1BinOp1*)
moreover {
assume *no-call2* $e1 \ \text{pc}$
hence *no-call2* $(e1 \ \llbracket \text{bop} \rrbracket \ e2) \ \text{pc} \ \vee \ \text{pc} = \text{length} \ (compE2 \ e1)$ **by** (*auto simp add: no-call2-def*) }
ultimately show *?thesis using redo*

by(*auto simp del: call1.simps calls1.simps split: if-split-asm split del: if-split*)(*blast intro: BinOp- τ ExecrI1 BinOp- τ ExecI1 exec-move-BinOpI1*)+
next
case (*Bin1OpRed2 E' v*)
note [*simp*] = $\langle e1' = \text{Val } v \rangle \langle e' = \text{Val } v \langle \text{bop} \rangle E' \rangle$
and *red* = $\langle \text{True}, P, t \vdash 1 \langle e2, (h, xs) \rangle - \text{ta} \rightarrow \langle E', (h', xs') \rangle \rangle$
from *red* **have** $\tau: \tau \text{move1 } P \ h \ (\text{Val } v \langle \text{bop} \rangle e2) = \tau \text{move1 } P \ h \ e2$ **by**(*auto simp add: $\tau \text{move1}.simps \tau \text{moves1}.simps$*)
from *bisim1* **have** *s: xcp = None xs = loc*
and *exec1: $\tau \text{Exec-mover-a } P \ t \ e1 \ h \ (\text{stk}, \text{loc}, \text{pc}, \text{None}) \ ([v], \text{xs}, \text{length} (\text{compE2 } e1), \text{None})$*
by(*auto dest: bisim1Val2D1*)
from *exec1* **have** $\tau \text{Exec-mover-a } P \ t \ (e1 \langle \text{bop} \rangle e2) \ h \ (\text{stk}, \text{loc}, \text{pc}, \text{None}) \ ([v], \text{xs}, \text{length} (\text{compE2 } e1), \text{None})$
by(*rule BinOp- τ ExecrI1*)
moreover
from *IH2[OF red] bsok* **obtain** *pc'' stk'' loc'' xcp''*
where *bisim': $P, e2, h' \vdash (E', xs') \leftrightarrow (\text{stk}'', \text{loc}'', \text{pc}'', \text{xcp}'')$*
and *exec': $? \text{exec } \text{ta } e2 \ e2 \ E' \ h \ [] \ \text{xs } 0 \ \text{None } h' \ \text{pc}'' \ \text{stk}'' \ \text{loc}'' \ \text{xcp}''$* **by** *auto*
have $? \text{exec } \text{ta} \ (e1 \langle \text{bop} \rangle e2) \ (\text{Val } v \langle \text{bop} \rangle e2) \ (\text{Val } v \langle \text{bop} \rangle E') \ h \ ([] \ @ \ [v]) \ \text{xs} \ (\text{length} (\text{compE2 } e1) + 0) \ \text{None } h' \ (\text{length} (\text{compE2 } e1) + \text{pc}'') \ (\text{stk}'' \ @ \ [v]) \ \text{loc}'' \ \text{xcp}''$
proof(*cases $\tau \text{move1 } P \ h \ (\text{Val } v \langle \text{bop} \rangle e2)$*)
case *True*
with *exec' τ* **have** [*simp*]: *h = h' and e: sim-move e2 E' P t e2 h ([], xs, 0, None) (stk'', loc'', pc'', xcp'')* **by** *auto*
from *e* **have** *sim-move (Val v $\langle \text{bop} \rangle e2$) (Val v $\langle \text{bop} \rangle E'$) P t (e1 $\langle \text{bop} \rangle e2$) h ([] @ [v], xs, length (compE2 e1) + 0, None) (stk'' @ [v], loc'', length (compE2 e1) + pc'', xcp'')*
by(*fastforce dest: BinOp- τ ExecrI2 BinOp- τ ExecI2*)
with *True* **show** *?thesis* **by** *auto*
next
case *False*
with *exec' τ* **obtain** *pc' stk' loc' xcp'*
where *e: $\tau \text{Exec-mover-a } P \ t \ e2 \ h \ ([], \text{xs}, 0, \text{None}) \ (\text{stk}', \text{loc}', \text{pc}', \text{xcp}')$*
and *e': exec-move-a P t e2 h (stk', loc', pc', xcp') (extTA2JVM (compP2 P) ta) h' (stk'', loc'', pc'', xcp'')*
and $\tau': \neg \tau \text{move2} (\text{compP2 } P) \ h \ \text{stk}' \ e2 \ \text{pc}' \ \text{xcp}'$
and *call: $\text{call1 } e2 = \text{None} \vee \text{no-call2 } e2 \ 0 \vee \text{pc}' = 0 \wedge \text{stk}' = [] \wedge \text{loc}' = \text{xs} \wedge \text{xcp}' = \text{None}$* **by** *auto*
from *e* **have** $\tau \text{Exec-mover-a } P \ t \ (e1 \langle \text{bop} \rangle e2) \ h \ ([] \ @ \ [v], \text{xs}, \text{length} (\text{compE2 } e1) + 0, \text{None}) \ (\text{stk}' \ @ \ [v], \text{loc}', \text{length} (\text{compE2 } e1) + \text{pc}', \text{xcp}')$ **by**(*rule BinOp- τ ExecrI2*)
moreover **from** *e'* **have** *exec-move-a P t (e1 $\langle \text{bop} \rangle e2$) h (stk' @ [v], loc', length (compE2 e1) + pc', xcp') (extTA2JVM (compP2 P) ta) h' (stk'' @ [v], loc'', length (compE2 e1) + pc'', xcp'')*
by(*rule exec-move-BinOpI2*)
moreover **from** *e'* **have** *pc' < length (compE2 e2)* **by** *auto*
with $\tau' \ e'$ **have** $\neg \tau \text{move2} (\text{compP2 } P) \ h \ (\text{stk}' \ @ \ [v]) \ (e1 \langle \text{bop} \rangle e2) \ (\text{length} (\text{compE2 } e1) + \text{pc}')$
by(*auto simp add: $\tau \text{instr-stk-drop-exec-move } \tau \text{move2-iff}$*)
moreover **from** *red* **have** $\text{call1} \ (e1' \langle \text{bop} \rangle e2) = \text{call1 } e2$ **by**(*auto*)
moreover **have** *no-call2 e2 0 \implies no-call2 (e1 $\langle \text{bop} \rangle e2$) (length (compE2 e1))*
by(*auto simp add: no-call2-def*)
ultimately **show** *?thesis* **using** *False call*
by(*auto simp del: split-paired-Ex call1.simps calls1.simps*) **blast**
qed
moreover **from** *bisim'*
have $P, e1 \langle \text{bop} \rangle e2, h' \vdash (\text{Val } v \langle \text{bop} \rangle E', xs') \leftrightarrow ((\text{stk}'' \ @ \ [v]), \text{loc}'', \text{length} (\text{compE2 } e1) + \text{pc}'',$

xcp')
by(rule *bisim1-bisims1.bisim1BinOp2*)
moreover from *bisim1* **have** $pc \neq \text{length}(\text{compE2 } e1) \longrightarrow \text{no-call2}(e1 \ll bop \gg e2) pc$
by(auto *simp add: no-call2-def dest: bisim-Val-pc-not-Invoke bisim1-pc-length-compE2*)
ultimately show *?thesis* **using** $\tau \text{exec1 } s$
apply(auto *simp del: split-paired-Ex call1.simps calls1.simps split: if-split-asm split del: if-split*)
apply(blast *intro: $\tau \text{Exec-mover-trans}$ |fastforce elim!: $\tau \text{Exec-mover-trans}$ simp del: split-paired-Ex*
call1.simps calls1.simps)
done
next
case (*Red1BinOp v1 v2 v*)
note [*simp*] = $\langle e1' = \text{Val } v1 \rangle \langle e2 = \text{Val } v2 \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Val } v \rangle \langle h' = h \rangle \langle xs' = xs \rangle$
and *binop* = $\langle \text{binop } bop \ v1 \ v2 = [\text{Inl } v] \rangle$
have $\tau: \tau \text{move1 } P \ h \ (\text{Val } v1 \ll bop \gg \text{Val } v2)$ **by**(rule $\tau \text{move1BinOp}$)
from *bisim1* **have** $s: xcp = \text{None } xs = \text{loc}$
and $\tau \text{Exec-mover-a } P \ t \ e1 \ h \ (\text{stk}, \text{loc}, \text{pc}, xcp) ([v1], \text{loc}, \text{length}(\text{compE2 } e1), \text{None})$
by(auto *dest: bisim1Val2D1*)
hence $\tau \text{Exec-mover-a } P \ t \ (e1 \ll bop \gg \text{Val } v2) \ h \ (\text{stk}, \text{loc}, \text{pc}, xcp) ([v1], \text{loc}, \text{length}(\text{compE2 } e1),$
None)
by-(rule *BinOp- $\tau \text{ExecrI1}$*)
also have $\tau \text{move2}(\text{compP2 } P) \ h \ [v1] \ (e1 \ll bop \gg \text{Val } v2) \ (\text{length}(\text{compE2 } e1) + 0) \ \text{None}$
by(rule $\tau \text{move2BinOp2}$)(rule $\tau \text{move2Val}$)
with *binop* **have** $\tau \text{Exec-mover-a } P \ t \ (e1 \ll bop \gg \text{Val } v2) \ h \ ([v1], \text{loc}, \text{length}(\text{compE2 } e1), \text{None})$
(*[v2, v1], loc, Suc (length (compE2 e1)), None*)
by-(rule $\tau \text{Execr1step}$, auto *intro!: exec-instr simp add: exec-move-def compP2-def*)
also (*rtranclp-trans*) **from** *binop*
have *exec-meth-a* ($\text{compP2 } P$) ($\text{compE2 } (e1 \ll bop \gg \text{Val } v2)$) ($\text{compxE2 } (e1 \ll bop \gg \text{Val } v2) \ 0 \ 0$) t
 $h \ ([v2, v1], \text{loc}, \text{Suc}(\text{length}(\text{compE2 } e1)), \text{None}) \ \varepsilon$
 $h \ ([v], \text{loc}, \text{Suc}(\text{Suc}(\text{length}(\text{compE2 } e1))), \text{None})$
by-(rule *exec-instr*, auto)
moreover have $\tau \text{move2}(\text{compP2 } P) \ h \ [v2, v1] \ (e1 \ll bop \gg \text{Val } v2) \ (\text{Suc}(\text{length}(\text{compE2 } e1)))$
None **by**(*simp add: $\tau \text{move2-iff}$*)
ultimately have $\tau \text{Exec-mover-a } P \ t \ (e1 \ll bop \gg \text{Val } v2) \ h \ (\text{stk}, \text{loc}, \text{pc}, xcp) ([v], \text{loc}, \text{Suc}(\text{Suc}(\text{length}(\text{compE2 } e1))), \text{None})$
by(*fastforce intro: rtranclp.rtrancl-into-rtrancl $\tau \text{exec-moveI}$ simp add: exec-move-def compP2-def*)
moreover
have $P, e1 \ll bop \gg \text{Val } v2, h \vdash (\text{Val } v, \text{loc}) \leftrightarrow ([v], \text{loc}, \text{length}(\text{compE2 } (e1 \ll bop \gg \text{Val } v2)), \text{None})$
by(rule *bisim1Val2*) *simp*
ultimately show *?thesis* **using** $s \ \tau$ **by** auto
next
case (*Red1BinOpFail v1 v2 a*)
note [*simp*] = $\langle e1' = \text{Val } v1 \rangle \langle e2 = \text{Val } v2 \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw } a \rangle \langle h' = h \rangle \langle xs' = xs \rangle$
and *binop* = $\langle \text{binop } bop \ v1 \ v2 = [\text{Inr } a] \rangle$
have $\tau: \tau \text{move1 } P \ h \ (\text{Val } v1 \ll bop \gg \text{Val } v2)$ **by**(rule $\tau \text{move1BinOp}$)
from *bisim1* **have** $s: xcp = \text{None } xs = \text{loc}$
and $\tau \text{Exec-mover-a } P \ t \ e1 \ h \ (\text{stk}, \text{loc}, \text{pc}, xcp) ([v1], \text{loc}, \text{length}(\text{compE2 } e1), \text{None})$
by(auto *dest: bisim1Val2D1*)
hence $\tau \text{Exec-mover-a } P \ t \ (e1 \ll bop \gg \text{Val } v2) \ h \ (\text{stk}, \text{loc}, \text{pc}, xcp) ([v1], \text{loc}, \text{length}(\text{compE2 } e1),$
None)
by-(rule *BinOp- $\tau \text{ExecrI1}$*)
also have $\tau \text{move2}(\text{compP2 } P) \ h \ [v1] \ (e1 \ll bop \gg \text{Val } v2) \ (\text{length}(\text{compE2 } e1) + 0) \ \text{None}$
by(rule $\tau \text{move2BinOp2}$)(rule $\tau \text{move2Val}$)
with *binop* **have** $\tau \text{Exec-mover-a } P \ t \ (e1 \ll bop \gg \text{Val } v2) \ h \ ([v1], \text{loc}, \text{length}(\text{compE2 } e1), \text{None})$
(*[v2, v1], loc, Suc (length (compE2 e1)), None*)

by–(rule $\tau\text{Execr1step}$, auto intro!: *exec-instr simp add: exec-move-def compP2-def*)
also (rtranclp-trans) **from** binop
have *exec-meth-a* (compP2 P) (compE2 (e1 «bop» Val v2)) (compxE2 (e1 «bop» Val v2) 0 0) t
 h ([v2, v1], loc, Suc (length (compE2 e1)), None) ε
 h ([v2, v1], loc, Suc (length (compE2 e1)), [a])
by–(rule *exec-instr*, auto)
moreover have τmove2 (compP2 P) h [v2, v1] (e1 «bop» Val v2) (Suc (length (compE2 e1)))
None **by**(*simp add: $\tau\text{move2-iff}$*)
ultimately have $\tau\text{Exec-movet-a}$ P t (e1 «bop» Val v2) h (stk, loc, pc, xcp) ([v2, v1], loc, Suc
(length (compE2 e1)), [a])
by(*fastforce intro: rtranclp-into-tranclp1 $\tau\text{exec-moveI}$ simp add: exec-move-def compP2-def*)
moreover
have P, e1 «bop» Val v2, $h \vdash$ (Throw a, loc) \leftrightarrow ([v2, v1], loc, length (compE2 e1) + length
(compE2 (Val v2)), [a])
by(rule *bisim1BinOpThrow*)
ultimately show ?thesis **using** s τ **by** auto
next
case (Bin1OpThrow1 a)
note [simp] = $\langle e1' = \text{Throw } a \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw } a \rangle \langle h' = h \rangle \langle xs' = xs \rangle$
have τ : τmove1 P h (Throw a «bop» e2) **by**(rule *$\tau\text{move1BinOpThrow1}$*)
from *bisim1* **have** xcp = [a] \vee xcp = None **by**(auto dest: *bisim1-ThrowD*)
thus ?thesis
proof
assume [simp]: xcp = [a]
with *bisim1* **have** P, e1 «bop» e2, $h \vdash$ (Throw a, xs) \leftrightarrow (stk, loc, pc, xcp)
by(auto intro: *bisim1-bisims1.intros*)
thus ?thesis **using** τ **by**(*fastforce*)
next
assume [simp]: xcp = None
with *bisim1* **obtain** pc' **where** $\tau\text{Exec-mover-a}$ P t e1 h (stk, loc, pc, None) ([Addr a], loc, pc',
[a])
and *bisim'*: P, e1, $h \vdash$ (Throw a, xs) \leftrightarrow ([Addr a], loc, pc', [a])
and [simp]: xs = loc
by(auto dest: *bisim1-Throw- $\tau\text{Exec-mover}$*)
hence $\tau\text{Exec-mover-a}$ P t (e1 «bop» e2) h (stk, loc, pc, None) ([Addr a], loc, pc', [a])
by–(rule *BinOp- $\tau\text{ExecrI1}$*)
moreover from *bisim'*
have P, e1 «bop» e2, $h \vdash$ (Throw a, xs) \leftrightarrow ([Addr a], loc, pc', [a])
by(auto intro: *bisim1-bisims1.bisim1BinOpThrow1*)
ultimately show ?thesis **using** τ **by** auto
qed
next
case (Bin1OpThrow2 v a)
note [simp] = $\langle e1' = \text{Val } v \rangle \langle e2 = \text{Throw } a \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw } a \rangle \langle h' = h \rangle \langle xs' = xs \rangle$
from *bisim1* **have** s: xcp = None xs = loc
and $\tau\text{Exec-mover-a}$ P t e1 h (stk, loc, pc, xcp) ([v], loc, length (compE2 e1), None)
by(auto dest: *bisim1Val2D1*)
hence $\tau\text{Exec-mover-a}$ P t (e1 «bop» Throw a) h (stk, loc, pc, xcp) ([v], loc, length (compE2 e1),
None)
by–(rule *BinOp- $\tau\text{ExecrI1}$*)
also have $\tau\text{Exec-mover-a}$ P t (e1 «bop» Throw a) h ([v], loc, length (compE2 e1), None) ([Addr a],
v], loc, Suc (length (compE2 e1)), [a])
by(rule *$\tau\text{Execr2step}$*)(auto *simp add: exec-move-def exec-meth-instr $\tau\text{move2-iff}$ $\tau\text{move1.simps}$
 $\tau\text{moves1.simps}$*)

```

also (rtranclp-trans)
have  $P, e1 \llbracket \text{bop} \rrbracket \text{Throw } a, h \vdash (\text{Throw } a, \text{loc}) \leftrightarrow ([\text{Addr } a] @ [v], \text{loc}, (\text{length } (\text{compE2 } e1) + \text{length } (\text{compE2 } (\text{addr } a))), [a])$ 
  by(rule bisim1BinOpThrow2[OF bisim1Throw2])
moreover have  $\tau \text{move1 } P h (e1' \llbracket \text{bop} \rrbracket e2)$  by(auto intro:  $\tau \text{move1BinOpThrow2}$ )
ultimately show ?thesis using s by auto
qed
next
case (bisim1BinOp2 e2 n e2' xs stk loc pc xcp e1 bop v1)
note IH2 = bisim1BinOp2.IH(2)
note bisim1 =  $\langle \bigwedge xs. P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$ 
note bisim2 =  $\langle P, e2, h \vdash (e2', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \rangle$ 
note red =  $\langle \text{True}, P, t \vdash 1 \langle \text{Val } v1 \llbracket \text{bop} \rrbracket e2', (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$ 
note bsok =  $\langle \text{bsok } (e1 \llbracket \text{bop} \rrbracket e2) n \rangle$ 
from red show ?case
proof cases
  case (Bin1OpRed2 E')
    note [simp] =  $\langle e' = \text{Val } v1 \llbracket \text{bop} \rrbracket E' \rangle$ 
    and red =  $\langle \text{True}, P, t \vdash 1 \langle e2', (h, xs) \rangle -ta \rightarrow \langle E', (h', xs') \rangle \rangle$ 
    from IH2[OF red] bsok obtain pc'' stk'' loc'' xcp''
    where bisim':  $P, e2, h' \vdash (E', xs') \leftrightarrow (\text{stk}'', \text{loc}'', \text{pc}'', \text{xcp}'')$ 
    and exec':  $?exec \text{ ta } e2 \ e2' \ E' \ h \ \text{stk} \ \text{loc} \ \text{pc} \ \text{xcp} \ h' \ \text{pc}'' \ \text{stk}'' \ \text{loc}'' \ \text{xcp}''$  by auto
    from red have  $\tau: \tau \text{move1 } P h (\text{Val } v1 \llbracket \text{bop} \rrbracket e2') = \tau \text{move1 } P h e2'$  by(auto simp add:  $\tau \text{move1.simps } \tau \text{moves1.simps}$ )
    have no-call2 e2 pc  $\implies$  no-call2 (e1  $\llbracket \text{bop} \rrbracket$  e2) (length (compE2 e1) + pc) by(auto simp add: no-call2-def)
    hence ?exec ta (e1  $\llbracket \text{bop} \rrbracket$  e2) (Val v1  $\llbracket \text{bop} \rrbracket$  e2') (Val v1  $\llbracket \text{bop} \rrbracket$  E') h (stk @ [v1]) loc (length (compE2 e1) + pc) xcp h' (length (compE2 e1) + pc'') (stk'' @ [v1]) loc'' xcp''
    using exec'  $\tau$ 
    apply(cases  $\tau \text{move1 } P h (\text{Val } v1 \llbracket \text{bop} \rrbracket e2')$ )
    apply(auto)
    apply(blast intro: BinOp- $\tau$ ExecrI2 BinOp- $\tau$ ExectI2 exec-move-BinOpI2)
    apply(blast intro: BinOp- $\tau$ ExecrI2 BinOp- $\tau$ ExectI2 exec-move-BinOpI2)
    apply(rule exI conjI BinOp- $\tau$ ExecrI2 exec-move-BinOpI2|assumption)+
    apply(fastforce simp add:  $\tau \text{instr-stk-drop-exec-move } \tau \text{move2-iff split: if-split-asm}$ )
    apply(rule exI conjI BinOp- $\tau$ ExecrI2 exec-move-BinOpI2|assumption)+
    apply(fastforce simp add:  $\tau \text{instr-stk-drop-exec-move } \tau \text{move2-iff split: if-split-asm}$ )
    apply(rule exI conjI BinOp- $\tau$ ExecrI2 exec-move-BinOpI2 rtranclp.rtrancl-refl|assumption)+
    apply(fastforce simp add:  $\tau \text{instr-stk-drop-exec-move } \tau \text{move2-iff split: if-split-asm}$ )
    done
    moreover from bisim'
    have  $P, e1 \llbracket \text{bop} \rrbracket e2, h' \vdash (\text{Val } v1 \llbracket \text{bop} \rrbracket E', xs') \leftrightarrow (\text{stk}'' @ [v1], \text{loc}'', \text{length } (\text{compE2 } e1) + \text{pc}'', \text{xcp}'')$ 
    by(rule bisim1-bisims1.bisim1BinOp2)
    ultimately show ?thesis using  $\tau$  by auto blast+
next
case (Red1BinOp v2 v)
note [simp] =  $\langle e2' = \text{Val } v2 \rangle \langle \text{ta} = \varepsilon \rangle \langle e' = \text{Val } v \rangle \langle h' = h \rangle \langle xs' = xs \rangle$ 
and binop =  $\langle \text{binop } \text{bop } v1 \ v2 = [\text{Inl } v] \rangle$ 
have  $\tau: \tau \text{move1 } P h (\text{Val } v1 \llbracket \text{bop} \rrbracket \text{Val } v2)$  by(rule  $\tau \text{move1BinOp}$ )
from bisim2 have s: xcp = None xs = loc
and  $\tau \text{Exec-mover-a } P \ t \ e2 \ h \ (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) ([v2], \text{loc}, \text{length } (\text{compE2 } e2), \text{None})$ 
by(auto dest: bisim1Val2D1)
hence  $\tau \text{Exec-mover-a } P \ t \ (e1 \llbracket \text{bop} \rrbracket e2) \ h \ (\text{stk} @ [v1], \text{loc}, \text{length } (\text{compE2 } e1) + \text{pc}, \text{xcp}) ([v2] @ [v1], \text{loc}, \text{length } (\text{compE2 } e1) + \text{length } (\text{compE2 } e2), \text{None})$ 

```


by-(rule *BinOp-τExecrI2*)
moreover from *binop*
have *exec-move-a P t (e1 «bop» e2) h ([v2, v1], loc, length (compE2 e1) + length (compE2 e2), None) ε*

$$h ([v], loc, Suc (length (compE2 e1) + length (compE2 e2)), None)$$
unfolding *exec-move-def* **by**-(rule *exec-instr, auto*)
moreover have $\tau move2 (compP2 P) h [v2, v1] (e1 «bop» e2) (length (compE2 e1) + length (compE2 e2)) None$
by(*simp add: τmove2-iff*)
ultimately have $\tau Exec-mover-a P t (e1 «bop» e2) h (stk @ [v1], loc, length (compE2 e1) + pc, xcp) ([v], loc, Suc (length (compE2 e1) + length (compE2 e2)), None)$
by(*auto intro: rtranclp.rtrancl-into-rtrancl τexec-moveI simp add: compP2-def*)
moreover
have $P, e1 «bop» e2, h \vdash (Val v, loc) \leftrightarrow ([v], loc, length (compE2 (e1 «bop» e2)), None)$
by(rule *bisim1Val2*) *simp*
ultimately show *?thesis using s τ by(auto)*
next
case (*Red1BinOpFail v2 a*)
note $[simp] = \langle e2' = Val v2 \rangle \langle ta = \varepsilon \rangle \langle e' = Throw a \rangle \langle h' = h \rangle \langle xs' = xs \rangle$
and $binop = \langle binop bop v1 v2 = [Inr a] \rangle$
have $\tau: \tau move1 P h (Val v1 «bop» Val v2)$ **by**(rule $\tau move1BinOp$)
from *bisim2* **have** $s: xcp = None xs = loc$
and $\tau Exec-mover-a P t e2 h (stk, loc, pc, xcp) ([v2], loc, length (compE2 e2), None)$
by(*auto dest: bisim1Val2D1*)
hence $\tau Exec-mover-a P t (e1 «bop» e2) h (stk @ [v1], loc, length (compE2 e1) + pc, xcp) ([v2] @ [v1], loc, length (compE2 e1) + length (compE2 e2), None)$
by-(rule *BinOp-τExecrI2*)
moreover from *binop*
have *exec-move-a P t (e1 «bop» e2) h ([v2, v1], loc, length (compE2 e1) + length (compE2 e2), None) ε*

$$h ([v2, v1], loc, length (compE2 e1) + length (compE2 e2), [a])$$
unfolding *exec-move-def* **by**-(rule *exec-instr, auto*)
moreover have $\tau move2 (compP2 P) h [v2, v1] (e1 «bop» e2) (length (compE2 e1) + length (compE2 e2)) None$
by(*simp add: τmove2-iff*)
ultimately have $\tau Exec-mover-a P t (e1 «bop» e2) h (stk @ [v1], loc, length (compE2 e1) + pc, xcp) ([v2, v1], loc, length (compE2 e1) + length (compE2 e2), [a])$
by(*auto intro: rtranclp-into-tranclp1 τexec-moveI simp add: compP2-def*)
moreover
have $P, e1 «bop» e2, h \vdash (Throw a, loc) \leftrightarrow ([v2, v1], loc, length (compE2 e1) + length (compE2 e2), [a])$
by(rule *bisim1BinOpThrow*)
ultimately show *?thesis using s τ by(auto)*
next
case (*Bin1OpThrow2 a*)
note $[simp] = \langle e2' = Throw a \rangle \langle ta = \varepsilon \rangle \langle h' = h \rangle \langle xs' = xs \rangle \langle e' = Throw a \rangle$
have $\tau: \tau move1 P h (Val v1 «bop» Throw a)$ **by**(rule $\tau move1BinOpThrow2$)
from *bisim2* **have** $xcp = [a] \vee xcp = None$ **by**(*auto dest: bisim1-ThrowD*)
thus *?thesis*
proof
assume $[simp]: xcp = [a]$
with *bisim2*
have $P, e1 «bop» e2, h \vdash (Throw a, xs) \leftrightarrow (stk @ [v1], loc, length (compE2 e1) + pc, xcp)$
by(*auto intro: bisim1BinOpThrow2*)

```

thus ?thesis using  $\tau$  by(fastforce)
next
assume [simp]: xcp = None
with bisim2 obtain pc'
  where  $\tau$ Exec-mover-a P t e2 h (stk, loc, pc, None) ([Addr a], loc, pc', [a])
  and bisim': P, e2, h  $\vdash$  (Throw a, xs)  $\leftrightarrow$  ([Addr a], loc, pc', [a]) and [simp]: xs = loc
  by(auto dest: bisim1-Throw- $\tau$ Exec-mover)
  hence  $\tau$ Exec-mover-a P t (e1 «bop» e2) h (stk @ [v1], loc, length (compE2 e1) + pc, None) ([Addr
a] @ [v1], loc, length (compE2 e1) + pc', [a])
  by-(rule BinOp- $\tau$ ExecrI2)
  moreover from bisim'
  have P, e1 «bop» e2, h  $\vdash$  (Throw a, xs)  $\leftrightarrow$  ([Addr a]@[v1], loc, length (compE2 e1) + pc', [a])
  by-(rule bisim1BinOpThrow2, auto)
  ultimately show ?thesis using  $\tau$  by auto
qed
qed auto
next
case bisim1BinOpThrow1 thus ?case by fastforce
next
case bisim1BinOpThrow2 thus ?case by fastforce
next
case bisim1BinOpThrow thus ?case by fastforce
next
case (bisim1LAss1 E n e xs stk loc pc xcp V)
note IH = bisim1LAss1.IH(2)
note bisim =  $\langle P, E, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
note red =  $\langle \text{True}, P, t \vdash 1 \langle V := e, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$ 
note bsok =  $\langle \text{bsok} (V := E) n \rangle$ 
from red show ?case
proof cases
  case (LAss1Red ee')
  note [simp] =  $\langle e' = V := ee' \rangle$ 
  and red =  $\langle \text{True}, P, t \vdash 1 \langle e, (h, xs) \rangle -ta \rightarrow \langle ee', (h', xs') \rangle \rangle$ 
from red have  $\tau$ move1 P h (V := e) =  $\tau$ move1 P h e by(auto simp add:  $\tau$ move1.simps  $\tau$ moves1.simps)
  moreover from red have call1 (V := e) = call1 e by auto
  moreover from IH[OF red] bsok
  obtain pc'' stk'' loc'' xcp'' where bisim: P, E, h'  $\vdash$  (ee', xs')  $\leftrightarrow$  (stk'', loc'', pc'', xcp'')
  and redo: ?exec ta E e ee' h stk loc pc xcp h' pc'' stk'' loc'' xcp'' by auto
  from bisim
  have P, V := E, h'  $\vdash$  (V := ee', xs')  $\leftrightarrow$  (stk'', loc'', pc'', xcp'')
  by(rule bisim1-bisims1.bisim1LAss1)
  moreover {
    assume no-call2 E pc
    hence no-call2 (V := E) pc by(auto simp add: no-call2-def) }
  ultimately show ?thesis using redo
  by(auto simp del: call1.simps calls1.simps split: if-split-asm split del: if-split)(blast intro:
LAss- $\tau$ ExecrI LAss- $\tau$ ExecI exec-move-LAssI)+
next
case (Red1LAss v)
note [simp] =  $\langle e = \text{Val } v \rangle \langle ta = \varepsilon \rangle \langle e' = \text{unit} \rangle \langle h' = h \rangle \langle xs' = xs[V := v] \rangle$ 
and V =  $\langle V < \text{length } xs \rangle$ 
from bisim have s: xcp = None xs = loc by(auto dest: bisim-Val-loc-eq-xcp-None)
from bisim have  $\tau$ Exec-mover-a P t E h (stk, loc, pc, xcp) ([v], loc, length (compE2 E), None)
by(auto dest: bisim1Val2D1)

```

hence $\tau Exec\text{-mover-}a P t (V:=E) h (stk, loc, pc, xcp) ([v], loc, length (compE2 E), None)$
by(rule *LAss- $\tau ExecrI$*)
moreover have $exec\text{-move-}a P t (V:=E) h ([v], loc, length (compE2 E), None) \varepsilon h ([], loc[V := v], Suc (length (compE2 E)), None)$
using $V s$ **by**(*auto intro: exec-instr simp add: exec-move-def*)
moreover have $\tau move2 (compP2 P) h [v] (V := E) (length (compE2 E)) None$ **by**(*simp add: $\tau move2\text{-iff}$*)
ultimately have $\tau Exec\text{-mover-}a P t (V:=E) h (stk, loc, pc, xcp) ([], loc[V := v], Suc (length (compE2 E)), None)$
by(*auto intro: rtrancl.rtrancl-into-rtrancl $\tau exec\text{-moveI}$ simp add: compP2-def*)
moreover have $\tau move1 P h (V := Val v)$ **by**(rule *$\tau move1LAssRed$*)
moreover have $P, V:=E, h \vdash (unit, loc[V := v]) \leftrightarrow ([], loc[V := v], Suc (length (compE2 E)), None)$
by(rule *bisim1LAss2*)
ultimately show *?thesis* **using** s **by** *auto*
next
case (*LAss1Throw a*)
note $[simp] = \langle e = Throw a \rangle \langle h' = h \rangle \langle xs' = xs \rangle \langle ta = \varepsilon \rangle \langle e' = Throw a \rangle$
have $\tau: \tau move1 P h (V:=e)$ **by**(*auto intro: $\tau move1LAssThrow$*)
from *bisim* **have** $xcp = [a] \vee xcp = None$ **by**(*auto dest: bisim1-ThrowD*)
thus *?thesis*
proof
assume $[simp]: xcp = [a]$
with *bisim* **have** $P, V:=E, h \vdash (Throw a, xs) \leftrightarrow (stk, loc, pc, xcp)$ **by**(*auto intro: bisim1LAssThrow*)
thus *?thesis* **using** τ **by**(*fastforce*)
next
assume $[simp]: xcp = None$
with *bisim* **obtain** pc'
where $\tau Exec\text{-mover-}a P t E h (stk, loc, pc, None) ([Addr a], loc, pc', [a])$
and $bisim': P, E, h \vdash (Throw a, xs) \leftrightarrow ([Addr a], loc, pc', [a])$ **and** $[simp]: xs = loc$
by(*auto dest: bisim1-Throw- $\tau Exec\text{-mover}$*)
hence $\tau Exec\text{-mover-}a P t (V:=E) h (stk, loc, pc, None) ([Addr a], loc, pc', [a])$
by-(rule *LAss- $\tau ExecrI$*)
moreover from *bisim'* **have** $P, V:=E, h \vdash (Throw a, xs) \leftrightarrow ([Addr a], loc, pc', [a])$
by-(rule *bisim1LAssThrow, auto*)
ultimately show *?thesis* **using** τ **by** *auto*
qed
qed
next
case *bisim1LAss2* **thus** *?case* **by** *fastforce*
next
case *bisim1LAssThrow* **thus** *?case* **by** *fastforce*
next
case (*bisim1AAcc1 a n a' xs stk loc pc xcp i*)
note $IH1 = bisim1AAcc1.IH(2)$
note $IH2 = bisim1AAcc1.IH(4)$
note $bisim1 = \langle P, a, h \vdash (a', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
note $bisim2 = \langle \bigwedge xs. P, i, h \vdash (i, xs) \leftrightarrow ([], xs, 0, None) \rangle$
note $bsok = \langle bsok (a[i]) n \rangle$
from $\langle True, P, t \vdash 1 \langle a'[i], (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$ **show** *?case*
proof *cases*
case (*AAcc1Red1 E'*)
note $[simp] = \langle e' = E'[i] \rangle$
and $red = \langle True, P, t \vdash 1 \langle a', (h, xs) \rangle -ta \rightarrow \langle E', (h', xs') \rangle \rangle$

from red have $\tau move1 P h (a'[i]) = \tau move1 P h a'$ **by** (*auto simp add: $\tau move1.simps \tau moves1.simps$*)
moreover from red have $call1 (a'[i]) = call1 a'$ **by** *auto*
moreover from *IH1[OF red] bsok*
obtain $pc'' stk'' loc'' xcp''$ **where** $bisim: P, a, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$
and $redo: ?exec \ ta \ a \ a' \ E' \ h \ stk \ loc \ pc \ xcp \ h' \ pc'' \ stk'' \ loc'' \ xcp''$ **by** *auto*
from bisim have $P, a[i], h' \vdash (E'[i], xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$
by (*rule bisim1-bisims1.bisim1Acc1*)
moreover {
assume *no-call2 a pc*
hence *no-call2 (a[i]) pc \vee pc = length (compE2 a)* **by** (*auto simp add: no-call2-def*) }
ultimately show *?thesis using redo*
by (*auto simp del: call1.simps calls1.simps split: if-split-asm split del: if-split*) (*blast intro: AAcc- τ ExecrI1 AAcc- τ ExecI1 exec-move-AAccI1*) +
next
case (*AAcc1Red2 E' v*)
note [*simp*] = $\langle a' = Val \ v \rangle \langle e' = Val \ v[E'] \rangle$
and $red = \langle True, P, t \vdash 1 \langle i, (h, xs) \rangle -ta \rightarrow \langle E', (h', xs') \rangle \rangle$
from red have $\tau: \tau move1 P h (Val \ v[i]) = \tau move1 P h i$ **by** (*auto simp add: $\tau move1.simps \tau moves1.simps$*)
from bisim1 have $s: xcp = None \ xs = loc$
and $exec1: \tau Exec-mover-a \ P \ t \ a \ h \ (stk, loc, pc, None) ([v], xs, length (compE2 a), None)$
by (*auto dest: bisim1Val2D1*)
from exec1 have $\tau Exec-mover-a \ P \ t \ (a[i]) \ h \ (stk, loc, pc, None) ([v], xs, length (compE2 a), None)$
by (*rule AAcc- τ ExecrI1*)
moreover
from *IH2[OF red] bsok* **obtain** $pc'' stk'' loc'' xcp''$
where $bisim': P, i, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$
and $exec': ?exec \ ta \ i \ i \ E' \ h \ [] \ xs \ 0 \ None \ h' \ pc'' \ stk'' \ loc'' \ xcp''$ **by** *auto*
have $?exec \ ta \ (a[i]) \ (Val \ v[i]) \ (Val \ v[E']) \ h \ ([] \ @ \ [v]) \ xs \ (length (compE2 a) + 0) \ None \ h' \ (length (compE2 a) + pc'')$ $(stk'' \ @ \ [v]) \ loc'' \ xcp''$
proof (*cases $\tau move1 P h (Val \ v[i])$*)
case *True*
with $exec' \ \tau$ **have** [*simp*]: $h = h'$ **and** $e: sim-move \ i \ E' \ P \ t \ i \ h \ ([], xs, 0, None) (stk'', loc'', pc'', xcp'')$ **by** *auto*
from e have $sim-move (a[i]) (a[E']) P t (a[i]) h ([] \ @ \ [v], xs, length (compE2 a) + 0, None) (stk'' \ @ \ [v], loc'', length (compE2 a) + pc'', xcp'')$
by (*fastforce dest: AAcc- τ ExecrI2 AAcc- τ ExecI2*)
with True show *?thesis by auto*
next
case *False*
with $exec' \ \tau$ **obtain** $pc' stk' loc' xcp'$
where $e: \tau Exec-mover-a \ P \ t \ i \ h \ ([], xs, 0, None) (stk', loc', pc', xcp')$
and $e': exec-move-a \ P \ t \ i \ h \ (stk', loc', pc', xcp') (extTA2JVM (compP2 P) ta) h' (stk'', loc'', pc'', xcp'')$
and $\tau': \neg \tau move2 (compP2 P) h \ stk' \ i \ pc' \ xcp'$
and $call: call1 \ i = None \ \vee \ no-call2 \ i \ 0 \ \vee \ pc' = 0 \ \wedge \ stk' = [] \ \wedge \ loc' = xs \ \wedge \ xcp' = None$ **by** *auto*
from e have $\tau Exec-mover-a \ P \ t \ (a[i]) \ h \ ([] \ @ \ [v], xs, length (compE2 a) + 0, None) (stk' \ @ \ [v], loc', length (compE2 a) + pc', xcp')$
by (*rule AAcc- τ ExecrI2*)
moreover from e' have $exec-move-a \ P \ t \ (a[i]) \ h \ (stk' \ @ \ [v], loc', length (compE2 a) + pc', xcp') (extTA2JVM (compP2 P) ta) h' (stk'' \ @ \ [v], loc'', length (compE2 a) + pc'', xcp'')$
by (*rule exec-move-AAccI2*)

moreover from $e' \tau'$ **have** $\neg \tau_{move2} (compP2 P) h (stk' @ [v]) (a[i]) (length (compE2 a) + pc') xcp'$
by(*auto simp add: $\tau_{instr-stk-drop-exec-move} \tau_{move2-iff}$*)
moreover have $call1 (a'[i]) = call1 i$ **by** *simp*
moreover have $no-call2 i 0 \implies no-call2 (a[i]) (length (compE2 a))$
by(*auto simp add: no-call2-def*)
ultimately show *?thesis using False call*
by(*auto simp del: split-paired-Ex call1.simps calls1.simps*) *blast*
qed
moreover from *bisim'*
have $P, a[i], h' \vdash (Val v [E'], xs') \leftrightarrow ((stk'' @ [v]), loc'', length (compE2 a) + pc'', xcp'')$
by(*rule bisim1-bisims1.bisim1AAcc2*)
moreover from *bisim1* **have** $pc \neq length (compE2 a) \longrightarrow no-call2 (a[i]) pc$
by(*auto simp add: no-call2-def dest: bisim-Val-pc-not-Invoke bisim1-pc-length-compE2*)
ultimately show *?thesis using $\tau_{exec1} s$*
apply(*auto simp del: split-paired-Ex call1.simps calls1.simps split: if-split-asm split del: if-split*)
apply(*blast intro: $\tau_{Exec-mover-trans}$ |fastforce elim!: $\tau_{Exec-mover-trans}$ simp del: split-paired-Ex call1.simps calls1.simps*)
done
next
case (*Red1AAcc A U len I v*)
hence [*simp*]: $a' = addr A e' = Val v i = Val (Intg I) h' = h xs' = xs$
 $ta = \{ReadMem A (ACell (nat (sint I))) v\}$
and *hA: typeof-addr h A = [Array-type U len]* **and** $I: 0 \leq s I \text{ sint } I < \text{int len}$
and *read: heap-read h A (ACell (nat (sint I))) v by auto*
have $\tau: \neg \tau_{move1} P h (addr A [Val (Intg I)])$ **by**(*auto simp add: $\tau_{move1} \tau_{moves1} \tau_{moves1}$*)
from *bisim1* **have** $s: xcp = None \ xs = loc$
and $\tau_{Exec-mover-a} P t a h (stk, loc, pc, xcp) ([Addr A], loc, length (compE2 a), None)$
by(*auto dest: bisim1Val2D1*)
hence $\tau_{Exec-mover-a} P t (a [Val (Intg I)]) h (stk, loc, pc, xcp) ([Addr A], loc, length (compE2 a), None)$
by-(*rule AAcc- $\tau_{ExecrI1}$*)
also have $\tau_{move2} (compP2 P) h [Addr A] (a [Val (Intg I)]) (length (compE2 a) + 0) None$
by(*rule $\tau_{move2AAcc2}$ (rule $\tau_{move2Val}$)*)
hence $\tau_{Exec-mover-a} P t (a [Val (Intg I)]) h ([Addr A], loc, length (compE2 a), None) ([Intg I, Addr A], loc, Suc (length (compE2 a)), None)$
by-(*rule $\tau_{Execr1step}$, auto intro!: exec-instr simp add: exec-move-def compP2-def*)
also (*rtranclp-trans*) **from** *hA I read*
have $exec-move-a P t (a [Val (Intg I)]) h ([Intg I, Addr A], loc, Suc (length (compE2 a)), None)$
 $\{ReadMem A (ACell (nat (sint I))) v\}$
 $h ([v], loc, Suc (Suc (length (compE2 a))), None)$
unfolding *exec-move-def* **by**-(*rule exec-instr, auto simp add: is-Ref-def*)
moreover have $\tau_{move2} (compP2 P) h [Intg I, Addr A] (a [Val (Intg I)]) (Suc (length (compE2 a))) None \implies False$
by(*simp add: $\tau_{move2-iff}$*)
moreover
have $P, a [Val (Intg I)], h \vdash (Val v, loc) \leftrightarrow ([v], loc, length (compE2 (a [Val (Intg I)])), None)$
by(*rule bisim1Val2*) *simp*
ultimately show *?thesis using s τ*
by(*auto simp add: ta-upd-simps*) *blast*
next
case (*Red1AAccNull v*)
note [*simp*] = $\langle a' = null \rangle \langle i = Val v \rangle \langle ta = \varepsilon \rangle \langle e' = THROW NullPointer \rangle \langle h' = h \rangle \langle xs' = xs \rangle$
from *bisim1* **have** $s: xcp = None \ xs = loc$

and $\tau Exec\text{-}mover\text{-}a P t a h (stk, loc, pc, xcp) ([Null], loc, length (compE2 a), None)$
by(*auto dest: bisim1Val2D1 intro: AAcc- τ ExecrI1*)
hence $\tau Exec\text{-}mover\text{-}a P t (a[i]) h (stk, loc, pc, xcp) ([Null], loc, length (compE2 a), None)$
by–(*rule AAcc- τ ExecrI1*)
also from *bisim2[of loc]* **have** $\tau Exec\text{-}mover\text{-}a P t i h ([], loc, 0, None) ([v], loc, length (compE2 i), None)$
by(*auto dest: bisim1Val2D1*)
hence $\tau Exec\text{-}mover\text{-}a P t (a[i]) h ([[] @ [Null], loc, length (compE2 a) + 0, None) ([v] @ [Null], loc, length (compE2 a) + length (compE2 i), None)$
by(*rule AAcc- τ ExecrI2*)
hence $\tau Exec\text{-}mover\text{-}a P t (a[i]) h ([Null], loc, length (compE2 a), None) ([v, Null], loc, length (compE2 a) + length (compE2 i), None)$ **by** *simp*
also (*rtranclp-trans*) **have** $exec\text{-}move\text{-}a P t (a[i]) h ([v, Null], loc, length (compE2 a) + length (compE2 i), None) \varepsilon h ([v, Null], loc, length (compE2 a) + length (compE2 i), [addr\text{-}of\text{-}sys\text{-}xcp\text{-}NullPointer])$
unfolding *exec-move-def* **by**–(*rule exec-instr, auto*)
moreover have $\neg \tau move2 (compP2 P) h [v, Null] (a[i]) (length (compE2 a) + length (compE2 i)) None$
by(*simp add: $\tau move2\text{-}iff$*)
moreover have $\neg \tau move1 P h (a'[i])$ **by**(*auto simp add: $\tau move1.simps \tau moves1.simps$*)
moreover
have $P, a[i], h \vdash (THROW NullPointer, xs) \leftrightarrow ([v, Null], xs, length (compE2 a) + length (compE2 i), [addr\text{-}of\text{-}sys\text{-}xcp\text{-}NullPointer])$
by(*rule bisim1-bisims1.bisim1AAccFail*)
ultimately show *?thesis* **using** *s* **by** *auto blast*
next
case (*Red1AAccBounds A U len I*)
hence [*simp*]: $a' = addr A e' = THROW ArrayIndexOutOfBounds i = Val (Intg I)$
 $ta = \varepsilon h' = h xs' = xs$
and $hA: typeof\text{-}addr h A = [Array\text{-}type U len]$ **and** $I: I < s 0 \vee int len \leq sint I$ **by** *auto*
from *bisim1* **have** $s: xcp = None xs = loc$
and $\tau Exec\text{-}mover\text{-}a P t a h (stk, loc, pc, xcp) ([Addr A], loc, length (compE2 a), None)$
by(*auto dest: bisim1Val2D1*)
hence $\tau Exec\text{-}mover\text{-}a P t (a[i]) h (stk, loc, pc, xcp) ([Addr A], loc, length (compE2 a), None)$
by–(*rule AAcc- τ ExecrI1*)
also from *bisim2[of loc]* **have** $\tau Exec\text{-}mover\text{-}a P t i h ([], loc, 0, None) ([Intg I], loc, length (compE2 i), None)$
by(*auto dest: bisim1Val2D1*)
hence $\tau Exec\text{-}mover\text{-}a P t (a[i]) h ([[] @ [Addr A], loc, length (compE2 a) + 0, None) ([Intg I] @ [Addr A], loc, length (compE2 a) + length (compE2 i), None)$
by(*rule AAcc- τ ExecrI2*)
hence $\tau Exec\text{-}mover\text{-}a P t (a[i]) h ([Addr A], loc, length (compE2 a), None) ([Intg I, Addr A], loc, length (compE2 a) + length (compE2 i), None)$ **by** *simp*
also (*rtranclp-trans*) **from** $I hA$
have $exec\text{-}move\text{-}a P t (a[i]) h ([Intg I, Addr A], loc, length (compE2 a) + length (compE2 i), None) \varepsilon h ([Intg I, Addr A], loc, length (compE2 a) + length (compE2 i), [addr\text{-}of\text{-}sys\text{-}xcp\text{-}ArrayIndexOutOfBounds])$
unfolding *exec-move-def* **by**–(*rule exec-instr, auto simp add: is-Ref-def*)
moreover have $\neg \tau move2 (compP2 P) h [Intg I, Addr A] (a[i]) (length (compE2 a) + length (compE2 i)) None$
by(*simp add: $\tau move2\text{-}iff$*)
moreover have $\neg \tau move1 P h (a'[i])$ **by**(*auto simp add: $\tau move1.simps \tau moves1.simps$*)
moreover
have $P, a[i], h \vdash (THROW ArrayIndexOutOfBounds, xs) \leftrightarrow ([Intg I, Addr A], xs, length (compE2 a) + length (compE2 i), [addr\text{-}of\text{-}sys\text{-}xcp\text{-}ArrayIndexOutOfBounds])$

```

a) + length (compE2 i), [addr-of-sys-xcpt ArrayIndexOutOfBounds])
  by(rule bisim1-bisims1.bisim1AAccFail)
  ultimately show ?thesis using s by auto blast
next
case (AAcc1Throw1 A)
note [simp] = ⟨a' = Throw A⟩ ⟨ta = ε⟩ ⟨e' = Throw A⟩ ⟨h' = h⟩ ⟨xs' = xs⟩
have τ: τmove1 P h (Throw A[i]) by(rule τmove1AAccThrow1)
from bisim1 have xcp = [A] ∨ xcp = None by(auto dest: bisim1-ThrowD)
thus ?thesis
proof
  assume [simp]: xcp = [A]
  with bisim1 have P, a[i], h ⊢ (Throw A, xs) ↔ (stk, loc, pc, xcp)
  by(auto intro: bisim1-bisims1.intros)
  thus ?thesis using τ by(fastforce)
next
assume [simp]: xcp = None
with bisim1 obtain pc' where τExec-mover-a P t a h (stk, loc, pc, None) ([Addr A], loc, pc',
[A])
  and bisim': P, a, h ⊢ (Throw A, xs) ↔ ([Addr A], loc, pc', [A])
  and [simp]: xs = loc
  by(auto dest: bisim1-Throw-τExec-mover)
hence τExec-mover-a P t (a[i]) h (stk, loc, pc, None) ([Addr A], loc, pc', [A])
  by-(rule AAacc-τExecrI1)
moreover from bisim'
have P, a[i], h ⊢ (Throw A, xs) ↔ ([Addr A], loc, pc', [A])
  by(auto intro: bisim1-bisims1.bisim1AAccThrow1)
ultimately show ?thesis using τ by auto
qed
next
case (AAcc1Throw2 v ad)
note [simp] = ⟨a' = Val v⟩ ⟨i = Throw ad⟩ ⟨ta = ε⟩ ⟨e' = Throw ad⟩ ⟨h' = h⟩ ⟨xs' = xs⟩
from bisim1 have s: xcp = None xs = loc
  and τExec-mover-a P t a h (stk, loc, pc, xcp) ([v], loc, length (compE2 a), None)
  by(auto dest: bisim1Val2D1)
hence τExec-mover-a P t (a[Throw ad]) h (stk, loc, pc, xcp) ([v], loc, length (compE2 a), None)
  by-(rule AAacc-τExecrI1)
also have τExec-mover-a P t (a[Throw ad]) h ([v], loc, length (compE2 a), None) ([Addr ad, v],
loc, Suc (length (compE2 a)), [ad])
  by(rule τExecr2step)(auto simp add: exec-move-def exec-meth-instr τmove2-iff τmove1.simps
τmoves1.simps)
also (rtranclp-trans)
  have P, a[Throw ad], h ⊢ (Throw ad, loc) ↔ ([Addr ad] @ [v], loc, (length (compE2 a) + length
(compE2 (addr ad))), [ad])
  by(rule bisim1AAccThrow2[OF bisim1Throw2])
  moreover have τmove1 P h (a'[Throw ad]) by(auto intro: τmove1AAccThrow2)
  ultimately show ?thesis using s by auto
qed
next
case (bisim1AAcc2 i n i' xs stk loc pc xcp a v1)
note IH2 = bisim1AAcc2.IH(2)
note bisim1 = ⟨∧xs. P, a, h ⊢ (a, xs) ↔ ([], xs, 0, None)⟩
note bisim2 = ⟨P, i, h ⊢ (i', xs) ↔ (stk, loc, pc, xcp)⟩
note red = ⟨True, P, t ⊢ 1 ⟨Val v1 [i'], (h, xs)⟩ -ta→ ⟨e', (h', xs')⟩⟩
note bsok = ⟨bsok (a[i]) n⟩

```

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from red show ?case
proof cases
  case (AAcc1Red2 E')
  note [simp] = ⟨e' = Val v1 [E'⟩
    and red = ⟨True, P, t ⊢ 1 ⟨i', (h, xs)⟩ - ta → ⟨E', (h', xs')⟩
  from IH2[OF red] bsok obtain pc'' stk'' loc'' xcp''
    where bisim': P, i, h' ⊢ (E', xs') ↔ (stk'', loc'', pc'', xcp'')
    and exec': ?exec ta i i' E' h stk loc pc xcp h' pc'' stk'' loc'' xcp'' by auto
  from red have  $\tau$ :  $\tau\text{move1 } P \ h \ (Val \ v1 \ [i']) = \tau\text{move1 } P \ h \ i'$  by(auto simp add: \tau\text{move1.simps}
   $\tau\text{moves1.simps}$ )
  have no-call2 i pc ⇒ no-call2 (a[i]) (length (compE2 a) + pc) by(auto simp add: no-call2-def)
  hence ?exec ta (a[i]) (Val v1 [i']) (Val v1 [E']) h (stk @ [v1]) loc (length (compE2 a) + pc) xcp
  h' (length (compE2 a) + pc'') (stk'' @ [v1]) loc'' xcp''
    using exec' \tau
    apply(cases \tau\text{move1 } P \ h \ (Val \ v1 \ [i']))
    apply(auto)
    apply(blast intro: AAcc-\tau\text{ExecrI2} AAcc-\tau\text{ExecI2} \text{exec-move-AAccI2})
    apply(blast intro: AAcc-\tau\text{ExecrI2} AAcc-\tau\text{ExecI2} \text{exec-move-AAccI2})
    apply(rule exI conjI AAcc-\tau\text{ExecrI2} \text{exec-move-AAccI2|assumption}) +
    apply(fastforce simp add: \tau\text{instr-stk-drop-exec-move} \tau\text{move2-iff split: if-split-asm})
    apply(rule exI conjI AAcc-\tau\text{ExecrI2} \text{exec-move-AAccI2|assumption}) +
    apply(fastforce simp add: \tau\text{instr-stk-drop-exec-move} \tau\text{move2-iff split: if-split-asm})
    apply(rule exI conjI AAcc-\tau\text{ExecrI2} \text{exec-move-AAccI2} rtranclp.rtrancl-refl|assumption) +
    apply(fastforce simp add: \tau\text{instr-stk-drop-exec-move} \tau\text{move2-iff split: if-split-asm}) +
    done
  moreover from bisim'
  have P, a[i], h' ⊢ (Val v1 [E'], xs') ↔ (stk''@[v1], loc'', length (compE2 a) + pc'', xcp'')
    by(rule bisim1-bisims1.bisim1AAcc2)
  ultimately show ?thesis using  $\tau$  by auto blast +
next
  case (Red1AAcc A U len I v)
  hence [simp]: v1 = Addr A e' = Val v i' = Val (Intg I)
    ta = {ReadMem A (ACell (nat (sint I))) v} h' = h xs' = xs
    and hA: typeof-addr h A = [Array-type U len] and I:  $0 \leq s \ I \ \text{sint } I < \text{int len}$ 
    and read: heap-read h A (ACell (nat (sint I))) v by auto
  have  $\tau$ :  $\neg \tau\text{move1 } P \ h \ (\text{addr } A \ [Val \ (Intg \ I)])$  by(auto simp add: \tau\text{move1.simps} \tau\text{moves1.simps})
  from bisim2 have s: xcp = None xs = loc
    and  $\tau\text{Exec-mover-a } P \ t \ i \ h \ (stk, loc, pc, xcp) \ ([Intg \ I], loc, length \ (compE2 \ i), None)$ 
    by(auto dest: bisim1Val2D1)
  hence  $\tau\text{Exec-mover-a } P \ t \ (a[i]) \ h \ (stk \ @ \ [Addr \ A], loc, length \ (compE2 \ a) + pc, xcp) \ ([Intg \ I] \ @ \ [Addr \ A], loc, length \ (compE2 \ a) + length \ (compE2 \ i), None)$ 
    by-(rule AAcc-\tau\text{ExecrI2})
  moreover from hA I read
  have exec-move-a P t (a[i]) h ([Intg I, Addr A], loc, length (compE2 a) + length (compE2 i),
  None)
    {ReadMem A (ACell (nat (sint I))) v}
    h ([v], loc, Suc (length (compE2 a) + length (compE2 i)), None)
  unfolding exec-move-def by-(rule exec-instr, auto simp add: is-Ref-def)
  moreover have  $\tau\text{move2} \ (compP2 \ P) \ h \ [Intg \ I, Addr \ A] \ (a[i]) \ (length \ (compE2 \ a) + length \ (compE2 \ i)) \ None \Rightarrow False$ 
    by(simp add: \tau\text{move2-iff})
  moreover
  have P, a[i], h ⊢ (Val v, loc) ↔ ([v], loc, length (compE2 (a[i])), None)
    by(rule bisim1Val2 simp)

```


ultimately show *?thesis* using s τ by(auto simp add: ta-upd-simps) blast

next

case (Red1AAccNull v)

note [simp] = $\langle v1 = \text{Null} \rangle \langle i' = \text{Val } v \rangle \langle ta = \varepsilon \rangle \langle e' = \text{THROW NullPointer} \rangle \langle h' = h \rangle \langle xs' = xs \rangle$

from bisim2 have [simp]: $xcp = \text{None } xs = loc$

and $\tau \text{Exec-mover-a } P \ t \ i \ h \ (stk, loc, pc, xcp) \ ([v], loc, length \ (compE2 \ i), \text{None})$

by(auto dest: bisim1Val2D1)

hence $\tau \text{Exec-mover-a } P \ t \ (a[i]) \ h \ (stk \ @ \ [Null], loc, length \ (compE2 \ a) + pc, xcp) \ ([v] \ @ \ [Null], loc, length \ (compE2 \ a) + length \ (compE2 \ i), \text{None})$

by-(rule AAcc- $\tau \text{ExecrI2}$)

moreover have $\text{exec-move-a } P \ t \ (a[i]) \ h \ ([v, \text{Null}], loc, length \ (compE2 \ a) + length \ (compE2 \ i), \text{None}) \ \varepsilon \ h \ ([v, \text{Null}], loc, length \ (compE2 \ a) + length \ (compE2 \ i), [addr-of-sys-xcpt \ \text{NullPointer}])$

unfolding exec-move-def by-(rule exec-instr , auto)

moreover have $\neg \tau \text{move2} \ (compP2 \ P) \ h \ [v, \text{Null}] \ (a[i]) \ (length \ (compE2 \ a) + length \ (compE2 \ i)) \ \text{None}$

by(simp add: $\tau \text{move2-iff}$)

moreover have $\neg \tau \text{move1} \ P \ h \ (\text{null}[i'])$ by(auto simp add: $\tau \text{move1.simps } \tau \text{moves1.simps}$)

moreover

have $P, a[i], h \vdash (\text{THROW NullPointer}, xs) \leftrightarrow ([v, \text{Null}], xs, length \ (compE2 \ a) + length \ (compE2 \ i), [addr-of-sys-xcpt \ \text{NullPointer}])$

by(rule bisim1-bisims1.bisim1AAccFail)

ultimately show *?thesis* by auto blast

next

case (Red1AAccBounds $A \ U \ len \ I$)

hence [simp]: $v1 = \text{Addr } A \ e' = \text{THROW ArrayIndexOutOfBounds } i' = \text{Val } (\text{Intg } I)$

$ta = \varepsilon \ h' = h \ xs' = xs$

and $hA: \text{typeof-addr } h \ A = [\text{Array-type } U \ len]$ and $I: I < s \ 0 \vee \text{int } len \leq \text{sint } I$ by auto

from bisim2 have $s: xcp = \text{None } xs = loc$

and $\tau \text{Exec-mover-a } P \ t \ i \ h \ (stk, loc, pc, xcp) \ ([\text{Intg } I], loc, length \ (compE2 \ i), \text{None})$

by(auto dest: bisim1Val2D1)

hence $\tau \text{Exec-mover-a } P \ t \ (a[i]) \ h \ (stk \ @ \ [\text{Addr } A], loc, length \ (compE2 \ a) + pc, xcp) \ ([\text{Intg } I] \ @ \ [\text{Addr } A], loc, length \ (compE2 \ a) + length \ (compE2 \ i), \text{None})$

by-(rule AAcc- $\tau \text{ExecrI2}$)

moreover from $I \ hA$

have $\text{exec-move-a } P \ t \ (a[i]) \ h \ ([\text{Intg } I, \text{Addr } A], loc, length \ (compE2 \ a) + length \ (compE2 \ i), \text{None}) \ \varepsilon \ h \ ([\text{Intg } I, \text{Addr } A], loc, length \ (compE2 \ a) + length \ (compE2 \ i), [addr-of-sys-xcpt \ \text{ArrayIndexOutOfBounds}])$

unfolding exec-move-def by-(rule exec-instr , auto simp add: is-Ref-def)

moreover have $\neg \tau \text{move2} \ (compP2 \ P) \ h \ [\text{Intg } I, \text{Addr } A] \ (a[i]) \ (length \ (compE2 \ a) + length \ (compE2 \ i)) \ \text{None}$

by(simp add: $\tau \text{move2-iff}$)

moreover have $\neg \tau \text{move1} \ P \ h \ (\text{addr } A[i'])$ by(auto simp add: $\tau \text{move1.simps } \tau \text{moves1.simps}$)

moreover

have $P, a[i], h \vdash (\text{THROW ArrayIndexOutOfBounds}, xs) \leftrightarrow ([\text{Intg } I, \text{Addr } A], xs, length \ (compE2 \ a) + length \ (compE2 \ i), [addr-of-sys-xcpt \ \text{ArrayIndexOutOfBounds}])$

by(rule bisim1-bisims1.bisim1AAccFail)

ultimately show *?thesis* using s by auto blast

next

case (AAcc1Throw2 A)

note [simp] = $\langle i' = \text{Throw } A \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw } A \rangle \langle h' = h \rangle \langle xs' = xs \rangle$

have $\tau: \tau \text{move1} \ P \ h \ (\text{Val } v1 \ [\ \text{Throw } A])$ by(rule $\tau \text{move1AAccThrow2}$)

from bisim2 have $xcp = [A] \vee xcp = \text{None}$ by(auto dest: bisim1-ThrowD)

thus *?thesis*

proof

```

assume [simp]: xcp = [A]
with bisim2
have P, a[i], h ⊢ (Throw A, xs) ↔ (stk @ [v1], loc, length (compE2 a) + pc, xcp)
  by(auto intro: bisim1-bisims1.intros)
thus ?thesis using τ by(auto)
next
assume [simp]: xcp = None
with bisim2 obtain pc' where τExec-mover-a P t i h (stk, loc, pc, None) ([Addr A], loc, pc',
[A])
  and bisim': P, i, h ⊢ (Throw A, xs) ↔ ([Addr A], loc, pc', [A])
  and [simp]: xs = loc
  by(auto dest: bisim1-Throw-τExec-mover)
hence τExec-mover-a P t (a[i]) h (stk @ [v1], loc, length (compE2 a) + pc, None) ([Addr A] @
[v1], loc, length (compE2 a) + pc', [A])
  by-(rule AAcc-τExecrI2)
moreover from bisim'
have P, a[i], h ⊢ (Throw A, xs) ↔ ([Addr A] @ [v1], loc, length (compE2 a) + pc', [A])
  by(rule bisim1-bisims1.bisim1AAccThrow2)
ultimately show ?thesis using τ by auto
qed
qed auto
next
case bisim1AAccThrow1 thus ?case by auto
next
case bisim1AAccThrow2 thus ?case by auto
next
case bisim1AAccFail thus ?case by auto
next
case (bisim1AAss1 a n a' xs stk loc pc xcp i e)
note IH1 = bisim1AAss1.IH(2)
note IH2 = bisim1AAss1.IH(4)
note IH3 = bisim1AAss1.IH(6)
note bisim1 = ⟨P, a, h ⊢ (a', xs) ↔ (stk, loc, pc, xcp)⟩
note bisim2 = ⟨∧xs. P, i, h ⊢ (i, xs) ↔ ([], xs, 0, None)⟩
note bisim3 = ⟨∧xs. P, e, h ⊢ (e, xs) ↔ ([], xs, 0, None)⟩
note bsok = ⟨bsok (a[i] := e) n⟩
from ⟨True, P, t ⊢ 1 ⟨a'[i] := e, (h, xs)⟩ -ta→ ⟨e', (h', xs')⟩⟩ show ?case
proof cases
  case (AAss1Red1 E')
    note [simp] = ⟨e' = E'[i] := e⟩
    and red = ⟨True, P, t ⊢ 1 ⟨a', (h, xs)⟩ -ta→ ⟨E', (h', xs')⟩⟩
    from red have τmove1 P h (a'[i] := e) = τmove1 P h a' by(auto simp add: τmove1.simps
τmoves1.simps)
    moreover from red have call1 (a'[i] := e) = call1 a' by auto
    moreover from IH1[OF red] bsok
    obtain pc'' stk'' loc'' xcp'' where bisim: P, a, h' ⊢ (E', xs') ↔ (stk'', loc'', pc'', xcp'')
      and redo: ?exec ta a a' E' h stk loc pc xcp h' pc'' stk'' loc'' xcp'' by auto
    from bisim
    have P, a[i] := e, h' ⊢ (E'[i] := e, xs') ↔ (stk'', loc'', pc'', xcp'')
      by(rule bisim1-bisims1.bisim1AAss1)
    moreover {
      assume no-call2 a pc
      hence no-call2 (a[i] := e) pc ∨ pc = length (compE2 a) by(auto simp add: no-call2-def) }
    ultimately show ?thesis using redo

```

by(*auto simp del: call1.simps calls1.simps split: if-split-asm split del: if-split*)(*blast intro: AAss- τ ExecrI1 AAss- τ ExecI1 exec-move-AAssI1*)+
next
case (*AAss1Red2 E' v*)
note [*simp*] = $\langle a' = \text{Val } v \rangle \langle e' = \text{Val } v[E^\top] := e \rangle$
and *red* = $\langle \text{True}, P, t \vdash 1 \langle i, (h, xs) \rangle -ta \rightarrow \langle E', (h', xs') \rangle \rangle$
from *red* **have** $\tau: \tau \text{move1 } P \ h \ (\text{Val } v[i] := e) = \tau \text{move1 } P \ h \ i$ **by**(*auto simp add: $\tau \text{move1}.simps \tau \text{moves1}.simps$*)
from *bisim1* **have** *s: xcp = None xs = loc*
and *exec1: $\tau \text{Exec-mover-a } P \ t \ a \ h \ (\text{stk}, \text{loc}, \text{pc}, \text{None}) \ ([v], \text{xs}, \text{length}(\text{compE2 } a), \text{None})$*
by(*auto dest: bisim1Val2D1*)
from *exec1* **have** $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (\text{stk}, \text{loc}, \text{pc}, \text{None}) \ ([v], \text{xs}, \text{length}(\text{compE2 } a), \text{None})$
by(*rule AAss- τ ExecrI1*)
moreover
from *IH2[OF red] bsok* **obtain** *pc'' stk'' loc'' xcp''*
where *bisim': $P, i, h' \vdash (E', xs') \leftrightarrow (\text{stk}'', \text{loc}'', \text{pc}'', \text{xcp}'')$*
and *exec': $?exec \ ta \ i \ i \ E' \ h \ [] \ xs \ 0 \ \text{None} \ h' \ \text{pc}'' \ \text{stk}'' \ \text{loc}'' \ \text{xcp}''$* **by** *auto*
have $?exec \ ta \ (a[i] := e) \ (\text{Val } v[i] := e) \ (\text{Val } v[E^\top] := e) \ h \ ([] \ @ \ [v]) \ \text{xs} \ (\text{length}(\text{compE2 } a) + 0) \ \text{None} \ h' \ (\text{length}(\text{compE2 } a) + \text{pc}'') \ (\text{stk}'' \ @ \ [v]) \ \text{loc}'' \ \text{xcp}''$
proof(*cases $\tau \text{move1 } P \ h \ (\text{Val } v[i] := e)$*)
case *True*
with *exec' τ* **have** [*simp*]: *h = h' and e: sim-move i E' P t i h* ($[], \text{xs}, 0, \text{None}$) (*stk'', loc'', pc'', xcp''*) **by** *auto*
from *e* **have** *sim-move* ($a[i] := e$) ($a[E^\top] := e$) *P t* ($a[i] := e$) *h* ($[] \ @ \ [v], \text{xs}, \text{length}(\text{compE2 } a) + 0, \text{None}$) (*stk'' @ [v], loc'', length(compE2 a) + pc'', xcp''*)
by(*fastforce dest: AAss- τ ExecrI2 AAss- τ ExecI2*)
with *True* **show** *?thesis* **by** *auto*
next
case *False*
with *exec' τ* **obtain** *pc' stk' loc' xcp'*
where *e: $\tau \text{Exec-mover-a } P \ t \ i \ h \ ([], \text{xs}, 0, \text{None}) \ (\text{stk}', \text{loc}', \text{pc}', \text{xcp}')$*
and *e': exec-move-a P t i h* (*stk', loc', pc', xcp'*) (*extTA2JVM (compP2 P) ta*) *h' (stk'', loc'', pc'', xcp'')*
and $\tau': \neg \tau \text{move2} \ (\text{compP2 } P) \ h \ \text{stk}' \ i \ \text{pc}' \ \text{xcp}'$
and *call: call1 i = None \vee no-call2 i 0 \vee pc' = 0 \wedge stk' = [] \wedge loc' = xs \wedge xcp' = None* **by** *auto*
from *e* **have** $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ ([] \ @ \ [v], \text{xs}, \text{length}(\text{compE2 } a) + 0, \text{None}) \ (\text{stk}' \ @ \ [v], \text{loc}', \text{length}(\text{compE2 } a) + \text{pc}', \text{xcp}')$ **by**(*rule AAss- τ ExecrI2*)
moreover **from** *e'* **have** *exec-move-a P t* ($a[i] := e$) *h* (*stk' @ [v], loc', length(compE2 a) + pc', xcp'*) (*extTA2JVM (compP2 P) ta*) *h' (stk'' @ [v], loc'', length(compE2 a) + pc'', xcp'')*
by(*rule exec-move-AAssI2*)
moreover **from** *e'* **have** *pc' < length(compE2 i)* **by**(*auto elim: exec-meth.cases*)
with $\tau' \ e'$ **have** $\neg \tau \text{move2} \ (\text{compP2 } P) \ h \ (\text{stk}' \ @ \ [v]) \ (a[i] := e) \ (\text{length}(\text{compE2 } a) + \text{pc}') \ \text{xcp}'$
by(*auto simp add: $\tau \text{instr-stk-drop-exec-move} \ \tau \text{move2-iff}$*)
moreover **from** *red* **have** *call1* ($a'[i] := e$) = *call1 i* **by** *auto*
moreover **have** *no-call2 i 0 \implies no-call2 (a[i] := e) (length(compE2 a))*
by(*auto simp add: no-call2-def*)
ultimately **show** *?thesis* **using** *False call*
by(*auto simp del: split-paired-Ex call1.simps calls1.simps*) *blast*
qed
moreover **from** *bisim'*
have $P, a[i] := e, h' \vdash (\text{Val } v[E^\top] := e, xs') \leftrightarrow ((\text{stk}'' \ @ \ [v]), \text{loc}'', \text{length}(\text{compE2 } a) + \text{pc}'', \text{xcp}'')$
by(*rule bisim1-bisims1.bisim1AAss2*)

moreover from *bisim1* **have** $pc \neq \text{length}(\text{compE2 } a) \longrightarrow \text{no-call2}(a[i] := e) pc$
by(*auto simp add: no-call2-def dest: bisim-Val-pc-not-Invoke bisim1-pc-length-compE2*)
ultimately show *?thesis* **using** $\tau \text{ exec1 } s$
apply(*auto simp del: split-paired-Ex call1.simps calls1.simps split: if-split-asm split del: if-split*)
apply(*blast intro: $\tau \text{Exec-mover-trans}$ |fastforce elim!: $\tau \text{Exec-mover-trans}$ simp del: split-paired-Ex call1.simps calls1.simps*)
done
next
case (*AAss1Red3 E' v v'*)
note [*simp*] = $\langle i = \text{Val } v' \rangle \langle a' = \text{Val } v \rangle \langle e' = \text{Val } v \mid \text{Val } v^\top := E' \rangle$
and $red = \langle \text{True}, P, t \vdash 1 \langle e, (h, xs) \rangle -ta \rightarrow \langle E', (h', xs') \rangle \rangle$
from *red* **have** $\tau: \tau \text{move1 } P h (\text{Val } v \mid \text{Val } v^\top := e) = \tau \text{move1 } P h e$ **by**(*auto simp add: $\tau \text{move1}.simps$ $\tau \text{moves1}.simps$*)
from *bisim1* **have** $s: xcp = \text{None } xs = \text{loc}$
and *exec1*: $\tau \text{Exec-mover-a } P t a h (stk, loc, pc, \text{None}) ([] @ [v], xs, \text{length}(\text{compE2 } a) + 0, \text{None})$
by(*auto dest: bisim1Val2D1*)
from *exec1* **have** $\tau \text{Exec-mover-a } P t (a[i] := e) h (stk, loc, pc, \text{None}) ([] @ [v], xs, \text{length}(\text{compE2 } a) + 0, \text{None})$
by(*rule AAss- $\tau \text{ExecrI1}$*)
also from *bisim2*[*of xs*]
have $\tau \text{Exec-mover-a } P t i h ([], xs, 0, \text{None}) ([v^\top], xs, \text{length}(\text{compE2 } i), \text{None})$
by(*auto dest: bisim1Val2D1*)
hence $\tau \text{Exec-mover-a } P t (a[i] := e) h ([] @ [v], xs, \text{length}(\text{compE2 } a) + 0, \text{None}) ([v^\top] @ [v], xs, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i), \text{None})$
by(*rule AAss- $\tau \text{ExecrI2}$*)
also (*rtranclp-trans*) **from** *IH3[OF red] bsok* **obtain** $pc'' stk'' loc'' xcp''$
where *bisim'*: $P, e, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$
and *exec'*: $?exec \text{ ta } e e E' h [] xs 0 \text{ None } h' pc'' stk'' loc'' xcp''$ **by** *auto*
have $?exec \text{ ta } (a[i] := e) (\text{Val } v \mid \text{Val } v^\top := e) (\text{Val } v \mid \text{Val } v^\top := E') h ([] @ [v', v], xs, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + 0) \text{ None } h' (\text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + pc'') (stk'' @ [v', v], loc'' xcp''$
proof(*cases $\tau \text{move1 } P h (\text{Val } v \mid \text{Val } v^\top := e)$*)
case *True*
with *exec'* τ **have** [*simp*]: $h = h'$ **and** $e: \text{sim-move } e E' P t e h ([], xs, 0, \text{None}) (stk'', loc'', pc'', xcp'')$ **by** *auto*
from e **have** $\text{sim-move } (\text{Val } v \mid \text{Val } v^\top := e) (\text{Val } v \mid \text{Val } v^\top := E') P t (a[i] := e) h ([] @ [v', v], xs, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + 0, \text{None}) (stk'' @ [v', v], loc'', \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + pc'', xcp'')$
by(*fastforce dest: AAss- τExecI3 AAss- $\tau \text{ExecrI3}$ simp del: compE2.simps compEs2.simps*)
with *True* **show** *?thesis* **by** *auto*
next
case *False*
with *exec'* τ **obtain** $pc' stk' loc' xcp'$
where $e: \tau \text{Exec-mover-a } P t e h ([], xs, 0, \text{None}) (stk', loc', pc', xcp')$
and $e': \text{exec-move-a } P t e h (stk', loc', pc', xcp') (\text{extTA2JVM } (\text{compP2 } P) \text{ ta}) h' (stk'', loc'', pc'', xcp'')$
and $\tau': \neg \tau \text{move2 } (\text{compP2 } P) h stk' e pc' xcp'$
and $\text{call}: \text{call1 } e = \text{None} \vee \text{no-call2 } e 0 \vee pc' = 0 \wedge stk' = [] \wedge loc' = xs \wedge xcp' = \text{None}$ **by** *auto*
from e **have** $\tau \text{Exec-mover-a } P t (a[i] := e) h ([] @ [v', v], xs, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + 0, \text{None}) (stk' @ [v', v], loc', \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + pc', xcp')$
by(*rule AAss- $\tau \text{ExecrI3}$*)
moreover from e' **have** $\text{exec-move-a } P t (a[i] := e) h (stk' @ [v', v], loc', \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + pc', xcp') (\text{extTA2JVM } (\text{compP2 } P) \text{ ta}) h' (stk'' @ [v', v], loc'', \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + pc'', xcp'')$

$a) + \text{length}(\text{compE2 } i) + \text{pc}'', \text{xcp}'')$
by(rule *exec-move-AAssI3*)
moreover from $e' \tau'$
have $\neg \tau \text{move2}(\text{compP2 } P) h(\text{stk}' @ [v', v]) (a[i] := e) (\text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + \text{pc}') \text{xcp}'$
by(auto simp add: $\tau \text{instr-stk-drop-exec-move } \tau \text{move2-iff}$)
moreover have $\text{call1}(a'[i] := e) = \text{call1 } e$ **by** simp
moreover have $\text{no-call2 } e \ 0 \implies \text{no-call2}(a[i] := e) (\text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i))$
by(auto simp add: *no-call2-def*)
ultimately show *?thesis* **using** *False call*
by(auto simp del: *split-paired-Ex call1.simps calls1.simps*) *blast*
qed
moreover from *bisim'*
have $P, a[i] := e, h' \vdash (\text{Val } v [\text{Val } v'] := E', \text{xs}') \leftrightarrow ((\text{stk}'' @ [v', v]), \text{loc}'', \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + \text{pc}'', \text{xcp}'')$
by(rule *bisim1-bisims1.bisim1AAss3*)
moreover from *bisim1* **have** $\text{pc} \neq \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) \longrightarrow \text{no-call2}(a[i] := e) \text{pc}$
by(auto simp add: *no-call2-def dest: bisim-Val-pc-not-Invoke bisim1-pc-length-compE2*)
ultimately show *?thesis* **using** $\tau \text{exec1 } s$
apply(auto simp del: *split-paired-Ex call1.simps calls1.simps split: if-split-asm split del: if-split*)
apply(*blast intro: \tau Exec-mover-trans|fastforce elim!: \tau Exec-mover-trans simp del: split-paired-Ex call1.simps calls1.simps*)
done
next
case (*Red1AAss A U len I v U'*)
hence [*simp*]: $a' = \text{addr } A \ e' = \text{unit } i = \text{Val}(\text{Intg } I)$
 $ta = \{ \text{WriteMem } A (\text{ACell}(\text{nat}(\text{sint } I))) \ v \} \ \text{xs}' = \text{xs} \ e = \text{Val } v$
and $hA: \text{typeof-addr } h \ A = \lfloor \text{Array-type } U \ \text{len} \rfloor$ **and** $I: 0 \leq s \ I \ \text{sint } I < \text{int } \text{len}$
and $v: \text{typeof}_h \ v = \lfloor U' \rfloor \ P \vdash U' \leq U$
and $h': \text{heap-write } h \ A (\text{ACell}(\text{nat}(\text{sint } I))) \ v \ h'$ **by** auto
have $\tau: \neg \tau \text{move1 } P \ h \ (\text{AAss}(\text{addr } A) (\text{Val}(\text{Intg } I)) (\text{Val } v))$ **by**(auto simp add: $\tau \text{move1}.\text{simps}$ $\tau \text{moves1}.\text{simps}$)
from *bisim1* **have** $s: \text{xcp} = \text{None} \ \text{xs} = \text{loc}$
and $\tau \text{Exec-mover-a } P \ t \ a \ h(\text{stk}, \text{loc}, \text{pc}, \text{xcp}) (\ [] @ [\text{Addr } A], \text{loc}, \text{length}(\text{compE2 } a) + 0, \text{None})$
by(auto dest: *bisim1Val2D1*)
hence $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h(\text{stk}, \text{loc}, \text{pc}, \text{xcp}) (\ [] @ [\text{Addr } A], \text{loc}, \text{length}(\text{compE2 } a) + 0, \text{None})$
by-(rule *AAss-\tau ExecrI1*)
also from *bisim2[of loc]*
have $\tau \text{Exec-mover-a } P \ t \ i \ h(\ [], \text{loc}, 0, \text{None}) ([\text{Intg } I], \text{loc}, \text{length}(\text{compE2 } i) + 0, \text{None})$
by(auto dest: *bisim1Val2D1*)
hence $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h(\ [] @ [\text{Addr } A], \text{loc}, \text{length}(\text{compE2 } a) + 0, \text{None}) ([\text{Intg } I] @ [\text{Addr } A], \text{loc}, \text{length}(\text{compE2 } a) + (\text{length}(\text{compE2 } i) + 0), \text{None})$
by(rule *AAss-\tau ExecrI2*)
also (*rtranclp-trans*) **have** $[\text{Intg } I] @ [\text{Addr } A] = \ [] @ [\text{Intg } I, \text{Addr } A]$ **by** simp
also note *add.assoc[symmetric]*
also from *bisim3[of loc]* **have** $\tau \text{Exec-mover-a } P \ t \ e \ h(\ [], \text{loc}, 0, \text{None}) ([v], \text{loc}, \text{length}(\text{compE2 } e), \text{None})$
by(auto dest: *bisim1Val2D1*)
hence $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h(\ [] @ [\text{Intg } I, \text{Addr } A], \text{loc}, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + 0, \text{None}) ([v] @ [\text{Intg } I, \text{Addr } A], \text{loc}, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + \text{length}(\text{compE2 } e), \text{None})$
by(rule *AAss-\tau ExecrI3*)

also (*rtranclp-trans*) **from** $hA\ I\ v\ h'$
have *exec-move-a* $P\ t\ (a[i] := e)\ h\ ([v, \text{Intg}\ I, \text{Addr}\ A], \text{loc}, \text{length}(\text{compE2}\ a) + \text{length}(\text{compE2}\ i) + \text{length}(\text{compE2}\ e), \text{None})$
 $\{\text{WriteMem}\ A\ (\text{ACell}\ (\text{nat}\ (\text{sint}\ I)))\ v\}$
 $h'(\ [], \text{loc}, \text{Suc}(\text{length}(\text{compE2}\ a) + \text{length}(\text{compE2}\ i) + \text{length}(\text{compE2}\ e))), \text{None})$
unfolding *exec-move-def* **by**–(*rule exec-instr, auto simp add: compP2-def is-Ref-def*)
moreover **have** $\tau\text{move2}\ (\text{compP2}\ P)\ h\ [v, \text{Intg}\ I, \text{Addr}\ A]\ (a[i] := e)\ (\text{length}(\text{compE2}\ a) + \text{length}(\text{compE2}\ i) + \text{length}(\text{compE2}\ e))\ \text{None} \implies \text{False}$
by(*simp add: $\tau\text{move2-iff}$*)
moreover
have $P, a[i] := e, h' \vdash (\text{unit}, \text{loc}) \leftrightarrow (\ [], \text{loc}, \text{Suc}(\text{length}(\text{compE2}\ a) + \text{length}(\text{compE2}\ i) + \text{length}(\text{compE2}\ e))), \text{None})$
by(*rule bisim1-bisims1.bisim1AAss4*)
ultimately show *?thesis* **using** $s\ \tau$ **by**(*auto simp add: ta-upd-simps*) **blast**
next
case (*Red1AAssNull* $v\ v'$)
note $[simp] = \langle a' = \text{null} \rangle \langle e' = \text{THROW}\ \text{NullPointer} \rangle \langle i = \text{Val}\ v \rangle \langle xs' = xs \rangle \langle ta = \varepsilon \rangle \langle h' = h \rangle \langle e = \text{Val}\ v' \rangle$
have $\tau: \neg \tau\text{move1}\ P\ h\ (\text{AAss}\ \text{null}\ (\text{Val}\ v)\ (\text{Val}\ v'))$ **by**(*auto simp add: $\tau\text{move1.simps}$ $\tau\text{moves1.simps}$*)
from *bisim1* **have** $s: \text{xcp} = \text{None}\ xs = \text{loc}$
and $\tau\text{Exec-mover-a}\ P\ t\ a\ h\ (\text{stk}, \text{loc}, \text{pc}, \text{xcp})\ (\ []\ @\ [\text{Null}], \text{loc}, \text{length}(\text{compE2}\ a) + 0, \text{None})$
by(*auto dest: bisim1Val2D1*)
hence $\tau\text{Exec-mover-a}\ P\ t\ (a[i] := e)\ h\ (\text{stk}, \text{loc}, \text{pc}, \text{xcp})\ (\ []\ @\ [\text{Null}], \text{loc}, \text{length}(\text{compE2}\ a) + 0, \text{None})$
by–(*rule AAss- $\tau\text{ExecrI1}$*)
also from *bisim2[of loc]* **have** $\tau\text{Exec-mover-a}\ P\ t\ i\ h\ (\ [], \text{loc}, 0, \text{None})\ ([v], \text{loc}, \text{length}(\text{compE2}\ i) + 0, \text{None})$
by(*auto dest: bisim1Val2D1*)
hence $\tau\text{Exec-mover-a}\ P\ t\ (a[i] := e)\ h\ (\ []\ @\ [\text{Null}], \text{loc}, \text{length}(\text{compE2}\ a) + 0, \text{None})\ ([v]\ @\ [\text{Null}], \text{loc}, \text{length}(\text{compE2}\ a) + (\text{length}(\text{compE2}\ i) + 0), \text{None})$
by(*rule AAss- $\tau\text{ExecrI2}$*)
also (*rtranclp-trans*) **have** $[v]\ @\ [\text{Null}] = \ []\ @\ [v, \text{Null}]$ **by** *simp*
also note *add.assoc[symmetric]*
also from *bisim3[of loc]* **have** $\tau\text{Exec-mover-a}\ P\ t\ e\ h\ (\ [], \text{loc}, 0, \text{None})\ ([v'], \text{loc}, \text{length}(\text{compE2}\ e), \text{None})$
by(*auto dest: bisim1Val2D1*)
hence $\tau\text{Exec-mover-a}\ P\ t\ (a[i] := e)\ h\ (\ []\ @\ [v, \text{Null}], \text{loc}, \text{length}(\text{compE2}\ a) + \text{length}(\text{compE2}\ i) + 0, \text{None})\ ([v']\ @\ [v, \text{Null}], \text{loc}, \text{length}(\text{compE2}\ a) + \text{length}(\text{compE2}\ i) + \text{length}(\text{compE2}\ e), \text{None})$
by(*rule AAss- $\tau\text{ExecrI3}$*)
also (*rtranclp-trans*)
have *exec-move-a* $P\ t\ (a[i] := e)\ h\ ([v', v, \text{Null}], \text{loc}, \text{length}(\text{compE2}\ a) + \text{length}(\text{compE2}\ i) + \text{length}(\text{compE2}\ e), \text{None})\ \varepsilon$
 $h\ ([v', v, \text{Null}], \text{loc}, \text{length}(\text{compE2}\ a) + \text{length}(\text{compE2}\ i) + \text{length}(\text{compE2}\ e), \lfloor \text{addr-of-sys-xcpt}\ \text{NullPointer} \rfloor)$
unfolding *exec-move-def* **by**–(*rule exec-instr, auto simp add: is-Ref-def*)
moreover **have** $\tau\text{move2}\ (\text{compP2}\ P)\ h\ [v', v, \text{Null}]\ (a[i] := e)\ (\text{length}(\text{compE2}\ a) + \text{length}(\text{compE2}\ i) + \text{length}(\text{compE2}\ e))\ \text{None} \implies \text{False}$
by(*simp add: $\tau\text{move2-iff}$*)
moreover
have $P, a[i] := e, h' \vdash (\text{THROW}\ \text{NullPointer}, \text{loc}) \leftrightarrow ([v', v, \text{Null}], \text{loc}, \text{length}(\text{compE2}\ a) + \text{length}(\text{compE2}\ i) + \text{length}(\text{compE2}\ e), \lfloor \text{addr-of-sys-xcpt}\ \text{NullPointer} \rfloor)$
by(*rule bisim1-bisims1.bisim1AAssFail*)

ultimately show *?thesis* using $s \tau$ by auto blast

next

case (Red1AAssBounds A U len I v)

hence [simp]: $a' = \text{addr } A \ e' = \text{THROW } \text{ArrayIndexOutOfBounds } i = \text{Val } (\text{Intg } I) \ xs' = xs \ ta = \varepsilon \ h' = h \ e = \text{Val } v$

and hA : $\text{typeof-addr } h \ A = \lfloor \text{Array-type } U \ \text{len} \rfloor$ and I : $I < s \ 0 \vee \text{int } \text{len} \leq \text{sint } I$ by auto

have τ : $\neg \tau \text{move1 } P \ h \ (AAss \ (\text{addr } A) \ i \ e)$ by (auto simp add: $\tau \text{move1.simps}$ $\tau \text{moves1.simps}$)

from bisim1 have s : $xcp = \text{None} \ xs = \text{loc}$

and $\tau \text{Exec-mover-a } P \ t \ a \ h \ (stk, \text{loc}, \text{pc}, \text{xcp}) \ (\llbracket \rrbracket @ [\text{Addr } A], \text{loc}, \text{length} (\text{compE2 } a) + 0, \text{None})$

by (auto dest: bisim1Val2D1)

hence $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (stk, \text{loc}, \text{pc}, \text{xcp}) \ (\llbracket \rrbracket @ [\text{Addr } A], \text{loc}, \text{length} (\text{compE2 } a) + 0, \text{None})$

by-(rule AAss- $\tau \text{ExecrI1}$)

also from bisim2[of loc]

have $\tau \text{Exec-mover-a } P \ t \ i \ h \ (\llbracket \rrbracket, \text{loc}, 0, \text{None}) \ (\llbracket \rrbracket @ [\text{Intg } I], \text{loc}, \text{length} (\text{compE2 } i) + 0, \text{None})$

by (auto dest: bisim1Val2D1)

hence $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (\llbracket \rrbracket @ [\text{Addr } A], \text{loc}, \text{length} (\text{compE2 } a) + 0, \text{None}) \ (\llbracket \rrbracket @ [\text{Intg } I] @ [\text{Addr } A], \text{loc}, \text{length} (\text{compE2 } a) + (\text{length} (\text{compE2 } i) + 0), \text{None})$

by (rule AAss- $\tau \text{ExecrI2}$)

also (rtranclp-trans) have $[\text{Intg } I] @ [\text{Addr } A] = \llbracket \rrbracket @ [\text{Intg } I, \text{Addr } A]$ by simp

also note $\text{add.assoc[symmetric]}$

also from bisim3[of loc]

have $\tau \text{Exec-mover-a } P \ t \ e \ h \ (\llbracket \rrbracket, \text{loc}, 0, \text{None}) \ ([v], \text{loc}, \text{length} (\text{compE2 } e), \text{None})$

by (auto dest: bisim1Val2D1)

hence $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (\llbracket \rrbracket @ [\text{Intg } I, \text{Addr } A], \text{loc}, \text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i) + 0, \text{None}) \ ([v] @ [\text{Intg } I, \text{Addr } A], \text{loc}, \text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i) + \text{length} (\text{compE2 } e), \text{None})$

by (rule AAss- $\tau \text{ExecrI3}$)

also (rtranclp-trans) from $hA \ I$

have $\text{exec-move-a } P \ t \ (a[i] := e) \ h \ ([v, \text{Intg } I, \text{Addr } A], \text{loc}, \text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i) + \text{length} (\text{compE2 } e), \text{None}) \ \varepsilon$

$h \ ([v, \text{Intg } I, \text{Addr } A], \text{loc}, \text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i) + \text{length} (\text{compE2 } e), \lfloor \text{addr-of-sys-xcpt } \text{ArrayIndexOutOfBounds} \rfloor)$

unfolding exec-move-def by-(rule exec-instr , auto simp add: is-Ref-def)

moreover have $\tau \text{move2} \ (\text{compP2 } P) \ h \ [v, \text{Intg } I, \text{Addr } A] \ (a[i] := e) \ (\text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i) + \text{length} (\text{compE2 } e)) \ \text{None} \implies \text{False}$

by (simp add: $\tau \text{move2-iff}$)

moreover

have $P, a[i] := e, h' \vdash (\text{THROW } \text{ArrayIndexOutOfBounds}, \text{loc}) \leftrightarrow ([v, \text{Intg } I, \text{Addr } A], \text{loc}, \text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i) + \text{length} (\text{compE2 } e), \lfloor \text{addr-of-sys-xcpt } \text{ArrayIndexOutOfBounds} \rfloor)$

by (rule bisim1-bisims1.bisim1AAssFail)

ultimately show *?thesis* using $s \tau$ by auto blast

next

case (Red1AAssStore A U len I v U')

hence [simp]: $a' = \text{addr } A \ e' = \text{THROW } \text{ArrayStore } i = \text{Val } (\text{Intg } I) \ xs' = xs \ ta = \varepsilon \ h' = h \ e = \text{Val } v$

and hA : $\text{typeof-addr } h \ A = \lfloor \text{Array-type } U \ \text{len} \rfloor$ and I : $0 \leq s \ I \ \text{sint } I < \text{int } \text{len}$

and U : $\neg P \vdash U' \leq U \ \text{typeof}_h \ v = \lfloor U' \rfloor$ by auto

have τ : $\neg \tau \text{move1 } P \ h \ (AAss \ (\text{addr } A) \ i \ e)$ by (auto simp add: $\tau \text{move1.simps}$ $\tau \text{moves1.simps}$)

from bisim1 have s : $xcp = \text{None} \ xs = \text{loc}$

and $\tau \text{Exec-mover-a } P \ t \ a \ h \ (stk, \text{loc}, \text{pc}, \text{xcp}) \ (\llbracket \rrbracket @ [\text{Addr } A], \text{loc}, \text{length} (\text{compE2 } a) + 0, \text{None})$

by (auto dest: bisim1Val2D1)

hence $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (stk, \text{loc}, \text{pc}, \text{xcp}) \ (\llbracket \rrbracket @ [\text{Addr } A], \text{loc}, \text{length} (\text{compE2 } a) + 0, \text{None})$

```

    by-(rule AAss-τExecrI1)
  also from bisim2[of loc]
  have τExec-mover-a P t i h ([], loc, 0, None) ([Intg I], loc, length (compE2 i) + 0, None)
    by(auto dest: bisim1Val2D1)
  hence τExec-mover-a P t (a[i] := e) h ([[] @ [Addr A], loc, length (compE2 a) + 0, None) ([Intg I] @ [Addr A], loc, length (compE2 a) + (length (compE2 i) + 0), None)
    by(rule AAss-τExecrI2)
  also (rtranclp-trans) have [Intg I] @ [Addr A] = [] @ [Intg I, Addr A] by simp
  also note add.assoc[symmetric]
  also from bisim3[of loc]
  have τExec-mover-a P t e h ([], loc, 0, None) ([v], loc, length (compE2 e), None)
    by(auto dest: bisim1Val2D1)
  hence τExec-mover-a P t (a[i] := e) h ([[] @ [Intg I, Addr A], loc, length (compE2 a) + length (compE2 i) + 0, None) ([v] @ [Intg I, Addr A], loc, length (compE2 a) + length (compE2 i) + length (compE2 e), None)
    by(rule AAss-τExecrI3)
  also (rtranclp-trans) from hA I U
  have exec-move-a P t (a[i] := e) h ([v, Intg I, Addr A], loc, length (compE2 a) + length (compE2 i) + length (compE2 e), None) ε
    h ([v, Intg I, Addr A], loc, length (compE2 a) + length (compE2 i) + length (compE2 e), [addr-of-sys-xcpt ArrayStore])
  unfolding exec-move-def by-(rule exec-instr, auto simp add: is-Ref-def compP2-def)
  moreover have τmove2 (compP2 P) h [v, Intg I, Addr A] (a[i] := e) (length (compE2 a) + length (compE2 i) + length (compE2 e)) None ⇒ False
    by(simp add: τmove2-iff)
  moreover
  have P, a[i] := e, h' ⊢ (THROW ArrayStore, loc) ↔ ([v, Intg I, Addr A], loc, length (compE2 a) + length (compE2 i) + length (compE2 e), [addr-of-sys-xcpt ArrayStore])
    by(rule bisim1-bisims1.bisim1AAssFail)
  ultimately show ?thesis using s τ by auto blast
next
case (AAss1Throw1 A)
hence [simp]: a' = Throw A ta = ε e' = Throw A h' = h xs' = xs by auto
have τ: τmove1 P h (Throw A[i] := e) by(rule τmove1AAssThrow1)
from bisim1 have xcp = [A] ∨ xcp = None by(auto dest: bisim1-ThrowD)
thus ?thesis
proof
  assume [simp]: xcp = [A]
  with bisim1 have P, a[i] := e, h ⊢ (Throw A, xs) ↔ (stk, loc, pc, xcp)
    by(auto intro: bisim1-bisims1.intros)
  thus ?thesis using τ by(fastforce)
next
assume [simp]: xcp = None
with bisim1 obtain pc' where τExec-mover-a P t a h (stk, loc, pc, None) ([Addr A], loc, pc', [A])
  [A])
  and bisim': P, a, h ⊢ (Throw A, xs) ↔ ([Addr A], loc, pc', [A])
  and [simp]: xs = loc
  by(auto dest: bisim1-Throw-τExec-mover)
hence τExec-mover-a P t (a[i] := e) h (stk, loc, pc, None) ([Addr A], loc, pc', [A])
  by-(rule AAss-τExecrI1)
moreover from bisim'
have P, a[i] := e, h ⊢ (Throw A, xs) ↔ ([Addr A], loc, pc', [A])
  by(auto intro: bisim1-bisims1.bisim1AAssThrow1)
ultimately show ?thesis using τ by auto

```


qed
next
case (*AAss1Throw2 v ad*)
note [*simp*] = $\langle a' = \text{Val } v \rangle \langle i = \text{Throw } ad \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw } ad \rangle \langle h' = h \rangle \langle xs' = xs \rangle$
from *bisim1* **have** *s*: *xcp* = *None* *xs* = *loc*
and $\tau\text{Exec-mover-a } P \ t \ a \ h \ (stk, \ loc, \ pc, \ xcp) \ ([v], \ loc, \ length \ (compE2 \ a), \ None)$
by(*auto dest: bisim1Val2D1*)
hence $\tau\text{Exec-mover-a } P \ t \ (a[\text{Throw } ad] := e) \ h \ (stk, \ loc, \ pc, \ xcp) \ ([v], \ loc, \ length \ (compE2 \ a), \ None)$
by-(*rule AAss- τ ExecrI1*)
also have $\tau\text{Exec-mover-a } P \ t \ (a[\text{Throw } ad] := e) \ h \ ([v], \ loc, \ length \ (compE2 \ a), \ None) \ ([Addr \ ad, \ v], \ loc, \ Suc \ (length \ (compE2 \ a)), \ [ad])$
by(*rule $\tau\text{Execr2step}$*)(*auto simp add: exec-move-def exec-meth-instr τ move2-iff τ move1.simps τ moves1.simps*)
also (*rtranclp-trans*)
have $P, a[\text{Throw } ad] := e, h \vdash \ (\text{Throw } ad, \ loc) \leftrightarrow \ ([Addr \ ad] \ @ \ [v], \ loc, \ (length \ (compE2 \ a) + length \ (compE2 \ (addr \ ad))), \ [ad])$
by(*rule bisim1AAssThrow2[OF bisim1Throw2]*)
moreover have $\tau\text{move1 } P \ h \ (a'[\text{Throw } ad] := e)$ **by**(*auto intro: $\tau\text{move1AAssThrow2}$*)
ultimately show *?thesis* **using** *s* **by** *auto*
next
case (*AAss1Throw3 va vi ad*)
note [*simp*] = $\langle a' = \text{Val } va \rangle \langle i = \text{Val } vi \rangle \langle e = \text{Throw } ad \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw } ad \rangle \langle h' = h \rangle \langle xs' = xs \rangle$
from *bisim1* **have** *s*: *xcp* = *None* *xs* = *loc*
and $\tau\text{Exec-mover-a } P \ t \ a \ h \ (stk, \ loc, \ pc, \ xcp) \ ([va], \ loc, \ length \ (compE2 \ a), \ None)$
by(*auto dest: bisim1Val2D1*)
hence $\tau\text{Exec-mover-a } P \ t \ (a[i] := \text{Throw } ad) \ h \ (stk, \ loc, \ pc, \ xcp) \ ([va], \ loc, \ length \ (compE2 \ a), \ None)$
by-(*rule AAss- τ ExecrI1*)
also from *bisim2[of loc]* **have** $\tau\text{Exec-mover-a } P \ t \ i \ h \ ([], \ loc, \ 0, \ None) \ ([vi], \ loc, \ length \ (compE2 \ i), \ None)$
by(*auto dest: bisim1Val2D1*)
from *AAss- τ ExecrI2*[*OF this, of a e va*]
have $\tau\text{Exec-mover-a } P \ t \ (a[i] := \text{Throw } ad) \ h \ ([va], \ loc, \ length \ (compE2 \ a), \ None) \ ([vi, \ va], \ loc, \ length \ (compE2 \ a) + length \ (compE2 \ i), \ None)$ **by** *simp*
also (*rtranclp-trans*)
have $\tau\text{Exec-mover-a } P \ t \ (a[i] := \text{Throw } ad) \ h \ ([vi, \ va], \ loc, \ length \ (compE2 \ a) + length \ (compE2 \ i), \ None) \ ([Addr \ ad, \ vi, \ va], \ loc, \ Suc \ (length \ (compE2 \ a) + length \ (compE2 \ i)), \ [ad])$
by(*rule $\tau\text{Execr2step}$*)(*auto simp add: exec-move-def exec-meth-instr τ move2-iff τ move1.simps τ moves1.simps*)
also (*rtranclp-trans*)
have $P, a[i] := \text{Throw } ad, h \vdash \ (\text{Throw } ad, \ loc) \leftrightarrow \ ([Addr \ ad] \ @ \ [vi, \ va], \ loc, \ (length \ (compE2 \ a) + length \ (compE2 \ i) + length \ (compE2 \ (addr \ ad))), \ [ad])$
by(*rule bisim1AAssThrow3[OF bisim1Throw2]*)
moreover have $\tau\text{move1 } P \ h \ (AAss \ a' \ (\text{Val } vi) \ (\text{Throw } ad))$ **by**(*auto intro: $\tau\text{move1AAssThrow3}$*)
ultimately show *?thesis* **using** *s* **by** *auto*
qed
next
case (*bisim1AAss2 i n i' xs stk loc pc xcp a e v1*)
note *IH2* = *bisim1AAss2.IH(2)*
note *IH3* = *bisim1AAss2.IH(6)*
note *bisim2* = $\langle P, i, h \vdash \ (i', \ xs) \leftrightarrow \ (stk, \ loc, \ pc, \ xcp) \rangle$
note *bisim1* = $\langle \bigwedge xs. P, a, h \vdash \ (a, \ xs) \leftrightarrow \ ([], \ xs, \ 0, \ None) \rangle$

note $bisim3 = \langle \bigwedge xs. P, e, h \vdash (e, xs) \leftrightarrow ([], xs, \theta, None) \rangle$
note $bsok = \langle bsok (a[i] := e) n \rangle$
from $\langle True, P, t \vdash 1 \langle Val v1[i'] := e, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$ **show** $?case$
proof *cases*
case $(AAss1Red2 E')$
note $[simp] = \langle e' = Val v1[E'] := e \rangle$
and $red = \langle True, P, t \vdash 1 \langle i', (h, xs) \rangle -ta \rightarrow \langle E', (h', xs') \rangle \rangle$
from red **have** $\tau: \tau move1 P h (Val v1[i'] := e) = \tau move1 P h i'$ **by** $(auto simp add: \tau move1.simps \tau moves1.simps)$
from $IH2[OF red] bsok$ **obtain** $pc'' stk'' loc'' xcp''$
where $bisim': P, i, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$
and $exec': ?exec ta i i' E' h stk loc pc xcp h' pc'' stk'' loc'' xcp''$ **by** *auto*
have $?exec ta (a[i] := e) (Val v1[i'] := e) (Val v1[E'] := e) h (stk @ [v1]) loc (length (compE2 a) + pc) xcp h' (length (compE2 a) + pc'') (stk'' @ [v1]) loc'' xcp''$
proof $(cases \tau move1 P h (Val v1[i'] := e))$
case *True*
with $exec' \tau$ **have** $[simp]: h = h'$ **and** $e: sim-move i' E' P t i h (stk, loc, pc, xcp) (stk'', loc'', pc'', xcp'')$ **by** *auto*
from e **have** $sim-move (Val v1[i'] := e) (Val v1[E'] := e) P t (a[i] := e) h (stk @ [v1], loc, length (compE2 a) + pc, xcp) (stk'' @ [v1], loc'', length (compE2 a) + pc'')$
by $(fastforce dest: AAss-\tau ExecrI2 AAss-\tau ExecI2 simp del: compE2.simps compEs2.simps)$
with *True* **show** $?thesis$ **by** *auto*
next
case *False*
with $exec' \tau$ **obtain** $pc' stk' loc' xcp'$
where $e: \tau Exec-mover-a P t i h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$
and $e': exec-move-a P t i h (stk', loc', pc', xcp') (extTA2JVM (compP2 P) ta) h' (stk'', loc'', pc'', xcp'')$
and $\tau': \neg \tau move2 (compP2 P) h stk' i pc' xcp'$
and $call: call1 i' = None \vee no-call2 i pc \vee pc' = pc \wedge stk' = stk \wedge loc' = loc \wedge xcp' = xcp$ **by** *auto*
from e **have** $\tau Exec-mover-a P t (a[i] := e) h (stk @ [v1], loc, length (compE2 a) + pc, xcp) (stk' @ [v1], loc', length (compE2 a) + pc')$ **by** $(rule AAss-\tau ExecrI2)$
moreover from e' **have** $exec-move-a P t (a[i] := e) h (stk' @ [v1], loc', length (compE2 a) + pc', xcp') (extTA2JVM (compP2 P) ta) h' (stk'' @ [v1], loc'', length (compE2 a) + pc'')$
by $(rule exec-move-AAssI2)$
moreover from e' **have** $pc' < length (compE2 i)$ **by** $(auto elim: exec-meth.cases)$
with $\tau' e'$ **have** $\neg \tau move2 (compP2 P) h (stk' @ [v1]) (a[i] := e) (length (compE2 a) + pc')$
by $(auto simp add: \tau instr-stk-drop-exec-move \tau move2-iff)$
moreover from red **have** $call1 (Val v1[i'] := e) = call1 i'$ **by** *auto*
moreover have $no-call2 i pc \implies no-call2 (a[i] := e) (length (compE2 a) + pc)$
by $(auto simp add: no-call2-def)$
ultimately show $?thesis$ **using** *False call* **by** $(auto simp del: split-paired-Ex call1.simps calls1.simps)$
qed
moreover from $bisim'$
have $P, a[i] := e, h' \vdash (Val v1[E'] := e, xs') \leftrightarrow ((stk'' @ [v1]), loc'', length (compE2 a) + pc'', xcp'')$
by $(rule bisim1-bisims1.bisim1AAss2)$
ultimately show $?thesis$
apply $(auto simp del: split-paired-Ex call1.simps calls1.simps split: if-split-asm split del: if-split)$
apply $(blast intro: \tau Exec-mover-trans) +$
done

next
case (*AAss1Red3* $E' v'$)
note [*simp*] = $\langle i' = \text{Val } v' \rangle \langle e' = \text{Val } v1 \lfloor \text{Val } v' \rfloor := E' \rangle$
and $\text{red} = \langle \text{True}, P, t \vdash 1 \langle e, (h, xs) \rangle -ta \rightarrow \langle E', (h', xs') \rangle \rangle$
from red **have** $\tau: \tau \text{move1 } P h (\text{Val } v1 \lfloor \text{Val } v' \rfloor := e) = \tau \text{move1 } P h e$
by(*auto simp add: \(\tau \text{move1.simps} \tau \text{moves1.simps}\)*)
from *bisim2* **have** $s: xcp = \text{None } xs = \text{loc}$
and $\text{exec1}: \tau \text{Exec-mover-a } P t i h (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) ([v'], xs, \text{length} (\text{compE2 } i), \text{None})$
by(*auto dest: bisim1Val2D1*)
hence $\tau \text{Exec-mover-a } P t (a[i] := e) h (\text{stk} @ [v1], \text{loc}, \text{length} (\text{compE2 } a) + \text{pc}, \text{xcp}) ([v'] @ [v1], xs, \text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i), \text{None})$
by-(*rule AAss-\(\tau \text{ExecrI2}\)*)
moreover from *IH3[OF red] bsok* **obtain** $pc'' \text{stk}'' \text{loc}'' \text{xcp}''$
where $\text{bisim}' : P, e, h' \vdash (E', xs') \leftrightarrow (\text{stk}'', \text{loc}'', \text{pc}'', \text{xcp}'')$
and $\text{exec}' : ?\text{exec } ta e e E' h [] xs 0 \text{None } h' \text{pc}'' \text{stk}'' \text{loc}'' \text{xcp}''$ **by** *auto*
have $?\text{exec } ta (a[i] := e) (\text{Val } v1 \lfloor \text{Val } v' \rfloor := e) (\text{Val } v1 \lfloor \text{Val } v' \rfloor := E') h ([] @ [v', v1]) xs (\text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i) + 0) \text{None } h' (\text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i) + \text{pc}'') (\text{stk}'' @ [v', v1]) \text{loc}'' \text{xcp}''$
proof(*cases \(\tau \text{move1 } P h (\text{Val } v1 \lfloor \text{Val } v' \rfloor := e)\)*)
case *True*
with $\text{exec}' \tau$ **have** [*simp*]: $h = h'$
and $e: \text{sim-move } e E' P t e h ([] , xs, 0, \text{None}) (\text{stk}'', \text{loc}'', \text{pc}'', \text{xcp}'')$ **by** *auto*
from e **have** $\text{sim-move} (\text{Val } v1 \lfloor \text{Val } v' \rfloor := e) (\text{Val } v1 \lfloor \text{Val } v' \rfloor := E') P t (a[i] := e) h ([] @ [v', v1], xs, \text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i) + 0, \text{None}) (\text{stk}'' @ [v', v1], \text{loc}'', \text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i) + \text{pc}'', \text{xcp}'')$
by(*fastforce dest: AAss-\(\tau \text{ExecI3} \text{AAss-\(\tau \text{ExecrI3} \text{simp del: compE2.simps compEs2.simps}\)*))
with *True* **show** $?\text{thesis}$ **by** *auto*
next
case *False*
with $\text{exec}' \tau$ **obtain** $pc' \text{stk}' \text{loc}' \text{xcp}'$
where $e: \tau \text{Exec-mover-a } P t e h ([] , xs, 0, \text{None}) (\text{stk}', \text{loc}', \text{pc}', \text{xcp}')$
and $e': \text{exec-move-a } P t e h (\text{stk}', \text{loc}', \text{pc}', \text{xcp}') (\text{extTA2JVM} (\text{compP2 } P) ta) h' (\text{stk}'', \text{loc}'', \text{pc}'', \text{xcp}'')$
and $\tau': \neg \tau \text{move2} (\text{compP2 } P) h \text{stk}' e \text{pc}' \text{xcp}'$
and $\text{call}: \text{call1 } e = \text{None} \vee \text{no-call2 } e 0 \vee \text{pc}' = 0 \wedge \text{stk}' = [] \wedge \text{loc}' = xs \wedge \text{xcp}' = \text{None}$ **by** *auto*
from e **have** $\tau \text{Exec-mover-a } P t (a[i] := e) h ([] @ [v', v1], xs, \text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i) + 0, \text{None}) (\text{stk}' @ [v', v1], \text{loc}', \text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i) + \text{pc}', \text{xcp}')$
by(*rule AAss-\(\tau \text{ExecrI3}\)*)
moreover from e' **have** $\text{exec-move-a } P t (a[i] := e) h (\text{stk}' @ [v', v1], \text{loc}', \text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i) + \text{pc}', \text{xcp}') (\text{extTA2JVM} (\text{compP2 } P) ta) h' (\text{stk}'' @ [v', v1], \text{loc}'', \text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i) + \text{pc}'', \text{xcp}'')$
by(*rule exec-move-AAssI3*)
moreover from $e' \tau'$ **have** $\neg \tau \text{move2} (\text{compP2 } P) h (\text{stk}' @ [v', v1]) (a[i] := e) (\text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i) + \text{pc}') \text{xcp}'$
by(*auto simp add: \(\tau \text{instr-stk-drop-exec-move} \tau \text{move2-iff}\)*)
moreover from red **have** $\text{call1} (\text{Val } v1 \lfloor \text{Val } v' \rfloor := e) = \text{call1 } e$ **by** *auto*
moreover have $\text{no-call2 } e 0 \implies \text{no-call2} (a[i] := e) (\text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i))$
by(*auto simp add: no-call2-def*)
ultimately show $?\text{thesis}$ **using** *False call* **by**(*auto simp del: split-paired-Ex call1.simps calls1.simps*)
blast
qed
moreover from *bisim'*
have $P, a[i] := e, h' \vdash (\text{Val } v1 \lfloor \text{Val } v' \rfloor := E', xs') \leftrightarrow ((\text{stk}'' @ [v', v1]), \text{loc}'', \text{length} (\text{compE2 } a))$

```

+ length (compE2 i) + pc'', xcp'')
  by(rule bisim1-bisims1.bisim1AAss3)
  moreover from bisim2 have pc ≠ length (compE2 i) → no-call2 (a[i] := e) (length (compE2
a) + pc)
  by(auto simp add: no-call2-def dest: bisim-Val-pc-not-Invoke bisim1-pc-length-compE2)
  ultimately show ?thesis using τ exec1 s
  apply(auto simp del: split-paired-Ex call1.simps calls1.simps split: if-split-asm split del: if-split)
  apply(blast intro: τExec-mover-trans|fastforce elim!: τExec-mover-trans simp del: split-paired-Ex
call1.simps calls1.simps)+
  done
next
case (Red1AAss A U len I v U')
hence [simp]: v1 = Addr A e' = unit i' = Val (Intg I)
  ta = {WriteMem A (ACell (nat (sint I))) v} xs' = xs e = Val v
  and hA: typeof-addr h A = [Array-type U len] and I: 0 ≤ s I sint I < int len
  and v: typeofh v = [U'] P ⊢ U' ≤ U
  and h': heap-write h A (ACell (nat (sint I))) v h' by auto
have τ: ¬ τmove1 P h (AAss (addr A) (Val (Intg I)) (Val v)) by(auto simp add: τmove1.simps
τmoves1.simps)
from bisim2 have s: xcp = None xs = loc
  and τExec-mover-a P t i h (stk, loc, pc, xcp) ([Intg I], loc, length (compE2 i), None)
  by(auto dest: bisim1Val2D1)
hence τExec-mover-a P t (a[i] := e) h (stk @ [Addr A], loc, length (compE2 a) + pc, xcp) ([Intg
I] @ [Addr A], loc, length (compE2 a) + length (compE2 i), None)
  by-(rule AAss-τExecrI2)
hence τExec-mover-a P t (a[i] := e) h (stk @ [Addr A], loc, length (compE2 a) + pc, xcp) ([Intg
I], Addr A], loc, length (compE2 a) + length (compE2 i) + 0, None) by simp
  also from bisim3[of loc] have τExec-mover-a P t e h ([], loc, 0, None) ([v], loc, length (compE2
e), None)
  by(auto dest: bisim1Val2D1)
hence τExec-mover-a P t (a[i] := e) h ([Intg I], Addr A], loc, length (compE2 a) + length
(compE2 i) + 0, None) ([v] @ [Intg I], Addr A], loc, length (compE2 a) + length (compE2 i) + length
(compE2 e), None)
  by(rule AAss-τExecrI3)
  also (rtranclp-trans) from hA I v h'
  have exec-move-a P t (a[i] := e) h ([v], Intg I, Addr A], loc, length (compE2 a) + length (compE2
i) + length (compE2 e), None)
    {WriteMem A (ACell (nat (sint I))) v}
    h' ([], loc, Suc (length (compE2 a) + length (compE2 i) + length (compE2
e)), None)
  unfolding exec-move-def by-(rule exec-instr, auto simp add: compP2-def is-Ref-def)
  moreover have τmove2 (compP2 P) h [v], Intg I, Addr A] (a[i] := e) (length (compE2 a) +
length (compE2 i) + length (compE2 e)) None ⇒ False
  by(simp add: τmove2-iff)
  moreover
  have P, a[i] := e, h' ⊢ (unit, loc) ↔ ([], loc, Suc (length (compE2 a) + length (compE2 i) +
length (compE2 e)), None)
  by(rule bisim1-bisims1.bisim1AAss4)
  ultimately show ?thesis using s τ by(auto simp add: ta-upd-simps) blast
next
case (Red1AAssNull v v')
note [simp] = ⟨v1 = Null⟩ ⟨e' = THROW NullPointer⟩ ⟨i' = Val v⟩ ⟨xs' = xs⟩ ⟨ta = ε⟩ ⟨h' = h⟩
⟨e = Val v'⟩
have τ: ¬ τmove1 P h (AAss null (Val v) (Val v')) by(auto simp add: τmove1.simps τmoves1.simps)

```

from *bisim2* **have** $s: xcp = \text{None}$ $xs = \text{loc}$
and $\tau\text{Exec-mover-a } P \ t \ i \ h \ (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \ ([v], \text{loc}, \text{length}(\text{compE2 } i), \text{None})$
by(*auto dest: bisim1Val2D1*)
hence $\tau\text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (\text{stk} \ @ \ [\text{Null}], \text{loc}, \text{length}(\text{compE2 } a) + \text{pc}, \text{xcp}) \ ([v] \ @ \ [\text{Null}], \text{loc}, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i), \text{None})$
by–(*rule AAss- τ ExecrI2*)
hence $\tau\text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (\text{stk} \ @ \ [\text{Null}], \text{loc}, \text{length}(\text{compE2 } a) + \text{pc}, \text{xcp}) \ ([\] \ @ \ [v, \text{Null}], \text{loc}, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + 0, \text{None})$ **by** *simp*
also from *bisim3[of loc]* **have** $\tau\text{Exec-mover-a } P \ t \ e \ h \ ([\], \text{loc}, 0, \text{None}) \ ([v'], \text{loc}, \text{length}(\text{compE2 } e), \text{None})$
by(*auto dest: bisim1Val2D1*)
hence $\tau\text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ ([\] \ @ \ [v, \text{Null}], \text{loc}, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + 0, \text{None}) \ ([v'] \ @ \ [v, \text{Null}], \text{loc}, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + \text{length}(\text{compE2 } e), \text{None})$
by(*rule AAss- τ ExecrI3*)
also (*rtranclp-trans*)
have $\text{exec-move-a } P \ t \ (a[i] := e) \ h \ ([v', v, \text{Null}], \text{loc}, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + \text{length}(\text{compE2 } e), \text{None}) \ \varepsilon$
 $h \ ([v', v, \text{Null}], \text{loc}, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + \text{length}(\text{compE2 } e), \text{[addr-of-sys-xcpt NullPointer]})$
unfolding *exec-move-def* **by**–(*rule exec-instr, auto simp add: is-Ref-def*)
moreover have $\tau\text{move2}(\text{compP2 } P) \ h \ [v', v, \text{Null}] \ (a[i] := e) \ (\text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + \text{length}(\text{compE2 } e)) \ \text{None} \implies \text{False}$
by(*simp add: τ move2-iff*)
moreover
have $P, a[i] := e, h' \vdash (\text{THROW NullPointer}, \text{loc}) \leftrightarrow ([v', v, \text{Null}], \text{loc}, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + \text{length}(\text{compE2 } e), \text{[addr-of-sys-xcpt NullPointer]})$
by(*rule bisim1-bisims1.bisim1AAssFail*)
ultimately show *?thesis* **using** $s \ \tau$ **by** *auto blast*
next
case (*Red1AAssBounds A U len I v*)
hence [*simp*]: $v1 = \text{Addr } A \ e' = \text{THROW ArrayIndexOutOfBounds } i' = \text{Val}(\text{Intg } I) \ xs' = xs \ ta = \varepsilon \ h' = h \ e = \text{Val } v$
and $hA: \text{typeof-addr } h \ A = \text{[Array-type } U \ \text{len}]$ **and** $I: I < s \ 0 \vee \text{int } \text{len} \leq \text{sint } I$ **by** *auto*
have $\tau: \neg \tau\text{move1 } P \ h \ (\text{addr } A[i'] := e)$ **by**(*auto simp add: τ move1.simps τ moves1.simps*)
from *bisim2* **have** $s: xcp = \text{None}$ $xs = \text{loc}$
and $\tau\text{Exec-mover-a } P \ t \ i \ h \ (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \ ([\text{Intg } I], \text{loc}, \text{length}(\text{compE2 } i), \text{None})$
by(*auto dest: bisim1Val2D1*)
hence $\tau\text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (\text{stk} \ @ \ [\text{Addr } A], \text{loc}, \text{length}(\text{compE2 } a) + \text{pc}, \text{xcp}) \ ([\text{Intg } I] \ @ \ [\text{Addr } A], \text{loc}, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i), \text{None})$
by–(*rule AAss- τ ExecrI2*)
hence $\tau\text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (\text{stk} \ @ \ [\text{Addr } A], \text{loc}, \text{length}(\text{compE2 } a) + \text{pc}, \text{xcp}) \ ([\] \ @ \ [\text{Intg } I, \text{Addr } A], \text{loc}, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + 0, \text{None})$ **by** *simp*
also from *bisim3[of loc]* **have** $\tau\text{Exec-mover-a } P \ t \ e \ h \ ([\], \text{loc}, 0, \text{None}) \ ([v], \text{loc}, \text{length}(\text{compE2 } e), \text{None})$
by(*auto dest: bisim1Val2D1*)
hence $\tau\text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ ([\] \ @ \ [\text{Intg } I, \text{Addr } A], \text{loc}, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + 0, \text{None}) \ ([v] \ @ \ [\text{Intg } I, \text{Addr } A], \text{loc}, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + \text{length}(\text{compE2 } e), \text{None})$
by(*rule AAss- τ ExecrI3*)
also (*rtranclp-trans*) **from** $hA \ I$
have $\text{exec-move-a } P \ t \ (a[i] := e) \ h \ ([v, \text{Intg } I, \text{Addr } A], \text{loc}, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + \text{length}(\text{compE2 } e), \text{None}) \ \varepsilon$
 $h \ ([v, \text{Intg } I, \text{Addr } A], \text{loc}, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + \text{length}(\text{compE2 } e), \text{[addr-of-sys-xcpt NullPointer]})$

$(\text{compE2 } e), \lfloor \text{addr-of-sys-xcpt ArrayIndexOutOfBounds} \rfloor$
unfolding *exec-move-def* **by**–(rule *exec-instr*, auto simp add: *is-Ref-def*)
moreover have $\tau \text{move2 } (\text{compP2 } P) h [v, \text{Intg } I, \text{Addr } A] (a[i] := e) (\text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + \text{length } (\text{compE2 } e)) \text{None} \implies \text{False}$
by(simp add: $\tau \text{move2-iff}$)
moreover
have $P, a[i] := e, h' \vdash (\text{THROW ArrayIndexOutOfBounds}, \text{loc}) \leftrightarrow ([v, \text{Intg } I, \text{Addr } A], \text{loc}, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + \text{length } (\text{compE2 } e), \lfloor \text{addr-of-sys-xcpt ArrayIndexOutOfBounds} \rfloor)$
by(rule *bisim1-bisims1.bisim1AAssFail*)
ultimately show *?thesis* **using** $s \tau$ **by** auto blast
next
case (*Red1AAssStore A U len I v U'*)
hence [*simp*]: $v1 = \text{Addr } A \ e' = \text{THROW ArrayStore } i' = \text{Val } (\text{Intg } I) \ xs' = xs \ ta = \varepsilon \ h' = h \ e = \text{Val } v$
and $hA: \text{typeof-addr } h \ A = \lfloor \text{Array-type } U \ \text{len} \rfloor$ **and** $I: 0 \leq s \ I \ \text{sint } I < \text{int } \text{len}$
and $U: \neg P \vdash U' \leq U \ \text{typeof}_h \ v = \lfloor U' \rfloor$ **by** auto
have $\tau: \neg \tau \text{move1 } P \ h \ (\text{addr } A[i'] := e)$ **by**(auto simp add: $\tau \text{move1.simps } \tau \text{moves1.simps}$)
from *bisim2* **have** $s: \text{xcp} = \text{None} \ xs = \text{loc}$
and $\tau \text{Exec-mover-a } P \ t \ i \ h \ (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) ([\text{Intg } I], \text{loc}, \text{length } (\text{compE2 } i), \text{None})$
by(auto dest: *bisim1Val2D1*)
hence $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (\text{stk} @ [\text{Addr } A], \text{loc}, \text{length } (\text{compE2 } a) + \text{pc}, \text{xcp}) ([\text{Intg } I] @ [\text{Addr } A], \text{loc}, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i), \text{None})$
by–(rule *AAss- τ ExecrI2*)
hence $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (\text{stk} @ [\text{Addr } A], \text{loc}, \text{length } (\text{compE2 } a) + \text{pc}, \text{xcp}) ([\text{Intg } I, \text{Addr } A], \text{loc}, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + 0, \text{None})$ **by** simp
also from *bisim3[of loc]*
have $\tau \text{Exec-mover-a } P \ t \ e \ h \ ([], \text{loc}, 0, \text{None}) ([v], \text{loc}, \text{length } (\text{compE2 } e), \text{None})$
by(auto dest: *bisim1Val2D1*)
hence $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ ([] @ [\text{Intg } I, \text{Addr } A], \text{loc}, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + 0, \text{None}) ([v] @ [\text{Intg } I, \text{Addr } A], \text{loc}, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + \text{length } (\text{compE2 } e), \text{None})$
by(rule *AAss- τ ExecrI3*)
also (*rtranclp-trans*) **from** $hA \ I \ U$
have $\text{exec-move-a } P \ t \ (a[i] := e) \ h \ ([v, \text{Intg } I, \text{Addr } A], \text{loc}, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + \text{length } (\text{compE2 } e), \text{None}) \ \varepsilon$
 $h \ ([v, \text{Intg } I, \text{Addr } A], \text{loc}, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + \text{length } (\text{compE2 } e), \lfloor \text{addr-of-sys-xcpt ArrayStore} \rfloor)$
unfolding *exec-move-def* **by**–(rule *exec-instr*, auto simp add: *is-Ref-def compP2-def*)
moreover have $\tau \text{move2 } (\text{compP2 } P) h [v, \text{Intg } I, \text{Addr } A] (a[i] := e) (\text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + \text{length } (\text{compE2 } e)) \text{None} \implies \text{False}$
by(simp add: $\tau \text{move2-iff}$)
moreover
have $P, a[i] := e, h' \vdash (\text{THROW ArrayStore}, \text{loc}) \leftrightarrow ([v, \text{Intg } I, \text{Addr } A], \text{loc}, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + \text{length } (\text{compE2 } e), \lfloor \text{addr-of-sys-xcpt ArrayStore} \rfloor)$
by(rule *bisim1-bisims1.bisim1AAssFail*)
ultimately show *?thesis* **using** $s \tau$ **by** auto fast
next
case (*AAss1Throw2 A*)
note [*simp*] = $\langle i' = \text{Throw } A \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw } A \rangle \langle h' = h \rangle \langle xs' = xs \rangle$
have $\tau: \tau \text{move1 } P \ h \ (\text{Val } v1 \lfloor \text{Throw } A \rfloor := e)$ **by**(rule *$\tau \text{move1AAssThrow2}$*)
from *bisim2* **have** $\text{xcp} = \lfloor A \rfloor \vee \text{xcp} = \text{None}$ **by**(auto dest: *bisim1-ThrowD*)
thus *?thesis*
proof
assume [*simp*]: $\text{xcp} = \lfloor A \rfloor$

```

with bisim2
have  $P, a[i] := e, h \vdash (\text{Throw } A, xs) \leftrightarrow (\text{stk} @ [v1], \text{loc}, \text{length}(\text{compE2 } a) + \text{pc}, \text{xcp})$ 
  by(auto intro: bisim1-bisims1.intros)
thus ?thesis using  $\tau$  by(fastforce)
next
assume [simp]:  $\text{xcp} = \text{None}$ 
with bisim2 obtain pc' where  $\tau \text{Exec-mover-a } P \ t \ i \ h \ (\text{stk}, \text{loc}, \text{pc}, \text{None}) \ ([\text{Addr } A], \text{loc}, \text{pc}', [A], [A])$ 
  and bisim':  $P, i, h \vdash (\text{Throw } A, xs) \leftrightarrow ([\text{Addr } A], \text{loc}, \text{pc}', [A])$ 
  and [simp]:  $xs = \text{loc}$ 
  by(auto dest: bisim1-Throw- $\tau$ Exec-mover)
hence  $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (\text{stk} @ [v1], \text{loc}, \text{length}(\text{compE2 } a) + \text{pc}, \text{None}) \ ([\text{Addr } A] @ [v1], \text{loc}, \text{length}(\text{compE2 } a) + \text{pc}', [A])$ 
  by-(rule AAss- $\tau$ ExecrI2)
moreover from bisim'
have  $P, a[i] := e, h \vdash (\text{Throw } A, xs) \leftrightarrow ([\text{Addr } A] @ [v1], \text{loc}, \text{length}(\text{compE2 } a) + \text{pc}', [A])$ 
  by(rule bisim1-bisims1.bisim1AAssThrow2)
ultimately show ?thesis using  $\tau$  by auto
qed
next
case (AAss1Throw3 vi ad)
note [simp] =  $\langle i' = \text{Val } vi \rangle \langle e = \text{Throw } ad \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw } ad \rangle \langle h' = h \rangle \langle xs' = xs \rangle$ 
from bisim2 have s:  $\text{xcp} = \text{None} \ xs = \text{loc}$ 
  and  $\tau \text{Exec-mover-a } P \ t \ i \ h \ (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \ ([vi], \text{loc}, \text{length}(\text{compE2 } i), \text{None})$ 
  by(auto dest: bisim1Val2D1)
hence  $\tau \text{Exec-mover-a } P \ t \ (a[i] := \text{Throw } ad) \ h \ (\text{stk} @ [v1], \text{loc}, \text{length}(\text{compE2 } a) + \text{pc}, \text{xcp}) \ ([vi] @ [v1], \text{loc}, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i), \text{None})$ 
  by-(rule AAss- $\tau$ ExecrI2)
  also have  $\tau \text{Exec-mover-a } P \ t \ (a[i] := \text{Throw } ad) \ h \ ([vi] @ [v1], \text{loc}, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i), \text{None}) \ ([\text{Addr } ad, vi, v1], \text{loc}, \text{Suc}(\text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i)), [ad])$ 
  by(rule  $\tau$ Execr2step)(auto simp add: exec-move-def exec-meth-instr  $\tau$ move2-iff  $\tau$ move1.simps  $\tau$ moves1.simps)
  also (rtranclp-trans)
  have  $P, a[i] := \text{Throw } ad, h \vdash (\text{Throw } ad, \text{loc}) \leftrightarrow ([\text{Addr } ad] @ [vi, v1], \text{loc}, (\text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + \text{length}(\text{compE2 } (\text{addr } ad))), [ad])$ 
  by(rule bisim1AAssThrow3[OF bisim1Throw2])
  moreover have  $\tau \text{move1 } P \ h \ (\text{AAss } (\text{Val } v1) \ (\text{Val } vi) \ (\text{Throw } ad))$  by(auto intro:  $\tau$ move1AAssThrow3)
  ultimately show ?thesis using s by auto
qed auto
next
case (bisim1AAss3 e n ee xs stk loc pc xcp a i v v')
note IH3 = bisim1AAss3.IH(2)
note bisim3 =  $\langle P, e, h \vdash (ee, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \rangle$ 
note bsok =  $\langle \text{bsok } (a[i] := e) \ n \rangle$ 
from  $\langle \text{True}, P, t \vdash 1 \ \langle \text{Val } v \ [ \text{Val } v' ] := ee, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$  show ?case
proof cases
case (AAss1Red3 E')
note [simp] =  $\langle e' = \text{Val } v \ [ \text{Val } v' ] := E' \rangle$ 
and red =  $\langle \text{True}, P, t \vdash 1 \ \langle ee, (h, xs) \rangle -ta \rightarrow \langle E', (h', xs') \rangle \rangle$ 
from red have  $\tau: \tau \text{move1 } P \ h \ (\text{Val } v \ [ \text{Val } v' ] := ee) = \tau \text{move1 } P \ h \ ee$  by(auto simp add:  $\tau$ move1.simps  $\tau$ moves1.simps)
from IH3[OF red] bsok obtain pc'' stk'' loc'' xcp''
  where bisim':  $P, e, h' \vdash (E', xs') \leftrightarrow (\text{stk}'', \text{loc}'', \text{pc}'', \text{xcp}'')$ 
  and exec': ?exec ta e ee E' h stk loc pc xcp h' pc'' stk'' loc'' xcp'' by auto

```

have *no-call2* e $pc \implies$ *no-call2* ($a[i] := e$) ($\text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + pc$)
by(*auto simp add: no-call2-def*)
hence $?exec\ ta$ ($a[i] := e$) ($\text{Val } v \lfloor \text{Val } v^\frown := ee$) ($\text{Val } v \lfloor \text{Val } v^\frown := E'$) h ($\text{stk} @ [v', v]$) loc ($\text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + pc$) xcp h' ($\text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + pc'$) ($\text{stk}'' @ [v', v]$) loc'' xcp''
using *exec' τ*
apply(*cases τ move1 P h ($\text{Val } v \lfloor \text{Val } v^\frown := ee$)*)
apply(*auto*)
apply(*blast intro: AAss- τ ExecrI3 AAss- τ ExecI3 exec-move-AAssI3*)
apply(*blast intro: AAss- τ ExecrI3 AAss- τ ExecI3 exec-move-AAssI3*)
apply(*rule exI conjI AAss- τ ExecrI3 exec-move-AAssI3|assumption*) +
apply(*fastforce simp add: τ instr-stk-drop-exec-move τ move2-iff split: if-split-asm*)
apply(*rule exI conjI AAss- τ ExecrI3 exec-move-AAssI3|assumption*) +
apply(*fastforce simp add: τ instr-stk-drop-exec-move τ move2-iff split: if-split-asm*)
apply(*rule exI conjI AAss- τ ExecrI3 exec-move-AAssI3 rtranclp.rtrancl-refl|assumption*) +
apply(*fastforce simp add: τ instr-stk-drop-exec-move τ move2-iff split: if-split-asm*) +
done
moreover from *bisim'*
have $P, a[i] := e, h' \vdash (\text{Val } v \lfloor \text{Val } v^\frown := E', xs^\frown) \leftrightarrow (\text{stk}'' @ [v', v], loc'', \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + pc'', xcp'')$
by(*rule bisim1-bisims1.bisim1AAss3*)
ultimately show $?thesis$ **using** τ **by** *auto blast+*
next
case (*Red1AAss A U len I V U'*)
hence [*simp*]: $v = \text{Addr } A$ $e' = \text{unit } v' = \text{Intg } I$ $xs' = xs$ $ee = \text{Val } V$
 $ta = \{\text{WriteMem } A (\text{ACell } (\text{nat } (\text{sint } I))) V\}$
and hA : *typeof-addr* h $A = \lfloor \text{Array-type } U \text{ len} \rfloor$ **and** I : $0 \leq s$ I *sint* $I < \text{int len}$
and v : *typeof_h* $V = \lfloor U' \rfloor$ $P \vdash U' \leq U$
and h' : *heap-write* h A ($\text{ACell } (\text{nat } (\text{sint } I)))$ V h' **by** *auto*
have τ : $\neg \tau$ move1 P h ($\text{AAss } (\text{addr } A)$) ($\text{Val } (\text{Intg } I)$) ($\text{Val } V$) **by**(*auto simp add: τ move1.simps τ moves1.simps*)
from *bisim3* **have** s : $xcp = \text{None}$ $xs = loc$
and *exec1*: τ Exec-mover-a P t e h (stk, loc, pc, xcp) ($\lfloor V \rfloor, loc, \text{length}(\text{compE2 } e), \text{None}$)
by(*auto dest: bisim1Val2D1*)
hence τ Exec-mover-a P t ($a[i] := e$) h ($\text{stk} @ [\text{Intg } I, \text{Addr } A], loc, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + pc, xcp$) ($\lfloor V \rfloor @ [\text{Intg } I, \text{Addr } A], loc, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + \text{length}(\text{compE2 } e), \text{None}$)
by-(*rule AAss- τ ExecrI3*)
moreover from hA I v h'
have *exec-move-a* P t ($a[i] := e$) h ($\lfloor V, \text{Intg } I, \text{Addr } A \rfloor, loc, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + \text{length}(\text{compE2 } e), \text{None}$)
 $\{\text{WriteMem } A (\text{ACell } (\text{nat } (\text{sint } I))) V\}$
 $h' (\lfloor \rfloor, loc, \text{Suc } (\text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + \text{length}(\text{compE2 } e)), \text{None})$
unfolding *exec-move-def* **by**-(*rule exec-instr, auto simp add: compP2-def is-Ref-def*)
moreover have τ move2 ($\text{compP2 } P$) h ($\lfloor V, \text{Intg } I, \text{Addr } A \rfloor$) ($a[i] := e$) ($\text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + \text{length}(\text{compE2 } e)$) $\text{None} \implies \text{False}$
by(*simp add: τ move2-iff*)
moreover
have $P, a[i] := e, h' \vdash (\text{unit}, loc) \leftrightarrow (\lfloor \rfloor, loc, \text{Suc } (\text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + \text{length}(\text{compE2 } e)), \text{None})$
by(*rule bisim1-bisims1.bisim1AAss4*)
ultimately show $?thesis$ **using** s τ **by**(*auto simp add: ta-upd-simps*) *blast*
next


```

case (Red1AAssNull V')
note [simp] = ⟨v = Null⟩ ⟨e' = THROW NullPointer⟩ ⟨xs' = xs⟩ ⟨ta = ε⟩ ⟨h' = h⟩ ⟨ee = Val V'⟩
have τ: ¬ τ move1 P h (AAss null (Val v') (Val V')) by(auto simp add: τ move1.simps τ moves1.simps)
from bisim3 have s: xcp = None xs = loc
  and τExec-mover-a P t e h (stk, loc, pc, xcp) ([V'], loc, length (compE2 e), None)
  by(auto dest: bisim1Val2D1)
hence τExec-mover-a P t (a[i] := e) h (stk @ [v', Null], loc, length (compE2 a) + length (compE2 i) + pc, xcp) ([V'] @ [v', Null], loc, length (compE2 a) + length (compE2 i) + length (compE2 e), None)
  by-(rule AAss-τExecrI3)
moreover
have exec-move-a P t (a[i] := e) h ([V', v', Null], loc, length (compE2 a) + length (compE2 i) + length (compE2 e), None) ε
  h ([V', v', Null], loc, length (compE2 a) + length (compE2 i) + length (compE2 e), [addr-of-sys-xcpt NullPointer])
  unfolding exec-move-def by-(rule exec-instr, auto simp add: is-Ref-def)
moreover have τmove2 (compP2 P) h [V', v', Null] (a[i] := e) (length (compE2 a) + length (compE2 i) + length (compE2 e)) None ⇒ False
  by(simp add: τmove2-iff)
moreover
have P, a[i] := e, h' ⊢ (THROW NullPointer, loc) ↔ ([V', v', Null], loc, length (compE2 a) + length (compE2 i) + length (compE2 e), [addr-of-sys-xcpt NullPointer])
  by(rule bisim1-bisims1.bisim1AAssFail)
ultimately show ?thesis using s τ by auto blast
next
case (Red1AAssBounds A U len I V)
hence [simp]: v = Addr A e' = THROW ArrayIndexOutOfBounds v' = Intg I xs' = xs ta = ε h' = h ee = Val V
  and hA: typeof-addr h A = [Array-type U len] and I: I < s 0 ∨ int len ≤ sint I by auto
have τ: ¬ τ move1 P h (addr A [Val (Intg I)] := ee) by(auto simp add: τ move1.simps τ moves1.simps)
from bisim3 have s: xcp = None xs = loc
  and τExec-mover-a P t e h (stk, loc, pc, xcp) ([V], loc, length (compE2 e), None)
  by(auto dest: bisim1Val2D1)
hence τExec-mover-a P t (a[i] := e) h (stk @ [Intg I, Addr A], loc, length (compE2 a) + length (compE2 i) + pc, xcp) ([V] @ [Intg I, Addr A], loc, length (compE2 a) + length (compE2 i) + length (compE2 e), None)
  by-(rule AAss-τExecrI3)
moreover from hA I
have exec-move-a P t (a[i] := e) h ([V, Intg I, Addr A], loc, length (compE2 a) + length (compE2 i) + length (compE2 e), None) ε
  h ([V, Intg I, Addr A], loc, length (compE2 a) + length (compE2 i) + length (compE2 e), [addr-of-sys-xcpt ArrayIndexOutOfBounds])
  unfolding exec-move-def by-(rule exec-instr, auto simp add: is-Ref-def)
moreover have τmove2 (compP2 P) h [V, Intg I, Addr A] (a[i] := e) (length (compE2 a) + length (compE2 i) + length (compE2 e)) None ⇒ False
  by(simp add: τmove2-iff)
moreover
have P, a[i] := e, h' ⊢ (THROW ArrayIndexOutOfBounds, loc) ↔ ([V, Intg I, Addr A], loc, length (compE2 a) + length (compE2 i) + length (compE2 e), [addr-of-sys-xcpt ArrayIndexOutOfBounds])
  by(rule bisim1-bisims1.bisim1AAssFail)
ultimately show ?thesis using s τ by auto blast
next
case (Red1AAssStore A U len I V U')
hence [simp]: v = Addr A e' = THROW ArrayStore v' = Intg I xs' = xs ta = ε h' = h ee = Val V

```

and hA : $\text{typeof-addr } h \ A = \lfloor \text{Array-type } U \ \text{len} \rfloor$ **and** I : $0 \leq I \ \text{int } I < \text{int } \text{len}$
and U : $\neg P \vdash U' \leq U \ \text{typeof}_h \ V = \lfloor U' \rfloor$ **by** *auto*
have τ : $\neg \tau \text{move1 } P \ h \ (\text{addr } A \lfloor \text{Val } (\text{Intg } I) \rfloor := ee)$ **by** (*auto simp add: $\tau \text{move1}.$ *simps* $\tau \text{moves1}.$ *simps*)
from *bisim3* **have** s : $xcp = \text{None } xs = \text{loc}$
and $\tau \text{Exec-mover-a } P \ t \ e \ h \ (\text{stk}, \text{loc}, \text{pc}, xcp)$ ($\lfloor V \rfloor, \text{loc}, \text{length } (\text{compE2 } e), \text{None}$)
by (*auto dest: bisim1Val2D1*)
hence $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (\text{stk} \ @ \ \lfloor \text{Intg } I, \text{Addr } A \rfloor, \text{loc}, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + \text{pc}, xcp)$ ($\lfloor V \rfloor \ @ \ \lfloor \text{Intg } I, \text{Addr } A \rfloor, \text{loc}, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + \text{length } (\text{compE2 } e), \text{None}$)
by -- (*rule AAss- $\tau \text{ExecrI3}$*)
moreover from $hA \ I \ U$
have $\text{exec-move-a } P \ t \ (a[i] := e) \ h \ (\lfloor V, \text{Intg } I, \text{Addr } A \rfloor, \text{loc}, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + \text{length } (\text{compE2 } e), \text{None}) \ \varepsilon$
 $h \ (\lfloor V, \text{Intg } I, \text{Addr } A \rfloor, \text{loc}, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + \text{length } (\text{compE2 } e), \lfloor \text{addr-of-sys-xcpt } \text{ArrayStore} \rfloor)$
unfolding *exec-move-def* **by** -- (*rule exec-instr, auto simp add: is-Ref-def compP2-def*)
moreover have $\tau \text{move2 } (\text{compP2 } P) \ h \ \lfloor V, \text{Intg } I, \text{Addr } A \rfloor \ (a[i] := e) \ (\text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + \text{length } (\text{compE2 } e)) \ \text{None} \implies \text{False}$
by (*simp add: $\tau \text{move2-iff}$*)
moreover
have $P, a[i] := e, h' \vdash (\text{THROW } \text{ArrayStore}, \text{loc}) \leftrightarrow (\lfloor V, \text{Intg } I, \text{Addr } A \rfloor, \text{loc}, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + \text{length } (\text{compE2 } e), \lfloor \text{addr-of-sys-xcpt } \text{ArrayStore} \rfloor)$
by (*rule bisim1-bisims1.bisim1AAssFail*)
ultimately show *?thesis* **using** $s \ \tau$ **by** *auto blast*
next
case (*AAss1Throw3 A*)
note $\lfloor \text{simp} \rfloor = \langle ee = \text{Throw } A \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw } A \rangle \langle h' = h \rangle \langle xs' = xs \rangle$
have τ : $\tau \text{move1 } P \ h \ (AAss \ (\text{Val } v) \ (\text{Val } v') \ (\text{Throw } A))$ **by** (*rule $\tau \text{move1AAssThrow3}$*)
from *bisim3* **have** $xcp = \lfloor A \rfloor \vee xcp = \text{None}$ **by** (*auto dest: bisim1-ThrowD*)
thus *?thesis*
proof
assume $\lfloor \text{simp} \rfloor$: $xcp = \lfloor A \rfloor$
with *bisim3*
have $P, a[i] := e, h \vdash (\text{Throw } A, xs) \leftrightarrow (\text{stk} \ @ \ \lfloor v', v \rfloor, \text{loc}, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + \text{pc}, xcp)$
by (*auto intro: bisim1-bisims1.intros*)
thus *?thesis* **using** τ **by** (*fastforce*)
next
assume $\lfloor \text{simp} \rfloor$: $xcp = \text{None}$
with *bisim3* **obtain** pc' **where** $\tau \text{Exec-mover-a } P \ t \ e \ h \ (\text{stk}, \text{loc}, \text{pc}, \text{None})$ ($\lfloor \text{Addr } A \rfloor, \text{loc}, pc', \lfloor A \rfloor$)
and bisim' : $P, e, h \vdash (\text{Throw } A, xs) \leftrightarrow (\lfloor \text{Addr } A \rfloor, \text{loc}, pc', \lfloor A \rfloor)$
and $\lfloor \text{simp} \rfloor$: $xs = \text{loc}$
by (*auto dest: bisim1-Throw- $\tau \text{Exec-mover}$*)
hence $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (\text{stk} \ @ \ \lfloor v', v \rfloor, \text{loc}, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + \text{pc}, \text{None})$ ($\lfloor \text{Addr } A \rfloor \ @ \ \lfloor v', v \rfloor, \text{loc}, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + pc', \lfloor A \rfloor$)
by -- (*rule AAss- $\tau \text{ExecrI3}$*)
moreover from bisim'
have $P, a[i] := e, h \vdash (\text{Throw } A, xs) \leftrightarrow (\lfloor \text{Addr } A \rfloor \ @ \ \lfloor v', v \rfloor, \text{loc}, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + pc', \lfloor A \rfloor)$
by (*rule bisim1-bisims1.bisim1AAssThrow3*)
ultimately show *?thesis* **using** τ **by** *auto*
qed
qed *auto**

```

next
  case bisim1AAssThrow1 thus ?case by auto
next
  case bisim1AAssThrow2 thus ?case by auto
next
  case bisim1AAssThrow3 thus ?case by auto
next
  case bisim1AAssFail thus ?case by auto
next
  case bisim1AAss4 thus ?case by auto
next
  case (bisim1ALength a n a' xs stk loc pc xcp)
  note IH = bisim1ALength.IH(2)
  note bisim =  $\langle P, a, h \vdash (a', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
  note red =  $\langle True, P, t \vdash 1 \langle a'.length, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$ 
  note bsok =  $\langle bsok (a.length) n \rangle$ 
  from red show ?case
  proof cases
    case (ALength1Red ee')
    note [simp] =  $\langle e' = ee'.length \rangle$ 
    and red =  $\langle True, P, t \vdash 1 \langle a', (h, xs) \rangle -ta \rightarrow \langle ee', (h', xs') \rangle \rangle$ 
    from red have  $\tau move1 P h (a'.length) = \tau move1 P h a'$  by (auto simp add:  $\tau move1.simps$ 
 $\tau moves1.simps$ )
    moreover have call1 (a'.length) = call1 a' by auto
    moreover from IH[OF red] bsok
    obtain pc'' stk'' loc'' xcp'' where bisim:  $P, a, h' \vdash (ee', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$ 
    and redo: ?exec ta a a' ee' h stk loc pc xcp h' pc'' stk'' loc'' xcp'' by auto
    from bisim have  $P, a.length, h' \vdash (ee'.length, xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$ 
    by (rule bisim1-bisims1.bisim1ALength)
    moreover {
      assume no-call2 a pc
      hence no-call2 (a.length) pc by (auto simp add: no-call2-def) }
    ultimately show ?thesis using redo
    by (auto simp del: call1.simps calls1.simps split: if-split-asm split del: if-split) (blast intro:
ALength- $\tau$ ExecrI ALength- $\tau$ ExecI exec-move-ALengthI) +
  next
  case (Red1ALength A U len)
  hence [simp]: a' = addr A ta =  $\varepsilon e' = Val (Intg (word-of-int (int len)))$ 
    h' = h xs' = xs
    and hA: typeof-addr h A = [Array-type U len] by auto
  from bisim have s: xcp = None xs = loc by (auto dest: bisim-Val-loc-eq-xcp-None)
  from bisim have  $\tau Exec-mover-a P t a h (stk, loc, pc, xcp) ([Addr A], loc, length (compE2 a),$ 
None)
    by (auto dest: bisim1Val2D1)
  hence  $\tau Exec-mover-a P t (a.length) h (stk, loc, pc, xcp) ([Addr A], loc, length (compE2 a), None)$ 
    by (rule ALength- $\tau$ ExecrI)
  moreover from hA
  have exec-move-a P t (a.length) h ([Addr A], loc, length (compE2 a), None)  $\varepsilon h'$  ([Intg (word-of-int
(int len))], loc, Suc (length (compE2 a)), None)
    by (auto intro!: exec-instr simp add: is-Ref-def exec-move-def)
  moreover have  $\tau move2 (compP2 P) h [Addr A] (a.length) (length (compE2 a)) None \implies False$ 
  by (simp add:  $\tau move2-iff$ )
  moreover have  $\neg \tau move1 P h (addr A.length)$  by (auto simp add:  $\tau move1.simps \tau moves1.simps$ )
  moreover

```

have $P, a \cdot \text{length}, h' \vdash (\text{Val } (\text{Intg } (\text{word-of-int } (\text{int len}))), \text{loc}) \leftrightarrow ([\text{Intg } (\text{word-of-int } (\text{int len}))], \text{loc}, \text{length } (\text{compE2 } (a \cdot \text{length})), \text{None})$
by(rule *bisim1Val2*) *simp*
ultimately show *?thesis using s by(auto) blast*
next
case *Red1ALengthNull*
note $[\text{simp}] = \langle a' = \text{null} \rangle \langle e' = \text{THROW NullPointer} \rangle \langle h' = h \rangle \langle xs' = xs \rangle \langle ta = \varepsilon \rangle$
have $\neg \tau \text{move1 } P h (\text{null} \cdot \text{length})$ **by**(auto *simp add: \(\tau \text{move1} \cdot \text{simps } \tau \text{moves1} \cdot \text{simps}\)*)
moreover from *bisim* **have** $s: xcp = \text{None } xs = \text{loc}$
and $\tau \text{Exec-mover-a } P t a h (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) ([\text{Null}], \text{loc}, \text{length } (\text{compE2 } a), \text{None})$
by(auto *dest: bisim1Val2D1*)
hence $\tau \text{Exec-mover-a } P t (a \cdot \text{length}) h (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) ([\text{Null}], \text{loc}, \text{length } (\text{compE2 } a), \text{None})$
by-(rule *ALength-\(\tau \text{ExecrI}\)*)
moreover have $\text{exec-move-a } P t (a \cdot \text{length}) h ([\text{Null}], \text{loc}, \text{length } (\text{compE2 } a), \text{None}) \varepsilon h ([\text{Null}], \text{loc}, \text{length } (\text{compE2 } a), [\text{addr-of-sys-xcpt NullPointer}])$
unfolding *exec-move-def* **by** -(rule *exec-instr, auto simp add: is-Ref-def*)
moreover have $\tau \text{move2 } (\text{compP2 } P) h [\text{Null}] (a \cdot \text{length}) (\text{length } (\text{compE2 } a)) \text{None} \implies \text{False}$
by(*simp add: \(\tau \text{move2-iff}\)*)
moreover
have $P, a \cdot \text{length}, h \vdash (\text{THROW NullPointer}, \text{loc}) \leftrightarrow ([\text{Null}], \text{loc}, \text{length } (\text{compE2 } a), [\text{addr-of-sys-xcpt NullPointer}])$
by(auto *intro!: bisim1-bisims1.bisim1ALengthNull*)
ultimately show *?thesis using s by auto blast*
next
case (*ALength1Throw A*)
note $[\text{simp}] = \langle a' = \text{Throw } A \rangle \langle h' = h \rangle \langle xs' = xs \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw } A \rangle$
have $\tau: \tau \text{move1 } P h (\text{Throw } A \cdot \text{length})$ **by**(auto *intro: \(\tau \text{move1ALengthThrow}\)*)
from *bisim* **have** $xcp = [A] \vee xcp = \text{None}$ **by**(auto *dest: bisim1-ThrowD*)
thus *?thesis*
proof
assume $[\text{simp}]: xcp = [A]$
with *bisim* **have** $P, a \cdot \text{length}, h \vdash (\text{Throw } A, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp})$
by(auto *intro: bisim1-bisims1.bisim1ALengthThrow*)
thus *?thesis using \(\tau\)* **by**(*fastforce*)
next
assume $[\text{simp}]: xcp = \text{None}$
with *bisim* **obtain** pc'
where $\tau \text{Exec-mover-a } P t a h (\text{stk}, \text{loc}, \text{pc}, \text{None}) ([\text{Addr } A], \text{loc}, \text{pc}', [A])$
and $\text{bisim}' : P, a, h \vdash (\text{Throw } A, xs) \leftrightarrow ([\text{Addr } A], \text{loc}, \text{pc}', [A])$ **and** $[\text{simp}]: xs = \text{loc}$
by(auto *dest: bisim1-Throw-\(\tau \text{Exec-mover}\)*)
hence $\tau \text{Exec-mover-a } P t (a \cdot \text{length}) h (\text{stk}, \text{loc}, \text{pc}, \text{None}) ([\text{Addr } A], \text{loc}, \text{pc}', [A])$
by-(rule *ALength-\(\tau \text{ExecrI}\)*)
moreover from *bisim'* **have** $P, a \cdot \text{length}, h \vdash (\text{Throw } A, xs) \leftrightarrow ([\text{Addr } A], \text{loc}, \text{pc}', [A])$
by(rule *bisim1-bisims1.bisim1ALengthThrow*)
ultimately show *?thesis using \(\tau\)* **by** *auto*
qed
qed
next
case *bisim1ALengthThrow* **thus** *?case by auto*
next
case *bisim1ALengthNull* **thus** *?case by auto*
next
case (*bisim1FAcc E n e xs stk loc pc xcp F D*)
note $IH = \text{bisim1FAcc.IH}(2)$

note $bisim = \langle P, E, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
note $red = \langle True, P, t \vdash 1 \langle e \cdot F\{D\}, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$
note $bsok = \langle bsok (E \cdot F\{D\}) n \rangle$
from red **show** $?case$
proof $cases$
case $(FAcc1Red ee')$
note $[simp] = \langle e' = ee' \cdot F\{D\} \rangle$
and $red = \langle True, P, t \vdash 1 \langle e, (h, xs) \rangle -ta \rightarrow \langle ee', (h', xs') \rangle \rangle$
from red **have** $\tau move1 P h (e \cdot F\{D\}) = \tau move1 P h e$ **by** $(auto simp add: \tau move1.simps \tau moves1.simps)$
moreover **have** $call1 (e \cdot F\{D\}) = call1 e$ **by** $auto$
moreover **from** $IH[OF red] bsok$
obtain $pc'' stk'' loc'' xcp''$ **where** $bisim: P, E, h' \vdash (ee', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$
and $redo: ?exec ta E e ee' h stk loc pc xcp h' pc'' stk'' loc'' xcp''$ **by** $auto$
from $bisim$
have $P, E \cdot F\{D\}, h' \vdash (ee' \cdot F\{D\}, xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$
by $(rule bisim1-bisims1.bisim1FAcc)$
moreover {
assume $no-call2 E pc$
hence $no-call2 (E \cdot F\{D\}) pc$ **by** $(auto simp add: no-call2-def)$ }
ultimately **show** $?thesis$ **using** $redo$
by $(auto simp del: call1.simps calls1.simps split: if-split-asm split del: if-split)(blast intro: FAcc-\tau ExecrI FAcc-\tau ExecI exec-move-FAccI)+$
next
case $(Red1FAcc a v)$
hence $[simp]: e = addr a ta = \{\!| ReadMem a (CField D F) v \!\} e' = Val v h' = h xs' = xs$
and $read: heap-read h a (CField D F) v$ **by** $auto$
from $bisim$ **have** $s: xcp = None xs = loc$ **by** $(auto dest: bisim-Val-loc-eq-xcp-None)$
from $bisim$ **have** $\tau Exec-mover-a P t E h (stk, loc, pc, xcp) ([Addr a], loc, length (compE2 E), None)$
by $(auto dest: bisim1Val2D1)$
hence $\tau Exec-mover-a P t (E \cdot F\{D\}) h (stk, loc, pc, xcp) ([Addr a], loc, length (compE2 E), None)$
by $(rule FAcc-\tau ExecrI)$
moreover **from** $read$
have $exec-move-a P t (E \cdot F\{D\}) h ([Addr a], loc, length (compE2 E), None)$
 $\{\!| ReadMem a (CField D F) v \!\} h' ([v], loc, Suc (length (compE2 E)), None)$
unfolding $exec-move-def$ **by** $(auto intro!: exec-instr)$
moreover **have** $\tau move2 (compP2 P) h [Addr a] (E \cdot F\{D\}) (length (compE2 E)) None \implies False$
by $(simp add: \tau move2-iff)$
moreover **have** $\neg \tau move1 P h (addr a \cdot F\{D\})$ **by** $(auto simp add: \tau move1.simps \tau moves1.simps)$
moreover
have $P, E \cdot F\{D\}, h' \vdash (Val v, loc) \leftrightarrow ([v], loc, length (compE2 (E \cdot F\{D\})), None)$
by $(rule bisim1Val2) simp$
ultimately **show** $?thesis$ **using** s **by** $(auto simp add: ta-upd-simps) blast$
next
case $Red1FAccNull$
note $[simp] = \langle e = null \rangle \langle e' = THROW NullPointer \rangle \langle h' = h \rangle \langle xs' = xs \rangle \langle ta = \varepsilon \rangle$
have $\neg \tau move1 P h (null \cdot F\{D\})$ **by** $(auto simp add: \tau move1.simps \tau moves1.simps)$
moreover **from** $bisim$ **have** $s: xcp = None xs = loc$
and $\tau Exec-mover-a P t E h (stk, loc, pc, xcp) ([Null], loc, length (compE2 E), None)$
by $(auto dest: bisim1Val2D1)$
hence $\tau Exec-mover-a P t (E \cdot F\{D\}) h (stk, loc, pc, xcp) ([Null], loc, length (compE2 E), None)$
by $-(rule FAcc-\tau ExecrI)$
moreover

have $\text{exec-move-a } P \ t \ (E \cdot F\{D\}) \ h \ ([\text{Null}], \text{loc}, \text{length} \ (\text{compE2 } E), \text{None}) \ \varepsilon \ h \ ([\text{Null}], \text{loc}, \text{length} \ (\text{compE2 } E), \lfloor \text{addr-of-sys-xcpt } \text{NullPointer} \rfloor)$
unfolding exec-move-def **by** $\text{-(rule exec-instr, auto simp add: compP2-def dest: sees-field-idemp)}$
moreover have $\tau \text{move2} \ (\text{compP2 } P) \ h \ [\text{Null}] \ (E \cdot F\{D\}) \ (\text{length} \ (\text{compE2 } E)) \ \text{None} \implies \text{False}$
by $(\text{simp add: } \tau \text{move2-iff})$
moreover
have $P, E \cdot F\{D\}, h \vdash (\text{THROW } \text{NullPointer}, \text{loc}) \leftrightarrow ([\text{Null}], \text{loc}, \text{length} \ (\text{compE2 } E), \lfloor \text{addr-of-sys-xcpt } \text{NullPointer} \rfloor)$
by $(\text{rule bisim1-bisims1.bisim1FAccNull})$
ultimately show $?thesis$ **using** s **by** auto blast
next
case $(\text{FAcc1Throw } a)$
note $[\text{simp}] = \langle e = \text{Throw } a \rangle \langle h' = h \rangle \langle xs' = xs \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw } a \rangle$
have $\tau: \tau \text{move1 } P \ h \ (e \cdot F\{D\})$ **by** $(\text{auto intro: } \tau \text{move1FAccThrow})$
from bisim **have** $xcp = \lfloor a \rfloor \vee xcp = \text{None}$ **by** $(\text{auto dest: bisim1-ThrowD})$
thus $?thesis$
proof
assume $[\text{simp}]: xcp = \lfloor a \rfloor$
with bisim **have** $P, E \cdot F\{D\}, h \vdash (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, xcp)$
by $(\text{auto intro: bisim1-bisims1.bisim1FAccThrow})$
thus $?thesis$ **using** τ **by** (fastforce)
next
assume $[\text{simp}]: xcp = \text{None}$
with bisim **obtain** pc'
where $\tau \text{Exec-mover-a } P \ t \ E \ h \ (\text{stk}, \text{loc}, \text{pc}, \text{None}) \ ([\text{Addr } a], \text{loc}, \text{pc}', \lfloor a \rfloor)$
and $\text{bisim}' : P, E, h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], \text{loc}, \text{pc}', \lfloor a \rfloor)$ **and** $[\text{simp}]: xs = \text{loc}$
by $(\text{auto dest: bisim1-Throw-}\tau \text{Exec-mover})$
hence $\tau \text{Exec-mover-a } P \ t \ (E \cdot F\{D\}) \ h \ (\text{stk}, \text{loc}, \text{pc}, \text{None}) \ ([\text{Addr } a], \text{loc}, \text{pc}', \lfloor a \rfloor)$
by $\text{-(rule FAcc-}\tau \text{ExecrI)}$
moreover from bisim' **have** $P, E \cdot F\{D\}, h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], \text{loc}, \text{pc}', \lfloor a \rfloor)$
by $(\text{rule bisim1-bisims1.bisim1FAccThrow})$
ultimately show $?thesis$ **using** τ **by** auto
qed
qed
next
case bisim1FAccThrow **thus** $?case$ **by** auto
next
case bisim1FAccNull **thus** $?case$ **by** auto
next
case $(\text{bisim1FAss1 } e1 \ n \ e1' \ xs \ \text{stk} \ \text{loc} \ \text{pc} \ xcp \ e2 \ F \ D)$
note $\text{IH1} = \text{bisim1FAss1.IH}(2)$
note $\text{IH2} = \text{bisim1FAss1.IH}(4)$
note $\text{bisim1} = \langle P, e1, h \vdash (e1', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, xcp) \rangle$
note $\text{bisim2} = \langle \bigwedge xs. P, e2, h \vdash (e2, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$
note $\text{bsok} = \langle \text{bsok} \ (e1 \cdot F\{D\} := e2) \ n \rangle$
from $\langle \text{True}, P, t \vdash 1 \ (e1' \cdot F\{D\} := e2, (h, xs)) \text{-ta} \rightarrow \langle e', (h', xs') \rangle \rangle$ **show** $?case$
proof cases
case $(\text{FAss1Red1 } E')$
note $[\text{simp}] = \langle e' = E' \cdot F\{D\} := e2 \rangle$
and $\text{red} = \langle \text{True}, P, t \vdash 1 \ (e1', (h, xs)) \text{-ta} \rightarrow \langle E', (h', xs') \rangle \rangle$
from red **have** $\tau \text{move1 } P \ h \ (e1' \cdot F\{D\} := e2) = \tau \text{move1 } P \ h \ e1'$ **by** $(\text{auto simp add: } \tau \text{move1.simps } \tau \text{moves1.simps})$
moreover from red **have** $\text{call1} \ (e1' \cdot F\{D\} := e2) = \text{call1 } e1'$ **by** auto
moreover from $\text{IH1}[\text{OF red}] \ \text{bsok}$

obtain $pc''\ stk''\ loc''\ xcp''$ **where** $bisim: P, e1, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$
and $redo: ?exec\ ta\ e1\ e1'\ E'\ h\ stk\ loc\ pc\ xcp\ h'\ pc''\ stk''\ loc''\ xcp''$ **by** *auto*
from *bisim*
have $P, e1 \cdot F\{D\} := e2, h' \vdash (E' \cdot F\{D\} := e2, xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$
by(*rule bisim1-bisims1.bisim1FAss1*)
moreover {
assume *no-call2 e1 pc*
hence *no-call2 (e1 · F{D} := e2) pc* \vee *pc = length (compE2 e1)* **by**(*auto simp add: no-call2-def*)
}

ultimately show *?thesis using redo*
by(*auto simp del: call1.simps calls1.simps split: if-split-asm split del: if-split*)(*blast intro: FAss- τ ExecrI1 FAss- τ ExecI1 exec-move-FAssI1*)
next
case (*FAss1Red2 E' v*)
note [*simp*] = $\langle e1' = Val\ v \ \langle e' = Val\ v \cdot F\{D\} := E' \rangle$
and $red = \langle True, P, t \vdash 1 \ \langle e2, (h, xs) \rangle -ta \rightarrow \langle E', (h', xs') \rangle$
from red **have** $\tau: \tau move1\ P\ h\ (Val\ v \cdot F\{D\} := e2) = \tau move1\ P\ h\ e2$ **by**(*auto simp add: $\tau move1$.simps $\tau moves1$.simps*)
from *bisim1* **have** $s: xcp = None\ xs = loc$
and $exec1: \tau Exec-mover-a\ P\ t\ e1\ h\ (stk, loc, pc, None)\ ([v], xs, length\ (compE2\ e1), None)$
by(*auto dest: bisim1Val2D1*)
from $exec1$ **have** $\tau Exec-mover-a\ P\ t\ (e1 \cdot F\{D\} := e2)\ h\ (stk, loc, pc, None)\ ([v], xs, length\ (compE2\ e1), None)$
by(*rule FAss- τ ExecrI1*)
moreover
from *IH2[OF red] bsok* **obtain** $pc''\ stk''\ loc''\ xcp''$
where $bisim': P, e2, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$
and $exec': ?exec\ ta\ e2\ e2'\ E'\ h\ []\ xs\ 0\ None\ h'\ pc''\ stk''\ loc''\ xcp''$ **by** *auto*
have $?exec\ ta\ (e1 \cdot F\{D\} := e2)\ (Val\ v \cdot F\{D\} := e2)\ (Val\ v \cdot F\{D\} := E')\ h\ ([]\ @\ [v])\ xs\ (length\ (compE2\ e1) + 0)\ None\ h'\ (length\ (compE2\ e1) + pc'')\ (stk''\ @\ [v])\ loc''\ xcp''$
proof(*cases $\tau move1\ P\ h\ (Val\ v \cdot F\{D\} := e2)$*)
case *True*
with $exec'\ \tau$ **have** [*simp*]: $h = h'$ **and** $e: sim-move\ e2\ E'\ P\ t\ e2\ h\ ([], xs, 0, None)\ (stk'', loc'', pc'', xcp'')$ **by** *auto*
from e **have** $sim-move\ (Val\ v \cdot F\{D\} := e2)\ (Val\ v \cdot F\{D\} := E')\ P\ t\ (e1 \cdot F\{D\} := e2)\ h\ ([]\ @\ [v], xs, length\ (compE2\ e1) + 0, None)\ (stk''\ @\ [v], loc'', length\ (compE2\ e1) + pc'', xcp'')$
by(*fastforce dest: FAss- τ ExecrI2 FAss- τ ExecI2 simp del: compE2.simps compEs2.simps*)
with *True* **show** *?thesis by auto*
next
case *False*
with $exec'\ \tau$ **obtain** $pc'\ stk'\ loc'\ xcp'$
where $e: \tau Exec-mover-a\ P\ t\ e2\ h\ ([], xs, 0, None)\ (stk', loc', pc', xcp')$
and $e': exec-move-a\ P\ t\ e2\ h\ (stk', loc', pc', xcp')\ (extTA2JVM\ (compP2\ P)\ ta)\ h'\ (stk'', loc'', pc'', xcp'')$
and $\tau': \neg\ \tau move2\ (compP2\ P)\ h\ stk'\ e2\ pc'\ xcp'$
and $call: call1\ e2 = None \vee no-call2\ e2\ 0 \vee pc' = 0 \wedge stk' = [] \wedge loc' = xs \wedge xcp' = None$ **by** *auto*
from e **have** $\tau Exec-mover-a\ P\ t\ (e1 \cdot F\{D\} := e2)\ h\ ([]\ @\ [v], xs, length\ (compE2\ e1) + 0, None)\ (stk'\ @\ [v], loc', length\ (compE2\ e1) + pc', xcp')$
by(*rule FAss- τ ExecrI2*)
moreover from e' **have** $exec-move-a\ P\ t\ (e1 \cdot F\{D\} := e2)\ h\ (stk'\ @\ [v], loc', length\ (compE2\ e1) + pc', xcp')\ (extTA2JVM\ (compP2\ P)\ ta)\ h'\ (stk''\ @\ [v], loc'', length\ (compE2\ e1) + pc'', xcp'')$
by(*rule exec-move-FAssI2*)
moreover from e' **have** $pc' < length\ (compE2\ e2)$ **by**(*auto elim: exec-meth.cases*)

with $\tau' e'$ **have** $\neg \tau \text{move2} (\text{compP2 } P) h (\text{stk}' @ [v]) (e1 \cdot F\{D\} := e2) (\text{length} (\text{compE2 } e1) + pc') xcp'$
by(*auto simp add: $\tau \text{move2-iff}$ $\tau \text{instr-stk-drop-exec-move}$*)
moreover have $\text{call1} (e1' \cdot F\{D\} := e2) = \text{call1 } e2$ **by** *simp*
moreover have $\text{no-call2 } e2 0 \implies \text{no-call2} (e1 \cdot F\{D\} := e2) (\text{length} (\text{compE2 } e1))$
by(*auto simp add: no-call2-def*)
ultimately show *?thesis using False call*
by(*auto simp del: split-paired-Ex call1.simps calls1.simps*) **blast**
qed
moreover from *bisim'*
have $P, e1 \cdot F\{D\} := e2, h' \vdash (\text{Val } v \cdot F\{D\} := E', xs') \leftrightarrow ((\text{stk}'' @ [v]), \text{loc}'', \text{length} (\text{compE2 } e1) + pc'', xcp'')$
by(*rule bisim1-bisims1.bisim1FAss2*)
moreover from *bisim1* **have** $pc \neq \text{length} (\text{compE2 } e1) \longrightarrow \text{no-call2} (e1 \cdot F\{D\} := e2) pc$
by(*auto simp add: no-call2-def dest: bisim-Val-pc-not-Invoke bisim1-pc-length-compE2*)
ultimately show *?thesis using $\tau \text{exec1 } s$*
apply(*auto simp del: split-paired-Ex call1.simps calls1.simps split: if-split-asm split del: if-split*)
apply(*blast intro: $\tau \text{Exec-mover-trans}$ |fastforce elim!: $\tau \text{Exec-mover-trans}$ simp del: split-paired-Ex call1.simps calls1.simps*)
done
next
case (*Red1FAss a v*)
note [*simp*] = $\langle e1' = \text{addr } a \rangle \langle e2 = \text{Val } v \rangle \langle ta = \llbracket \text{WriteMem } a (\text{CField } D F) v \rrbracket \rangle \langle e' = \text{unit} \rangle \langle xs' = xs \rangle$
and *write* = $\langle \text{heap-write } h a (\text{CField } D F) v h' \rangle$
have $\tau: \neg \tau \text{move1 } P h (e1' \cdot F\{D\} := e2)$ **by**(*auto simp add: $\tau \text{move1.simps}$ $\tau \text{moves1.simps}$*)
from *bisim1* **have** $s: xcp = \text{None } xs = \text{loc}$
and $\tau \text{Exec-mover-a } P t e1 h (\text{stk}, \text{loc}, pc, xcp) ([\text{Addr } a], \text{loc}, \text{length} (\text{compE2 } e1), \text{None})$
by(*auto dest: bisim1Val2D1*)
hence $\tau \text{Exec-mover-a } P t (e1 \cdot F\{D\} := e2) h (\text{stk}, \text{loc}, pc, xcp) ([\text{Addr } a], \text{loc}, \text{length} (\text{compE2 } e1), \text{None})$
by-(*rule FAss- $\tau \text{ExecrI1}$*)
also have $\tau \text{move2} (\text{compP2 } P) h [\text{Addr } a] (e1 \cdot F\{D\} := \text{Val } v) (\text{length} (\text{compE2 } e1)) \text{None}$ **by**(*simp add: $\tau \text{move2-iff}$*)
hence $\tau \text{Exec-mover-a } P t (e1 \cdot F\{D\} := e2) h ([\text{Addr } a], \text{loc}, \text{length} (\text{compE2 } e1), \text{None}) ([v, \text{Addr } a], \text{loc}, \text{Suc} (\text{length} (\text{compE2 } e1)), \text{None})$
by-(*rule $\tau \text{Execr1step}$, auto intro!: exec-instr simp add: exec-move-def compP2-def*)
also (*rtranclp-trans*) **from** *write*
have $\text{exec-move-a } P t (e1 \cdot F\{D\} := e2) h ([v, \text{Addr } a], \text{loc}, \text{Suc} (\text{length} (\text{compE2 } e1)), \text{None}) \llbracket \text{WriteMem } a (\text{CField } D F) v \rrbracket$
 $h' ([], \text{loc}, \text{Suc} (\text{Suc} (\text{length} (\text{compE2 } e1))), \text{None})$
unfolding *exec-move-def* **by**(*auto intro!: exec-instr*)
moreover have $\tau \text{move2} (\text{compP2 } P) h [v, \text{Addr } a] (e1 \cdot F\{D\} := e2) (\text{Suc} (\text{length} (\text{compE2 } e1))) \text{None} \implies \text{False}$
by(*simp add: $\tau \text{move2-iff}$*)
moreover
have $P, e1 \cdot F\{D\} := e2, h' \vdash (\text{unit}, \text{loc}) \leftrightarrow ([], \text{loc}, \text{Suc} (\text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2)), \text{None})$
by(*rule bisim1-bisims1.bisim1FAss3*)
ultimately show *?thesis using $s \tau$* **by**(*auto simp del: fun-upd-apply simp add: ta-upd-simps*) **blast**
next
case (*Red1FAssNull v*)
note [*simp*] = $\langle e1' = \text{null} \rangle \langle e2 = \text{Val } v \rangle \langle xs' = xs \rangle \langle ta = \varepsilon \rangle \langle e' = \text{THROW } \text{NullPointer} \rangle \langle h' = h \rangle$
have $\tau: \neg \tau \text{move1 } P h (e1' \cdot F\{D\} := e2)$ **by**(*auto simp add: $\tau \text{move1.simps}$ $\tau \text{moves1.simps}$*)


```

from bisim1 have s: xcp = None xs = loc
  and  $\tau$ Exec-mover-a P t e1 h (stk, loc, pc, xcp) ([Null], loc, length (compE2 e1), None)
  by(auto dest: bisim1Val2D1)
hence  $\tau$ Exec-mover-a P t (e1·F{D} := e2) h (stk, loc, pc, xcp) ([Null], loc, length (compE2 e1),
None)
  by-(rule FAss- $\tau$ ExecrI1)
also have  $\tau$ move2 (compP2 P) h [Null] (e1·F{D} := Val v) (length (compE2 e1)) None by(simp
add:  $\tau$ move2-iff)
  hence  $\tau$ Exec-mover-a P t (e1·F{D} := e2) h ([Null], loc, length (compE2 e1), None) ([v, Null],
loc, Suc (length (compE2 e1)), None)
  by-(rule  $\tau$ Execr1step, auto intro!: exec-instr simp add: exec-move-def compP2-def)
also (rtranclp-trans)
have exec-move-a P t (e1·F{D} := e2) h ([v, Null], loc, Suc (length (compE2 e1)), None)  $\varepsilon$ 
  h' ([v, Null], loc, Suc (length (compE2 e1)), [addr-of-sys-xcpt NullPointer])
  by(auto intro!: exec-instr simp add: exec-move-def)
moreover have  $\tau$ move2 (compP2 P) h [v, Null] (e1·F{D} := e2) (Suc (length (compE2 e1)))
None  $\implies$  False
  by(simp add:  $\tau$ move2-iff)
moreover
have P, e1·F{D} := e2, h  $\vdash$  (THROW NullPointer, loc)  $\leftrightarrow$  ([v, Null], loc, length (compE2 e1) +
length (compE2 e2), [addr-of-sys-xcpt NullPointer])
  by(rule bisim1-bisims1.bisim1FAssNull)
ultimately show ?thesis using s  $\tau$  by(auto simp del: fun-upd-apply) blast
next
case (FAss1Throw1 a)
note [simp] =  $\langle e1' = \text{Throw } a \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw } a \rangle \langle h' = h \rangle \langle xs' = xs \rangle$ 
have  $\tau$ :  $\tau$ move1 P h (Throw a·F{D} := e2) by(rule  $\tau$ move1FAssThrow1)
from bisim1 have xcp = [a]  $\vee$  xcp = None by(auto dest: bisim1-ThrowD)
thus ?thesis
proof
  assume [simp]: xcp = [a]
  with bisim1
have P, e1·F{D} := e2, h  $\vdash$  (Throw a, xs)  $\leftrightarrow$  (stk, loc, pc, xcp)
  by(auto intro: bisim1-bisims1.intros)
thus ?thesis using  $\tau$  by(fastforce)
next
assume [simp]: xcp = None
with bisim1 obtain pc' where  $\tau$ Exec-mover-a P t e1 h (stk, loc, pc, None) ([Addr a], loc, pc',
[a])
  and bisim': P, e1, h  $\vdash$  (Throw a, xs)  $\leftrightarrow$  ([Addr a], loc, pc', [a])
  and [simp]: xs = loc
  by(auto dest: bisim1-Throw- $\tau$ Exec-mover)
hence  $\tau$ Exec-mover-a P t (e1·F{D} := e2) h (stk, loc, pc, None) ([Addr a], loc, pc', [a])
  by-(rule FAss- $\tau$ ExecrI1)
moreover from bisim'
have P, e1·F{D} := e2, h  $\vdash$  (Throw a, xs)  $\leftrightarrow$  ([Addr a], loc, pc', [a])
  by(rule bisim1-bisims1.bisim1FAssThrow1)
ultimately show ?thesis using  $\tau$  by auto
qed
next
case (FAss1Throw2 v ad)
note [simp] =  $\langle e1' = \text{Val } v \rangle \langle e2 = \text{Throw } ad \rangle \langle e' = \text{Throw } ad \rangle \langle h' = h \rangle \langle xs' = xs \rangle$ 
from bisim1 have s: xcp = None xs = loc
  and  $\tau$ Exec-mover-a P t e1 h (stk, loc, pc, xcp) ([v], loc, length (compE2 e1), None)

```

by(*auto dest: bisim1Val2D1*)
hence $\tau Exec\text{-mover-a } P \ t \ (e1 \cdot F\{D\} := Throw \ ad) \ h \ (stk, \ loc, \ pc, \ xcp) \ ([v], \ loc, \ length \ (compE2 \ e1), \ None)$
by-(*rule FAss- τ ExecrI1*)
also have $\tau Exec\text{-mover-a } P \ t \ (e1 \cdot F\{D\} := Throw \ ad) \ h \ ([v], \ loc, \ length \ (compE2 \ e1), \ None)$
(*[Addr ad, v], loc, Suc (length (compE2 e1)), [ad]*)
by(*rule τ Execr2step*)(*auto simp add: exec-move-def exec-meth-instr τ move2-iff τ move1.simps τ moves1.simps*)
also (*rtranclp-trans*)
have $P, e1 \cdot F\{D\} := Throw \ ad, h \vdash (Throw \ ad, \ loc) \leftrightarrow ([Addr \ ad] \ @ \ [v], \ loc, \ (length \ (compE2 \ e1) + length \ (compE2 \ (addr \ ad))), \ [ad])$
by(*rule bisim1FAssThrow2[OF bisim1Throw2]*)
moreover have $\tau move1 \ P \ h \ (FAss \ e1' \ F \ D \ (Throw \ ad))$ **by**(*auto intro: τ move1FAssThrow2*)
ultimately show *?thesis using s by auto*
qed
next
case (*bisim1FAss2 e2 n e2' xs stk loc pc xcp e1 F D v1*)
note *IH2 = bisim1FAss2.IH(2)*
note *bisim1 = $\langle \bigwedge xs. P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, None) \rangle$*
note *bisim2 = $\langle P, e2, h \vdash (e2', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$*
note *bsok = $\langle bsok \ (e1 \cdot F\{D\} := e2) \ n \rangle$*
note *red = $\langle True, P, t \vdash 1 \ \langle Val \ v1 \cdot F\{D\} := e2', (h, xs) \rangle - ta \rightarrow \langle e', (h', xs') \rangle$*
from red show *?case*
proof cases
case (*FAss1Red2 E'*)
note [*simp*] = $\langle e' = Val \ v1 \cdot F\{D\} := E' \rangle$
and *red = $\langle True, P, t \vdash 1 \ \langle e2', (h, xs) \rangle - ta \rightarrow \langle E', (h', xs') \rangle$*
from *IH2[OF red] bsok obtain pc'' stk'' loc'' xcp''*
where *bisim': $P, e2, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$*
and *exec': $?exec \ ta \ e2 \ e2' \ E' \ h \ stk \ loc \ pc \ xcp \ h' \ pc'' \ stk'' \ loc'' \ xcp''$ by auto*
from red have $\tau: \tau move1 \ P \ h \ (Val \ v1 \cdot F\{D\} := e2') = \tau move1 \ P \ h \ e2'$ **by**(*auto simp add: τ move1.simps τ moves1.simps*)
have *no-call2 e2 pc \implies no-call2 (e1 \cdot F{D} := e2) (length (compE2 e1) + pc)* **by**(*auto simp add: no-call2-def*)
hence *?exec ta (e1 \cdot F{D} := e2) (Val v1 \cdot F{D} := e2') (Val v1 \cdot F{D} := E') h (stk @ [v1]) loc (length (compE2 e1) + pc) xcp h' (length (compE2 e1) + pc'') (stk'' @ [v1]) loc'' xcp''*
using *exec' τ*
apply(*cases τ move1 P h (Val v1 \cdot F{D} := e2')*)
apply(*auto*)
apply(*blast intro: FAss- τ ExecrI2 FAss- τ ExecI2 exec-move-FAssI2*)
apply(*blast intro: FAss- τ ExecrI2 FAss- τ ExecI2 exec-move-FAssI2*)
apply(*rule exI conjI FAss- τ ExecrI2 exec-move-FAssI2|assumption*)
apply(*fastforce simp add: τ instr-stk-drop-exec-move τ move2-iff split: if-split-asm*)
apply(*rule exI conjI FAss- τ ExecrI2 exec-move-FAssI2|assumption*)
apply(*fastforce simp add: τ instr-stk-drop-exec-move τ move2-iff split: if-split-asm*)
apply(*rule exI conjI FAss- τ ExecrI2 exec-move-FAssI2 rtranclp.rtrancl-refl|assumption*)
apply(*fastforce simp add: τ instr-stk-drop-exec-move τ move2-iff split: if-split-asm*)
done
moreover from *bisim'*
have $P, e1 \cdot F\{D\} := e2, h' \vdash (Val \ v1 \cdot F\{D\} := E', xs') \leftrightarrow (stk'' \ @ \ [v1], \ loc'', \ length \ (compE2 \ e1) + pc'', \ xcp'')$
by(*rule bisim1-bisims1.bisim1FAss2*)
ultimately show *?thesis using τ by auto blast+*
next

```

case (Red1FAss a v)
note [simp] = ⟨v1 = Addr a⟩ ⟨e2' = Val v⟩ ⟨ta = {WriteMem a (CField D F) v}⟩ ⟨e' = unit⟩ ⟨xs'
= xs⟩
  and ha = ⟨heap-write h a (CField D F) v h'⟩
  have  $\tau: \neg \tau \text{move1 } P \ h \ (\text{addr } a \cdot F\{D\} := e2')$  by(auto simp add:  $\tau \text{move1.simps}$   $\tau \text{moves1.simps}$ )
  from bisim2 have s: xcp = None xs = loc
    and  $\tau \text{Exec-mover-a } P \ t \ e2 \ h \ (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \ ([v], \text{loc}, \text{length}(\text{compE2 } e2), \text{None})$ 
    by(auto dest: bisim1Val2D1)
  hence  $\tau \text{Exec-mover-a } P \ t \ (e1 \cdot F\{D\} := e2) \ h \ (\text{stk} \ @ \ [v1], \text{loc}, \text{length}(\text{compE2 } e1) + \text{pc}, \text{xcp}) \ ([v]$ 
 $\ @ \ [v1], \text{loc}, \text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2), \text{None})$ 
    by-(rule FAss- $\tau \text{ExecrI2}$ )
  moreover from ha
  have exec-move-a P t (e1·F{D} := e2) h ([v, Addr a], loc, length (compE2 e1) + length (compE2
e2), None) {WriteMem a (CField D F) v}
     $h' \ ([], \text{loc}, \text{Suc}(\text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2)), \text{None})$ 
    by(auto intro!: exec-instr simp add: exec-move-def)
  moreover have  $\tau \text{move2} \ (\text{compP2 } P) \ h \ [v, \text{Addr } a] \ (e1 \cdot F\{D\} := e2) \ (\text{length}(\text{compE2 } e1) + \text{length}$ 
(compE2 e2)) None  $\implies \text{False}$ 
    by(simp add:  $\tau \text{move2-iff}$ )
  moreover
  have P, e1·F{D} := e2, h' ⊢ (unit, loc) ↔ ([], loc, Suc (length (compE2 e1) + length (compE2
e2)), None)
    by(rule bisim1-bisims1.bisim1FAss3)
  ultimately show ?thesis using s  $\tau$  by(auto simp del: fun-upd-apply simp add: ta-upd-simps) blast
next
  case (Red1FAssNull v)
  note [simp] = ⟨v1 = Null⟩ ⟨e2' = Val v⟩ ⟨xs' = xs⟩ ⟨ta =  $\varepsilon$ ⟩ ⟨e' = THROW NullPointer⟩ ⟨h' = h⟩
  have  $\tau: \neg \tau \text{move1 } P \ h \ (\text{null} \cdot F\{D\} := e2')$  by(auto simp add:  $\tau \text{move1.simps}$   $\tau \text{moves1.simps}$ )
  from bisim2 have s: xcp = None xs = loc
    and  $\tau \text{Exec-mover-a } P \ t \ e2 \ h \ (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \ ([v], \text{loc}, \text{length}(\text{compE2 } e2), \text{None})$ 
    by(auto dest: bisim1Val2D1)
  hence  $\tau \text{Exec-mover-a } P \ t \ (e1 \cdot F\{D\} := e2) \ h \ (\text{stk} \ @ \ [\text{Null}], \text{loc}, \text{length}(\text{compE2 } e1) + \text{pc}, \text{xcp})$ 
([v] @ [Null], loc, length (compE2 e1) + length (compE2 e2), None)
    by-(rule FAss- $\tau \text{ExecrI2}$ )
  moreover have exec-move-a P t (e1·F{D} := e2) h ([v, Null], loc, length (compE2 e1) + length
(compE2 e2), None)  $\varepsilon$ 
     $h' \ ([v, \text{Null}], \text{loc}, \text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2),$ 
 $[\text{addr-of-sys-xcpt } \text{NullPointer}])$ 
    by(auto intro!: exec-instr simp add: exec-move-def)
  moreover have  $\tau \text{move2} \ (\text{compP2 } P) \ h \ [v, \text{Null}] \ (e1 \cdot F\{D\} := e2) \ (\text{length}(\text{compE2 } e1) + \text{length}$ 
(compE2 e2)) None  $\implies \text{False}$ 
    by(simp add:  $\tau \text{move2-iff}$ )
  moreover
  have P, e1·F{D} := e2, h ⊢ (THROW NullPointer, loc) ↔ ([v, Null], loc, length (compE2 e1) +
length (compE2 e2), [addr-of-sys-xcpt NullPointer])
    by(rule bisim1-bisims1.bisim1FAssNull)
  ultimately show ?thesis using s  $\tau$  by(auto simp del: fun-upd-apply) blast
next
  case (FAss1Throw2 a)
  note [simp] = ⟨e2' = Throw a⟩ ⟨ta =  $\varepsilon$ ⟩ ⟨h' = h⟩ ⟨xs' = xs⟩ ⟨e' = Throw a⟩
  have  $\tau: \tau \text{move1 } P \ h \ (\text{FAss} \ (\text{Val } v1) \ F \ D \ (\text{Throw } a))$  by(rule  $\tau \text{move1FAssThrow2}$ )
  from bisim2 have xcp = [a] ∨ xcp = None by(auto dest: bisim1-ThrowD)
  thus ?thesis
  proof

```

```

assume [simp]: xcp = [a]
with bisim2
have P, e1·F{D} := e2, h ⊢ (Throw a, xs) ↔ (stk @ [v1], loc, length (compE2 e1) + pc, xcp)
  by(auto intro: bisim1FAssThrow2)
thus ?thesis using τ by(fastforce)
next
assume [simp]: xcp = None
with bisim2 obtain pc'
  where τExec-mover-a P t e2 h (stk, loc, pc, None) ([Addr a], loc, pc', [a])
  and bisim': P, e2, h ⊢ (Throw a, xs) ↔ ([Addr a], loc, pc', [a]) and [simp]: xs = loc
  by(auto dest: bisim1-Throw-τExec-mover)
  hence τExec-mover-a P t (e1·F{D} := e2) h (stk @ [v1], loc, length (compE2 e1) + pc, None)
  ([Addr a] @ [v1], loc, length (compE2 e1) + pc', [a])
  by-(rule FAss-τExecrI2)
  moreover from bisim'
  have P, e1·F{D} := e2, h ⊢ (Throw a, xs) ↔ ([Addr a]@[v1], loc, length (compE2 e1) + pc',
  [a])
  by-(rule bisim1FAssThrow2, auto)
  ultimately show ?thesis using τ by auto
  qed
qed auto
next
case bisim1FAssThrow1 thus ?case by fastforce
next
case bisim1FAssThrow2 thus ?case by fastforce
next
case bisim1FAssNull thus ?case by fastforce
next
case bisim1FAss3 thus ?case by fastforce
next
case (bisim1CAS1 e1 n e1' xs stk loc pc xcp e2 e3 D F)
note IH1 = bisim1CAS1.IH(2)
note IH2 = bisim1CAS1.IH(4)
note IH3 = bisim1CAS1.IH(6)
note bisim1 = ⟨P, e1, h ⊢ (e1', xs) ↔ (stk, loc, pc, xcp)⟩
note bisim2 = ⟨∧xs. P, e2, h ⊢ (e2, xs) ↔ ([], xs, 0, None)⟩
note bisim3 = ⟨∧xs. P, e3, h ⊢ (e3, xs) ↔ ([], xs, 0, None)⟩
note bsok = ⟨bsok - n⟩
from ⟨True, P, t ⊢1 ⟨-, (h, xs)⟩ -ta→ ⟨e', (h', xs')⟩⟩ show ?case
proof cases
  case (CAS1Red1 E')
  note [simp] = ⟨e' = E'.compareAndSwap(D·F, e2, e3)⟩
  and red = ⟨True, P, t ⊢1 ⟨e1', (h, xs)⟩ -ta→ ⟨E', (h', xs')⟩⟩
  from red have τmove1 P h (e1'.compareAndSwap(D·F, e2, e3)) = τmove1 P h e1' by(auto simp
  add: τmove1.simps τmoves1.simps)
  moreover from red have call1 (e1'.compareAndSwap(D·F, e2, e3)) = call1 e1' by auto
  moreover from IH1[OF red] bsok
  obtain pc'' stk'' loc'' xcp'' where bisim: P, e1, h' ⊢ (E', xs') ↔ (stk'', loc'', pc'', xcp'')
  and redo: ?exec ta e1 e1' E' h stk loc pc xcp h' pc'' stk'' loc'' xcp'' by auto
  from bisim
  have P, e1·compareAndSwap(D·F, e2, e3), h' ⊢ (E'.compareAndSwap(D·F, e2, e3), xs') ↔ (stk'',
  loc'', pc'', xcp'')
  by(rule bisim1-bisims1.bisim1CAS1)
  moreover {

```

assume $no\text{-}call2\ e1\ pc$
hence $no\text{-}call2\ (e1 \cdot compareAndSwap(D \cdot F, e2, e3))\ pc \vee pc = length\ (compE2\ e1)$ **by**($auto\ simp\ add: no\text{-}call2\text{-}def$) }
ultimately show $?thesis$ **using** $redo$
by($auto\ simp\ del: call1.simps\ calls1.simps\ split: if\text{-}split\text{-}asm\ split\ del: if\text{-}split$)($blast\ intro: CAS\text{-}\tau ExecrI1\ CAS\text{-}\tau ExectI1\ exec\text{-}move\text{-}CASI1$)+
next
case ($CAS1Red2\ E'\ v$)
note [$simp$] = $\langle e1' = Val\ v \rangle \langle e' = Val\ v \cdot compareAndSwap(D \cdot F, E', e3) \rangle$
and $red = \langle True, P, t \vdash 1 \langle e2, (h, xs) \rangle \text{-}ta \rightarrow \langle E', (h', xs') \rangle \rangle$
from red **have** $\tau: \tau move1\ P\ h\ (Val\ v \cdot compareAndSwap(D \cdot F, e2, e3)) = \tau move1\ P\ h\ e2$ **by**($auto\ simp\ add: \tau move1.simps\ \tau moves1.simps$)
from $bisim1$ **have** $s: xcp = None\ xs = loc$
and $exec1: \tau Exec\text{-}mover\text{-}a\ P\ t\ e1\ h\ (stk, loc, pc, None)\ ([v], xs, length\ (compE2\ e1), None)$
by($auto\ dest: bisim1Val2D1$)
from $exec1$ **have** $\tau Exec\text{-}mover\text{-}a\ P\ t\ (e1 \cdot compareAndSwap(D \cdot F, e2, e3))\ h\ (stk, loc, pc, None)$
 $([v], xs, length\ (compE2\ e1), None)$
by($rule\ CAS\text{-}\tau ExecrI1$)
moreover
from $IH2[OF\ red]\ bsok$ **obtain** $pc''\ stk''\ loc''\ xcp''$
where $bisim': P, e2, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$
and $exec': ?exec\ ta\ e2\ e2\ E'\ h\ []\ xs\ 0\ None\ h'\ pc''\ stk''\ loc''\ xcp''$ **by** $auto$
have $?exec\ ta\ (e1 \cdot compareAndSwap(D \cdot F, e2, e3))\ (Val\ v \cdot compareAndSwap(D \cdot F, e2, e3))\ (Val\ v \cdot compareAndSwap(D \cdot F, E', e3))\ h\ ([]\ @\ [v])\ xs\ (length\ (compE2\ e1) + 0)\ None\ h'\ (length\ (compE2\ e1) + pc'')$ $(stk''\ @\ [v])\ loc''\ xcp''$
proof($cases\ \tau move1\ P\ h\ (Val\ v \cdot compareAndSwap(D \cdot F, e2, e3))$)
case $True$
with $exec'\ \tau$ **have** [$simp$]: $h = h'$ **and** $e: sim\text{-}move\ e2\ E'\ P\ t\ e2\ h\ ([], xs, 0, None)\ (stk'', loc'', pc'', xcp'')$ **by** $auto$
from e **have** $sim\text{-}move\ (e1 \cdot compareAndSwap(D \cdot F, e2, e3))\ (e1 \cdot compareAndSwap(D \cdot F, E', e3))\ P\ t\ (e1 \cdot compareAndSwap(D \cdot F, e2, e3))\ h\ ([]\ @\ [v], xs, length\ (compE2\ e1) + 0, None)\ (stk''\ @\ [v], loc'', length\ (compE2\ e1) + pc'', xcp'')$
by($fastforce\ dest: CAS\text{-}\tau ExecrI2\ CAS\text{-}\tau ExectI2$)
with $True$ **show** $?thesis$ **by** $auto$
next
case $False$
with $exec'\ \tau$ **obtain** $pc'\ stk'\ loc'\ xcp'$
where $e: \tau Exec\text{-}mover\text{-}a\ P\ t\ e2\ h\ ([], xs, 0, None)\ (stk', loc', pc', xcp')$
and $e': exec\text{-}move\text{-}a\ P\ t\ e2\ h\ (stk', loc', pc', xcp')\ (extTA2JVM\ (compP2\ P)\ ta)\ h'\ (stk'', loc'', pc'', xcp'')$
and $\tau': \neg\ \tau move2\ (compP2\ P)\ h\ stk'\ e2\ pc'\ xcp'$
and $call: call1\ e2 = None \vee no\text{-}call2\ e2\ 0 \vee pc' = 0 \wedge stk' = [] \wedge loc' = xs \wedge xcp' = None$ **by** $auto$
from e **have** $\tau Exec\text{-}mover\text{-}a\ P\ t\ (e1 \cdot compareAndSwap(D \cdot F, e2, e3))\ h\ ([]\ @\ [v], xs, length\ (compE2\ e1) + 0, None)\ (stk'\ @\ [v], loc', length\ (compE2\ e1) + pc', xcp')$ **by**($rule\ CAS\text{-}\tau ExecrI2$)
moreover from e' **have** $exec\text{-}move\text{-}a\ P\ t\ (e1 \cdot compareAndSwap(D \cdot F, e2, e3))\ h\ (stk'\ @\ [v], loc', length\ (compE2\ e1) + pc', xcp')\ (extTA2JVM\ (compP2\ P)\ ta)\ h'\ (stk''\ @\ [v], loc'', length\ (compE2\ e1) + pc'', xcp'')$
by($rule\ exec\text{-}move\text{-}CASI2$)
moreover from e' **have** $pc' < length\ (compE2\ e2)$ **by**($auto\ elim: exec\text{-}meth.cases$)
with $\tau'\ e'$ **have** $\neg\ \tau move2\ (compP2\ P)\ h\ (stk'\ @\ [v])\ (e1 \cdot compareAndSwap(D \cdot F, e2, e3))\ (length\ (compE2\ e1) + pc')\ xcp'$
by($auto\ simp\ add: \tau instr\text{-}stk\text{-}drop\text{-}exec\text{-}move\ \tau move2\text{-}iff$)
moreover from red **have** $call1\ (e1' \cdot compareAndSwap(D \cdot F, e2, e3)) = call1\ e2$ **by** $auto$

moreover have $no\text{-}call2\ e2\ 0 \implies no\text{-}call2\ (e1 \cdot compareAndSwap(D \cdot F, e2, e3))\ (length\ (compE2\ e1))$
by(*auto simp add: no-call2-def*)
ultimately show *?thesis using False call*
by(*auto simp del: split-paired-Ex call1.simps calls1.simps*) **blast**
qed
moreover from *bisim'*
have $P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h' \vdash (Val\ v \cdot compareAndSwap(D \cdot F, E', e3), xs') \leftrightarrow ((stk''\ @\ [v]), loc'', length\ (compE2\ e1) + pc'', xcp'')$
by(*rule bisim1-bisims1.bisim1CAS2*)
moreover from *bisim1* **have** $pc \neq length\ (compE2\ e1) \longrightarrow no\text{-}call2\ (e1 \cdot compareAndSwap(D \cdot F, e2, e3))\ pc$
by(*auto simp add: no-call2-def dest: bisim-Val-pc-not-Invoke bisim1-pc-length-compE2*)
ultimately show *?thesis using $\tau\ exec1\ s$*
apply(*auto simp del: split-paired-Ex call1.simps calls1.simps split: if-split-asm split del: if-split*)
apply(*blast intro: $\tau\ Exec\ mover\ trans|fastforce\ elim!: \tau\ Exec\ mover\ trans\ simp\ del: split\ paired\ Ex\ call1\ .\ simp\ calls1\ .\ simp$*)
done
next
case (*CAS1Red3 E' v v'*)
note [*simp*] = $\langle e2 = Val\ v' \rangle \langle e1' = Val\ v \rangle \langle e' = Val\ v \cdot compareAndSwap(D \cdot F, Val\ v', E') \rangle$
and $red = \langle True, P, t \vdash 1 \langle e3, (h, xs) \rangle -ta \rightarrow \langle E', (h', xs') \rangle$
from *red* **have** $\tau: \tau\ move1\ P\ h\ (Val\ v \cdot compareAndSwap(D \cdot F, Val\ v', e3)) = \tau\ move1\ P\ h\ e3$ **by**(*auto simp add: $\tau\ move1\ .\ simp\ \tau\ moves1\ .\ simp$*)
from *bisim1* **have** $s: xcp = None\ xs = loc$
and $exec1: \tau\ Exec\ mover\ a\ P\ t\ e1\ h\ (stk, loc, pc, None)\ (\ []\ @\ [v], xs, length\ (compE2\ e1) + 0, None)$
by(*auto dest: bisim1Val2D1*)
from *exec1* **have** $\tau\ Exec\ mover\ a\ P\ t\ (e1 \cdot compareAndSwap(D \cdot F, e2, e3))\ h\ (stk, loc, pc, None)\ (\ []\ @\ [v], xs, length\ (compE2\ e1) + 0, None)$
by(*rule CAS- $\tau\ ExecrI1$*)
also from *bisim2[of xs]*
have $\tau\ Exec\ mover\ a\ P\ t\ e2\ h\ (\ [], xs, 0, None)\ ([v'], xs, length\ (compE2\ e2), None)$
by(*auto dest: bisim1Val2D1*)
hence $\tau\ Exec\ mover\ a\ P\ t\ (e1 \cdot compareAndSwap(D \cdot F, e2, e3))\ h\ (\ []\ @\ [v], xs, length\ (compE2\ e1) + 0, None)\ ([v']\ @\ [v], xs, length\ (compE2\ e1) + length\ (compE2\ e2), None)$
by(*rule CAS- $\tau\ ExecrI2$*)
also (*rtranclp-trans*) **from** *IH3[OF red] bsok* **obtain** $pc''\ stk''\ loc''\ xcp''$
where $bisim': P, e3, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$
and $exec': ?exec\ ta\ e3\ e3\ E'\ h\ \ []\ xs\ 0\ None\ h'\ pc''\ stk''\ loc''\ xcp''$ **by** *auto*
have $?exec\ ta\ (e1 \cdot compareAndSwap(D \cdot F, e2, e3))\ (Val\ v \cdot compareAndSwap(D \cdot F, Val\ v', e3))\ (Val\ v \cdot compareAndSwap(D \cdot F, Val\ v', E'))\ h\ (\ []\ @\ [v', v], xs, length\ (compE2\ e1) + length\ (compE2\ e2) + 0)\ None\ h'\ (length\ (compE2\ e1) + length\ (compE2\ e2) + pc'')\ (stk''\ @\ [v', v])\ loc''\ xcp''$
proof(*cases $\tau\ move1\ P\ h\ (Val\ v \cdot compareAndSwap(D \cdot F, Val\ v', e3))$*)
case *True*
with $exec'\ \tau$ **have** [*simp*]: $h = h'$ **and** $e: sim\ move\ e3\ E'\ P\ t\ e3\ h\ (\ [], xs, 0, None)\ (stk'', loc'', pc'', xcp'')$ **by** *auto*
from e **have** $sim\ move\ (Val\ v \cdot compareAndSwap(D \cdot F, Val\ v', e3))\ (Val\ v \cdot compareAndSwap(D \cdot F, Val\ v', E'))\ P\ t\ (e1 \cdot compareAndSwap(D \cdot F, e2, e3))\ h\ (\ []\ @\ [v', v], xs, length\ (compE2\ e1) + length\ (compE2\ e2) + 0, None)\ (stk''\ @\ [v', v], loc'', length\ (compE2\ e1) + length\ (compE2\ e2) + pc'', xcp'')$
by(*fastforce dest: CAS- $\tau\ ExecrI3\ CAS\ \tau\ ExecrI3\ simp\ del: compE2.\ simp\ compEs2.\ simp$*)
with *True* **show** *?thesis by auto*
next
case *False*

with $exec' \tau$ **obtain** $pc' \ stk' \ loc' \ xcp'$
where $e: \tau Exec-mover-a \ P \ t \ e3 \ h \ (\ [], \ xs, \ 0, \ None) \ (stk', \ loc', \ pc', \ xcp')$
and $e': exec-move-a \ P \ t \ e3 \ h \ (stk', \ loc', \ pc', \ xcp') \ (extTA2JVM \ (compP2 \ P) \ ta) \ h' \ (stk'', \ loc'', \ pc'', \ xcp'')$
and $\tau': \neg \tau move2 \ (compP2 \ P) \ h \ stk' \ e3 \ pc' \ xcp'$
and $call: call1 \ e3 = None \vee no-call2 \ e3 \ 0 \vee pc' = 0 \wedge stk' = [] \wedge loc' = xs \wedge xcp' = None$ **by**
auto
from e **have** $\tau Exec-mover-a \ P \ t \ (e1 \cdot compareAndSwap(D \cdot F, \ e2, \ e3)) \ h \ (\ [] \ @ \ [v', \ v], \ xs, \ length \ (compE2 \ e1) + length \ (compE2 \ e2) + 0, \ None) \ (stk' \ @ \ [v', \ v], \ loc', \ length \ (compE2 \ e1) + length \ (compE2 \ e2) + pc', \ xcp')$ **by**(rule *CAS- τ ExecrI3*)
moreover from e' **have** $exec-move-a \ P \ t \ (e1 \cdot compareAndSwap(D \cdot F, \ e2, \ e3)) \ h \ (stk' \ @ \ [v', \ v], \ loc', \ length \ (compE2 \ e1) + length \ (compE2 \ e2) + pc', \ xcp') \ (extTA2JVM \ (compP2 \ P) \ ta) \ h' \ (stk'' \ @ \ [v', \ v], \ loc'', \ length \ (compE2 \ e1) + length \ (compE2 \ e2) + pc'', \ xcp'')$
by(rule *exec-move-CASI3*)
moreover from $e' \ \tau'$
have $\neg \tau move2 \ (compP2 \ P) \ h \ (stk' \ @ \ [v', \ v]) \ (e1 \cdot compareAndSwap(D \cdot F, \ e2, \ e3)) \ (length \ (compE2 \ e1) + length \ (compE2 \ e2) + pc') \ xcp'$
by(*auto simp add: τ instr-stk-drop-exec-move τ move2-iff*)
moreover have $call1 \ (e1' \cdot compareAndSwap(D \cdot F, \ e2, \ e3)) = call1 \ e3$ **by** *simp*
moreover have $no-call2 \ e3 \ 0 \implies no-call2 \ (e1 \cdot compareAndSwap(D \cdot F, \ e2, \ e3)) \ (length \ (compE2 \ e1) + length \ (compE2 \ e2))$
by(*auto simp add: no-call2-def*)
ultimately show *?thesis using False call*
by(*auto simp del: split-paired-Ex call1.simps calls1.simps*) **blast**
qed
moreover from *bisim'*
have $P, e1 \cdot compareAndSwap(D \cdot F, \ e2, \ e3), h' \vdash (Val \ v \cdot compareAndSwap(D \cdot F, \ Val \ v', \ E'), \ xs') \leftrightarrow ((stk'' \ @ \ [v', \ v]), \ loc'', \ length \ (compE2 \ e1) + length \ (compE2 \ e2) + pc'', \ xcp'')$
by(rule *bisim1-bisims1.bisim1CAS3*)
moreover from *bisim1* **have** $pc \neq length \ (compE2 \ e1) + length \ (compE2 \ e2) \implies no-call2 \ (e1 \cdot compareAndSwap(D \cdot F, \ e2, \ e3)) \ pc$
by(*auto simp add: no-call2-def dest: bisim-Val-pc-not-Invoke bisim1-pc-length-compE2*)
ultimately show *?thesis using $\tau exec1$*
apply(*auto simp del: split-paired-Ex call1.simps calls1.simps split: if-split-asm split del: if-split*)
apply(*blast intro: $\tau Exec-mover-trans$ fastforce elim!: $\tau Exec-mover-trans$ simp del: split-paired-Ex call1.simps calls1.simps*)
done
next
case (*CAS1Null v v'*)
note [*simp*] = $\langle e1' = null \rangle \langle e' = THROW \ NullPointer \rangle \langle e2 = Val \ v \rangle \langle xs' = xs \rangle \langle ta = \varepsilon \rangle \langle h' = h \rangle \langle e3 = Val \ v' \rangle$
have $\tau: \neg \tau move1 \ P \ h \ (AAss \ null \ (Val \ v) \ (Val \ v'))$ **by**(*auto simp add: $\tau move1$.simps $\tau moves1$.simps*)
from *bisim1* **have** $s: xcp = None \ xs = loc$
and $\tau Exec-mover-a \ P \ t \ e1 \ h \ (stk, \ loc, \ pc, \ xcp) \ (\ [] \ @ \ [Null], \ loc, \ length \ (compE2 \ e1) + 0, \ None)$
by(*auto dest: bisim1Val2D1*)
hence $\tau Exec-mover-a \ P \ t \ (e1 \cdot compareAndSwap(D \cdot F, \ e2, \ e3)) \ h \ (stk, \ loc, \ pc, \ xcp) \ (\ [] \ @ \ [Null], \ loc, \ length \ (compE2 \ e1) + 0, \ None)$
by-(rule *CAS- τ ExecrI1*)
also from *bisim2*[of *loc*] **have** $\tau Exec-mover-a \ P \ t \ e2 \ h \ (\ [], \ loc, \ 0, \ None) \ ([v], \ loc, \ length \ (compE2 \ e2) + 0, \ None)$
by(*auto dest: bisim1Val2D1*)
hence $\tau Exec-mover-a \ P \ t \ (e1 \cdot compareAndSwap(D \cdot F, \ e2, \ e3)) \ h \ (\ [] \ @ \ [Null], \ loc, \ length \ (compE2 \ e1) + 0, \ None) \ ([v] \ @ \ [Null], \ loc, \ length \ (compE2 \ e1) + (length \ (compE2 \ e2) + 0), \ None)$
by(rule *CAS- τ ExecrI2*)

also (*rtranclp-trans*) **have** $[v] @ [Null] = [] @ [v, Null]$ **by** *simp*
also note *add.assoc[symmetric]*
also from *bisim3[of loc]* **have** $\tau Exec\text{-mover-a } P t e3 h ([], loc, 0, None) ([v'], loc, length (compE2 e3), None)$
by(*auto dest: bisim1Val2D1*)
hence $\tau Exec\text{-mover-a } P t (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) h ([] @ [v, Null], loc, length (compE2 e1) + length (compE2 e2) + 0, None) ([v'] @ [v, Null], loc, length (compE2 e1) + length (compE2 e2) + length (compE2 e3), None)$
by(*rule CAS- τ ExecrI3*)
also (*rtranclp-trans*)
have $exec\text{-move-a } P t (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) h ([v', v, Null], loc, length (compE2 e1) + length (compE2 e2) + length (compE2 e3), None) \varepsilon$
 $h ([v', v, Null], loc, length (compE2 e1) + length (compE2 e2) + length (compE2 e3), [addr\text{-of-sys-xcpt } NullPointer])$
unfolding *exec-move-def* **by**-(*rule exec-instr, auto simp add: is-Ref-def*)
moreover have $\tau move2 (compP2 P) h [v', v, Null] (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) (length (compE2 e1) + length (compE2 e2) + length (compE2 e3)) None \implies False$
by(*simp add: $\tau move2\text{-iff}$*)
moreover
have $P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h' \vdash (THROW NullPointer, loc) \leftrightarrow ([v', v, Null], loc, length (compE2 e1) + length (compE2 e2) + length (compE2 e3), [addr\text{-of-sys-xcpt } NullPointer])$
by(*rule bisim1-bisims1.bisim1CASFail*)
ultimately show *?thesis* **using** *s τ* **by**(*auto simp add: $\tau move1.simps$*) *blast*
next
case (*Red1CASSucceed a v v'*)
hence $[simp]: e1' = addr a e' = true e2 = Val v$
 $ta = \{\{ReadMem a (CField D F) v, WriteMem a (CField D F) v'\} xs' = xs e3 = Val v'$
and *read: heap-read h a (CField D F) v*
and *write: heap-write h a (CField D F) v' h' by auto*
have $\tau: \neg \tau move1 P h (CompareAndSwap (addr a) D F (Val v) (Val v'))$ **by**(*auto simp add: $\tau move1.simps \tau moves1.simps$*)
from *bisim1* **have** $s: xcp = None xs = loc$
and $\tau Exec\text{-mover-a } P t e1 h (stk, loc, pc, xcp) ([] @ [Addr a], loc, length (compE2 e1) + 0, None)$
by(*auto dest: bisim1Val2D1*)
hence $\tau Exec\text{-mover-a } P t (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) h (stk, loc, pc, xcp) ([] @ [Addr a], loc, length (compE2 e1) + 0, None)$
by-(*rule CAS- τ ExecrI1*)
also from *bisim2[of loc]*
have $\tau Exec\text{-mover-a } P t e2 h ([], loc, 0, None) ([v], loc, length (compE2 e2) + 0, None)$
by(*auto dest: bisim1Val2D1*)
hence $\tau Exec\text{-mover-a } P t (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) h ([] @ [Addr a], loc, length (compE2 e1) + 0, None) ([v] @ [Addr a], loc, length (compE2 e1) + (length (compE2 e2) + 0), None)$
by(*rule CAS- τ ExecrI2*)
also (*rtranclp-trans*) **have** $[v] @ [Addr a] = [] @ [v, Addr a]$ **by** *simp*
also note *add.assoc[symmetric]*
also from *bisim3[of loc]* **have** $\tau Exec\text{-mover-a } P t e3 h ([], loc, 0, None) ([v'], loc, length (compE2 e3), None)$
by(*auto dest: bisim1Val2D1*)
hence $\tau Exec\text{-mover-a } P t (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) h ([] @ [v, Addr a], loc, length (compE2 e1) + length (compE2 e2) + 0, None) ([v'] @ [v, Addr a], loc, length (compE2 e1) + length (compE2 e2) + length (compE2 e3), None)$
by(*rule CAS- τ ExecrI3*)
also (*rtranclp-trans*) **from** *read write*
have $exec\text{-move-a } P t (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) h ([v', v, Addr a], loc, length (compE2$

$e1) + \text{length}(\text{compE2 } e2) + \text{length}(\text{compE2 } e3), \text{None})$
 $\{ \text{ReadMem } a \text{ (CField } D \ F) \ v, \text{WriteMem } a \text{ (CField } D \ F) \ v' \}$
 $h' ([\text{Bool True}], \text{loc}, \text{Suc}(\text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2) +$
 $\text{length}(\text{compE2 } e3)), \text{None})$
unfolding *exec-move-def* **by**–(rule *exec-instr*, auto simp add: *compP2-def is-Ref-def*)
moreover have $\tau \text{move2}(\text{compP2 } P) \ h \ [v', v, \text{Addr } a] \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ (\text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2) + \text{length}(\text{compE2 } e3)) \ \text{None} \implies \text{False}$
by(simp add: $\tau \text{move2-iff}$)
moreover
have $P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), h' \vdash (\text{true}, \text{loc}) \leftrightarrow ([\text{Bool True}], \text{loc}, \text{length}(\text{compE2 } (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3))), \text{None})$
by(rule *bisim1Val2*) simp
ultimately show *?thesis* **using** $s \ \tau$ **by**(auto simp add: *ta-upd-simps*) blast
next
case (*Red1CASFail* $a \ v'' \ v \ v'$)
hence $[\text{simp}]: e1' = \text{addr } a \ e' = \text{false} \ e2 = \text{Val } v \ h' = h$
 $ta = \{ \text{ReadMem } a \text{ (CField } D \ F) \ v' \} \ xs' = xs \ e3 = \text{Val } v'$
and *read: heap-read* $h \ a \ \text{(CField } D \ F) \ v'' \ v \neq v''$ **by** *auto*
have $\tau: \neg \tau \text{move1 } P \ h \ (\text{CompareAndSwap}(\text{addr } a) \ D \ F \ (\text{Val } v) \ (\text{Val } v'))$ **by**(auto simp add: $\tau \text{move1.simps} \ \tau \text{moves1.simps}$)
from *bisim1* **have** $s: xcp = \text{None} \ xs = \text{loc}$
and $\tau \text{Exec-mover-a } P \ t \ e1 \ h \ (\text{stk}, \text{loc}, \text{pc}, xcp) \ ([\] \ @ \ [\text{Addr } a], \text{loc}, \text{length}(\text{compE2 } e1) + 0, \text{None})$
by(auto dest: *bisim1Val2D1*)
hence $\tau \text{Exec-mover-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ h \ (\text{stk}, \text{loc}, \text{pc}, xcp) \ ([\] \ @ \ [\text{Addr } a], \text{loc}, \text{length}(\text{compE2 } e1) + 0, \text{None})$
by–(rule *CAS- τ ExecrI1*)
also from *bisim2*[of *loc*]
have $\tau \text{Exec-mover-a } P \ t \ e2 \ h \ ([\], \text{loc}, 0, \text{None}) \ ([v], \text{loc}, \text{length}(\text{compE2 } e2) + 0, \text{None})$
by(auto dest: *bisim1Val2D1*)
hence $\tau \text{Exec-mover-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ h \ ([\] \ @ \ [\text{Addr } a], \text{loc}, \text{length}(\text{compE2 } e1) + 0, \text{None}) \ ([v] \ @ \ [\text{Addr } a], \text{loc}, \text{length}(\text{compE2 } e1) + (\text{length}(\text{compE2 } e2) + 0), \text{None})$
by(rule *CAS- τ ExecrI2*)
also (*rtranclp-trans*) **have** $[v] \ @ \ [\text{Addr } a] = [\] \ @ \ [v, \text{Addr } a]$ **by** *simp*
also note *add.assoc[symmetric]*
also from *bisim3*[of *loc*] **have** $\tau \text{Exec-mover-a } P \ t \ e3 \ h \ ([\], \text{loc}, 0, \text{None}) \ ([v'], \text{loc}, \text{length}(\text{compE2 } e3), \text{None})$
by(auto dest: *bisim1Val2D1*)
hence $\tau \text{Exec-mover-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ h \ ([\] \ @ \ [v, \text{Addr } a], \text{loc}, \text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2) + 0, \text{None}) \ ([v'] \ @ \ [v, \text{Addr } a], \text{loc}, \text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2) + \text{length}(\text{compE2 } e3), \text{None})$
by(rule *CAS- τ ExecrI3*)
also (*rtranclp-trans*) **from** *read*
have *exec-move-a* $P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ h \ ([v', v, \text{Addr } a], \text{loc}, \text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2) + \text{length}(\text{compE2 } e3), \text{None})$
 $\{ \text{ReadMem } a \text{ (CField } D \ F) \ v'' \}$
 $h \ ([\text{Bool False}], \text{loc}, \text{Suc}(\text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2) +$
 $\text{length}(\text{compE2 } e3)), \text{None})$
unfolding *exec-move-def* **by**–(rule *exec-instr*, auto simp add: *compP2-def is-Ref-def*)
moreover have $\tau \text{move2}(\text{compP2 } P) \ h \ [v', v, \text{Addr } a] \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ (\text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2) + \text{length}(\text{compE2 } e3)) \ \text{None} \implies \text{False}$
by(simp add: $\tau \text{move2-iff}$)
moreover
have $P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), h \vdash (\text{false}, \text{loc}) \leftrightarrow ([\text{Bool False}], \text{loc}, \text{length}(\text{compE2 } (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3))), \text{None})$

by(rule *bisim1Val2*) *simp*
ultimately show *?thesis using s* τ **by**(*auto simp add: ta-upd-simps*)*blast*
next
case (*CAS1Throw a*)
hence [*simp*]: $e1' = \text{Throw } a \text{ ta} = \varepsilon \ e' = \text{Throw } a \ h' = h \ xs' = xs$ **by** *auto*
have τ : $\tau \text{move1 } P \ h \ (\text{Throw } a \cdot \text{compareAndSwap}(D \cdot F, e2, e3))$ **by**(rule $\tau \text{move1CASThrow1}$)
from *bisim1* **have** $xcp = [a] \vee xcp = \text{None}$ **by**(*auto dest: bisim1-ThrowD*)
thus *?thesis*
proof
assume [*simp*]: $xcp = [a]$
with *bisim1* **have** $P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, xcp)$
by(*auto intro: bisim1-bisims1.intros*)
thus *?thesis using* τ **by**(*fastforce*)
next
assume [*simp*]: $xcp = \text{None}$
with *bisim1* **obtain** pc' **where** $\tau \text{Exec-mover-a } P \ t \ e1 \ h \ (stk, loc, pc, \text{None}) \ ([\text{Addr } a], loc, pc', [a])$
and $bisim': P, e1, h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], loc, pc', [a])$
and [*simp*]: $xs = loc$
by(*auto dest: bisim1-Throw- τ Exec-mover*)
hence $\tau \text{Exec-mover-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ h \ (stk, loc, pc, \text{None}) \ ([\text{Addr } a], loc, pc', [a])$
by-(rule *CAS- τ ExecrI1*)
moreover from *bisim'*
have $P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], loc, pc', [a])$
by(*auto intro: bisim1-bisims1.bisim1CASThrow1*)
ultimately show *?thesis using* τ **by** *auto*
qed
next
case (*CAS1Throw2 v ad*)
note [*simp*] = $\langle e1' = \text{Val } v \rangle \langle e2 = \text{Throw } ad \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw } ad \rangle \langle h' = h \rangle \langle xs' = xs \rangle$
from *bisim1* **have** $s: xcp = \text{None} \ xs = loc$
and $\tau \text{Exec-mover-a } P \ t \ e1 \ h \ (stk, loc, pc, xcp) \ ([v], loc, \text{length}(\text{compE2 } e1), \text{None})$
by(*auto dest: bisim1Val2D1*)
hence $\tau \text{Exec-mover-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, \text{Throw } ad, e3)) \ h \ (stk, loc, pc, xcp) \ ([v], loc, \text{length}(\text{compE2 } e1), \text{None})$
by-(rule *CAS- τ ExecrI1*)
also have $\tau \text{Exec-mover-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, \text{Throw } ad, e3)) \ h \ ([v], loc, \text{length}(\text{compE2 } e1), \text{None}) \ ([\text{Addr } ad, v], loc, \text{Suc}(\text{length}(\text{compE2 } e1)), [ad])$
by(rule $\tau \text{Execr2step}$)(*auto simp add: exec-move-def exec-meth-instr $\tau \text{move2-iff}$ $\tau \text{move1.simps}$ $\tau \text{moves1.simps}$*)
also (*rtranclp-trans*)
have $P, e1 \cdot \text{compareAndSwap}(D \cdot F, \text{Throw } ad, e3), h \vdash (\text{Throw } ad, loc) \leftrightarrow ([\text{Addr } ad] @ [v], loc, (\text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } (\text{addr } ad))), [ad])$
by(rule *bisim1CASThrow2[OF bisim1Throw2]*)
moreover have $\tau \text{move1 } P \ h \ (e1' \cdot \text{compareAndSwap}(D \cdot F, \text{Throw } ad, e3))$ **by**(*auto intro: $\tau \text{move1CASThrow2}$*)
ultimately show *?thesis using* s **by** *auto*
next
case (*CAS1Throw3 v v' ad*)
note [*simp*] = $\langle e1' = \text{Val } v \rangle \langle e2 = \text{Val } v' \rangle \langle e3 = \text{Throw } ad \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw } ad \rangle \langle h' = h \rangle \langle xs' = xs \rangle$
from *bisim1* **have** $s: xcp = \text{None} \ xs = loc$
and $\tau \text{Exec-mover-a } P \ t \ e1 \ h \ (stk, loc, pc, xcp) \ ([v], loc, \text{length}(\text{compE2 } e1), \text{None})$
by(*auto dest: bisim1Val2D1*)

hence $\tau Exec\text{-mover-a } P t (e1 \cdot compareAndSwap(D \cdot F, e2, Throw ad)) h (stk, loc, pc, xcp) ([v], loc, length (compE2 e1), None)$
by $-(rule \text{CAS-}\tau ExecrI1)$
also from $bisim2[of loc] \text{ have } \tau Exec\text{-mover-a } P t e2 h ([], loc, 0, None) ([v'], loc, length (compE2 e2), None)$
by $(auto \text{ dest: } bisim1Val2D1)$
from $CAS\text{-}\tau ExecrI2[OF \text{ this, of } e1 D F e3 v]$
have $\tau Exec\text{-mover-a } P t (e1 \cdot compareAndSwap(D \cdot F, e2, Throw ad)) h ([v], loc, length (compE2 e1), None) ([v', v], loc, length (compE2 e1) + length (compE2 e2), None) \text{ by } simp$
also $(rtranclp\text{-trans})$
have $\tau Exec\text{-mover-a } P t (e1 \cdot compareAndSwap(D \cdot F, e2, Throw ad)) h ([v', v], loc, length (compE2 e1) + length (compE2 e2), None) ([Addr ad, v', v], loc, Suc (length (compE2 e1) + length (compE2 e2)), [ad])$
by $(rule \tau Execr2step)(auto \text{ simp add: } exec\text{-move-def } exec\text{-meth-instr } \tau move2\text{-iff } \tau move1.simps \tau moves1.simps)$
also $(rtranclp\text{-trans})$
have $P, e1 \cdot compareAndSwap(D \cdot F, e2, Throw ad), h \vdash (Throw ad, loc) \leftrightarrow ([Addr ad] @ [v', v], loc, (length (compE2 e1) + length (compE2 e2) + length (compE2 (addr ad))), [ad])$
by $(rule \text{bisim1CASThrow3}[OF \text{ bisim1Throw2}])$
moreover have $\tau move1 P h (Val v \cdot compareAndSwap(D \cdot F, Val v', Throw ad)) \text{ by } (auto \text{ intro: } \tau move1CASThrow3)$
ultimately show ?thesis using s by auto
qed
next
case $(bisim1CAS2 e2 n e2' xs stk loc pc xcp e1 e3 D F v1)$
note $IH2 = bisim1CAS2.IH(2)$
note $IH3 = bisim1CAS2.IH(6)$
note $bisim2 = \langle P, e2, h \vdash (e2', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
note $bisim1 = \langle \bigwedge xs. P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, None) \rangle$
note $bisim3 = \langle \bigwedge xs. P, e3, h \vdash (e3, xs) \leftrightarrow ([], xs, 0, None) \rangle$
note $bsok = \langle bsok (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) n \rangle$
from $\langle True, P, t \vdash 1 \langle Val v1 \cdot compareAndSwap(D \cdot F, e2', e3), (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle \text{ show ?case}$
proof cases
case $(CAS1Red2 E')$
note $[simp] = \langle e' = Val v1 \cdot compareAndSwap(D \cdot F, E', e3) \rangle$
and $red = \langle True, P, t \vdash 1 \langle e2', (h, xs) \rangle -ta \rightarrow \langle E', (h', xs') \rangle \rangle$
from $red \text{ have } \tau: \tau move1 P h (Val v1 \cdot compareAndSwap(D \cdot F, e2', e3)) = \tau move1 P h e2' \text{ by } (auto \text{ simp add: } \tau move1.simps \tau moves1.simps)$
from $IH2[OF red] bsok \text{ obtain } pc'' stk'' loc'' xcp''$
where $bisim': P, e2, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$
and $exec': ?exec \text{ ta } e2 e2' E' h stk loc pc xcp h' pc'' stk'' loc'' xcp'' \text{ by } auto$
have $?exec \text{ ta } (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) (Val v1 \cdot compareAndSwap(D \cdot F, e2', e3)) (Val v1 \cdot compareAndSwap(D \cdot F, E', e3)) h (stk @ [v1]) loc (length (compE2 e1) + pc) xcp h' (length (compE2 e1) + pc'') (stk'' @ [v1]) loc'' xcp''$
proof $(cases \tau move1 P h (Val v1 \cdot compareAndSwap(D \cdot F, e2', e3)))$
case $True$
with $exec' \tau \text{ have } [simp]: h = h' \text{ and } e: \text{sim-move } e2' E' P t e2 h (stk, loc, pc, xcp) (stk'', loc'', pc'', xcp'') \text{ by } auto$
from $e \text{ have } \text{sim-move } (Val v1 \cdot compareAndSwap(D \cdot F, e2', e3)) (Val v1 \cdot compareAndSwap(D \cdot F, E', e3)) P t (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) h (stk @ [v1], loc, length (compE2 e1) + pc, xcp) (stk'' @ [v1], loc'', length (compE2 e1) + pc'', xcp'')$
by $(fastforce \text{ dest: } CAS\text{-}\tau ExecrI2 \text{ CAS-}\tau ExecrI2 \text{ simp del: } compE2.simps \text{ compEs2.simps})$
with $True \text{ show ?thesis by } auto$
next

case *False*
with *exec'* τ **obtain** *pc' stk' loc' xcp'*
where *e*: $\tau \text{Exec-mover-a } P \ t \ e2 \ h \ (stk, \ loc, \ pc, \ xcp) \ (stk', \ loc', \ pc', \ xcp')$
and *e'*: $\text{exec-move-a } P \ t \ e2 \ h \ (stk', \ loc', \ pc', \ xcp') \ (\text{extTA2JVM} \ (\text{compP2 } P) \ ta) \ h' \ (stk'', \ loc'', \ pc'', \ xcp'')$
and τ' : $\neg \tau \text{move2} \ (\text{compP2 } P) \ h \ stk' \ e2 \ pc' \ xcp'$
and *call*: $\text{call1 } e2' = \text{None} \vee \text{no-call2 } e2 \ pc \vee pc' = pc \wedge stk' = stk \wedge loc' = loc \wedge xcp' = xcp$
by *auto*
from *e* **have** $\tau \text{Exec-mover-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, \ e2, \ e3)) \ h \ (stk \ @ \ [v1], \ loc, \ \text{length} \ (\text{compE2 } e1) + pc, \ xcp) \ (stk' \ @ \ [v1], \ loc', \ \text{length} \ (\text{compE2 } e1) + pc', \ xcp')$ **by**(*rule CAS- τ ExecrI2*)
moreover from *e'* **have** $\text{exec-move-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, \ e2, \ e3)) \ h \ (stk' \ @ \ [v1], \ loc', \ \text{length} \ (\text{compE2 } e1) + pc', \ xcp')$ (*extTA2JVM (compP2 P) ta*) $h' \ (stk'' \ @ \ [v1], \ loc'', \ \text{length} \ (\text{compE2 } e1) + pc'', \ xcp'')$
by(*rule exec-move-CASI2*)
moreover from *e'* **have** $pc' < \text{length} \ (\text{compE2 } e2)$ **by**(*auto elim: exec-meth.cases*)
with $\tau' \ e'$ **have** $\neg \tau \text{move2} \ (\text{compP2 } P) \ h \ (stk' \ @ \ [v1]) \ (e1 \cdot \text{compareAndSwap}(D \cdot F, \ e2, \ e3)) \ (\text{length} \ (\text{compE2 } e1) + pc') \ xcp'$
by(*auto simp add: τ instr-stk-drop-exec-move τ move2-iff*)
moreover from *red* **have** $\text{call1} \ (\text{Val } v1 \cdot \text{compareAndSwap}(D \cdot F, \ e2', \ e3)) = \text{call1 } e2'$ **by** *auto*
moreover have $\text{no-call2 } e2 \ pc \implies \text{no-call2} \ (e1 \cdot \text{compareAndSwap}(D \cdot F, \ e2, \ e3)) \ (\text{length} \ (\text{compE2 } e1) + pc)$
by(*auto simp add: no-call2-def*)
ultimately show *?thesis* **using** *False call* **by**(*auto simp del: split-paired-Ex call1.simps calls1.simps*)

qed
moreover from *bisim'*
have $P, e1 \cdot \text{compareAndSwap}(D \cdot F, \ e2, \ e3), h' \vdash \ (\text{Val } v1 \cdot \text{compareAndSwap}(D \cdot F, \ E', \ e3), \ xs') \leftrightarrow \ ((stk'' \ @ \ [v1]), \ loc'', \ \text{length} \ (\text{compE2 } e1) + pc'', \ xcp'')$
by(*rule bisim1-bisims1.bisim1CAS2*)
ultimately show *?thesis*
apply(*auto simp del: split-paired-Ex call1.simps calls1.simps split: if-split-asm split del: if-split*)
apply(*blast intro: τ Exec-mover-trans*)
done
next
case (*CAS1Red3 E' v'*)
note [*simp*] = $\langle e2' = \text{Val } v' \rangle \langle e' = \text{Val } v1 \cdot \text{compareAndSwap}(D \cdot F, \ \text{Val } v', \ E') \rangle$
and *red* = $\langle \text{True}, P, t \vdash 1 \ \langle e3, (h, xs) \rangle \text{---} \text{ta} \rightarrow \langle E', (h', xs') \rangle \rangle$
from *red* **have** $\tau: \tau \text{move1 } P \ h \ (\text{Val } v1 \cdot \text{compareAndSwap}(D \cdot F, \ \text{Val } v', \ e3)) = \tau \text{move1 } P \ h \ e3$
by(*auto simp add: τ move1.simps τ moves1.simps*)
from *bisim2* **have** $s: xcp = \text{None} \ xs = loc$
and *exec1*: $\tau \text{Exec-mover-a } P \ t \ e2 \ h \ (stk, \ loc, \ pc, \ xcp) \ ([v'], \ xs, \ \text{length} \ (\text{compE2 } e2), \ \text{None})$
by(*auto dest: bisim1Val2D1*)
hence $\tau \text{Exec-mover-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, \ e2, \ e3)) \ h \ (stk \ @ \ [v1], \ loc, \ \text{length} \ (\text{compE2 } e1) + pc, \ xcp) \ ([v'] \ @ \ [v1], \ xs, \ \text{length} \ (\text{compE2 } e1) + \text{length} \ (\text{compE2 } e2), \ \text{None})$
by-(*rule CAS- τ ExecrI2*)
moreover from *IH3[OF red] bsok* **obtain** *pc'' stk'' loc'' xcp''*
where *bisim'*: $P, e3, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$
and *exec'*: $? \text{exec } ta \ e3 \ e3 \ E' \ h \ [] \ xs \ 0 \ \text{None} \ h' \ pc'' \ stk'' \ loc'' \ xcp''$ **by** *auto*
have $? \text{exec } ta \ (e1 \cdot \text{compareAndSwap}(D \cdot F, \ e2, \ e3)) \ (\text{Val } v1 \cdot \text{compareAndSwap}(D \cdot F, \ \text{Val } v', \ e3)) \ (\text{Val } v1 \ [\ \text{Val } v'] := E') \ h \ ([] \ @ \ [v', \ v1]) \ xs \ (\text{length} \ (\text{compE2 } e1) + \text{length} \ (\text{compE2 } e2) + 0) \ \text{None} \ h' \ (\text{length} \ (\text{compE2 } e1) + \text{length} \ (\text{compE2 } e2) + pc'') \ (stk'' \ @ \ [v', \ v1]) \ loc'' \ xcp''$
proof(*cases $\tau \text{move1 } P \ h \ (\text{Val } v1 \cdot \text{compareAndSwap}(D \cdot F, \ \text{Val } v', \ e3))$*)
case *True*
with *exec'* τ **have** [*simp*]: $h = h'$

and e : *sim-move* $e3 E' P t e3 h (\ [], xs, 0, None) (stk'', loc'', pc'', xcp'')$ **by** *auto*
from e **have** *sim-move* $(Val v1 \cdot compareAndSwap(D \cdot F, Val v', e3)) (Val v1 \cdot compareAndSwap(D \cdot F, Val v', E')) P t (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) h (\ [] @ [v', v1], xs, length (compE2 e1) + length (compE2 e2) + 0, None) (stk'' @ [v', v1], loc'', length (compE2 e1) + length (compE2 e2) + pc'', xcp'')$
by(*fastforce dest: CAS- τ ExecI3 CAS- τ ExecrI3 simp del: compE2.simps compEs2.simps*)
with *True show ?thesis by auto*
next
case *False*
with *exec' τ obtain pc' stk' loc' xcp'*
where e : $\tau Exec-mover-a P t e3 h (\ [], xs, 0, None) (stk', loc', pc', xcp')$
and e' : *exec-move-a* $P t e3 h (stk', loc', pc', xcp') (extTA2JVM (compP2 P) ta) h' (stk'', loc'', pc'', xcp'')$
and τ' : $\neg \tau move2 (compP2 P) h stk' e3 pc' xcp'$
and *call*: $call1 e3 = None \vee no-call2 e3 0 \vee pc' = 0 \wedge stk' = [] \wedge loc' = xs \wedge xcp' = None$ **by** *auto*
from e **have** $\tau Exec-mover-a P t (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) h (\ [] @ [v', v1], xs, length (compE2 e1) + length (compE2 e2) + 0, None) (stk' @ [v', v1], loc', length (compE2 e1) + length (compE2 e2) + pc', xcp')$ **by**(*rule CAS- τ ExecrI3*)
moreover from e' **have** *exec-move-a* $P t (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) h (stk' @ [v', v1], loc', length (compE2 e1) + length (compE2 e2) + pc', xcp') (extTA2JVM (compP2 P) ta) h' (stk'' @ [v', v1], loc'', length (compE2 e1) + length (compE2 e2) + pc'', xcp'')$
by(*rule exec-move-CASI3*)
moreover from $e' \tau'$ **have** $\neg \tau move2 (compP2 P) h (stk' @ [v', v1]) (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) (length (compE2 e1) + length (compE2 e2) + pc') xcp'$
by(*auto simp add: $\tau instr-stk-drop-exec-move \tau move2-iff$*)
moreover from *red* **have** $call1 (Val v1 \cdot compareAndSwap(D \cdot F, Val v', e3)) = call1 e3$ **by** *auto*
moreover have $no-call2 e3 0 \implies no-call2 (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) (length (compE2 e1) + length (compE2 e2))$
by(*auto simp add: no-call2-def*)
ultimately show *?thesis using False call by(auto simp del: split-paired-Ex call1.simps calls1.simps)*
blast
qed
moreover from *bisim'*
have $P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h' \vdash (Val v1 \cdot compareAndSwap(D \cdot F, Val v', E'), xs') \leftrightarrow ((stk'' @ [v', v1]), loc'', length (compE2 e1) + length (compE2 e2) + pc'', xcp'')$
by(*rule bisim1-bisims1.bisim1CAS3*)
moreover from *bisim2* **have** $pc \neq length (compE2 e2) \longrightarrow no-call2 (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) (length (compE2 e1) + pc)$
by(*auto simp add: no-call2-def dest: bisim-Val-pc-not-Invoke bisim1-pc-length-compE2*)
ultimately show *?thesis using $\tau exec1 s$*
apply(*auto simp del: split-paired-Ex call1.simps calls1.simps split: if-split-asm split del: if-split*)
apply(*blast intro: $\tau Exec-mover-trans|fastforce elim!: \tau Exec-mover-trans simp del: split-paired-Ex call1.simps calls1.simps$*)
done
next
case (*CAS1Null v v'*)
note [*simp*] = $\langle v1 = Null \rangle \langle e' = THROW NullPointer \rangle \langle e2' = Val v \rangle \langle xs' = xs \rangle \langle ta = \varepsilon \rangle \langle h' = h \rangle \langle e3 = Val v' \rangle$
have $\tau: \neg \tau move1 P h (CompareAndSwap null D F (Val v) (Val v'))$ **by**(*auto simp add: $\tau move1.simps \tau moves1.simps$*)
from *bisim2* **have** $s: xcp = None \ xs = loc$
and $\tau Exec-mover-a P t e2 h (stk, loc, pc, xcp) ([v], loc, length (compE2 e2), None)$
by(*auto dest: bisim1Val2D1*)

hence $\tauExec\text{-mover-a } P t (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) h (stk @ [Null], loc, length (compE2 e1) + pc, xcp) ([v] @ [Null], loc, length (compE2 e1) + length (compE2 e2), None)$
by–(rule CAS- $\tauExecrI2$)
hence $\tauExec\text{-mover-a } P t (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) h (stk @ [Null], loc, length (compE2 e1) + pc, xcp) ([v] @ [v, Null], loc, length (compE2 e1) + length (compE2 e2) + 0, None)$ **by** *simp*
also from *bisim3[of loc]* **have** $\tauExec\text{-mover-a } P t e3 h ([v], loc, 0, None) ([v'], loc, length (compE2 e3), None)$
by(*auto dest: bisim1Val2D1*)
hence $\tauExec\text{-mover-a } P t (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) h ([v] @ [v, Null], loc, length (compE2 e1) + length (compE2 e2) + 0, None) ([v'] @ [v, Null], loc, length (compE2 e1) + length (compE2 e2) + length (compE2 e3), None)$
by(rule CAS- $\tauExecrI3$)
also (*rtranclp-trans*)
have $exec\text{-move-a } P t (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) h ([v', v, Null], loc, length (compE2 e1) + length (compE2 e2) + length (compE2 e3), None) \varepsilon$
 $h ([v', v, Null], loc, length (compE2 e1) + length (compE2 e2) + length (compE2 e3), [addr\text{-of-sys-xcpt } NullPointer])$
unfolding *exec-move-def* **by**–(rule *exec-instr, auto simp add: is-Ref-def*)
moreover have $\tau move2 (compP2 P) h [v', v, Null] (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) (length (compE2 e1) + length (compE2 e2) + length (compE2 e3)) None \implies False$
by(*simp add: $\tau move2\text{-iff}$*)
moreover
have $P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h' \vdash (THROW NullPointer, loc) \leftrightarrow ([v', v, Null], loc, length (compE2 e1) + length (compE2 e2) + length (compE2 e3), [addr\text{-of-sys-xcpt } NullPointer])$
by(rule *bisim1-bisims1.bisim1CASFail*)
ultimately show *?thesis* **using** $s \tau$ **by** *auto blast*
next
case (*Red1CASSucceed a v v'*)
hence [*simp*]: $v1 = Addr a e' = true e2' = Val v$
 $ta = \{ReadMem a (CField D F) v, WriteMem a (CField D F) v'\} xs' = xs e3 = Val v'$
and *read: heap-read h a (CField D F) v*
and *write: heap-write h a (CField D F) v' h' by auto*
have $\tau: \neg \tau move1 P h (CompareAndSwap (addr a) D F (Val v) (Val v'))$ **by**(*auto simp add: $\tau move1.simps \tau moves1.simps$*)
from *bisim2* **have** $s: xcp = None xs = loc$
and $\tauExec\text{-mover-a } P t e2 h (stk, loc, pc, xcp) ([v], loc, length (compE2 e2), None)$
by(*auto dest: bisim1Val2D1*)
hence $\tauExec\text{-mover-a } P t (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) h (stk @ [Addr a], loc, length (compE2 e1) + pc, xcp) ([v] @ [Addr a], loc, length (compE2 e1) + length (compE2 e2), None)$
by–(rule CAS- $\tauExecrI2$)
hence $\tauExec\text{-mover-a } P t (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) h (stk @ [Addr a], loc, length (compE2 e1) + pc, xcp) ([v] @ [v, Addr a], loc, length (compE2 e1) + length (compE2 e2) + 0, None)$
by *simp*
also from *bisim3[of loc]* **have** $\tauExec\text{-mover-a } P t e3 h ([v], loc, 0, None) ([v'], loc, length (compE2 e3), None)$
by(*auto dest: bisim1Val2D1*)
hence $\tauExec\text{-mover-a } P t (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) h ([v] @ [v, Addr a], loc, length (compE2 e1) + length (compE2 e2) + 0, None) ([v'] @ [v, Addr a], loc, length (compE2 e1) + length (compE2 e2) + length (compE2 e3), None)$
by(rule CAS- $\tauExecrI3$)
also (*rtranclp-trans*) **from** *read write*
have $exec\text{-move-a } P t (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) h ([v', v, Addr a], loc, length (compE2 e1) + length (compE2 e2) + length (compE2 e3), None)$
 $\{ReadMem a (CField D F) v, WriteMem a (CField D F) v'\}$

$h' ([\text{Bool True}], \text{loc}, \text{Suc} (\text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2) + \text{length} (\text{compE2 } e3)), \text{None})$
unfolding *exec-move-def* **by**–(rule *exec-instr*, auto simp add: *compP2-def is-Ref-def*)
moreover have $\tau \text{move2} (\text{compP2 } P) h [v', v, \text{Addr } a] (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) (\text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2) + \text{length} (\text{compE2 } e3)) \text{None} \implies \text{False}$
by(simp add: $\tau \text{move2-iff}$)
moreover
have $P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), h' \vdash (\text{true}, \text{loc}) \leftrightarrow ([\text{Bool True}], \text{loc}, \text{length} (\text{compE2 } (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3))), \text{None})$
by(rule *bisim1Val2*) simp
ultimately show *?thesis* **using** $s \tau$ **by**(auto simp add: *ta-upd-simps*) *blast*
next
case (*Red1CASFail* $a v'' v v'$)
hence [simp]: $v1 = \text{Addr } a \ e' = \text{false} \ e2' = \text{Val } v \ h' = h$
 $ta = \{\text{ReadMem } a \ (\text{CField } D \ F) \ v''\} \ xs' = xs \ e3 = \text{Val } v'$
and read: *heap-read* $h a \ (\text{CField } D \ F) \ v'' \ v \neq v''$ **by** *auto*
have $\tau: \neg \tau \text{move1 } P h (\text{CompareAndSwap} (\text{addr } a) \ D \ F \ (\text{Val } v) \ (\text{Val } v'))$ **by**(auto simp add: $\tau \text{move1.simps} \ \tau \text{moves1.simps}$)
from *bisim2* **have** $s: xcp = \text{None} \ xs = \text{loc}$
and $\tau \text{Exec-mover-a } P \ t \ e2 \ h \ (\text{stk}, \text{loc}, \text{pc}, xcp) ([v], \text{loc}, \text{length} (\text{compE2 } e2), \text{None})$
by(auto dest: *bisim1Val2D1*)
hence $\tau \text{Exec-mover-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ h \ (\text{stk} \ @ \ [\text{Addr } a], \text{loc}, \text{length} (\text{compE2 } e1) + \text{pc}, xcp) ([v] \ @ \ [\text{Addr } a], \text{loc}, \text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2), \text{None})$
by–(rule *CAS- τ ExecrI2*)
hence $\tau \text{Exec-mover-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ h \ (\text{stk} \ @ \ [\text{Addr } a], \text{loc}, \text{length} (\text{compE2 } e1) + \text{pc}, xcp) ([\] \ @ \ [v, \text{Addr } a], \text{loc}, \text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2) + 0, \text{None})$
by *simp*
also from *bisim3[of loc]* **have** $\tau \text{Exec-mover-a } P \ t \ e3 \ h \ ([\], \text{loc}, 0, \text{None}) ([v'], \text{loc}, \text{length} (\text{compE2 } e3), \text{None})$
by(auto dest: *bisim1Val2D1*)
hence $\tau \text{Exec-mover-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ h \ ([\] \ @ \ [v, \text{Addr } a], \text{loc}, \text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2) + 0, \text{None}) ([v'] \ @ \ [v, \text{Addr } a], \text{loc}, \text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2) + \text{length} (\text{compE2 } e3), \text{None})$
by(rule *CAS- τ ExecrI3*)
also (*rtranclp-trans*) **from** *read*
have *exec-move-a* $P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ h \ ([v', v, \text{Addr } a], \text{loc}, \text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2) + \text{length} (\text{compE2 } e3), \text{None})$
 $\{\text{ReadMem } a \ (\text{CField } D \ F) \ v''\}$
 $h' ([\text{Bool False}], \text{loc}, \text{Suc} (\text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2) + \text{length} (\text{compE2 } e3)), \text{None})$
unfolding *exec-move-def* **by**–(rule *exec-instr*, auto simp add: *compP2-def is-Ref-def*)
moreover have $\tau \text{move2} (\text{compP2 } P) h [v', v, \text{Addr } a] (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) (\text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2) + \text{length} (\text{compE2 } e3)) \text{None} \implies \text{False}$
by(simp add: $\tau \text{move2-iff}$)
moreover
have $P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), h' \vdash (\text{false}, \text{loc}) \leftrightarrow ([\text{Bool False}], \text{loc}, \text{length} (\text{compE2 } (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3))), \text{None})$
by(rule *bisim1Val2*) simp
ultimately show *?thesis* **using** $s \tau$ **by**(auto simp add: *ta-upd-simps*) *blast*
next
case (*CAS1Throw2* ad)
note [simp] = $\langle e2' = \text{Throw } ad \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw } ad \rangle \langle h' = h \rangle \langle xs' = xs \rangle$
have $\tau: \tau \text{move1 } P h (\text{Val } v1 \cdot \text{compareAndSwap}(D \cdot F, \text{Throw } ad, e3))$ **by**(rule $\tau \text{move1CASThrow2}$)
from *bisim2* **have** $xcp = [ad] \vee xcp = \text{None}$ **by**(auto dest: *bisim1-ThrowD*)

```

thus ?thesis
proof
  assume [simp]: xcp = [ad]
  with bisim2
  have P, e1.compareAndSwap(D·F, e2, e3), h ⊢ (Throw ad, xs) ↔ (stk @ [v1], loc, length (compE2
e1) + pc, xcp)
    by(auto intro: bisim1-bisims1.intros)
  thus ?thesis using τ by(fastforce)
next
  assume [simp]: xcp = None
  with bisim2 obtain pc' where τExec-mover-a P t e2 h (stk, loc, pc, None) ([Addr ad], loc, pc',
[ad])
    and bisim': P, e2, h ⊢ (Throw ad, xs) ↔ ([Addr ad], loc, pc', [ad])
    and [simp]: xs = loc
    by(auto dest: bisim1-Throw-τExec-mover)
  hence τExec-mover-a P t (e1.compareAndSwap(D·F, e2, e3)) h (stk @ [v1], loc, length (compE2
e1) + pc, None) ([Addr ad] @ [v1], loc, length (compE2 e1) + pc', [ad])
    by-(rule CAS-τExecrI2)
  moreover from bisim'
  have P, e1.compareAndSwap(D·F, e2, e3), h ⊢ (Throw ad, xs) ↔ ([Addr ad] @ [v1], loc, length
(compE2 e1) + pc', [ad])
    by(rule bisim1-bisims1.bisim1CASThrow2)
  ultimately show ?thesis using τ by auto
qed
next
  case (CAS1Throw3 v' ad)
  note [simp] = ⟨e2' = Val v'⟩ ⟨e3 = Throw ad⟩ ⟨ta = ε⟩ ⟨e' = Throw ad⟩ ⟨h' = h⟩ ⟨xs' = xs⟩
  from bisim2 have s: xcp = None xs = loc
    and τExec-mover-a P t e2 h (stk, loc, pc, xcp) ([v'], loc, length (compE2 e2), None)
    by(auto dest: bisim1Val2D1)
  hence τExec-mover-a P t (e1.compareAndSwap(D·F, e2, Throw ad)) h (stk @ [v1], loc, length
(compE2 e1) + pc, xcp) ([v'] @ [v1], loc, length (compE2 e1) + length (compE2 e2), None)
    by-(rule CAS-τExecrI2)
  also have τExec-mover-a P t (e1.compareAndSwap(D·F, e2, Throw ad)) h ([v'] @ [v1], loc, length
(compE2 e1) + length (compE2 e2), None) ([Addr ad, v', v1], loc, Suc (length (compE2 e1) + length
(compE2 e2)), [ad])
    by(rule τExecr2step)(auto simp add: exec-move-def exec-meth-instr τmove2-iff τmove1.simps
τmoves1.simps)
  also (rtranclp-trans)
  have P, e1.compareAndSwap(D·F, e2, Throw ad), h ⊢ (Throw ad, loc) ↔ ([Addr ad] @ [v', v1],
loc, (length (compE2 e1) + length (compE2 e2) + length (compE2 (addr ad))), [ad])
    by(rule bisim1CASThrow3[OF bisim1Throw2])
  moreover have τmove1 P h (CompareAndSwap (Val v1) D F (Val v') (Throw ad)) by(auto intro:
τmove1CASThrow3)
  ultimately show ?thesis using s by auto
qed auto
next
  case (bisim1CAS3 e3 n e3' xs stk loc pc xcp e1 e2 D F v v')
  note IH3 = bisim1CAS3.IH(2)
  note bisim3 = ⟨P, e3, h ⊢ (e3', xs) ↔ (stk, loc, pc, xcp)⟩
  note bsok = ⟨bsok (e1.compareAndSwap(D·F, e2, e3)) n⟩
  from ⟨True, P, t ⊢ 1 ⟨Val v·compareAndSwap(D·F, Val v', e3'), (h, xs)⟩ -ta→ ⟨e', (h', xs')⟩⟩ show
?case
proof cases

```


case (*CAS1Red3 E'*)
note [*simp*] = $\langle e' = \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', E') \rangle$
and *red* = $\langle \text{True}, P, t \vdash 1 \langle e3', (h, xs) \rangle -ta \rightarrow \langle E', (h', xs') \rangle \rangle$
from *red* **have** $\tau: \tau \text{move1 } P \ h \ (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', e3')) = \tau \text{move1 } P \ h \ e3'$
by (*auto simp add: \tau move1.simps \tau moves1.simps*)
from *IH3[OF red] bsok* **obtain** *pc'' stk'' loc'' xcp''*
where *bisim'*: $P, e3, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$
and *exec'*: $?exec \ ta \ e3 \ e3' \ E' \ h \ stk \ loc \ pc \ xcp \ h' \ pc'' \ stk'' \ loc'' \ xcp''$ **by** *auto*
have *no-call2 e3 pc* \implies *no-call2 (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) (length (compE2 e1) + length (compE2 e2) + pc)*
by (*auto simp add: no-call2-def*)
hence $?exec \ ta \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', e3')) \ (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', E')) \ h \ (stk \ @ \ [v', v]) \ loc \ (length \ (compE2 \ e1) \ + \ length \ (compE2 \ e2) \ + \ pc) \ xcp \ h' \ (length \ (compE2 \ e1) \ + \ length \ (compE2 \ e2) \ + \ pc'') \ (stk'' \ @ \ [v', v]) \ loc'' \ xcp''$
using *exec' \tau*
apply (*cases \tau move1 P h (Val v \cdot compareAndSwap(D \cdot F, Val v', e3'))*)
apply (*auto*)
apply (*blast intro: CAS-\tau ExecrI3 CAS-\tau ExecI3 exec-move-CASI3*)
apply (*blast intro: CAS-\tau ExecrI3 CAS-\tau ExecI3 exec-move-CASI3*)
apply (*rule exI conjI CAS-\tau ExecrI3 exec-move-CASI3|assumption*) +
apply (*fastforce simp add: \tau instr-stk-drop-exec-move \tau move2-iff split: if-split-asm*)
apply (*rule exI conjI CAS-\tau ExecrI3 exec-move-CASI3|assumption*) +
apply (*fastforce simp add: \tau instr-stk-drop-exec-move \tau move2-iff split: if-split-asm*)
apply (*rule exI conjI CAS-\tau ExecrI3 exec-move-CASI3 rtranclp.rtrancl-refl|assumption*) +
apply (*fastforce simp add: \tau instr-stk-drop-exec-move \tau move2-iff split: if-split-asm*) +
done
moreover from *bisim'*
have $P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), h' \vdash (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', E'), xs') \leftrightarrow (stk'' \ @ \ [v', v], loc'', length \ (compE2 \ e1) \ + \ length \ (compE2 \ e2) \ + \ pc'', xcp'')$
by (*rule bisim1-bisims1.bisim1CAS3*)
ultimately show $?thesis$ **using** τ **by** *auto blast+*
next
case (*CAS1Null v''*)
note [*simp*] = $\langle v = \text{Null} \rangle \langle e' = \text{THROW } \text{NullPointer} \rangle \langle xs' = xs \rangle \langle ta = \varepsilon \rangle \langle h' = h \rangle \langle e3' = \text{Val } v'' \rangle$
have $\tau: \neg \tau \text{move1 } P \ h \ (\text{CompareAndSwap } \text{null } D \ F \ (\text{Val } v') \ (\text{Val } v''))$ **by** (*auto simp add: \tau move1.simps \tau moves1.simps*)
from *bisim3* **have** *s: xcp = None xs = loc*
and $\tau \text{Exec-mover-a } P \ t \ e3 \ h \ (stk, loc, pc, xcp) \ ([v''], loc, length \ (compE2 \ e3), \text{None})$
by (*auto dest: bisim1Val2D1*)
hence $\tau \text{Exec-mover-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ h \ (stk \ @ \ [v', \text{Null}], loc, length \ (compE2 \ e1) \ + \ length \ (compE2 \ e2) \ + \ pc, xcp) \ ([v''] \ @ \ [v', \text{Null}], loc, length \ (compE2 \ e1) \ + \ length \ (compE2 \ e2) \ + \ length \ (compE2 \ e3), \text{None})$
by $-(rule \ CAS-\tau \ ExecrI3)$
moreover
have $\text{exec-move-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ h \ ([v'', v', \text{Null}], loc, length \ (compE2 \ e1) \ + \ length \ (compE2 \ e2) \ + \ length \ (compE2 \ e3), \text{None}) \ \varepsilon$
 $h \ ([v'', v', \text{Null}], loc, length \ (compE2 \ e1) \ + \ length \ (compE2 \ e2) \ + \ length \ (compE2 \ e3), [addr-of-sys-xcpt \ \text{NullPointer}])$
unfolding *exec-move-def* **by** $-(rule \ exec-instr, \ auto \ simp \ add: \ is-Ref-def)$
moreover have $\tau \text{move2} \ (compP2 \ P) \ h \ [v'', v', \text{Null}] \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ (length \ (compE2 \ e1) \ + \ length \ (compE2 \ e2) \ + \ length \ (compE2 \ e3)) \ \text{None} \implies \text{False}$
by (*simp add: \tau move2-iff*)
moreover
have $P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), h' \vdash (\text{THROW } \text{NullPointer}, loc) \leftrightarrow ([v'', v', \text{Null}], loc,$

```

length (compE2 e1) + length (compE2 e2) + length (compE2 e3), [addr-of-sys-xcpt NullPointer])
  by(rule bisim1-bisims1.bisim1CASFail)
  ultimately show ?thesis using s  $\tau$  by auto blast
next
case (Red1CASSucceed a v'')
hence [simp]: v = Addr a e' = true e3' = Val v''
  ta = {ReadMem a (CField D F) v', WriteMem a (CField D F) v''} xs' = xs
  and read: heap-read h a (CField D F) v'
  and write: heap-write h a (CField D F) v'' h' by auto
  have  $\tau$ :  $\neg \tau\text{move1 } P h (CompareAndSwap (addr a) D F (Val v') (Val v''))$  by(auto simp add:
 $\tau\text{move1.simps } \tau\text{moves1.simps}$ )
  from bisim3 have s: xcp = None xs = loc
  and exec1:  $\tau\text{Exec-mover-a } P t e3 h (stk, loc, pc, xcp) ([v''], loc, length (compE2 e3), None)$ 
  by(auto dest: bisim1Val2D1)
  hence  $\tau\text{Exec-mover-a } P t (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) h (stk @ [v', Addr a], loc, length$ 
  (compE2 e1) + length (compE2 e2) + pc, xcp) ([v''] @ [v', Addr a], loc, length (compE2 e1) + length
  (compE2 e2) + length (compE2 e3), None)
  by-(rule CAS- $\tau\text{ExecrI3}$ )
  moreover from read write
  have exec-move-a  $P t (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) h ([v'', v', Addr a], loc, length (compE2$ 
  e1) + length (compE2 e2) + length (compE2 e3), None)
    {ReadMem a (CField D F) v', WriteMem a (CField D F) v''}
    h' ([Bool True], loc, Suc (length (compE2 e1) + length (compE2 e2) +
  length (compE2 e3)), None)
  unfolding exec-move-def by-(rule exec-instr, auto simp add: compP2-def is-Ref-def)
  moreover have  $\tau\text{move2 } (compP2 P) h [v'', v', Addr a] (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) (length$ 
  (compE2 e1) + length (compE2 e2) + length (compE2 e3)) None  $\implies$  False
  by(simp add:  $\tau\text{move2-iff}$ )
  moreover
  have  $P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h' \vdash (true, loc) \leftrightarrow ([Bool True], loc, length (compE2$ 
  (e1  $\cdot compareAndSwap(D \cdot F, e2, e3))), None)$ 
  by(rule bisim1Val2) simp
  ultimately show ?thesis using s  $\tau$  by(auto simp add: ta-upd-simps ac-simps) blast
next
case (Red1CASFail a v'' v''')
hence [simp]: v = Addr a e' = false e3' = Val v''' h' = h
  ta = {ReadMem a (CField D F) v''} xs' = xs
  and read: heap-read h a (CField D F) v'' v'  $\neq v''$  by auto
  have  $\tau$ :  $\neg \tau\text{move1 } P h (CompareAndSwap (addr a) D F (Val v') (Val v'''))$  by(auto simp add:
 $\tau\text{move1.simps } \tau\text{moves1.simps}$ )
  from bisim3 have s: xcp = None xs = loc
  and exec1:  $\tau\text{Exec-mover-a } P t e3 h (stk, loc, pc, xcp) ([v'''], loc, length (compE2 e3), None)$ 
  by(auto dest: bisim1Val2D1)
  hence  $\tau\text{Exec-mover-a } P t (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) h (stk @ [v', Addr a], loc, length$ 
  (compE2 e1) + length (compE2 e2) + pc, xcp) ([v'''] @ [v', Addr a], loc, length (compE2 e1) + length
  (compE2 e2) + length (compE2 e3), None)
  by-(rule CAS- $\tau\text{ExecrI3}$ )
  moreover from read
  have exec-move-a  $P t (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) h ([v''', v', Addr a], loc, length (compE2$ 
  e1) + length (compE2 e2) + length (compE2 e3), None)
    {ReadMem a (CField D F) v''}
    h' ([Bool False], loc, Suc (length (compE2 e1) + length (compE2 e2) +
  length (compE2 e3)), None)
  unfolding exec-move-def by-(rule exec-instr, auto simp add: compP2-def is-Ref-def)

```

moreover have $\tau move2$ ($compP2$ P) h [v''' , v' , $Addr$ a] ($e1 \cdot compareAndSwap(D \cdot F, e2, e3)$)
 $(length (compE2 e1) + length (compE2 e2) + length (compE2 e3))$ $None \implies False$
by($simp$ $add: \tau move2\text{-iff}$)
moreover
have $P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h' \vdash (false, loc) \leftrightarrow ([Bool False], loc, length (compE2$
 $(e1 \cdot compareAndSwap(D \cdot F, e2, e3)))$, $None$)
by($rule$ $bisim1Val2$) $simp$
ultimately show $?thesis$ **using** $s \tau$ **by**($auto$ $simp$ $add: ta\text{-upd-simps}$ $ac\text{-simps}$) $blast$
next
case ($CAS1Throw3$ A)
note [$simp$] = $\langle e3' = Throw A \rangle \langle ta = \varepsilon \rangle \langle e' = Throw A \rangle \langle h' = h \rangle \langle xs' = xs \rangle$
have $\tau: \tau move1 P h (CompareAndSwap (Val v) D F (Val v') (Throw A))$ **by**($rule$ $\tau move1CASThrow3$)
from $bisim3$ **have** $xcp = [A] \vee xcp = None$ **by**($auto$ $dest: bisim1\text{-ThrowD}$)
thus $?thesis$
proof
assume [$simp$]: $xcp = [A]$
with $bisim3$
have $P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h \vdash (Throw A, xs) \leftrightarrow (stk @ [v', v], loc, length$
 $(compE2 e1) + length (compE2 e2) + pc, xcp)$
by($auto$ $intro: bisim1\text{-bisims1.intros}$)
thus $?thesis$ **using** τ **by**($fastforce$)
next
assume [$simp$]: $xcp = None$
with $bisim3$ **obtain** pc' **where** $\tau Exec\text{-mover}\text{-}a P t e3 h (stk, loc, pc, None) ([Addr A], loc, pc',$
 $[A])$
and $bisim': P, e3, h \vdash (Throw A, xs) \leftrightarrow ([Addr A], loc, pc', [A])$
and [$simp$]: $xs = loc$
by($auto$ $dest: bisim1\text{-Throw}\text{-}\tau Exec\text{-mover}$)
hence $\tau Exec\text{-mover}\text{-}a P t (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) h (stk @ [v', v], loc, length (compE2$
 $e1) + length (compE2 e2) + pc, None) ([Addr A] @ [v', v], loc, length (compE2 e1) + length (compE2$
 $e2) + pc', [A])$
by-($rule$ $CAS\text{-}\tau ExecrI3$)
moreover from $bisim'$
have $P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h \vdash (Throw A, xs) \leftrightarrow ([Addr A] @ [v', v], loc, length$
 $(compE2 e1) + length (compE2 e2) + pc', [A])$
by($rule$ $bisim1\text{-bisims1.bisim1CASThrow3}$)
ultimately show $?thesis$ **using** τ **by** $auto$
qed
qed $auto$
next
case $bisim1CASThrow1$ **thus** $?case$ **by** $auto$
next
case $bisim1CASThrow2$ **thus** $?case$ **by** $auto$
next
case $bisim1CASThrow3$ **thus** $?case$ **by** $auto$
next
case $bisim1CASFail$ **thus** $?case$ **by** $auto$
next
case ($bisim1CallParams$ ps n ps' xs stk loc pc xcp obj $M' v$)
note $IHparam = bisim1CallParams.IH(2)$
note $bisim1 = \langle \bigwedge xs. P, obj, h \vdash (obj, xs) \leftrightarrow ([], xs, 0, None) \rangle$
note $bisim2 = \langle P, ps, h \vdash (ps', xs) [\leftrightarrow] (stk, loc, pc, xcp) \rangle$
note $red = \langle True, P, t \vdash 1 \langle Val v \cdot M'(ps'), (h, xs) \rangle \text{-}ta \rightarrow \langle e', (h', xs') \rangle \rangle$
note $bsok = \langle bsok (obj \cdot M'(ps)) n \rangle$

```

from bisim2 ⟨ps ≠ []⟩ have ps': ps' ≠ [] by(auto dest: bisims1-lengthD)
from red show ?case
proof cases
  case (Call1Params es')
  note [simp] = ⟨e' = Val v·M'(es')⟩
  and red = ⟨True,P,t ⊢ 1 ⟨ps', (h, xs)⟩ [-ta→] ⟨es', (h', xs')⟩⟩
  from red have  $\tau$ :  $\tau\text{move1 } P \ h \ (Val\ v \cdot M'(ps')) = \tau\text{moves1 } P \ h \ ps'$  by(auto simp add: \tau\text{move1.simps}
 $\tau\text{moves1.simps}$ )
  from IHparam[OF red] bsok obtain pc'' stk'' loc'' xcp''
  where bisim': P,ps,h' ⊢ (es', xs') [↔] (stk'', loc'', pc'', xcp'')
  and exec': ?execs ta ps ps' es' h stk loc pc xcp h' pc'' stk'' loc'' xcp'' by auto
  have ?exec ta (obj·M'(ps)) (Val v·M'(ps')) (Val v·M'(es')) h (stk @ [v]) loc (length (compE2 obj
+ pc) xcp h' (length (compE2 obj) + pc'') (stk'' @ [v]) loc'' xcp'')
  proof(cases \tau\text{move1 } P \ h \ (Val\ v \cdot M'(ps')))
  case True
  with exec' \tau show ?thesis by (auto intro: Call-\tau\ExecrI2 Call-\tau\ExecI2)
  next
  case False
  with exec' \tau obtain pc' stk' loc' xcp'
  where e:  $\tau\text{Exec-movesr-a } P \ t \ ps \ h \ (stk, loc, pc, xcp) \ (stk', loc', pc', xcp')$ 
  and e':  $\text{exec-moves-a } P \ t \ ps \ h \ (stk', loc', pc', xcp') \ (\text{extTA2JVM } (\text{compP2 } P) \ ta) \ h' \ (stk'', loc'',$ 
pc'', xcp'')
  and  $\tau'$ :  $\neg \tau\text{moves2 } (\text{compP2 } P) \ h \ stk' \ ps \ pc' \ xcp'$ 
  and call: (calls1 ps' = None ∨ no-calls2 ps pc ∨ pc' = pc ∧ stk' = stk ∧ loc' = loc ∧ xcp' =
xcp) by auto
  from e have  $\tau\text{Exec-mover-a } P \ t \ (\text{obj} \cdot M'(ps)) \ h \ (stk \ @ \ [v], loc, \text{length } (\text{compE2 } obj) + pc, xcp)$ 
(stk' @ [v], loc', length (compE2 obj) + pc', xcp') by(rule Call-\tau\ExecrI2)
  moreover from e' have  $\text{exec-move-a } P \ t \ (\text{obj} \cdot M'(ps)) \ h \ (stk' \ @ \ [v], loc', \text{length } (\text{compE2 } obj)$ 
+ pc', xcp') (extTA2JVM (compP2 P) ta) h' (stk'' @ [v], loc'', length (compE2 obj) + pc'', xcp'')
  by(rule exec-move-CallI2)
  moreover from  $\tau' \ e'$  have  $\tau\text{move2 } (\text{compP2 } P) \ h \ (stk' \ @ \ [v]) \ (\text{obj} \cdot M'(ps)) \ (\text{length } (\text{compE2}$ 
obj) + pc') xcp' ⇒ False
  by(auto simp add: \tau\text{move2-iff } \tau\text{moves2-iff } \tau\text{instr-stk-drop-exec-moves split: if-split-asm})
  moreover from red have call1 (Val v·M'(ps')) = calls1 ps' by(auto simp add: is-vals-conv)
  moreover from red have no-calls2 ps pc ⇒ no-call2 (obj·M'(ps)) (length (compE2 obj) + pc
∨ pc = length (compEs2 ps))
  by(auto simp add: no-call2-def no-calls2-def)
  ultimately show ?thesis using False call e
  by(auto simp del: split-paired-Ex call1.simps calls1.simps) blast+
  qed
  moreover from bisim'
  have P,obj·M'(ps),h' ⊢ (Val v·M'(es'), xs') ↔ ((stk'' @ [v]), loc'', length (compE2 obj) + pc'',
xcp'')
  by(rule bisim1-bisims1.bisim1CallParams)
  ultimately show ?thesis using  $\tau$ 
  by(auto simp del: call1.simps calls1.simps split: if-split-asm split del: if-split) blast+
  next
  case (Red1CallNull vs)
  note [simp] = ⟨h' = h⟩ ⟨xs' = xs⟩ ⟨ta =  $\varepsilon$ ⟩ ⟨v = Null⟩ ⟨ps' = map Val vs⟩ ⟨e' = THROW
NullPointer⟩
  from bisim2 have length: length ps = length vs by(auto dest: bisims1-lengthD)
  have xs = loc ∧ xcp = None ∧  $\tau\text{Exec-movesr-a } P \ t \ ps \ h \ (stk, loc, pc, xcp) \ (\text{rev } vs, loc, \text{length}$ 
(compEs2 ps), None)
  proof(cases pc < length (compEs2 ps))

```

```

case True
with bisim2 show ?thesis by(auto dest: bisims1-Val-τExec-moves)
next
case False
with bisim2 have pc = length (compEs2 ps)
  by(auto dest: bisims1-pc-length-compEs2)
with bisim2 show ?thesis by(auto dest: bisims1-Val-length-compEs2D)
qed
hence s: xs = loc xcp = None
  and  $\tau\text{Exec-movesr-a } P \ t \ ps \ h \ (stk, \ loc, \ pc, \ xcp) \ (rev \ vs, \ loc, \ length \ (compEs2 \ ps), \ None)$  by auto
hence  $\tau\text{Exec-mover-a } P \ t \ (obj \cdot M'(ps)) \ h \ (stk \ @ \ [Null], \ loc, \ length \ (compE2 \ obj) \ + \ pc, \ xcp) \ (rev \ vs \ @ \ [Null], \ loc, \ length \ (compE2 \ obj) \ + \ length \ (compEs2 \ ps), \ None)$ 
  by  $-(rule \ Call\text{-}\tau\text{ExecrI2})$ 
also {
  from length have exec-move-a  $P \ t \ (obj \cdot M'(ps)) \ h \ (rev \ vs \ @ \ [Null], \ loc, \ length \ (compE2 \ obj) \ + \ length \ (compEs2 \ ps), \ None) \ \varepsilon \ h \ (rev \ vs \ @ \ [Null], \ loc, \ length \ (compE2 \ obj) \ + \ length \ (compEs2 \ ps), \ [addr\text{-of-sys-xcpt} \ NullPointer])$ 
    unfolding exec-move-def by(cases ps)(auto intro: exec-instr)
    moreover have  $\tau\text{move2 } P \ h \ (rev \ vs \ @ \ [Null]) \ (obj \cdot M'(ps)) \ (length \ (compE2 \ obj) \ + \ length \ (compEs2 \ ps)) \ None$ 
      using length by(simp add: τmove2-iff)
    ultimately have  $\tau\text{Exec-movet-a } P \ t \ (obj \cdot M'(ps)) \ h \ (rev \ vs \ @ \ [Null], \ loc, \ length \ (compE2 \ obj) \ + \ length \ (compEs2 \ ps), \ None) \ (rev \ vs \ @ \ [Null], \ loc, \ length \ (compE2 \ obj) \ + \ length \ (compEs2 \ ps), \ [addr\text{-of-sys-xcpt} \ NullPointer])$ 
      by(auto intro: τexec-moveI) }
  also have  $\tau\text{move1 } P \ h \ (null \cdot M'(map \ Val \ vs))$ 
    by(auto simp add: τmove1.simps τmoves1.simps map-eq-append-conv)
  moreover
  from length have  $P, obj \cdot M'(ps), h \vdash (THROW \ NullPointer, \ loc) \leftrightarrow ((rev \ vs \ @ \ [Null]), \ loc, \ length \ (compE2 \ obj) \ + \ length \ (compEs2 \ ps), \ [addr\text{-of-sys-xcpt} \ NullPointer])$ 
    by $-(rule \ bisim1CallThrow, \ simp)$ 
  ultimately show ?thesis using s by auto
next
case (Call1ThrowParams vs a es')
note [simp] =  $\langle ta = \varepsilon \rangle \langle e' = Throw \ a \rangle \langle ps' = map \ Val \ vs \ @ \ Throw \ a \ \# \ es' \rangle \langle h' = h \rangle \langle xs' = xs \rangle$ 
have  $\tau: \tau\text{move1 } P \ h \ (Val \ v \cdot M'(map \ Val \ vs \ @ \ Throw \ a \ \# \ es'))$  by(rule τmove1CallThrowParams)
from bisim2 have [simp]: xs = loc and xcp = [a] ∨ xcp = None by(auto dest: bisims1-ThrowD)
from  $\langle xcp = [a] \vee xcp = None \rangle$  show ?thesis
proof
  assume [simp]: xcp = [a]
  with bisim2
  have  $P, obj \cdot M'(ps), h \vdash (Throw \ a, \ loc) \leftrightarrow (stk \ @ \ [v], \ loc, \ length \ (compE2 \ obj) \ + \ pc, \ [a])$ 
    by  $-(rule \ bisim1CallThrowParams, \ auto)$ 
  thus ?thesis using  $\tau$  by(fastforce)
next
assume [simp]: xcp = None
with bisim2 obtain pc'
  where exec: τExec-movesr-a  $P \ t \ ps \ h \ (stk, \ loc, \ pc, \ None) \ (Addr \ a \ \# \ rev \ vs, \ loc, \ pc', \ [a])$ 
  and bisim':  $P, \ ps, \ h \vdash (map \ Val \ vs \ @ \ Throw \ a \ \# \ es', \ loc) \ [\leftrightarrow] \ (Addr \ a \ \# \ rev \ vs, \ loc, \ pc', \ [a])$ 
  by(auto dest: bisims1-Throw-τExec-movesr)
from bisim'
have  $P, obj \cdot M'(ps), h \vdash (Throw \ a, \ loc) \leftrightarrow ((Addr \ a \ \# \ rev \ vs) \ @ \ [v], \ loc, \ length \ (compE2 \ obj) \ + \ pc', \ [a])$ 
  by(rule bisim1CallThrowParams)

```

```

with Call- $\tau$ ExecrI2[OF exec, of obj M' v]  $\tau$ 
show ?thesis by auto
qed
next
case (Red1CallExternal a Ta Ts Tr D vs va H')
hence [simp]: v = Addr a ps' = map Val vs e' = extRet2J (addr a·M'(map Val vs)) va H' = h'
xs' = xs
and Ta: typeof-addr h a = [Ta]
and iec: P  $\vdash$  class-type-of Ta sees M': Ts $\rightarrow$ Tr = Native in D
and redex: P, t  $\vdash$   $\langle$ a·M'(vs), h $\rangle$  -ta $\rightarrow$ ext  $\langle$ va, h $\rangle$  by auto
from bisim2 have [simp]: xs = loc by (auto dest: bisims-Val-loc-eq-xcp-None)
moreover from bisim2 have length ps = length ps'
by (rule bisims1-lengthD)
hence  $\tau$ :  $\tau$ move1 P h (addr a·M'(map Val vs)) :: 'addr expr1 =  $\tau$ move2 (compP2 P) h (rev vs @
[Addr a]) (obj·M'(ps)) (length (compE2 obj) + length (compEs2 ps)) None
using Ta iec by (auto simp add:  $\tau$ move1.simps  $\tau$ moves1.simps map-eq-append-conv  $\tau$ move2-iff
compP2-def)
obtain s: xcp = None xs = loc
and  $\tau$ Exec-movesr-a P t ps h (stk, loc, pc, xcp) (rev vs, loc, length (compEs2 ps), None)
proof (cases pc < length (compEs2 ps))
case True
with bisim2 show ?thesis by (auto dest: bisims1-Val- $\tau$ Exec-moves intro: that)
next
case False
with bisim2 have pc = length (compEs2 ps) by (auto dest: bisims1-pc-length-compEs2)
with bisim2 show ?thesis by -(rule that, auto dest!: bisims1-pc-length-compEs2D)
qed
from Call- $\tau$ ExecrI2[OF this(3), of obj M' v]
have  $\tau$ Exec-mover-a P t (obj·M'(ps)) h (stk @ [Addr a], loc, length (compE2 obj) + pc, xcp) (rev
vs @ [Addr a], loc, length (compE2 obj) + length (compEs2 ps), None) by simp
moreover from bisim2 have pc  $\leq$  length (compEs2 ps) by (rule bisims1-pc-length-compEs2)
hence no-call2 (obj·M'(ps)) (length (compE2 obj) + pc)  $\vee$  pc = length (compEs2 ps)
using bisim2 by (auto simp add: no-call2-def neq-Nil-conv dest: bisims-Val-pc-not-Invoke)
moreover {
assume pc = length (compEs2 ps)
with  $\langle$  $\tau$ Exec-movesr-a P t ps h (stk, loc, pc, xcp) (rev vs, loc, length (compEs2 ps), None) $\rangle$ 
have stk = rev vs xcp = None by auto }
moreover
let ?ret = extRet2JVM (length ps) h' (rev vs @ [Addr a]) loc undefined undefined (length (compE2
obj) + length (compEs2 ps)) [] va
let ?stk' = fst (hd (snd (snd ?ret)))
let ?xcp' = fst ?ret
let ?pc' = snd (snd (snd (snd (hd (snd (snd ?ret)))))
from bisim2 have [simp]: length ps = length vs by (auto dest: bisims1-lengthD)
from redex have redex': (ta, va, h')  $\in$  red-external-aggr (compP2 P) t a M' vs h
by -(rule red-external-imp-red-external-aggr, simp add: compP2-def)
with Ta iec
have exec-move-a P t (obj·M'(ps)) h (rev vs @ [Addr a], loc, length (compE2 obj) + length (compEs2
ps), None) (extTA2JVM (compP2 P) ta) h' (?stk', loc, ?pc', ?xcp')
unfolding exec-move-def
by -(rule exec-instr, cases va, (force simp add: compP2-def is-Ref-def simp del: split-paired-Ex
intro: external-WT'.intros)+)
moreover have P, obj·M'(ps), h'  $\vdash$  (extRet2J1 (addr a·M'(map Val vs)) va, loc)  $\leftrightarrow$  (?stk', loc,
?pc', ?xcp')

```

```

proof(cases va)
  case (RetVal v)
    have  $P, \text{obj} \cdot M'(ps), h' \vdash (\text{Val } v, \text{loc}) \leftrightarrow ([v], \text{loc}, \text{length}(\text{compE2}(\text{obj} \cdot M'(ps))), \text{None})$ 
      by(rule bisim1Val2) simp
    thus ?thesis unfolding RetVal by simp
  next
    case (RetExc ad) thus ?thesis by(auto intro: bisim1CallThrow)
  next
    case RetStaySame
    from bisims1-map-Val-append[OF bisims1Nil, of ps vs P h' loc]
    have  $P, ps, h' \vdash (\text{map Val vs}, \text{loc}) [\leftrightarrow] (\text{rev vs}, \text{loc}, \text{length}(\text{compEs2 ps}), \text{None})$  by simp
    hence  $P, \text{obj} \cdot M'(ps), h' \vdash (\text{addr } a \cdot M'(\text{map Val vs}), \text{loc}) \leftrightarrow (\text{rev vs} @ [\text{Addr } a], \text{loc}, \text{length}(\text{compE2}(\text{obj}) + \text{length}(\text{compEs2 ps}), \text{None}))$ 
      by(rule bisim1-bisims1.bisim1CallParams)
    thus ?thesis using RetStaySame by simp
  qed
  moreover from redex Ta iec
  have  $\tau \text{move1 } P h (\text{addr } a \cdot M'(\text{map Val vs}) :: \text{'addr expr1}) \implies ta = \varepsilon \wedge h' = h$ 
    by(fastforce simp add:  $\tau \text{move1} . \text{sims } \tau \text{moves1} . \text{sims map-eq-append-conv } \tau \text{external}'\text{-def } \tau \text{external-def}$ 
    dest:  $\tau \text{external}'\text{-red-external-TA-empty } \tau \text{external}'\text{-red-external-heap-unchanged sees-method-fun}$ )
  ultimately show ?thesis using  $\tau$ 
    apply(cases  $\tau \text{move1 } P h (\text{addr } a \cdot M'(\text{map Val vs}) :: \text{'addr expr1})$ )
    apply(auto simp del: split-paired-Ex simp add: compP2-def)
    apply(blast intro: rtranclp.rtrancl-into-rtrancl rtranclp-into-tranclp1  $\tau \text{exec-move1}$ ) +
    done
  qed(insert ps', auto)
next
  case bisim1CallThrowObj thus ?case by fastforce
next
  case bisim1CallThrowParams thus ?case by auto
next
  case bisim1CallThrow thus ?case by fastforce
next
  case (bisim1BlockSome1 e n V Ty v xs e')
  from  $\langle \text{True}, P, t \vdash 1 \{ \{ V: \text{Ty} = [v]; e \}, (h, xs) \} \rangle -ta \rightarrow \langle e', (h', xs') \rangle$  show ?case
  proof(cases)
    case Block1Some
    note [simp] =  $\langle ta = \varepsilon \rangle \langle e' = \{ V: \text{Ty} = \text{None}; e \} \rangle \langle h' = h \rangle \langle xs' = xs[V := v] \rangle$ 
      and  $\text{len} = \langle V < \text{length } xs \rangle$ 
    from len have exec:  $\tau \text{Exec-move1-a } P t \{ V: \text{Ty} = [v]; e \} h (\ [], xs, 0, \text{None}) (\ [], xs[V := v], \text{Suc}(\text{Suc } 0), \text{None})$ 
      by-(rule  $\tau \text{Exec2step}$ , auto intro: exec-instr simp add: exec-move-def  $\tau \text{move2-iff}$ )
    moreover have  $P, \{ V: \text{Ty} = [v]; e \}, h \vdash (\{ V: \text{Ty} = \text{None}; e \}, xs[V := v]) \leftrightarrow (\ [], xs[V := v], \text{Suc}(\text{Suc } 0), \text{None})$ 
      by(rule bisim1BlockSome4)(rule bisim1-refl)
    moreover have  $\tau \text{move1 } P h \{ V: \text{Ty} = [v]; e \}$  by(auto intro:  $\tau \text{move1BlockSome}$ )
    ultimately show ?thesis by auto
  qed
next
  case (bisim1BlockSome2 e n V Ty v xs)
  from  $\langle \text{True}, P, t \vdash 1 \{ \{ V: \text{Ty} = [v]; e \}, (h, xs) \} \rangle -ta \rightarrow \langle e', (h', xs') \rangle$  show ?case
  proof(cases)
    case Block1Some
    note [simp] =  $\langle ta = \varepsilon \rangle \langle e' = \{ V: \text{Ty} = \text{None}; e \} \rangle \langle h' = h \rangle \langle xs' = xs[V := v] \rangle$ 

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and $len = \langle V < length\ xs \rangle$
from len **have** $exec: \tau Exec\text{-}move\text{-}a\ P\ t\ \{V:Ty=[v];\ e\}\ h\ ([v],\ xs,\ Suc\ 0,\ None)\ ([],\ xs[V := v],\ Suc\ (Suc\ 0),\ None)$
by $(rule\ \tau Exec\ 1\ step,\ auto\ intro: exec\text{-}instr\ \tau move\ 2\ Block\ Some\ 2\ simp: exec\text{-}move\text{-}def)$
moreover **have** $P, \{V:Ty=[v];\ e\}, h \vdash (\{V:Ty=None; e\}, xs[V := v]) \leftrightarrow ([], xs[V := v], Suc\ (Suc\ 0), None)$
by $(rule\ bisim\ 1\ Block\ Some\ 4)(rule\ bisim\ 1\ \text{-}refl)$
moreover **have** $\tau move\ 1\ P\ h\ \{V:Ty=[v];\ e\}$ **by** $(auto\ intro: \tau move\ 1\ Block\ Some)$
ultimately **show** $?thesis$ **by** $auto$
qed
next
case $(bisim\ 1\ Block\ Some\ 4\ E\ n\ e\ xs\ stk\ loc\ pc\ xcp\ V\ Ty\ v)$
note $IH = bisim\ 1\ Block\ Some\ 4.IH(2)$
note $bisim = \langle P, E, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
note $bsok = \langle bsok\ \{V:Ty=[v];\ E\}\ n \rangle$
hence $[simp]: n = V$ **by** $simp$
from $\langle True, P, t \vdash 1\ \{V:Ty=None; e\}, (h, xs) \rangle \text{-}ta \rightarrow \langle e', (h', xs') \rangle$ **show** $?case$
proof $cases$
case $(Block\ 1\ Red\ E')$
note $[simp] = \langle e' = \{V:Ty=None; E'\} \rangle$
and $red = \langle True, P, t \vdash 1\ \langle e, (h, xs) \rangle \text{-}ta \rightarrow \langle E', (h', xs') \rangle \rangle$
from red **have** $\tau: \tau move\ 1\ P\ h\ \{V:Ty=None; e\} = \tau move\ 1\ P\ h\ e$ **by** $(auto\ simp\ add: \tau move\ 1.simps\ \tau moves\ 1.simps)$
from $IH[OF\ red]\ bsok$ **obtain** $pc''\ stk''\ loc''\ xcp''$
where $bisim': P, E, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$
and $exec': ?exec\ ta\ E\ e\ E'\ h\ stk\ loc\ pc\ xcp\ h'\ pc''\ stk''\ loc''\ xcp''$ **by** $auto$
have $no\text{-}call\ 2\ E\ pc \implies no\text{-}call\ 2\ (\{V:Ty=[v];\ E'\})\ (Suc\ (Suc\ pc))$ **by** $(auto\ simp\ add: no\text{-}call\ 2\text{-}def)$
hence $?exec\ ta\ \{V:Ty=[v];\ E'\}\ \{V:Ty=None; e'\}\ \{V:Ty=None; E'\}\ h\ stk\ loc\ (Suc\ (Suc\ pc))\ xcp\ h'\ (Suc\ (Suc\ pc''))\ stk''\ loc''\ xcp''$
using $exec'\ \tau$
by $(cases\ \tau move\ 1\ P\ h\ \{V:Ty=None; e'\})(auto,\ (blast\ intro: exec\text{-}move\text{-}Block\ Some\ I\ Block\ \text{-}\tau\ Exec\ r\ I\ \text{-}Some\ Block\ \text{-}\tau\ Exec\ r\ I\ \text{-}Some)+)$
with $bisim'\ \tau$ **show** $?thesis$ **by** $auto(blast\ intro: bisim\ 1\ \text{-}bisims\ 1.bisim\ 1\ Block\ Some\ 4)+$
next
case $(Red\ 1\ Block\ u)$
note $[simp] = \langle e = Val\ u \rangle \langle ta = \varepsilon \rangle \langle e' = Val\ u \rangle \langle h' = h \rangle \langle xs' = xs \rangle$
have $\tau move\ 1\ P\ h\ \{V:Ty=None; Val\ u\}$ **by** $(rule\ \tau move\ 1\ Block\ Red)$
moreover **from** $bisim$ **have** $[simp]: xcp = None\ loc = xs$
and $exec: \tau Exec\ \text{-}mover\ \text{-}a\ P\ t\ E\ h\ (stk,\ loc,\ pc,\ xcp)\ ([u],\ loc,\ length\ (compE\ 2\ E),\ None)$ **by** $(auto\ dest: bisim\ 1\ Val\ 2\ D1)$
moreover
have $P, \{V:Ty=[v];\ E'\}, h \vdash (Val\ u, xs) \leftrightarrow ([u], xs, length\ (compE\ 2\ \{V:Ty=[v];\ E'\}), None)$
by $(rule\ bisim\ 1\ Val\ 2)\ simp$
ultimately **show** $?thesis$ **by** $(fastforce\ elim!: Block\ \text{-}\tau\ Exec\ r\ I\ \text{-}Some)$
next
case $(Block\ 1\ Throw\ a)$
note $[simp] = \langle e = Throw\ a \rangle \langle ta = \varepsilon \rangle \langle e' = Throw\ a \rangle \langle h' = h \rangle \langle xs' = xs \rangle$
have $\tau: \tau move\ 1\ P\ h\ \{V:Ty=None; e\}$ **by** $(auto\ intro: \tau move\ 1\ Block\ Throw)$
from $bisim$ **have** $xcp = [a] \vee xcp = None$ **by** $(auto\ dest: bisim\ 1\ \text{-}Throw\ D)$
thus $?thesis$
proof
assume $[simp]: xcp = [a]$
with $bisim$ **have** $P, \{V:Ty=[v];\ E'\}, h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, Suc\ (Suc\ pc), xcp)$
by $(auto\ intro: bisim\ 1\ Block\ Throw\ Some)$


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thus ?thesis using  $\tau$  by(fastforce)
next
assume [simp]: xcp = None
with bisim obtain pc'
  where  $\tau$ Exec-mover-a P t E h (stk, loc, pc, None) ([Addr a], loc, pc', [a])
  and bisim': P, E, h  $\vdash$  (Throw a, xs)  $\leftrightarrow$  ([Addr a], loc, pc', [a]) and [simp]: xs = loc
  by(auto dest: bisim1-Throw- $\tau$ Exec-mover)
  hence  $\tau$ Exec-mover-a P t {V:Ty=[v]; E} h (stk, loc, Suc (Suc pc), None) ([Addr a], loc, Suc
(Suc pc'), [a])
  by(auto intro: Block- $\tau$ ExecrI-Some)
moreover from bisim' have P, {V:Ty=[v]; E}, h  $\vdash$  (Throw a, xs)  $\leftrightarrow$  ([Addr a], loc, Suc (Suc
pc'), [a])
  by(auto intro: bisim1-bisims1.bisim1BlockThrowSome)
  ultimately show ?thesis using  $\tau$  by auto
qed
qed
next
case bisim1BlockThrowSome thus ?case by auto
next
case (bisim1BlockNone E n e xs stk loc pc xcp V Ty)
note IH = bisim1BlockNone.IH(2)
note bisim =  $\langle P, E, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
note bsok =  $\langle bsok \{V:Ty=None; E\} n \rangle$ 
hence [simp]: n = V by simp
from  $\langle True, P, t \vdash 1 \{V:Ty=None; e\}, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle$  show ?case
proof cases
  case (Block1Red E')
  note [simp] =  $\langle e' = \{V:Ty=None; E'\} \rangle$ 
  and red =  $\langle True, P, t \vdash 1 \langle e, (h, xs) \rangle -ta \rightarrow \langle E', (h', xs') \rangle \rangle$ 
  from red have  $\tau: \tau move1 P h \{V:Ty=None; e\} = \tau move1 P h e$  by(auto simp add:  $\tau move1.simps$ 
 $\tau moves1.simps$ )
  moreover have call1 ( $\{V:Ty=None; e\}$ ) = call1 e by auto
  moreover from IH[OF red] bsok
  obtain pc'' stk'' loc'' xcp'' where bisim: P, E, h'  $\vdash$  (E', xs')  $\leftrightarrow$  (stk'', loc'', pc'', xcp'')
  and redo: ?exec ta E e E' h stk loc pc xcp h' pc'' stk'' loc'' xcp'' by auto
  from bisim
  have P, {V:Ty=None; E}, h'  $\vdash$  ( $\{V:Ty=None; E'\}, xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$ 
  by(rule bisim1-bisims1.bisim1BlockNone)
  moreover {
    assume no-call2 E pc
    hence no-call2  $\{V:Ty=None; E\}$  pc by(auto simp add: no-call2-def) }
  ultimately show ?thesis using redo
  by(auto simp add: exec-move-BlockNone simp del: call1.simps calls1.simps split: if-split-asm split
del: if-split)(blast intro: Block- $\tau$ ExecrI-None Block- $\tau$ ExecI-None)+
next
case (Red1Block u)
note [simp] =  $\langle e = Val u \rangle \langle ta = \varepsilon \rangle \langle e' = Val u \rangle \langle h' = h \rangle \langle xs' = xs \rangle$ 
have  $\tau move1 P h \{V:Ty=None; Val u\}$  by(rule  $\tau move1BlockRed$ )
moreover from bisim have [simp]: xcp = None loc = xs
  and exec:  $\tau$ Exec-mover-a P t E h (stk, loc, pc, xcp) ([u], loc, length (compE2 E), None) by(auto
dest: bisim1Val2D1)
  moreover
  have P, {V:Ty=None; E}, h  $\vdash$  (Val u, xs)  $\leftrightarrow$  ([u], xs, length (compE2  $\{V:Ty=None; E\}$ ), None)
  by(rule bisim1Val2) simp

```

ultimately show *?thesis* **by**(*fastforce* *intro*: *Block- τ ExecrI-None*)

next

case (*Block1Throw a*)

note [*simp*] = $\langle e = \text{Throw } a \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw } a \rangle \langle h' = h \rangle \langle xs' = xs \rangle$

have τ : $\tau\text{move1 } P \ h \ \{V: \text{Ty}=\text{None}; e\}$ **by**(*auto* *intro*: $\tau\text{move1BlockThrow}$)

from *bisim* **have** $xcp = [a] \vee xcp = \text{None}$ **by**(*auto* *dest*: *bisim1-ThrowD*)

thus *?thesis*

proof

assume [*simp*]: $xcp = [a]$

with *bisim* **have** $P, \{V: \text{Ty}=\text{None}; E\}, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, xcp)$

by(*auto* *intro*: *bisim1BlockThrowNone*)

thus *?thesis* **using** τ **by**(*fastforce*)

next

assume [*simp*]: $xcp = \text{None}$

with *bisim* **obtain** pc'

where $\tau\text{Exec-mover-a } P \ t \ E \ h \ (stk, loc, pc, \text{None}) \ ([\text{Addr } a], loc, pc', [a])$

and $bisim': P, E, h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], loc, pc', [a])$ **and** [*simp*]: $xs = loc$

by(*auto* *dest*: *bisim1-Throw- τ Exec-mover*)

hence $\tau\text{Exec-mover-a } P \ t \ \{V: \text{Ty}=\text{None}; E\} \ h \ (stk, loc, pc, \text{None}) \ ([\text{Addr } a], loc, pc', [a])$

by(*auto* *intro*: *Block- τ ExecrI-None*)

moreover from *bisim'* **have** $P, \{V: \text{Ty}=\text{None}; E\}, h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], loc, pc', [a])$

by(*auto* *intro*: *bisim1-bisims1.bisim1BlockThrowNone*)

ultimately show *?thesis* **using** τ **by** *auto*

qed

qed

next

case *bisim1BlockThrowNone* **thus** *?case* **by** *auto*

next

case (*bisim1Sync1 e1 n e1' xs stk loc pc xcp e2 V*)

note *IH* = *bisim1Sync1.IH*(2)

note *bisim1* = $\langle P, e1, h \vdash (e1', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$

note *bisim2* = $\langle \wedge xs. P, e2, h \vdash (e2, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$

note *red* = $\langle \text{True}, P, t \vdash 1 \ \langle \text{sync}_V (e1') \ e2, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$

note *bsok* = $\langle \text{bsok} (\text{sync}_V (e1') \ e2) \ n \rangle$

hence [*simp*]: $n = V$ **by** *simp*

from *red* **show** *?case*

proof *cases*

case (*Synchronized1Red1 E1'*)

note [*simp*] = $\langle e' = \text{sync}_V (E1') \ e2 \rangle$

and *red* = $\langle \text{True}, P, t \vdash 1 \ \langle e1', (h, xs) \rangle -ta \rightarrow \langle E1', (h', xs') \rangle \rangle$

from *red* **have** τ : $\tau\text{move1 } P \ h \ (\text{sync}_V (e1') \ e2) = \tau\text{move1 } P \ h \ e1'$ **by**(*auto* *simp* *add*: $\tau\text{move1.simps}$ $\tau\text{moves1.simps}$)

moreover have $\text{call1} (\text{sync}_V (e1') \ e2) = \text{call1 } e1'$ **by** *auto*

moreover from *IH[OF red] bsok*

obtain $pc'' \ stk'' \ loc'' \ xcp''$ **where** $bisim: P, e1, h' \vdash (E1', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$

and *redo*: $?exec \ ta \ e1 \ e1' \ E1' \ h \ stk \ loc \ pc \ xcp \ h' \ pc'' \ stk'' \ loc'' \ xcp''$ **by** *auto*

from *bisim*

have $P, \text{sync}_V (e1) \ e2, h' \vdash (\text{sync}_V (E1') \ e2, xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$

by(*rule* *bisim1-bisims1.bisim1Sync1*)

moreover {

assume *no-call2 e1 pc*

hence *no-call2* ($\text{sync}_V (e1) \ e2$) *pc* **by**(*auto* *simp* *add*: *no-call2-def*) }

ultimately show *?thesis* **using** *redo*

by(*auto* *simp* *del*: *call1.simps* *calls1.simps* *split*: *if-split-asm* *split* *del*: *if-split*)(*blast* *intro*:

$\text{Sync-}\tau\text{ExecrI Sync-}\tau\text{ExecI exec-move-SyncI1})+$
next
case *Synchronized1Null*
note $[simp] = \langle e1' = null \rangle \langle e' = \text{THROW NullPointer} \rangle \langle ta = \varepsilon \rangle \langle h' = h \rangle \langle xs' = xs[V := Null] \rangle$
and $V = \langle V < \text{length } xs \rangle$
from *bisim1* **have** $[simp]: xcp = \text{None } xs = \text{loc}$
and $\text{exec}: \tau\text{Exec-mover-a } P t e1 h (stk, loc, pc, xcp) ([Null], loc, \text{length } (\text{compE2 } e1), \text{None})$
by(*auto dest: bisim1Val2D1*)
from *Sync-}\tau\text{ExecrI}[OF exec]*
have $\tau\text{Exec-mover-a } P t (\text{sync}_V (e1) e2) h (stk, loc, pc, xcp) ([Null], loc, \text{length } (\text{compE2 } e1),$
 $\text{None})$ **by** *simp*
also from V
have $\tau\text{Exec-mover-a } P t (\text{sync}_V (e1) e2) h ([Null], loc, \text{length } (\text{compE2 } e1), \text{None}) ([Null], \text{loc}[V$
 $:= Null], \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e1))), \text{None})$
by $-(\text{rule } \tau\text{Execr2step, auto intro: exec-instr } \tau\text{move2-}\tau\text{moves2.intros simp add: exec-move-def})$
also (*rtranclp-trans*)
have $\text{exec-move-a } P t (\text{sync}_V (e1) e2) h ([Null], \text{loc}[V := Null], \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e1))),$
 $\text{None}) \varepsilon$
 $h ([Null], \text{loc}[V := Null], \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e1))), \text{[addr-of-sys-xcpt$
 $\text{NullPointer]})$
unfolding *exec-move-def* **by**(*rule exec-instr*) *auto*
moreover **have** $\neg \tau\text{move2 } (\text{compP2 } P) h [Null] (\text{sync}_V (e1) e2) (\text{Suc } (\text{Suc } (\text{length } (\text{compE2}$
 $e1)))) \text{None}$
by(*simp add: } \tau\text{move2-iff}*)
moreover
have $P, \text{sync}_V (e1) e2, h \vdash (\text{THROW NullPointer}, \text{loc}[V := Null]) \leftrightarrow ([Null], (\text{loc}[V := Null]),$
 $\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e1))), \text{[addr-of-sys-xcpt NullPointer]})$
by(*rule bisim1SyncI1*)
moreover **have** $\neg \tau\text{move1 } P h (\text{sync}_V (null) e2)$ **by**(*auto simp add: } \tau\text{move1.simps } \tau\text{moves1.simps}*)
ultimately show *?thesis* **by** *auto blast*
next
case (*Lock1Synchronized a*)
note $[simp] = \langle e1' = \text{addr } a \rangle \langle ta = \{\text{Lock} \rightarrow a, \text{SyncLock } a\} \rangle \langle e' = \text{insync}_V (a) e2 \rangle \langle h' = h \rangle \langle xs' = xs[V := \text{Addr } a] \rangle$
and $V = \langle V < \text{length } xs \rangle$
from *bisim1* **have** $[simp]: xcp = \text{None } xs = \text{loc}$
and $\text{exec}: \tau\text{Exec-mover-a } P t e1 h (stk, loc, pc, xcp) ([\text{Addr } a], loc, \text{length } (\text{compE2 } e1), \text{None})$
by(*auto dest: bisim1Val2D1*)
from *Sync-}\tau\text{ExecrI}[OF exec]*
have $\tau\text{Exec-mover-a } P t (\text{sync}_V (e1) e2) h (stk, loc, pc, xcp) ([\text{Addr } a], loc, \text{length } (\text{compE2 } e1),$
 $\text{None})$ **by** *simp*
also from V
have $\tau\text{Exec-mover-a } P t (\text{sync}_V (e1) e2) h ([\text{Addr } a], loc, \text{length } (\text{compE2 } e1), \text{None}) ([\text{Addr } a],$
 $\text{loc}[V := \text{Addr } a], \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e1))), \text{None})$
by $-(\text{rule } \tau\text{Execr2step, auto intro: exec-instr } \tau\text{move2-}\tau\text{moves2.intros simp add: exec-move-def})$
also (*rtranclp-trans*)
have $\text{exec-move-a } P t (\text{sync}_V (e1) e2) h ([\text{Addr } a], \text{loc}[V := \text{Addr } a], \text{Suc } (\text{Suc } (\text{length } (\text{compE2}$
 $e1))), \text{None})$
 $(\{\text{Lock} \rightarrow a, \text{SyncLock } a\})$
 $h ([], \text{loc}[V := \text{Addr } a], \text{Suc } (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e1))))), \text{None})$
unfolding *exec-move-def* **by** $-(\text{rule } \text{exec-instr}, \text{ auto simp add: is-Ref-def})$
moreover **have** $\neg \tau\text{move2 } (\text{compP2 } P) h [\text{Addr } a] (\text{sync}_V (e1) e2) (\text{Suc } (\text{Suc } (\text{length } (\text{compE2}$
 $e1)))) \text{None}$
by(*simp add: } \tau\text{move2-iff}*)

```

moreover
from bisim1Sync4[OF bisim1-refl, of P h V e1 e2 a loc[V := Addr a]]
have  $P, \text{sync}_V (e1) e2, h \vdash (\text{insync}_V (a) e2, \text{loc}[V := \text{Addr } a]) \leftrightarrow ([], \text{loc}[V := \text{Addr } a], \text{Suc } (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e1))))), \text{None})$  by simp
moreover have  $\neg \tau \text{move1 } P h (\text{sync}_V (\text{addr } a) e2)$  by(auto simp add: \tau move1.simps \tau moves1.simps)
ultimately show ?thesis by(auto simp add: eval-nat-numeral ta-upd-simps) blast
next
case (Synchronized1Throw1 a)
note  $[\text{simp}] = \langle e1' = \text{Throw } a \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw } a \rangle \langle h' = h \rangle \langle xs' = xs \rangle$ 
have  $\tau: \tau \text{move1 } P h (\text{sync}_V (\text{Throw } a) e2)$  by(rule \tau move1SyncThrow)
from bisim1 have  $xcp = [a] \vee xcp = \text{None}$  by(auto dest: bisim1-ThrowD)
thus ?thesis
proof
assume  $[\text{simp}]: xcp = [a]$ 
with bisim1
have  $P, \text{sync}_V (e1) e2, h \vdash (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a])$ 
by(auto intro: bisim1SyncThrow)
thus ?thesis using \tau by(fastforce)
next
assume  $[\text{simp}]: xcp = \text{None}$ 
with bisim1 obtain  $pc'$ 
where  $\tau \text{Exec-mover-a } P t e1 h (\text{stk}, \text{loc}, \text{pc}, \text{None}) ([\text{Addr } a], \text{loc}, \text{pc}', [a])$ 
and  $\text{bisim}' : P, e1, h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], \text{loc}, \text{pc}', [a])$  and  $[\text{simp}]: xs = \text{loc}$ 
by(auto dest: bisim1-Throw-\tau Exec-mover)
hence  $\tau \text{Exec-mover-a } P t (\text{sync}_V (e1) e2) h (\text{stk}, \text{loc}, \text{pc}, \text{None}) ([\text{Addr } a], \text{loc}, \text{pc}', [a])$ 
by-(rule Sync-\tau ExecrI)
moreover from bisim'
have  $P, \text{sync}_V (e1) e2, h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], \text{loc}, \text{pc}', [a])$ 
by -(rule bisim1-bisims1.bisim1SyncThrow, auto)
ultimately show ?thesis using \tau by fastforce
qed
qed
next
case (bisim1Sync2 e1 n e2 V v xs)
note  $\text{bisim1} = \langle \bigwedge xs. P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$ 
note  $\text{bisim2} = \langle \bigwedge xs. P, e2, h \vdash (e2, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$ 
from  $\langle \text{True}, P, t \vdash 1 \langle \text{sync}_V (\text{Val } v) e2, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$  show ?case
proof cases
case (Lock1Synchronized a)
note  $[\text{simp}] = \langle v = \text{Addr } a \rangle \langle ta = \{\!\{ \text{Lock} \rightarrow a, \text{SyncLock } a \}\!\} \rangle \langle e' = \text{insync}_V (a) e2 \rangle$ 
 $\langle h' = h \rangle \langle xs' = xs[V := \text{Addr } a] \rangle$ 
and  $V = \langle V < \text{length } xs \rangle$ 
from  $V$ 
have  $\tau \text{Exec-mover-a } P t (\text{sync}_V (e1) e2) h ([\text{Addr } a, \text{Addr } a], xs, \text{Suc } (\text{length } (\text{compE2 } e1)), \text{None})$ 
 $([\text{Addr } a], xs[V := \text{Addr } a], \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e1))))), \text{None})$ 
by -(rule \tau Execr1step, auto intro: exec-instr simp add: \tau move2-iff exec-move-def)
moreover
have  $\text{exec-move-a } P t (\text{sync}_V (e1) e2) h ([\text{Addr } a], xs[V := \text{Addr } a], \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e1))))), \text{None})$ 
 $(\{\!\{ \text{Lock} \rightarrow a, \text{SyncLock } a \}\!\})$ 
 $h ([], xs[V := \text{Addr } a], \text{Suc } (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e1))))), \text{None})$ 
unfolding exec-move-def by -(rule exec-instr, auto simp add: is-Ref-def)
moreover have  $\neg \tau \text{move2 } (\text{compP2 } P) h [\text{Addr } a] (\text{sync}_V (e1) e2) (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e1)))) \text{None}$ 

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by(*simp add: τ move2-iff*)
moreover
from *bisim1Sync4*[*OF bisim1-refl, of P h V e1 e2 a xs[V := Addr a]*]
have $P, \text{sync}_V (e1) e2, h \vdash (\text{insync}_V (a) e2, xs[V := Addr a]) \leftrightarrow ([], xs[V := Addr a], \text{Suc} (\text{Suc} (\text{Suc} (\text{length} (\text{compE2} e1))))), \text{None})$ **by** *simp*
moreover have $\neg \tau \text{move1} P h (\text{sync}_V (\text{addr } a) e2)$ **by**(*auto simp add: τ move1.simps τ moves1.simps*)
ultimately show *?thesis* **by**(*auto simp add: eval-nat-numeral ta-upd-simps*) *blast*
next
case *Synchronized1Null*
note [*simp*] = $\langle v = \text{Null} \rangle \langle e' = \text{THROW NullPointer} \rangle \langle ta = \varepsilon \rangle \langle h' = h \rangle \langle xs' = xs[V := \text{Null}] \rangle$
and $V = \langle V < \text{length } xs \rangle$
from V
have $\tau \text{Exec-mover-a } P t (\text{sync}_V (e1) e2) h ([\text{Null}, \text{Null}], xs, \text{Suc} (\text{length} (\text{compE2} e1)), \text{None})$
 $([\text{Null}], xs[V := \text{Null}], \text{Suc} (\text{Suc} (\text{length} (\text{compE2} e1))), \text{None})$
by $-(\text{rule } \tau \text{Execr1step, auto intro: exec-instr simp add: exec-move-def } \tau \text{move2-iff})$
also have $\text{exec-move-a } P t (\text{sync}_V (e1) e2) h ([\text{Null}], xs[V := \text{Null}], \text{Suc} (\text{Suc} (\text{length} (\text{compE2} e1))), \text{None}) \varepsilon$
 $h ([\text{Null}], xs[V := \text{Null}], \text{Suc} (\text{Suc} (\text{length} (\text{compE2} e1))), [\text{addr-of-sys-xcpt}$
 $\text{NullPointer}]$)
unfolding *exec-move-def* **by**(*rule exec-instr*) *auto*
moreover have $\neg \tau \text{move2} (\text{compP2 } P) h [\text{Null}] (\text{sync}_V (e1) e2) (\text{Suc} (\text{Suc} (\text{length} (\text{compE2} e1)))) \text{None}$
by(*simp add: τ move2-iff*)
moreover
have $P, \text{sync}_V (e1) e2, h \vdash (\text{THROW NullPointer}, xs[V := \text{Null}]) \leftrightarrow ([\text{Null}], (xs[V := \text{Null}]), \text{Suc} (\text{Suc} (\text{length} (\text{compE2} e1))))$, $[\text{addr-of-sys-xcpt NullPointer}]$
by(*rule bisim1Sync11*)
moreover have $\neg \tau \text{move1} P h (\text{sync}_V (\text{null}) e2)$ **by**(*auto simp add: τ move1.simps τ moves1.simps*)
ultimately show *?thesis* **by**(*auto simp add: eval-nat-numeral*) *blast*
qed auto
next
case (*bisim1Sync3 e1 n e2 V v xs*)
note *bisim1* = $\langle \bigwedge xs. P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$
note *bisim2* = $\langle \bigwedge xs. P, e2, h \vdash (e2, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$
from $\langle \text{True}, P, t \vdash 1 \rangle \langle \text{sync}_V (\text{Val } v) e2, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle$ **show** *?case*
proof cases
case (*Lock1Synchronized a*)
note [*simp*] = $\langle v = \text{Addr } a \rangle \langle ta = \{\!\!| \text{Lock} \!\!\rightarrow a, \text{SyncLock } a \!\!\} \rangle \langle e' = \text{insync}_V (a) e2 \rangle \langle h' = h \rangle \langle xs' = xs[V := \text{Addr } a] \rangle$
and $V = \langle V < \text{length } xs \rangle$
have $\text{exec-move-a } P t (\text{sync}_V (e1) e2) h ([\text{Addr } a], xs[V := \text{Addr } a], \text{Suc} (\text{Suc} (\text{length} (\text{compE2} e1))))$, $\text{None})$
 $(\{\!\!| \text{Lock} \!\!\rightarrow a, \text{SyncLock } a \!\!\})$
 $h ([], xs[V := \text{Addr } a], \text{Suc} (\text{Suc} (\text{Suc} (\text{length} (\text{compE2} e1))))$, $\text{None})$
unfolding *exec-move-def* **by** $-(\text{rule } \text{exec-instr, auto simp add: is-Ref-def})$
moreover have $\neg \tau \text{move2} (\text{compP2 } P) h [\text{Addr } a] (\text{sync}_V (e1) e2) (\text{Suc} (\text{Suc} (\text{length} (\text{compE2} e1)))) \text{None}$
by(*simp add: τ move2-iff*)
moreover
from *bisim1Sync4*[*OF bisim1-refl, of P h V e1 e2 a xs[V := Addr a]*]
have $P, \text{sync}_V (e1) e2, h \vdash (\text{insync}_V (a) e2, xs[V := \text{Addr } a]) \leftrightarrow ([], xs[V := \text{Addr } a], \text{Suc} (\text{Suc} (\text{Suc} (\text{length} (\text{compE2} e1))))$, $\text{None})$ **by** *simp*
moreover have $\neg \tau \text{move1} P h (\text{sync}_V (\text{addr } a) e2)$ **by**(*auto simp add: τ move1.simps τ moves1.simps*)
ultimately show *?thesis* **by**(*auto simp add: eval-nat-numeral ta-upd-simps*) *blast*

next
case *Synchronized1Null*
note $[simp] = \langle v = Null \rangle \langle e' = THROW\ NullPointer \rangle \langle ta = \varepsilon \rangle \langle h' = h \rangle \langle xs' = xs[V := Null] \rangle$
and $V = \langle V < length\ xs \rangle$
have $exec-move-a\ P\ t\ (sync_V\ (e1)\ e2)\ h\ ([Null],\ xs[V := Null],\ Suc\ (Suc\ (length\ (compE2\ e1))),\ None)\ \varepsilon$
 $h\ ([Null],\ xs[V := Null],\ Suc\ (Suc\ (length\ (compE2\ e1))),\ [addr-of-sys-xcpt\ NullPointer])$
unfolding *exec-move-def* **by**(*rule exec-instr*) *auto*
moreover **have** $\neg\ \tau move2\ (compP2\ P)\ h\ [Null]\ (sync_V\ (e1)\ e2)\ (Suc\ (Suc\ (length\ (compE2\ e1))))\ None$
by(*simp add: \tau move2-iff*)
moreover
have $P, sync_V\ (e1)\ e2, h \vdash (THROW\ NullPointer,\ xs[V := Null]) \leftrightarrow ([Null],\ (xs[V := Null]),\ Suc\ (Suc\ (length\ (compE2\ e1))),\ [addr-of-sys-xcpt\ NullPointer])$
by(*rule bisim1Sync11*)
moreover **have** $\neg\ \tau move1\ P\ h\ (sync_V\ (null)\ e2)$ **by**(*auto simp add: \tau move1.simps \tau moves1.simps*)
ultimately show *?thesis* **by**(*auto simp add: eval-nat-numeral*) *blast*
qed *auto*

next
case (*bisim1Sync4 e2 n e2' xs stk loc pc xcp e1 V a*)
note $IH = bisim1Sync4.IH(2)$
note $bisim2 = \langle P, e2, h \vdash (e2', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
note $bisim1 = \langle \bigwedge xs. P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, None) \rangle$
note $bsok = \langle bsok\ (sync_V\ (e1)\ e2)\ n \rangle$
note $red = \langle True, P, t \vdash 1 \langle insync_V\ (a)\ e2', (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$
from *red* **show** *?case*
proof *cases*
case (*Synchronized1Red2 E'*)
note $[simp] = \langle e' = insync_V\ (a)\ E' \rangle$
and $red = \langle True, P, t \vdash 1 \langle e2', (h, xs) \rangle -ta \rightarrow \langle E', (h', xs') \rangle \rangle$
from *red* **have** $\tau: \tau move1\ P\ h\ (insync_V\ (a)\ e2') = \tau move1\ P\ h\ e2'$ **by**(*auto simp add: \tau move1.simps \tau moves1.simps*)
from $IH[OF\ red]\ bsok$ **obtain** $pc''\ stk''\ loc''\ xcp''$
where $bisim': P, e2, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$
and $exec': ?exec\ ta\ e2\ e2'\ E'\ h\ stk\ loc\ pc\ xcp\ h'\ pc''\ stk''\ loc''\ xcp''$ **by** *auto*
have $no-call2\ e2\ pc \implies no-call2\ (sync_V\ (e1)\ e2)\ (Suc\ (Suc\ (Suc\ (length\ (compE2\ e1) + pc))))$
by(*auto simp add: no-call2-def*)
hence $?exec\ ta\ (sync_V\ (e1)\ e2)\ (insync_V\ (a)\ e2')\ (insync_V\ (a)\ E')\ h\ stk\ loc\ (Suc\ (Suc\ (Suc\ (length\ (compE2\ e1) + pc))))\ xcp\ h'\ (Suc\ (Suc\ (Suc\ (length\ (compE2\ e1) + pc''))))\ stk''\ loc''\ xcp''$
using $exec'\ \tau$
by(*cases \tau move1\ P\ h\ (insync_V\ (a)\ e2') (auto, (blast intro: exec-move-SyncI2\ Insync-\tau\ ExecrI\ Insync-\tau\ ExectI) +)*)
moreover **from** $bisim'$
have $P, sync_V\ (e1)\ e2, h' \vdash (insync_V\ (a)\ E', xs') \leftrightarrow (stk'', loc'', (Suc\ (Suc\ (Suc\ (length\ (compE2\ e1) + pc'')))), xcp'')$
by(*rule bisim1-bisims1.bisim1Sync4*)
ultimately show *?thesis* **using** τ **by** *auto blast+*

next
case (*Unlock1Synchronized a' v*)
note $[simp] = \langle e2' = Val\ v \rangle \langle e' = Val\ v \rangle \langle ta = \{\!| Unlock \rightarrow a', SyncUnlock\ a' \}\!| \rangle \langle h' = h \rangle \langle xs' = xs \rangle$
and $V = \langle V < length\ xs \rangle$ **and** $xsV = \langle xs ! V = Addr\ a' \rangle$
from $bisim2$ **have** $[simp]: xcp = None\ xs = loc$
and $exec: \tau Exec-mover-a\ P\ t\ e2\ h\ (stk, loc, pc, xcp)\ ([v], loc, length\ (compE2\ e2), None)$

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  by(auto dest: bisim1Val2D1)
let ?pc1 = (Suc (Suc (Suc (length (compE2 e1) + length (compE2 e2))))))
note Insync- $\tau$ ExecrI[OF exec, of V e1]
also from V xsV have  $\tau$ Exec-mover-a P t (syncV (e1) e2) h ([v], loc, ?pc1, None) ([Addr a', v],
loc, Suc ?pc1, None)
  by -(rule  $\tau$ Execr1step,auto simp add: exec-move-def intro: exec-instr  $\tau$ move2- $\tau$ moves2.intros)
  also (rtranclp-trans)
  have exec-move-a P t (syncV (e1) e2) h ([Addr a', v], loc, Suc ?pc1, None) ( $\{\!|$ Unlock $\rightarrow$ a', Syn-
cUnlock a' $\!\}$ ) h ([v], loc, Suc (Suc ?pc1), None)
    unfolding exec-move-def by(rule exec-instr)(auto simp add: is-Ref-def)
  moreover have  $\neg$   $\tau$ move2 (compP2 P) h [Addr a', v] (syncV (e1) e2) (Suc ?pc1) None by(simp
add:  $\tau$ move2-iff)
  moreover
  from bisim1Sync6[of P h V e1 e2 v xs]
  have P, syncV (e1) e2, h  $\vdash$  (Val v, xs)  $\leftrightarrow$  ([v], xs, Suc (Suc ?pc1), None)
    by(auto simp add: eval-nat-numeral)
  moreover have  $\neg$   $\tau$ move1 P h (insyncV (a) e2') by(auto simp add:  $\tau$ move1.simps  $\tau$ moves1.simps)
  ultimately show ?thesis by(auto simp add: ta-upd-simps) blast
next
case (Unlock1SynchronizedNull v)
note [simp] =  $\langle e2' = \text{Val } v \rangle \langle e' = \text{THROW } \text{NullPointer} \rangle \langle ta = \varepsilon \rangle \langle h' = h \rangle \langle xs' = xs \rangle$ 
  and V =  $\langle V < \text{length } xs \rangle$  and xsV =  $\langle xs ! V = \text{Null} \rangle$ 
from bisim2 have [simp]: xcp = None xs = loc
  and exec:  $\tau$ Exec-mover-a P t e2 h (stk, loc, pc, xcp) ([v], loc, length (compE2 e2), None)
  by(auto dest: bisim1Val2D1)
let ?pc1 = (Suc (Suc (Suc (length (compE2 e1) + length (compE2 e2))))))
note Insync- $\tau$ ExecrI[OF exec, of V e1]
also from V xsV have  $\tau$ Exec-mover-a P t (syncV (e1) e2) h ([v], loc, ?pc1, None) ([Null, v], loc,
Suc ?pc1, None)
  by -(rule  $\tau$ Execr1step,auto intro: exec-instr  $\tau$ move2- $\tau$ moves2.intros simp add: exec-move-def)
  also (rtranclp-trans)
  have exec-move-a P t (syncV (e1) e2) h ([Null, v], loc, Suc ?pc1, None)  $\varepsilon$  h ([Null, v], loc, Suc
?pc1, [addr-of-sys-xcpt NullPointer])
    unfolding exec-move-def by(rule exec-instr)(auto simp add: is-Ref-def)
  moreover have  $\neg$   $\tau$ move2 (compP2 P) h [Null, v] (syncV (e1) e2) (Suc ?pc1) None by(simp
add:  $\tau$ move2-iff)
  moreover
  from bisim1Sync12[of P h V e1 e2 addr-of-sys-xcpt NullPointer xs]
  have P, syncV (e1) e2, h  $\vdash$  (THROW NullPointer, xs)  $\leftrightarrow$  ([Null, v], xs, Suc ?pc1, [addr-of-sys-xcpt
NullPointer])
    by(auto simp add: eval-nat-numeral)
  moreover have  $\neg$   $\tau$ move1 P h (insyncV (a) e2') by(auto simp add:  $\tau$ move1.simps  $\tau$ moves1.simps)
  ultimately show ?thesis by auto blast
next
case (Unlock1SynchronizedFail a' v)
note [simp] =  $\langle e2' = \text{Val } v \rangle \langle e' = \text{THROW } \text{IllegalMonitorState} \rangle \langle ta = \{\!|$ UnlockFail $\rightarrow$ a' $\!\}$   $\rangle \langle xs' =$ 
xs  $\rangle \langle h' = h \rangle$ 
  and V =  $\langle V < \text{length } xs \rangle$  and xsV =  $\langle xs ! V = \text{Addr } a' \rangle$ 
from bisim2 have [simp]: xcp = None xs = loc
  and exec:  $\tau$ Exec-mover-a P t e2 h (stk, loc, pc, xcp) ([v], loc, length (compE2 e2), None)
  by(auto dest: bisim1Val2D1)
let ?pc1 = (Suc (Suc (Suc (length (compE2 e1) + length (compE2 e2))))))
note Insync- $\tau$ ExecrI[OF exec, of V e1]
also from V xsV have  $\tau$ Exec-mover-a P t (syncV (e1) e2) h ([v], loc, ?pc1, None) ([Addr a', v],

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$loc, Suc \ ?pc1, None)$
by $-(rule \ \tau Execr1step, auto \ intro: \ exec\text{-instr} \ \tau move2\text{-}\tau moves2.intros \ simp \ add: \ exec\text{-move}\text{-def})$
also $(rtranclp\text{-trans})$
have $exec\text{-move}\text{-a} \ P \ t \ (sync_V \ (e1) \ e2) \ h \ ([Addr \ a', \ v], \ loc, \ Suc \ ?pc1, \ None) \ \{\!| \ UnlockFail \rightarrow a' \!\}$ h
 $([Addr \ a', \ v], \ loc, \ Suc \ ?pc1, \ [addr\text{-of}\text{-sys}\text{-xcpt} \ IllegalMonitorState])$
unfolding $exec\text{-move}\text{-def}$ **by** $(rule \ exec\text{-instr})(auto \ simp \ add: \ is\text{-Ref}\text{-def})$
moreover **have** $\neg \ \tau move2 \ (compP2 \ P) \ h \ [Addr \ a', \ v] \ (sync_V \ (e1) \ e2) \ (Suc \ ?pc1) \ None$ **by** $(simp \ add: \ \tau move2\text{-iff})$
moreover
from $bisim1Sync12[of \ P \ h \ V \ e1 \ e2 \ addr\text{-of}\text{-sys}\text{-xcpt} \ IllegalMonitorState \ xs \ Addr \ a' \ v]$
have $P, sync_V \ (e1) \ e2, h \vdash \ (THROW \ IllegalMonitorState, xs) \leftrightarrow \ ([Addr \ a', \ v], xs, Suc \ ?pc1, [addr\text{-of}\text{-sys}\text{-xcpt} \ IllegalMonitorState])$
by $(auto \ simp \ add: \ eval\text{-nat}\text{-numeral})$
moreover **have** $\neg \ \tau move1 \ P \ h \ (insync_V \ (a) \ Val \ v)$ **by** $(auto \ simp \ add: \ \tau move1.simps \ \tau moves1.simps)$
ultimately show $?thesis$ **by** $(auto \ simp \ add: \ ta\text{-upd}\text{-simps}) \ blast$
next
case $(Synchronized1Throw2 \ a' \ ad)$
note $[simp] = \langle e2' = Throw \ ad \rangle \langle ta = \{\!| \ Unlock \rightarrow a' \!\}, SyncUnlock \ a' \!\rangle \langle e' = Throw \ ad \rangle$
 $\langle h' = h \rangle \langle xs' = xs \rangle$ **and** $xsV = \langle xs \ ! \ V = Addr \ a' \rangle$ **and** $V = \langle V < length \ xs \rangle$
let $?pc = 6 + length \ (compE2 \ e1) + length \ (compE2 \ e2)$
let $?stk = Addr \ ad \ \# \ drop \ (size \ stk - 0) \ stk$
from $bisim2$ **have** $[simp]: \ xs = loc$ **by** $(auto \ dest: \ bisim1\text{-Throw}D)$
from $bisim2$
have $\tau Exec\text{-movet}\text{-a} \ P \ t \ (sync_V \ (e1) \ e2) \ h \ (stk, \ loc, \ Suc \ (Suc \ (Suc \ (length \ (compE2 \ e1) + pc))),$
 $xcp) \ ([Addr \ ad], \ loc, \ ?pc, \ None)$
by $(auto \ intro: \ bisim1\text{-insync}\text{-Throw}\text{-exec})$
also **from** $xsV \ V$
have $\tau Exec\text{-movet}\text{-a} \ P \ t \ (sync_V \ (e1) \ e2) \ h \ ([Addr \ ad], \ loc, \ ?pc, \ None) \ ([Addr \ a', \ Addr \ ad], \ loc,$
 $Suc \ ?pc, \ None)$
by $-(rule \ \tau Exec1step, auto \ intro: \ exec\text{-instr} \ \tau move2Sync7 \ simp \ add: \ exec\text{-move}\text{-def})$
also $(tranclp\text{-trans})$
have $exec\text{-move}\text{-a} \ P \ t \ (sync_V \ (e1) \ e2) \ h \ ([Addr \ a', \ Addr \ ad], \ loc, \ Suc \ ?pc, \ None) \ (\{\!| \ Unlock \rightarrow a' \!\},$
 $SyncUnlock \ a' \!\}) \ h \ ([Addr \ ad], \ loc, \ Suc \ (Suc \ ?pc), \ None)$
unfolding $exec\text{-move}\text{-def}$ **by** $(rule \ exec\text{-instr})(auto \ simp \ add: \ is\text{-Ref}\text{-def})$
moreover **have** $\neg \ \tau move2 \ (compP2 \ P) \ h \ [Addr \ a', \ Addr \ ad] \ (sync_V \ (e1) \ e2) \ (Suc \ ?pc) \ None$
by $(simp \ add: \ \tau move2\text{-iff})$
moreover
hence $P, \ sync_V \ (e1) \ e2, \ h \vdash \ (Throw \ ad, \ loc) \leftrightarrow \ ([Addr \ ad], \ loc, \ 8 + length \ (compE2 \ e1) + length$
 $(compE2 \ e2), \ None)$
by $(auto \ intro: \ bisim1Sync9)$
moreover **have** $\neg \ \tau move1 \ P \ h \ (insync_V \ (a) \ Throw \ ad)$ **by** $(auto \ simp \ add: \ \tau move1.simps \ \tau moves1.simps)$
ultimately show $?thesis$ **by** $(auto \ simp \ add: \ add.assoc \ ta\text{-upd}\text{-simps})(blast \ intro: \ tranclp\text{-into}\text{-rtranclp})$
next
case $(Synchronized1Throw2Fail \ a' \ ad)$
note $[simp] = \langle e2' = Throw \ ad \rangle \langle ta = \{\!| \ UnlockFail \rightarrow a' \!\}\rangle \langle e' = THROW \ IllegalMonitorState \rangle \langle h' = h \rangle$
 $\langle xs' = xs \rangle$
and $xsV = \langle xs \ ! \ V = Addr \ a' \rangle$ **and** $V = \langle V < length \ xs \rangle$
let $?pc = 6 + length \ (compE2 \ e1) + length \ (compE2 \ e2)$
let $?stk = Addr \ ad \ \# \ drop \ (size \ stk - 0) \ stk$
from $bisim2$ **have** $[simp]: \ xs = loc$ **by** $(auto \ dest: \ bisim1\text{-Throw}D)$
from $bisim2$
have $\tau Exec\text{-movet}\text{-a} \ P \ t \ (sync_V \ (e1) \ e2) \ h \ (stk, \ loc, \ Suc \ (Suc \ (Suc \ (length \ (compE2 \ e1) + pc))),$
 $xcp) \ ([Addr \ ad], \ loc, \ ?pc, \ None)$


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  by(auto intro: bisim1-insync-Throw-exec)
also from xsV V
have  $\tau$ Exec-movet-a P t (syncV (e1) e2) h ([Addr ad], loc, ?pc, None) ([Addr a', Addr ad], loc,
Suc ?pc, None)
  by -(rule  $\tau$ Exec1step,auto intro: exec-instr  $\tau$ move2Sync7 simp add: exec-move-def)
also (tranclp-trans)
have exec-move-a P t (syncV (e1) e2) h ([Addr a', Addr ad], loc, Suc ?pc, None)  $\{\!|$ UnlockFail $\rightarrow$ a' $\!\}$ 
h ([Addr a', Addr ad], loc, Suc ?pc, [addr-of-sys-xcpt IllegalMonitorState])
  unfolding exec-move-def by(rule exec-instr)(auto simp add: is-Ref-def)
moreover have  $\neg$   $\tau$ move2 (compP2 P) h [Addr a', Addr ad] (syncV (e1) e2) (Suc ?pc) None
by(simp add:  $\tau$ move2-iff)
moreover
hence P, syncV (e1) e2, h  $\vdash$  (THROW IllegalMonitorState, loc)  $\leftrightarrow$  ([Addr a', Addr ad], loc, 7 +
length (compE2 e1) + length (compE2 e2), [addr-of-sys-xcpt IllegalMonitorState])
  by(auto intro: bisim1Sync14)
moreover have  $\neg$   $\tau$ move1 P h (insyncV (a) e2') by(auto simp add:  $\tau$ move1.simps  $\tau$ moves1.simps)
ultimately show ?thesis by(auto simp add: add.assoc ta-upd-simps)(blast intro: tranclp-into-rtranclp)
next
case (Synchronized1Throw2Null ad)
note [simp] =  $\langle$ e2' = Throw ad $\rangle$   $\langle$ ta =  $\varepsilon$  $\rangle$   $\langle$ e' = THROW NullPointer $\rangle$   $\langle$ h' = h $\rangle$   $\langle$ xs' = xs $\rangle$ 
  and xsV =  $\langle$ xs ! V = Null $\rangle$  and V =  $\langle$ V < length xs $\rangle$ 
let ?pc = 6 + length (compE2 e1) + length (compE2 e2)
let ?stk = Addr ad # drop (size stk - 0) stk
from bisim2 have [simp]: xs = loc by(auto dest: bisim1-ThrowD)
from bisim2
have  $\tau$ Exec-movet-a P t (syncV (e1) e2) h (stk, loc, Suc (Suc (Suc (length (compE2 e1) + pc))),
xcp) ([Addr ad], loc, ?pc, None)
  by(auto intro: bisim1-insync-Throw-exec)
also from xsV V
have  $\tau$ Exec-movet-a P t (syncV (e1) e2) h ([Addr ad], loc, ?pc, None) ([Null, Addr ad], loc, Suc
?pc, None)
  by -(rule  $\tau$ Exec1step,auto intro: exec-instr simp add: exec-move-def  $\tau$ move2-iff)
also (tranclp-trans)
have exec-move-a P t (syncV (e1) e2) h ([Null, Addr ad], loc, Suc ?pc, None)  $\varepsilon$  h ([Null, Addr
ad], loc, Suc ?pc, [addr-of-sys-xcpt NullPointer])
  unfolding exec-move-def by(rule exec-instr)(auto simp add: is-Ref-def)
moreover have  $\neg$   $\tau$ move2 (compP2 P) h [Null, Addr ad] (syncV (e1) e2) (Suc ?pc) None by(simp
add:  $\tau$ move2-iff)
moreover
hence P, syncV (e1) e2, h  $\vdash$  (THROW NullPointer, loc)  $\leftrightarrow$  ([Null, Addr ad], loc, 7 + length
(compE2 e1) + length (compE2 e2), [addr-of-sys-xcpt NullPointer])
  by(auto intro: bisim1Sync14)
moreover have  $\neg$   $\tau$ move1 P h (insyncV (a) e2') by(auto simp add:  $\tau$ move1.simps  $\tau$ moves1.simps)
ultimately show ?thesis by(auto simp add: add.assoc)(blast intro: tranclp-into-rtranclp)
qed
next
case (bisim1Sync5 e1 n e2 V a v xs)
note bisim2 =  $\langle$  $\bigwedge$ xs. P,e2,h  $\vdash$  (e2, xs)  $\leftrightarrow$  ([], xs, 0, None) $\rangle$ 
note bisim1 =  $\langle$  $\bigwedge$ xs. P,e1,h  $\vdash$  (e1, xs)  $\leftrightarrow$  ([], xs, 0, None) $\rangle$ 
from  $\langle$ True,P,t  $\vdash$ 1  $\langle$ insyncV (a) Val v,(h, xs) $\rangle$   $\rightarrow$ ta $\rightarrow$   $\langle$ e',(h', xs') $\rangle$  $\rangle$  show ?case
proof cases
case (Unlock1Synchronized a')
note [simp] =  $\langle$ e' = Val v $\rangle$   $\langle$ ta =  $\{\!|$ Unlock $\rightarrow$ a', SyncUnlock a' $\!\}$  $\rangle$   $\langle$ h' = h $\rangle$   $\langle$ xs' = xs $\rangle$ 
  and V =  $\langle$ V < length xs $\rangle$  and xsV =  $\langle$ xs ! V = Addr a' $\rangle$ 

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let ?pc1 = 4 + length (compE2 e1) + length (compE2 e2)
have exec-move-a P t (sync_V (e1) e2) h ([Addr a', v], xs, ?pc1, None) {Unlock→a', SyncUnlock
a'} h ([v], xs, Suc ?pc1, None)
  unfolding exec-move-def by(rule exec-instr)(auto simp add: is-Ref-def)
  moreover have ¬ τmove2 (compP2 P) h [Addr a', v] (sync_V (e1) e2) ?pc1 None by(simp add:
τmove2-iff)
  moreover
  from bisim1Sync6[of P h V e1 e2 v xs]
  have P,sync_V (e1) e2,h ⊢ (Val v, xs) ↔ ([v], xs, Suc ?pc1, None)
    by(auto simp add: eval-nat-numeral)
  moreover have ¬ τmove1 P h (insync_V (a) Val v) by(auto simp add: τmove1.simps τmoves1.simps)
  ultimately show ?thesis using xsV by(auto simp add: eval-nat-numeral ta-upd-simps) blast
next
case Unlock1SynchronizedNull
note [simp] = ⟨e' = THROW NullPointer⟩ ⟨ta = ε⟩ ⟨h' = h⟩ ⟨xs' = xs⟩
  and V = ⟨V < length xs⟩ and xsV = ⟨xs ! V = Null⟩
let ?pc1 = 4 + length (compE2 e1) + length (compE2 e2)
have exec-move-a P t (sync_V (e1) e2) h ([Null, v], xs, ?pc1, None) ε h ([Null, v], xs, ?pc1,
[addr-of-sys-xcpt NullPointer])
  unfolding exec-move-def by(rule exec-instr)(auto simp add: is-Ref-def)
  moreover have ¬ τmove2 (compP2 P) h [Null, v] (sync_V (e1) e2) ?pc1 None by(simp add:
τmove2-iff)
  moreover
  from bisim1Sync12[of P h V e1 e2 addr-of-sys-xcpt NullPointer xs Null v]
  have P,sync_V (e1) e2,h ⊢ (THROW NullPointer,xs) ↔ ([Null, v],xs,?pc1,[addr-of-sys-xcpt Null-
Pointer])
    by(auto simp add: eval-nat-numeral)
  moreover have ¬ τmove1 P h (insync_V (a) Val v) by(auto simp add: τmove1.simps τmoves1.simps)
  ultimately show ?thesis using xsV by(auto simp add: eval-nat-numeral) blast
next
case (Unlock1SynchronizedFail a')
note [simp] = ⟨e' = THROW IllegalMonitorState⟩ ⟨ta = {UnlockFail→a'}⟩ ⟨xs' = xs⟩ ⟨h' = h⟩
  and V = ⟨V < length xs⟩ and xsV = ⟨xs ! V = Addr a'⟩
let ?pc1 = 4 + length (compE2 e1) + length (compE2 e2)
have exec-move-a P t (sync_V (e1) e2) h ([Addr a', v], xs, ?pc1, None) {UnlockFail→a'} h ([Addr
a', v], xs, ?pc1, [addr-of-sys-xcpt IllegalMonitorState])
  unfolding exec-move-def by(rule exec-instr)(auto simp add: is-Ref-def)
  moreover have ¬ τmove2 (compP2 P) h [Addr a', v] (sync_V (e1) e2) ?pc1 None by(simp add:
τmove2-iff)
  moreover
  from bisim1Sync12[of P h V e1 e2 addr-of-sys-xcpt IllegalMonitorState xs Addr a' v]
  have P,sync_V (e1) e2,h ⊢ (THROW IllegalMonitorState,xs) ↔ ([Addr a', v],xs,?pc1,[addr-of-sys-xcpt
IllegalMonitorState])
    by(auto simp add: eval-nat-numeral)
  moreover have ¬ τmove1 P h (insync_V (a) Val v) by(auto simp add: τmove1.simps τmoves1.simps)
  ultimately show ?thesis using xsV by(auto simp add: eval-nat-numeral ta-upd-simps) blast
qed auto
next
case bisim1Sync6 thus ?case by auto
next
case (bisim1Sync7 e1 n e2 V a ad xs)
note bisim2 = ⟨∧xs. P,e2,h ⊢ (e2, xs) ↔ ([], xs, 0, None)⟩
note bisim1 = ⟨∧xs. P,e1,h ⊢ (e1, xs) ↔ ([], xs, 0, None)⟩
from ⟨True,P,t ⊢ 1 (insync_V (a) Throw ad,(h, xs)) -ta→ ⟨e',(h', xs')⟩ show ?case

```

proof cases

case (*Synchronized1Throw2 a'*)

note [*simp*] = $\langle ta = \{\!| \text{Unlock} \rightarrow a' \!\}, \text{SyncUnlock } a' \!\} \rangle \langle e' = \text{Throw } ad \rangle \langle h' = h \rangle \langle xs' = xs \rangle$

and $xsV = \langle xs ! V = \text{Addr } a' \rangle$ **and** $V = \langle V < \text{length } xs \rangle$

let $?pc = 6 + \text{length } (\text{compE2 } e1) + \text{length } (\text{compE2 } e2)$

from $xsV V$

have $\tau \text{Exec-mover-a } P t (\text{sync}_V (e1) e2) h ([\text{Addr } ad], xs, ?pc, \text{None}) ([\text{Addr } a', \text{Addr } ad], xs, \text{Suc } ?pc, \text{None})$

by $-(\text{rule } \tau \text{Execr1step,auto intro: exec-instr simp add: exec-move-def } \tau \text{move2-iff})$

moreover have $\text{exec-move-a } P t (\text{sync}_V (e1) e2) h ([\text{Addr } a', \text{Addr } ad], xs, \text{Suc } ?pc, \text{None}) \{\!| \text{Unlock} \rightarrow a' \!\} h ([\text{Addr } ad], xs, \text{Suc } (\text{Suc } ?pc), \text{None})$

unfolding *exec-move-def* **by** $(\text{rule } \text{exec-instr})(\text{auto simp add: is-Ref-def})$

moreover have $\neg \tau \text{move2 } (\text{compP2 } P) h [\text{Addr } a', \text{Addr } ad] (\text{sync}_V (e1) e2) (\text{Suc } ?pc) \text{None}$

by $(\text{simp add: } \tau \text{move2-iff})$

moreover

have $P, \text{sync}_V (e1) e2, h \vdash (\text{Throw } ad, xs) \leftrightarrow ([\text{Addr } ad], xs, 8 + \text{length } (\text{compE2 } e1) + \text{length } (\text{compE2 } e2), \text{None})$

by $(\text{auto intro: bisim1Sync9})$

moreover have $\neg \tau \text{move1 } P h (\text{insync}_V (a) \text{Throw } ad)$ **by** $(\text{auto simp add: } \tau \text{move1.simps } \tau \text{moves1.simps})$

ultimately show *?thesis* **by** $(\text{auto simp add: add.assoc eval-nat-numeral ta-upd-simps})$ **blast**

next

case (*Synchronized1Throw2Fail a'*)

note [*simp*] = $\langle ta = \{\!| \text{UnlockFail} \rightarrow a' \!\} \rangle \langle e' = \text{THROW IllegalMonitorState} \rangle \langle h' = h \rangle \langle xs' = xs \rangle$

and $xsV = \langle xs ! V = \text{Addr } a' \rangle$ **and** $V = \langle V < \text{length } xs \rangle$

let $?pc = 6 + \text{length } (\text{compE2 } e1) + \text{length } (\text{compE2 } e2)$

from $xsV V$

have $\tau \text{Exec-mover-a } P t (\text{sync}_V (e1) e2) h ([\text{Addr } ad], xs, ?pc, \text{None}) ([\text{Addr } a', \text{Addr } ad], xs, \text{Suc } ?pc, \text{None})$

by $-(\text{rule } \tau \text{Execr1step,auto intro: exec-instr simp add: exec-move-def } \tau \text{move2-iff})$

moreover have $\text{exec-move-a } P t (\text{sync}_V (e1) e2) h ([\text{Addr } a', \text{Addr } ad], xs, \text{Suc } ?pc, \text{None}) \{\!| \text{UnlockFail} \rightarrow a' \!\} h ([\text{Addr } a', \text{Addr } ad], xs, \text{Suc } ?pc, [\text{addr-of-sys-xcpt IllegalMonitorState}])$

unfolding *exec-move-def* **by** $(\text{rule } \text{exec-instr})(\text{auto simp add: is-Ref-def})$

moreover have $\neg \tau \text{move2 } (\text{compP2 } P) h [\text{Addr } a', \text{Addr } ad] (\text{sync}_V (e1) e2) (\text{Suc } ?pc) \text{None}$

by $(\text{simp add: } \tau \text{move2-iff})$

moreover

have $P, \text{sync}_V (e1) e2, h \vdash (\text{THROW IllegalMonitorState}, xs) \leftrightarrow ([\text{Addr } a', \text{Addr } ad], xs, 7 + \text{length } (\text{compE2 } e1) + \text{length } (\text{compE2 } e2), [\text{addr-of-sys-xcpt IllegalMonitorState}])$

by $(\text{auto intro: bisim1Sync14})$

moreover have $\neg \tau \text{move1 } P h (\text{insync}_V (a) \text{Throw } ad)$ **by** $(\text{auto simp add: } \tau \text{move1.simps } \tau \text{moves1.simps})$

ultimately show *?thesis* **by** $(\text{auto simp add: add.assoc ta-upd-simps})$ **blast**

next

case *Synchronized1Throw2Null*

note [*simp*] = $\langle ta = \varepsilon \rangle \langle e' = \text{THROW NullPointer} \rangle \langle h' = h \rangle \langle xs' = xs \rangle$

and $xsV = \langle xs ! V = \text{Null} \rangle$ **and** $V = \langle V < \text{length } xs \rangle$

let $?pc = 6 + \text{length } (\text{compE2 } e1) + \text{length } (\text{compE2 } e2)$

from $xsV V$

have $\tau \text{Exec-mover-a } P t (\text{sync}_V (e1) e2) h ([\text{Addr } ad], xs, ?pc, \text{None}) ([\text{Null}, \text{Addr } ad], xs, \text{Suc } ?pc, \text{None})$

by $-(\text{rule } \tau \text{Execr1step,auto intro: exec-instr simp add: exec-move-def } \tau \text{move2-iff})$

moreover have $\text{exec-move-a } P t (\text{sync}_V (e1) e2) h ([\text{Null}, \text{Addr } ad], xs, \text{Suc } ?pc, \text{None}) \varepsilon h ([\text{Null}, \text{Addr } ad], xs, \text{Suc } ?pc, [\text{addr-of-sys-xcpt NullPointer}])$

unfolding *exec-move-def* **by** $(\text{rule } \text{exec-instr})(\text{auto simp add: is-Ref-def})$

moreover have $\neg \tau \text{move2} (\text{compP2 } P) h [\text{Null}, \text{Addr } ad] (\text{sync}_V (e1) e2) (\text{Suc } ?pc) \text{None}$ **by**(*simp* *add: \tau move2-iff*)

moreover

have $P, \text{sync}_V (e1) e2, h \vdash (\text{THROW } \text{NullPointer}, xs) \leftrightarrow ([\text{Null}, \text{Addr } ad], xs, 7 + \text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2), [\text{addr-of-sys-xcpt } \text{NullPointer}])$

by(*auto intro: bisim1Sync14*)

moreover have $\neg \tau \text{move1 } P h (\text{insync}_V (a) \text{Throw } ad)$ **by**(*auto simp add: \tau move1.simps \tau moves1.simps*)

ultimately show *?thesis* **by**(*auto simp add: add.assoc*) *blast*

qed auto

next

case (*bisim1Sync8 e1 n e2 V a ad xs*)

note $\text{bisim2} = \langle \bigwedge xs. P, e2, h \vdash (e2, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$

note $\text{bisim1} = \langle \bigwedge xs. P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$

from $\langle \text{True}, P, t \vdash 1 \langle \text{insync}_V (a) \text{Throw } ad, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$ **show** *?case*

proof cases

case (*Synchronized1Throw2 a'*)

note $[\text{simp}] = \langle ta = \{\!\!| \text{Unlock} \rightarrow a' \}\!\!\}, \text{SyncUnlock } a' \rangle \langle e' = \text{Throw } ad \rangle \langle h' = h \rangle \langle xs' = xs \rangle$

and $xsV = \langle xs ! V = \text{Addr } a' \rangle$ **and** $V = \langle V < \text{length } xs \rangle$

let $?pc = 7 + \text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2)$

have *exec-move-a P t (sync_V (e1) e2) h ([Addr a', Addr ad], xs, ?pc, None) \{\!\!| \text{Unlock} \rightarrow a', \text{SyncUnlock } a' \}\!\!\} h ([Addr ad], xs, \text{Suc } ?pc, None)*

unfolding *exec-move-def* **by**(*rule exec-instr*)(*auto simp add: is-Ref-def*)

moreover have $\neg \tau \text{move2} (\text{compP2 } P) h [\text{Addr } a', \text{Addr } ad] (\text{sync}_V (e1) e2) ?pc \text{None}$ **by**(*simp add: \tau move2-iff*)

moreover

have $P, \text{sync}_V (e1) e2, h \vdash (\text{Throw } ad, xs) \leftrightarrow ([\text{Addr } ad], xs, 8 + \text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2), \text{None})$

by(*auto intro: bisim1Sync9*)

moreover have $\neg \tau \text{move1 } P h (\text{insync}_V (a) \text{Throw } ad)$ **by**(*auto simp add: \tau move1.simps \tau moves1.simps*)

ultimately show *?thesis using xsV* **by**(*auto simp add: add.assoc eval-nat-numeral ta-upd-simps*) *blast*

next

case (*Synchronized1Throw2Fail a'*)

note $[\text{simp}] = \langle ta = \{\!\!| \text{UnlockFail} \rightarrow a' \}\!\!\} \langle e' = \text{THROW } \text{IllegalMonitorState} \rangle \langle h' = h \rangle \langle xs' = xs \rangle$

and $xsV = \langle xs ! V = \text{Addr } a' \rangle$ **and** $V = \langle V < \text{length } xs \rangle$

let $?pc = 7 + \text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2)$

have *exec-move-a P t (sync_V (e1) e2) h ([Addr a', Addr ad], xs, ?pc, None) \{\!\!| \text{UnlockFail} \rightarrow a' \}\!\!\} h ([Addr a', Addr ad], xs, ?pc, [\text{addr-of-sys-xcpt } \text{IllegalMonitorState}])*

unfolding *exec-move-def* **by**(*rule exec-instr*)(*auto simp add: is-Ref-def*)

moreover have $\neg \tau \text{move2} (\text{compP2 } P) h [\text{Addr } a', \text{Addr } ad] (\text{sync}_V (e1) e2) ?pc \text{None}$ **by**(*simp add: \tau move2-iff*)

moreover

have $P, \text{sync}_V (e1) e2, h \vdash (\text{THROW } \text{IllegalMonitorState}, xs) \leftrightarrow ([\text{Addr } a', \text{Addr } ad], xs, ?pc, [\text{addr-of-sys-xcpt } \text{IllegalMonitorState}])$

by(*auto intro: bisim1Sync14*)

moreover have $\neg \tau \text{move1 } P h (\text{insync}_V (a) \text{Throw } ad)$ **by**(*auto simp add: \tau move1.simps \tau moves1.simps*)

ultimately show *?thesis using xsV* **by**(*auto simp add: add.assoc ta-upd-simps*) *blast*

next

case *Synchronized1Throw2Null*

note $[\text{simp}] = \langle ta = \varepsilon \rangle \langle e' = \text{THROW } \text{NullPointer} \rangle \langle h' = h \rangle \langle xs' = xs \rangle$

and $xsV = \langle xs ! V = \text{Null} \rangle$ **and** $V = \langle V < \text{length } xs \rangle$

```

let ?pc =  $\gamma$  + length (compE2 e1) + length (compE2 e2)
have exec-move-a P t (syncV (e1) e2) h ([Null, Addr ad], xs, ?pc, None)  $\varepsilon$  h ([Null, Addr ad], xs,
?pc, [addr-of-sys-xcpt NullPointer])
  unfolding exec-move-def by(rule exec-instr)(auto simp add: is-Ref-def)
  moreover have  $\neg \tau$ move2 (compP2 P) h [Null, Addr ad] (syncV (e1) e2) ?pc None by(simp
add:  $\tau$ move2-iff)
  moreover
  have P, syncV (e1) e2, h  $\vdash$  (THROW NullPointer, xs)  $\leftrightarrow$  ([Null, Addr ad], xs, ?pc, [addr-of-sys-xcpt
NullPointer])
    by(auto intro: bisim1Sync14)
    moreover have  $\neg \tau$ move1 P h (insyncV (a) Throw ad) by(auto simp add:  $\tau$ move1.simps
 $\tau$ moves1.simps)
      ultimately show ?thesis using xsV by(auto simp add: add.assoc) blast
qed auto
next
case bisim1Sync9 thus ?case by auto
next
case bisim1Sync10 thus ?case by auto
next
case bisim1Sync11 thus ?case by auto
next
case bisim1Sync12 thus ?case by auto
next
case bisim1Sync14 thus ?case by auto
next
case bisim1SyncThrow thus ?case by auto
next
case bisim1InSync thus ?case by simp
next
case (bisim1Seq1 e1 n e1' xs stk loc pc xcp e2)
note IH = bisim1Seq1.IH(2)
note bisim1 =  $\langle P, e1, h \vdash (e1', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
note bisim2 =  $\langle \bigwedge xs. P, e2, h \vdash (e2, xs) \leftrightarrow ([], xs, 0, None) \rangle$ 
note red =  $\langle True, P, t \vdash 1 \langle e1';; e2, (h, xs) \rangle -ta\rightarrow \langle e', (h', xs') \rangle \rangle$ 
note bsok =  $\langle bsok (e1;; e2) n \rangle$ 
from red show ?case
proof cases
  case (Seq1Red E')
  note [simp] =  $\langle e' = E';; e2 \rangle$ 
  and red =  $\langle True, P, t \vdash 1 \langle e1', (h, xs) \rangle -ta\rightarrow \langle E', (h', xs') \rangle \rangle$ 
  from red have  $\tau: \tau$ move1 P h (e1';; e2) =  $\tau$ move1 P h e1' by(auto simp add:  $\tau$ move1.simps
 $\tau$ moves1.simps)
  moreover have call1 (e1';; e2) = call1 e1' by auto
  moreover from IH[OF red] bsok
  obtain pc'' stk'' loc'' xcp'' where bisim: P, e1, h'  $\vdash$  (E', xs')  $\leftrightarrow$  (stk'', loc'', pc'', xcp'')
  and redo: ?exec ta e1 e1' E' h stk loc pc xcp h' pc'' stk'' loc'' xcp'' by auto
  from bisim have P, e1;; e2, h'  $\vdash$  (E';; e2, xs')  $\leftrightarrow$  (stk'', loc'', pc'', xcp'')
  by(rule bisim1-bisims1.bisim1Seq1)
  moreover {
    assume no-call2 e1 pc
    hence no-call2 (e1;; e2) pc by(auto simp add: no-call2-def) }
  ultimately show ?thesis using redo
  by(auto simp del: call1.simps calls1.simps split: if-split-asm split del: if-split)(blast intro:
Seq- $\tau$ ExecrI1 Seq- $\tau$ ExecI1 exec-move-SeqI1)+

```

```

next
  case (Red1Seq v)
  note [simp] = ⟨e1' = Val v⟩ ⟨ta = ε⟩ ⟨h' = h⟩ ⟨xs' = xs⟩ ⟨e' = e2⟩
  from bisim1 have s: xcp = None xs = loc
    and τExec-mover-a P t e1 h (stk, loc, pc, xcp) ([v], loc, length (compE2 e1), None)
    by(auto dest: bisim1Val2D1)
  hence τExec-mover-a P t (e1;; e2) h (stk, loc, pc, xcp) ([v], loc, length (compE2 e1), None)
    by-(rule Seq-τExecrI1)
  moreover have exec-move-a P t (e1;; e2) h ([v], loc, length (compE2 e1), None) ε h ([], loc, Suc
    (length (compE2 e1)), None)
    unfolding exec-move-def by(rule exec-instr, auto)
  moreover have τmove2 (compP2 P) h [v] (e1;;e2) (length (compE2 e1)) None by(simp add:
    τmove2-iff)
  ultimately have τExec-mover-a P t (e1;; e2) h (stk, loc, pc, xcp) ([], loc, Suc (length (compE2
    e1)), None)
    by(auto intro: rtrancl.rtrancl-into-rtrancl τexec-moveI simp add: compP2-def)
  moreover from bisim1-refl
  have P, e1;; e2, h ⊢ (e2, xs) ↔ ([], loc, Suc (length (compE2 e1) + 0), None)
    unfolding s by(rule bisim1Seq2)
  moreover have τmove1 P h (Val v;; e2) by(rule τmove1SeqRed)
  ultimately show ?thesis by(auto)
next
  case (Seq1Throw a)
  note [simp] = ⟨e1' = Throw a⟩ ⟨ta = ε⟩ ⟨e' = Throw a⟩ ⟨h' = h⟩ ⟨xs' = xs⟩
  have τ: τmove1 P h (Throw a;; e2) by(rule τmove1SeqThrow)
  from bisim1 have xcp = [a] ∨ xcp = None by(auto dest: bisim1-ThrowD)
  thus ?thesis
  proof
    assume [simp]: xcp = [a]
    with bisim1 have P, e1;; e2, h ⊢ (Throw a, xs) ↔ (stk, loc, pc, xcp)
      by(auto intro: bisim1SeqThrow1)
    thus ?thesis using τ by(fastforce)
  next
    assume [simp]: xcp = None
    with bisim1 obtain pc'
      where τExec-mover-a P t e1 h (stk, loc, pc, None) ([Addr a], loc, pc', [a])
        and bisim': P, e1, h ⊢ (Throw a, xs) ↔ ([Addr a], loc, pc', [a]) and [simp]: xs = loc
        by(auto dest: bisim1-Throw-τExec-mover)
    hence τExec-mover-a P t (e1;;e2) h (stk, loc, pc, None) ([Addr a], loc, pc', [a])
      by-(rule Seq-τExecrI1)
    moreover from bisim'
    have P, e1;;e2, h ⊢ (Throw a, xs) ↔ ([Addr a], loc, pc', [a])
      by(rule bisim1SeqThrow1)
    ultimately show ?thesis using τ by auto
  qed
  qed
next
  case bisim1SeqThrow1 thus ?case by fastforce
next
  case (bisim1Seq2 e2 n e2' xs stk loc pc xcp e1)
  note IH = bisim1Seq2.IH(2)
  note bisim2 = ⟨P, e2, h ⊢ (e2', xs) ↔ (stk, loc, pc, xcp)⟩
  note bisim1 = ⟨∧xs. P, e1, h ⊢ (e1, xs) ↔ ([], xs, 0, None)⟩
  note red = ⟨True, P, t ⊢ 1 ⟨e2', (h, xs)⟩ -ta→ ⟨e', (h', xs')⟩⟩

```

note $bsok = \langle bsok (e1;; e2) n \rangle$
from $IH[OF\ red] bsok$ **obtain** $pc''\ stk''\ loc''\ xcp''$
where $bisim': P, e2, h' \vdash (e', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$
and $exec': ?exec\ ta\ e2\ e2'\ e'\ h\ stk\ loc\ pc\ xcp\ h'\ pc''\ stk''\ loc''\ xcp''$ **by** $auto$
have $no-call2\ e2\ pc \implies no-call2\ (e1;; e2)\ (Suc\ (length\ (compE2\ e1) + pc))$
by $(auto\ simp\ add: no-call2-def)$
hence $?exec\ ta\ (e1;; e2)\ e2'\ e'\ h\ stk\ loc\ (Suc\ (length\ (compE2\ e1) + pc))\ xcp\ h'\ (Suc\ (length\ (compE2\ e1) + pc''))\ stk''\ loc''\ xcp''$
using $exec'$ **by** $(cases\ \tau\ move1\ P\ h\ e2)\ (auto, (blast\ intro: Seq-\tau\ ExecrI2\ Seq-\tau\ ExecI2\ exec-move-SeqI2)+)$
moreover from $bisim'$
have $P, e1;; e2, h' \vdash (e', xs') \leftrightarrow (stk'', loc'', Suc\ (length\ (compE2\ e1) + pc''), xcp'')$
by $(rule\ bisim1-bisims1.bisim1Seq2)$
ultimately show $?case$ **by** $(auto\ split: if-split-asm)\ blast+$
next
case $(bisim1Cond1\ E\ n\ e\ xs\ stk\ loc\ pc\ xcp\ e1\ e2)$
note $IH = bisim1Cond1.IH(2)$
note $bisim = \langle P, E, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
note $bisim1 = \langle \bigwedge xs. P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, None) \rangle$
note $bisim2 = \langle \bigwedge xs. P, e2, h \vdash (e2, xs) \leftrightarrow ([], xs, 0, None) \rangle$
note $bsok = \langle bsok\ (if\ (E)\ e1\ else\ e2)\ n \rangle$
from $\langle True, P, t \vdash 1\ \langle if\ (e)\ e1\ else\ e2, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle$ **show** $?case$
proof cases
case $(Cond1Red\ b')$
note $[simp] = \langle e' = if\ (b')\ e1\ else\ e2 \rangle$
and $red = \langle True, P, t \vdash 1\ \langle e, (h, xs) \rangle -ta \rightarrow \langle b', (h', xs') \rangle$
from red **have** $\tau\ move1\ P\ h\ (if\ (e)\ e1\ else\ e2) = \tau\ move1\ P\ h\ e$ **by** $(auto\ simp\ add: \tau\ move1.simps\ \tau\ moves1.simps)$
moreover have $call1\ (if\ (e)\ e1\ else\ e2) = call1\ e$ **by** $auto$
moreover from $IH[OF\ red] bsok$
obtain $pc''\ stk''\ loc''\ xcp''$ **where** $bisim: P, E, h' \vdash (b', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$
and $redo: ?exec\ ta\ E\ e\ b'\ h\ stk\ loc\ pc\ xcp\ h'\ pc''\ stk''\ loc''\ xcp''$ **by** $auto$
from $bisim$
have $P, if\ (E)\ e1\ else\ e2, h' \vdash (if\ (b')\ e1\ else\ e2, xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$
by $(rule\ bisim1-bisims1.bisim1Cond1)$
moreover {
assume $no-call2\ E\ pc$
hence $no-call2\ (if\ (E)\ e1\ else\ e2)\ pc$ **by** $(auto\ simp\ add: no-call2-def)$ **}**
ultimately show $?thesis$ **using** $redo$
by $(auto\ simp\ del: call1.simps\ calls1.simps\ split: if-split-asm\ split\ del: if-split)(blast\ intro: Cond-\tau\ ExecrI1\ Cond-\tau\ ExecI1\ exec-move-CondI1)+$
next
case $Red1CondT$
note $[simp] = \langle e = true \rangle \langle e' = e1 \rangle \langle ta = \varepsilon \rangle \langle h' = h \rangle \langle xs' = xs \rangle$
from $bisim$ **have** $s: xcp = None\ xs = loc$
and $\tau\ Exec-mover-a\ P\ t\ E\ h\ (stk, loc, pc, xcp)\ ([Bool\ True], loc, length\ (compE2\ E), None)$
by $(auto\ dest: bisim1Val2D1)$
hence $\tau\ Exec-mover-a\ P\ t\ (if\ (E)\ e1\ else\ e2)\ h\ (stk, loc, pc, xcp)\ ([Bool\ True], loc, length\ (compE2\ E), None)$
by $-(rule\ Cond-\tau\ ExecrI1)$
moreover have $exec-move-a\ P\ t\ (if\ (E)\ e1\ else\ e2)\ h\ ([Bool\ True], loc, length\ (compE2\ E), None)$
 $\varepsilon\ h\ ([], loc, Suc\ (length\ (compE2\ E)), None)$
unfolding $exec-move-def$ **by** $(rule\ exec-instr, auto)$
moreover have $\tau\ move2\ (compP2\ P)\ h\ [Bool\ True]\ (if\ (E)\ e1\ else\ e2)\ (length\ (compE2\ E))\ None$
by $(simp\ add: \tau\ move2-iff)$

ultimately have $\tau Exec\text{-movet}\text{-}a P t$ (if (E) e1 else e2) h (stk, loc, pc, xcp) ([], loc, Suc (length (compE2 E)), None)

by(auto intro: rtranclp-into-tranclp1 $\tau exec\text{-move}I$ simp add: compP2-def)

moreover have $\tau move1 P h$ (if (true) e1 else e2) **by**(rule $\tau move1CondRed$)

moreover

from bisim1-refl

have P, if (E) e1 else e2, $h \vdash (e1, xs) \leftrightarrow ([], loc, Suc (length (compE2 E) + 0), None)$

unfolding s **by**(rule bisim1CondThen)

ultimately show ?thesis **by** (fastforce)

next

case Red1CondF

note [simp] = $\langle e = false \rangle \langle e' = e2 \rangle \langle ta = \varepsilon \rangle \langle h' = h \rangle \langle xs' = xs \rangle$

from bisim **have** s: xcp = None xs = loc

and $\tau Exec\text{-mover}\text{-}a P t E h$ (stk, loc, pc, xcp) ([Bool False], loc, length (compE2 E), None)

by(auto dest: bisim1Val2D1)

hence $\tau Exec\text{-mover}\text{-}a P t$ (if (E) e1 else e2) h (stk, loc, pc, xcp) ([Bool False], loc, length (compE2 E), None)

by-(rule Cond- $\tau ExecrI1$)

moreover have $exec\text{-move}\text{-}a P t$ (if (E) e1 else e2) h ([Bool False], loc, length (compE2 E), None)

εh ([], loc, Suc (Suc (length (compE2 E) + length (compE2 e1))), None)

unfolding $exec\text{-move}\text{-}def$ **by**(rule $exec\text{-instr}$)(auto)

moreover have $\tau move2$ (compP2 P) h [Bool False] (if (E) e1 else e2) (length (compE2 E)) None

by(rule $\tau move2CondRed$)

ultimately have $\tau Exec\text{-movet}\text{-}a P t$ (if (E) e1 else e2) h (stk, loc, pc, xcp) ([], loc, Suc (Suc (length (compE2 E) + length (compE2 e1))), None)

by(auto intro: rtranclp-into-tranclp1 $\tau exec\text{-move}I$ simp add: compP2-def)

moreover have $\tau move1 P h$ (if (false) e1 else e2) **by**(rule $\tau move1CondRed$)

moreover

from bisim1-refl

have P, if (E) e1 else e2, $h \vdash (e2, loc) \leftrightarrow ([], loc, (Suc (Suc (length (compE2 E) + length (compE2 e1) + 0))), None)$

unfolding s **by**(rule bisim1CondElse)

ultimately show ?thesis **using** s **by** auto(blast intro: tranclp-into-rtranclp)

next

case (Cond1Throw a)

note [simp] = $\langle e = Throw a \rangle \langle ta = \varepsilon \rangle \langle e' = Throw a \rangle \langle h' = h \rangle \langle xs' = xs \rangle$

have τ : $\tau move1 P h$ (if (Throw a) e1 else e2) **by**(rule $\tau move1CondThrow$)

from bisim **have** xcp = [a] \vee xcp = None **by**(auto dest: bisim1-ThrowD)

thus ?thesis

proof

assume [simp]: xcp = [a]

with bisim

have P, if (E) e1 else e2, $h \vdash (Throw a, xs) \leftrightarrow (stk, loc, pc, [a])$

by(auto intro: bisim1-bisims1.bisim1CondThrow)

thus ?thesis **using** τ **by**(fastforce)

next

assume [simp]: xcp = None

with bisim **obtain** pc'

where $\tau Exec\text{-mover}\text{-}a P t E h$ (stk, loc, pc, None) ([Addr a], loc, pc', [a])

and bisim': P, E, $h \vdash (Throw a, xs) \leftrightarrow ([Addr a], loc, pc', [a])$ **and** [simp]: xs = loc

by(auto dest: bisim1-Throw- $\tau Exec\text{-mover}$)

hence $\tau Exec\text{-mover}\text{-}a P t$ (if (E) e1 else e2) h (stk, loc, pc, None) ([Addr a], loc, pc', [a])

by-(rule Cond- $\tau ExecrI1$)

moreover from bisim'


```

have P, if (E) e1 else e2, h ⊢ (Throw a, xs) ↔ ([Addr a], loc, pc', [a])
  by-(rule bisim1CondThrow, auto)
ultimately show ?thesis using τ by auto
qed
qed
next
case (bisim1CondThen e1 n e1' xs stk loc pc xcp e e2)
note IH = bisim1CondThen.IH(2)
note bisim1 = ⟨P, e1, h ⊢ (e1', xs) ↔ (stk, loc, pc, xcp)⟩
note bisim = ⟨∧xs. P, e, h ⊢ (e, xs) ↔ ([], xs, 0, None)⟩
note bisim2 = ⟨∧xs. P, e2, h ⊢ (e2, xs) ↔ ([], xs, 0, None)⟩
note bsok = ⟨bsok (if (e) e1 else e2) n⟩
from IH[OF ⟨True, P, t ⊢ 1 ⟨e1', (h, xs)⟩ -ta→ ⟨e', (h', xs')⟩⟩] bsok obtain pc'' stk'' loc'' xcp''
  where bisim': P, e1, h' ⊢ (e', xs') ↔ (stk'', loc'', pc'', xcp'')
  and exec': ?exec ta e1 e1' e' h stk loc pc xcp h' pc'' stk'' loc'' xcp'' by auto
have no-call2 e1 pc ⇒ no-call2 (if (e) e1 else e2) (Suc (length (compE2 e) + pc))
  by(auto simp add: no-call2-def)
hence ?exec ta (if (e) e1 else e2) e1' e' h stk loc (Suc (length (compE2 e) + pc)) xcp h' (Suc
(length (compE2 e) + pc'')) stk'' loc'' xcp''
  using exec' by(cases τ move1 P h e1')(auto, (blast intro: Cond-τExecrI2 Cond-τExecI2 exec-move-CondI2)+)
moreover from bisim'
have P, if (e) e1 else e2, h' ⊢ (e', xs') ↔ (stk'', loc'', Suc (length (compE2 e) + pc''), xcp'')
  by(rule bisim1-bisims1.bisim1CondThen)
ultimately show ?case
  by(auto split: if-split-asm) blast+
next
case (bisim1CondElse e2 n e2' xs stk loc pc xcp e e1)
note IH = bisim1CondElse.IH(2)
note bisim2 = ⟨P, e2, h ⊢ (e2', xs) ↔ (stk, loc, pc, xcp)⟩
note bisim = ⟨∧xs. P, e, h ⊢ (e, xs) ↔ ([], xs, 0, None)⟩
note bisim1 = ⟨∧xs. P, e1, h ⊢ (e1, xs) ↔ ([], xs, 0, None)⟩
from IH[OF ⟨True, P, t ⊢ 1 ⟨e2', (h, xs)⟩ -ta→ ⟨e', (h', xs')⟩⟩] ⟨bsok (if (e) e1 else e2) n⟩
obtain pc'' stk'' loc'' xcp''
  where bisim': P, e2, h' ⊢ (e', xs') ↔ (stk'', loc'', pc'', xcp'')
  and exec': ?exec ta e2 e2' e' h stk loc pc xcp h' pc'' stk'' loc'' xcp'' by auto
have no-call2 e2 pc ⇒ no-call2 (if (e) e1 else e2) (Suc (Suc (length (compE2 e) + length (compE2
e1) + pc)))
  by(auto simp add: no-call2-def)
hence ?exec ta (if (e) e1 else e2) e2' e' h stk loc (Suc (Suc (length (compE2 e) + length (compE2
e1) + pc))) xcp h' (Suc (Suc (length (compE2 e) + length (compE2 e1) + pc''))) stk'' loc'' xcp''
  using exec' by(cases τ move1 P h e2')(auto, (blast intro: Cond-τExecrI3 Cond-τExecI3 exec-move-CondI3)+)
moreover from bisim'
have P, if (e) e1 else e2, h' ⊢ (e', xs') ↔ (stk'', loc'', Suc (Suc (length (compE2 e) + length (compE2
e1) + pc'')), xcp'')
  by(rule bisim1-bisims1.bisim1CondElse)
ultimately show ?case
  by(auto split: if-split-asm) blast+
next
case bisim1CondThrow thus ?case by auto
next
case (bisim1While1 c n e xs)
note bisim1 = ⟨∧xs. P, c, h ⊢ (c, xs) ↔ ([], xs, 0, None)⟩
note bisim2 = ⟨∧xs. P, e, h ⊢ (e, xs) ↔ ([], xs, 0, None)⟩
from ⟨True, P, t ⊢ 1 ⟨while (c) e, (h, xs)⟩ -ta→ ⟨e', (h', xs')⟩⟩ show ?case

```

proof cases
case *Red1While*
note $[simp] = \langle ta = \varepsilon \rangle \langle e' = \text{if } (c) (e;; \text{while } (c) e) \text{ else unit} \rangle \langle h' = h \rangle \langle xs' = xs \rangle$
have $\tau\text{move1 } P h (\text{while } (c) e) \text{ by}(\text{rule } \tau\text{move1WhileRed})$
moreover
have $P, \text{while } (c) e, h \vdash (\text{if } (c) (e;; \text{while } (c) e) \text{ else unit}, xs) \leftrightarrow ([], xs, 0, \text{None})$
by(rule *bisim1-bisims1.bisim1While3[OF bisim1-refl]*)
moreover have *sim12-size* (*while* (*c*) *e*) > *sim12-size* *e'* **by**(*simp*)
ultimately show *?thesis* **by** *auto*
qed
next
case (*bisim1While3 c n c' xs stk loc pc xcp e*)
note $IH = \text{bisim1While3.IH}(2)$
note $\text{bisim1} = \langle P, c, h \vdash (c', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
note $\text{bisim2} = \langle \bigwedge xs. P, e, h \vdash (e, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$
note $\text{bsok} = \langle \text{bsok } (\text{while } (c) e) n \rangle$
from $\langle \text{True}, P, t \vdash 1 \langle \text{if } (c') (e;; \text{while } (c) e) \text{ else unit}, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$ **show** *?case*
proof cases
case (*Cond1Red b'*)
note $[simp] = \langle e' = \text{if } (b') (e;; \text{while } (c) e) \text{ else unit} \rangle$
and $\text{red} = \langle \text{True}, P, t \vdash 1 \langle c', (h, xs) \rangle -ta \rightarrow \langle b', (h', xs') \rangle \rangle$
from *red* **have** $\tau\text{move1 } P h (\text{if } (c') (e;; \text{while } (c) e) \text{ else unit}) = \tau\text{move1 } P h c' \text{ by}(\text{auto } \text{simp } \text{add: } \tau\text{move1.simps } \tau\text{moves1.simps})$
moreover from *red* **have** $\text{call1 } (\text{if } (c') (e;; \text{while } (c) e) \text{ else unit}) = \text{call1 } c' \text{ by } \text{auto}$
moreover from $IH[\text{OF } \text{red}] \text{ bsok}$
obtain $pc'' \text{ stk}'' \text{ loc}'' \text{ xcp}''$ **where** $\text{bisim}: P, c, h' \vdash (b', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$
and $\text{redo}: ?\text{exec } ta \text{ c } c' b' h \text{ stk } \text{loc } \text{pc } \text{xcp } h' \text{ pc}'' \text{ stk}'' \text{ loc}'' \text{ xcp}'' \text{ by } \text{auto}$
from *bisim*
have $P, \text{while } (c) e, h' \vdash (\text{if } (b') (e;; \text{while } (c) e) \text{ else unit}, xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$
by(rule *bisim1-bisims1.bisim1While3*)
moreover {
assume *no-call2 c pc*
hence *no-call2* (*while* (*c*) *e*) *pc* **by**(*auto simp add: no-call2-def*) }
ultimately show *?thesis* **using** *redo*
by(*auto simp del: call1.simps calls1.simps split: if-split-asm split del: if-split*)(*blast intro: While- τ ExecrI1 While- τ ExecI1 exec-move-WhileI1*)
next
case *Red1CondT*
note $[simp] = \langle c' = \text{true} \rangle \langle e' = e;; \text{while } (c) e \rangle \langle ta = \varepsilon \rangle \langle h' = h \rangle \langle xs' = xs \rangle$
from *bisim1* **have** $s: xcp = \text{None } xs = \text{loc}$
and $\tau\text{Exec-mover-a } P t c h (stk, loc, pc, xcp) ([\text{Bool True}], \text{loc}, \text{length } (\text{compE2 } c), \text{None})$
by(*auto dest: bisim1Val2D1*)
hence $\tau\text{Exec-mover-a } P t (\text{while } (c) e) h (stk, loc, pc, xcp) ([\text{Bool True}], \text{loc}, \text{length } (\text{compE2 } c), \text{None})$
by-(rule *While- τ ExecrI1*)
moreover have $\text{exec-move-a } P t (\text{while } (c) e) h ([\text{Bool True}], \text{loc}, \text{length } (\text{compE2 } c), \text{None}) \varepsilon h$
 $([], \text{loc}, \text{Suc } (\text{length } (\text{compE2 } c)), \text{None})$
unfolding *exec-move-def* **by**(rule *exec-instr, auto*)
moreover have $\tau\text{move2 } (\text{compP2 } P) h [\text{Bool True}] (\text{while } (c) e) (\text{length } (\text{compE2 } c)) \text{None}$ **by**(*simp add: $\tau\text{move2-iff}$*)
ultimately have $\tau\text{Exec-movet-a } P t (\text{while } (c) e) h (stk, loc, pc, xcp) ([], \text{loc}, \text{Suc } (\text{length } (\text{compE2 } c)), \text{None})$
by(*auto intro: rtranclp-into-tranclp1 $\tau\text{exec-moveI simp add: compP2-def}$*)
moreover have $\tau\text{move1 } P h (\text{if } (c') (e;; \text{while } (c) e) \text{ else unit}) \text{ by}(\text{auto } \text{simp } \text{add: } \tau\text{move1.simps})$

```

τmoves1.simps)
  moreover from bisim1-refl
  have P, while (c) e, h ⊢ (e;; while (c) e, xs) ↔ ([], loc, Suc (length (compE2 c) + 0), None)
    unfolding s by(rule bisim1While4)
    ultimately show ?thesis by (fastforce)
next
case Red1CondF
note [simp] = ⟨c' = false⟩ ⟨e' = unit⟩ ⟨ta = ε⟩ ⟨h' = h⟩ ⟨xs' = xs⟩
from bisim1 have s: xcp = None xs = loc
  and τExec-mover-a P t c h (stk, loc, pc, xcp) ([Bool False], loc, length (compE2 c), None)
  by(auto dest: bisim1Val2D1)
hence τExec-mover-a P t (while (c) e) h (stk, loc, pc, xcp) ([Bool False], loc, length (compE2 c),
None)
  by-(rule While-τExecrI1)
moreover have exec-move-a P t (while (c) e) h ([Bool False], loc, length (compE2 c), None) ε h
([], loc, Suc (Suc (Suc (length (compE2 c) + length (compE2 e))))), None)
  by(auto intro!: exec-instr simp add: exec-move-def)
moreover have τmove2 (compP2 P) h [Bool False] (while (c) e) (length (compE2 c)) None
by(simp add: τmove2-iff)
  ultimately have τExec-mover-a P t (while (c) e) h (stk, loc, pc, xcp) ([], loc, Suc (Suc (Suc
(length (compE2 c) + length (compE2 e))))), None)
  by(auto intro: rtranclp.rtrancl-into-rtrancl τexec-moveI simp add: compP2-def)
moreover have τmove1 P h (if (false) (e;;while (c) e) else unit) by(rule τmove1CondRed)
moreover have P, while (c) e, h ⊢ (unit, xs) ↔ ([], loc, (Suc (Suc (Suc (length (compE2 c) +
length (compE2 e))))), None)
  unfolding s by(rule bisim1While7)
  ultimately show ?thesis using s by auto
next
case (Cond1Throw a)
note [simp] = ⟨c' = Throw a⟩ ⟨ta = ε⟩ ⟨e' = Throw a⟩ ⟨h' = h⟩ ⟨xs' = xs⟩
have τ: τmove1 P h (if (c') (e;; while (c) e) else unit) by(auto intro: τmove1CondThrow)
from bisim1 have xcp = [a] ∨ xcp = None by(auto dest: bisim1-ThrowD)
thus ?thesis
proof
  assume [simp]: xcp = [a]
  with bisim1
  have P, while (c) e, h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])
    by(auto intro: bisim1-bisims1.bisim1WhileThrow1)
  thus ?thesis using τ by(fastforce)
next
assume [simp]: xcp = None
with bisim1 obtain pc'
  where τExec-mover-a P t c h (stk, loc, pc, None) ([Addr a], loc, pc', [a])
  and bisim': P, c, h ⊢ (Throw a, xs) ↔ ([Addr a], loc, pc', [a]) and [simp]: xs = loc
  by(auto dest: bisim1-Throw-τExec-mover)
hence τExec-mover-a P t (while (c) e) h (stk, loc, pc, None) ([Addr a], loc, pc', [a])
  by-(rule While-τExecrI1)
moreover from bisim'
have P, while (c) e, h ⊢ (Throw a, xs) ↔ ([Addr a], loc, pc', [a])
  by-(rule bisim1WhileThrow1, auto)
ultimately show ?thesis using τ by auto
qed
qed
next

```

case (*bisim1While4 E n e xs stk loc pc xcp c*)
note *IH = bisim1While4.IH(2)*
note *bisim2 = ⟨P, E, h ⊢ (e, xs) ↔ (stk, loc, pc, xcp)⟩*
note *bisim1 = ⟨∧xs. P, c, h ⊢ (c, xs) ↔ ([], xs, 0, None)⟩*
note *bsok = bsok (while (c) E) n*
from $\langle \text{True}, P, t \vdash 1 \langle e;; \text{while} (c) E, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$ **show** *?case*
proof *cases*
case (*Seq1Red E'*)
note [*simp*] = $\langle e' = E'; \text{while} (c) E \rangle$
and *red = ⟨True, P, t ⊢ 1 ⟨e, (h, xs)⟩ -ta → ⟨E', (h', xs')⟩⟩*
from *red* **have** $\tau: \tau \text{move1 } P h (e;; \text{while} (c) E) = \tau \text{move1 } P h e$ **by** (*auto simp add: τmove1.simps τmoves1.simps*)
with *IH[OF red] bsok* **obtain** *pc'' stk'' loc'' xcp''*
where *bisim: P, E, h' ⊢ (E', xs') ↔ (stk'', loc'', pc'', xcp'')*
and *exec': ?exec ta E e E' h stk loc pc xcp h' pc'' stk'' loc'' xcp''* **by** *auto*
have *?exec ta (while (c) E) (e;; while (c) E) (E'; while (c) E) h stk loc (Suc (length (compE2 c) + pc)) xcp h' (Suc (length (compE2 c) + pc'')) stk'' loc'' xcp''*
proof (*cases τmove1 P h (e;; while (c) E)*)
case *True*
with *exec'* **show** *?thesis* **using** τ **by** (*fastforce intro: While-τExecrI2 While-τExecI2*)
next
case *False*
with *exec' τ* **obtain** *pc' stk' loc' xcp'*
where *e: τExec-mover-a P t E h (stk, loc, pc, xcp) (stk', loc', pc', xcp')*
and *e': exec-move-a P t E h (stk', loc', pc', xcp') (extTA2JVM (compP2 P) ta) h' (stk'', loc'', pc'', xcp'')*
and $\tau': \neg \tau \text{move2} (compP2 P) h \text{stk}' E \text{pc}' \text{xcp}'$
and *call: (call1 e = None ∨ no-call2 E pc ∨ pc' = pc ∧ stk' = stk ∧ loc' = loc ∧ xcp' = xcp)*
by *auto*
from *e* **have** $\tau \text{Exec-mover-a } P t (while (c) E) h (stk, loc, Suc (length (compE2 c) + pc), xcp) (stk', loc', Suc (length (compE2 c) + pc'), xcp')$ **by** (*rule While-τExecrI2*)
moreover
from *e'* **have** $\text{exec-move-a } P t (while (c) E) h (stk', loc', Suc (length (compE2 c) + pc'), xcp') (extTA2JVM (compP2 P) ta) h' (stk'', loc'', Suc (length (compE2 c) + pc''), xcp'')$
by (*rule exec-move-WhileI2*)
moreover from $\tau' e'$ **have** $\neg \tau \text{move2} (compP2 P) h \text{stk}' (while (c) E) (Suc (length (compE2 c) + pc')) \text{xcp}'$
by (*auto simp add: τmove2-iff*)
moreover have $\text{call1} (e;; \text{while} (c) E) = \text{call1 } e$ **by** *simp*
moreover have $\text{no-call2 } E \text{pc} \implies \text{no-call2} (while (c) E) (Suc (length (compE2 c) + pc))$
by (*auto simp add: no-call2-def*)
ultimately show *?thesis* **using** *False call* **by** (*auto simp del: split-paired-Ex call1.simps calls1.simps*)
qed
with *bisim τ* **show** *?thesis* **by** *auto* (*blast intro: bisim1-bisims1.bisim1While4*)
next
case (*Red1Seq v*)
note [*simp*] = $\langle e = \text{Val } v \rangle \langle ta = \varepsilon \rangle \langle e' = \text{while} (c) E \rangle \langle h' = h \rangle \langle xs' = xs \rangle$
from *bisim2* **have** *s: xcp = None xs = loc*
and $\tau \text{Exec-mover-a } P t E h (stk, loc, pc, xcp) ([v], loc, \text{length} (compE2 E), \text{None})$
by (*auto dest: bisim1Val2D1*)
hence $\tau \text{Exec-mover-a } P t (while (c) E) h (stk, loc, \text{Suc} (length (compE2 c) + pc), xcp) ([v], loc, \text{Suc} (length (compE2 c) + length (compE2 E)), \text{None})$
by $-(rule \text{While-}\tau \text{ExecrI2})$
moreover

have *exec-move-a* $P t (while (c) E) h ([v], loc, Suc (length (compE2 c) + length (compE2 E)), None) \varepsilon h ([], loc, Suc (Suc (length (compE2 c) + length (compE2 E))), None)$
unfolding *exec-move-def* **by**(*rule exec-instr, auto*)
moreover have $\tau move2 (compP2 P) h [v] (while (c) E) (Suc (length (compE2 c) + length (compE2 E))) None$ **by**(*simp add: $\tau move2$ -iff*)
ultimately have $\tau Exec-movet-a P t (while (c) E) h (stk, loc, Suc (length (compE2 c) + pc), xcp) ([], loc, Suc (Suc (length (compE2 c) + length (compE2 E))), None)$
by(*auto intro: rtranclp-into-tranclp1 $\tau exec-moveI$ simp add: compP2-def*)
moreover
have $P, while (c) E, h \vdash (while (c) E, xs) \leftrightarrow ([], xs, (Suc (Suc (length (compE2 c) + length (compE2 E))))), None)$
unfolding s **by**(*rule bisim1While6*)
moreover have $\tau move1 P h (e;; while (c) E)$ **by**(*auto intro: $\tau move1SeqRed$*)
ultimately show *?thesis* **using** s **by**(*auto*)(*blast intro: tranclp-into-rtranclp*)
next
case (*Seq1Throw a*)
note [*simp*] = $\langle e = Throw a \rangle \langle ta = \varepsilon \rangle \langle e' = Throw a \rangle \langle h' = h \rangle \langle xs' = xs \rangle$
have $\tau: \tau move1 P h (e;; while (c) E)$ **by**(*auto intro: $\tau move1SeqThrow$*)
from *bisim2* **have** $xcp = [a] \vee xcp = None$ **by**(*auto dest: bisim1-ThrowD*)
thus *?thesis*
proof
assume [*simp*]: $xcp = [a]$
with *bisim2*
have $P, while (c) E, h \vdash (Throw a, xs) \leftrightarrow (stk, loc, Suc (length (compE2 c) + pc), xcp)$
by(*auto intro: bisim1WhileThrow2*)
thus *?thesis* **using** τ **by**(*fastforce*)
next
assume [*simp*]: $xcp = None$
with *bisim2* **obtain** pc'
where $\tau Exec-mover-a P t E h (stk, loc, pc, None) ([Addr a], loc, pc', [a])$
and $bisim': P, E, h \vdash (Throw a, xs) \leftrightarrow ([Addr a], loc, pc', [a])$ **and** [*simp*]: $xs = loc$
by(*auto dest: bisim1-Throw- $\tau Exec-mover$*)
hence $\tau Exec-mover-a P t (while (c) E) h (stk, loc, Suc (length (compE2 c) + pc), None) ([Addr a], loc, Suc (length (compE2 c) + pc'), [a])$
by-(*rule While- $\tau ExecrI2$*)
moreover from *bisim'*
have $P, while (c) E, h \vdash (Throw a, xs) \leftrightarrow ([Addr a], loc, Suc (length (compE2 c) + pc'), [a])$
by-(*rule bisim1WhileThrow2, auto*)
ultimately show *?thesis* **using** τ **by** *auto*
qed
qed
next
case (*bisim1While6 c n e xs*)
note *bisim1* = $\langle \bigwedge xs. P, c, h \vdash (c, xs) \leftrightarrow ([], xs, 0, None) \rangle$
note *bisim2* = $\langle \bigwedge xs. P, e, h \vdash (e, xs) \leftrightarrow ([], xs, 0, None) \rangle$
from $\langle True, P, t \vdash 1 \langle while (c) e, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$ **show** *?case*
proof *cases*
case *Red1While*
note [*simp*] = $\langle ta = \varepsilon \rangle \langle e' = if (c) (e;; while (c) e) else unit \rangle \langle h' = h \rangle \langle xs' = xs \rangle$
have $\tau move1 P h (while (c) e)$ **by**(*rule $\tau move1WhileRed$*)
moreover
have $P, while (c) e, h \vdash (if (c) (e;; while (c) e) else unit, xs) \leftrightarrow ([], xs, 0, None)$
by(*rule bisim1-bisims1.bisim1While3[OF bisim1-refl]*)
moreover have $\tau Exec-movet-a P t (while (c) e) h ([], xs, Suc (Suc (length (compE2 c) + length$

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(compE2 e))), None) ([], xs, 0, None)
  by(rule  $\tau$ Exec1step)(auto simp add: exec-move-def  $\tau$ move2-iff intro: exec-instr)
  ultimately show ?thesis by(fastforce)
qed
next
  case bisim1While7 thus ?case by fastforce
next
  case bisim1WhileThrow1 thus ?case by auto
next
  case bisim1WhileThrow2 thus ?case by auto
next
  case (bisim1Throw1 E n e xs stk loc pc xcp)
  note IH = bisim1Throw1.IH(2)
  note bisim =  $\langle P, E, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
  note red =  $\langle True, P, t \vdash 1 \langle throw\ e, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$ 
  note bsok =  $\langle bsok\ (throw\ E)\ n \rangle$ 
  from red show ?case
  proof cases
    case (Throw1Red E')
    note [simp] =  $\langle e' = throw\ E' \rangle$ 
    and red =  $\langle True, P, t \vdash 1 \langle e, (h, xs) \rangle -ta \rightarrow \langle E', (h', xs') \rangle \rangle$ 
    from red have  $\tau move1\ P\ h\ (throw\ e) = \tau move1\ P\ h\ e$  by(auto simp add:  $\tau move1.simps$ 
 $\tau moves1.simps$ )
    moreover have call1 (throw e) = call1 e by auto
    moreover from IH[OF red] bsok
    obtain pc'' stk'' loc'' xcp'' where bisim:  $P, E, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$ 
    and redo:  $?exec\ ta\ E\ e\ E'\ h\ stk\ loc\ pc\ xcp\ h'\ pc''\ stk''\ loc''\ xcp''$  by auto
    from bisim
    have  $P, throw\ E, h' \vdash (throw\ E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$ 
    by(rule bisim1-bisims1.bisim1Throw1)
    moreover {
      assume no-call2 E pc
      hence no-call2 (throw E) pc by(auto simp add: no-call2-def) }
    ultimately show ?thesis using redo
    by(auto simp del: call1.simps calls1.simps split: if-split-asm split del: if-split)(blast intro:
    Throw- $\tau$ ExecrI Throw- $\tau$ ExecrI exec-move-ThrowI)+
  next
    case Red1ThrowNull
    note [simp] =  $\langle e = null \rangle \langle ta = \varepsilon \rangle \langle e' = THROW\ NullPointer \rangle \langle h' = h \rangle \langle xs' = xs \rangle$ 
    from bisim have s:  $xcp = None\ xs = loc$ 
    and  $\tau Exec-mover-a\ P\ t\ E\ h\ (stk, loc, pc, xcp)\ ([Null], loc, length\ (compE2\ E), None)$ 
    by(auto dest: bisim1Val2D1)
    hence  $\tau Exec-mover-a\ P\ t\ (throw\ E)\ h\ (stk, loc, pc, xcp)\ ([Null], loc, length\ (compE2\ E), None)$ 
    by-(rule Throw- $\tau$ ExecrI)
    also have  $\tau Exec-mover-a\ P\ t\ (throw\ E)\ h\ ([Null], loc, length\ (compE2\ E), None)\ ([Null], loc,$ 
     $length\ (compE2\ E), [addr-of-sys-xcpt\ NullPointer])$ 
    by(rule  $\tau$ Exec1step)(auto intro: exec-instr  $\tau$ move2- $\tau$ moves2.intros simp add: exec-move-def)
    also have  $P, throw\ E, h \vdash (THROW\ NullPointer, xs) \leftrightarrow ([Null], loc, length\ (compE2\ E),$ 
     $[addr-of-sys-xcpt\ NullPointer])$ 
    unfolding s by(rule bisim1ThrowNull)
    moreover have  $\tau move1\ P\ h\ (throw\ e)$  by(auto intro:  $\tau move1ThrowNull$ )
    ultimately show ?thesis by auto
  next
    case (Throw1Throw a)

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note [simp] = ⟨e = Throw a⟩ ⟨ta = ε⟩ ⟨e' = Throw a⟩ ⟨h' = h⟩ ⟨xs' = xs⟩
have τ: τmove1 P h (throw (Throw a)) by(rule τmove1ThrowThrow)
from bisim have xcp = [a] ∨ xcp = None by(auto dest: bisim1-ThrowD)
thus ?thesis
proof
  assume xcp = [a]
  with bisim show ?thesis using τ by(fastforce intro: bisim1ThrowThrow)
next
  assume [simp]: xcp = None
  from bisim obtain pc'
    where τExec-mover-a P t E h (stk, loc, pc, None) ([Addr a], loc, pc', [a])
    and bisim: P, E, h ⊢ (Throw a, xs) ↔ ([Addr a], loc, pc', [a]) and s: xs = loc
    by(auto dest: bisim1-Throw-τExec-mover)
  hence τExec-mover-a P t (throw E) h (stk, loc, pc, None) ([Addr a], loc, pc', [a])
    by -(rule Throw-τExecrI)
  moreover from bisim have P, throw E, h ⊢ (Throw a, xs) ↔ ([Addr a], loc, pc', [a])
    by(rule bisim1ThrowThrow)
  ultimately show ?thesis using τ by auto
qed
qed
next
  case bisim1Throw2 thus ?case by auto
next
  case bisim1ThrowNull thus ?case by auto
next
  case bisim1ThrowThrow thus ?case by auto
next
  case (bisim1Try E n e xs stk loc pc xcp e2 C' V)
  note IH = bisim1Try.IH(2)
  note bisim1 = ⟨P, E, h ⊢ (e, xs) ↔ (stk, loc, pc, xcp)⟩
  note bisim2 = ⟨∧xs. P, e2, h ⊢ (e2, xs) ↔ ([], xs, 0, None)⟩
  note red = ⟨True, P, t ⊢ 1 ⟨try e catch(C' V) e2, (h, xs)⟩ -ta→ ⟨e', (h', xs')⟩⟩
  note bsok = ⟨bsok (try E catch(C' V) e2) n⟩
  from red show ?case
  proof cases
    case (Try1Red E')
    note [simp] = ⟨e' = try E' catch(C' V) e2⟩
    and red = ⟨True, P, t ⊢ 1 ⟨e, (h, xs)⟩ -ta→ ⟨E', (h', xs')⟩⟩
    from red have τmove1 P h (try e catch(C' V) e2) = τmove1 P h e by(auto simp add: τmove1.simps
τmoves1.simps)
    moreover have call1 (try e catch(C' V) e2) = call1 e by auto
    moreover from IH[OF red] bsok
    obtain pc'' stk'' loc'' xcp'' where bisim: P, E, h' ⊢ (E', xs') ↔ (stk'', loc'', pc'', xcp'')
    and redo: ?exec ta E e E' h stk loc pc xcp h' pc'' stk'' loc'' xcp'' by auto
    from bisim
    have P, try E catch(C' V) e2, h' ⊢ (try E' catch(C' V) e2, xs') ↔ (stk'', loc'', pc'', xcp'')
    by(rule bisim1-bisims1.bisim1Try)
    moreover {
      assume no-call2 E pc
      hence no-call2 (try E catch(C' V) e2) pc by(auto simp add: no-call2-def) }
    ultimately show ?thesis using redo
    by(auto simp del: call1.simps calls1.simps split: if-split-asm split del: if-split)(blast intro:
Try-τExecrI1 Try-τExecrI2 exec-move-TryI1)+
  next

```

```

case (Red1Try v)
note [simp] = ⟨e = Val v⟩ ⟨ta = ε⟩ ⟨e' = Val v⟩ ⟨h' = h⟩ ⟨xs' = xs⟩
have τ: τmove1 P h (try Val v catch(C' V) e2) by(rule τmove1TryRed)
from bisim1 have s: xcp = None xs = loc
  and τExec-mover-a P t E h (stk, loc, pc, xcp) ([v], loc, length (compE2 E), None)
  by(auto dest: bisim1Val2D1)
hence τExec-mover-a P t (try E catch(C' V) e2) h (stk, loc, pc, xcp) ([v], loc, length (compE2 E), None)
  by-(rule Try-τExecrI1)
also have τExec-mover-a P t (try E catch(C' V) e2) h ([v], loc, length (compE2 E), None) ([v], loc, length (compE2 (try E catch(C' V) e2)), None)
  by(rule τExecr1step)(auto intro: exec-instr simp add: exec-move-def τmove2-iff)
also (rtranclp-trans)
have P, try E catch(C' V) e2, h ⊢ (Val v, xs) ↔ ([v], xs, length (compE2 (try E catch(C' V) e2)), None)
  by(rule bisim1Val2) simp
ultimately show ?thesis using s τ by(auto)
next
case (Red1TryCatch a D)
hence [simp]: e = Throw a ta = ε e' = {V:Class C'=None; e2} h' = h xs' = xs[V := Addr a]
  and ha: typeof-addr h a = [Class-type D] and sub: P ⊢ D ≤* C'
  and V: V < length xs by auto
from bisim1 have [simp]: xs = loc and xcp: xcp = [a] ∨ xcp = None
  by(auto dest: bisim1-ThrowD)
from xcp have τExec-mover-a P t (try E catch(C' V) e2) h (stk, loc, pc, xcp) ([Addr a], loc, Suc (length (compE2 E)), None)
proof
  assume [simp]: xcp = [a]
  with bisim1 have match-ex-table (compP2 P) (cname-of h a) pc (compE2 E 0 0) = None
    by(auto dest: bisim1-xcp-Some-not-caught[where pc'=0] simp add: compP2-def)
  moreover from bisim1 have pc < length (compE2 E)
    by(auto dest: bisim1-ThrowD)
  ultimately show ?thesis using ha sub unfolding ⟨xcp = [a]⟩
    by-(rule τExecr1step[unfolded exec-move-def, OF exec-catch[where d=0, simplified]],
      auto simp add: τmove2-iff matches-ex-entry-def compP2-def match-ex-table-append-not-pcs
      cname-of-def)
  next
  assume [simp]: xcp = None
  with bisim1 obtain pc' where τExec-mover-a P t E h (stk, loc, pc, None) ([Addr a], loc, pc', [a], [a])
    and bisim': P, E, h ⊢ (Throw a, xs) ↔ ([Addr a], loc, pc', [a]) and s: xs = loc
    by(auto dest: bisim1-Throw-τExec-mover)
  hence τExec-mover-a P t (try E catch(C' V) e2) h (stk, loc, pc, None) ([Addr a], loc, pc', [a])
    by-(rule Try-τExecrI1)
  also from bisim' have match-ex-table (compP2 P) (cname-of h a) pc' (compE2 E 0 0) = None
    by(auto dest: bisim1-xcp-Some-not-caught[where pc'=0] simp add: compP2-def)
  with ha sub bisim1-ThrowD[OF bisim']
  have τExec-mover-a P t (try E catch(C' V) e2) h ([Addr a], loc, pc', [a]) ([Addr a], loc, Suc (length (compE2 E)), None)
    by-(rule τExecr1step[unfolded exec-move-def, OF exec-catch[where d=0, simplified]], auto
      simp add: τmove2-iff matches-ex-entry-def compP2-def match-ex-table-append-not-pcs cname-of-def)
  finally (rtranclp-trans) show ?thesis by simp
qed
also let ?pc' = Suc (length (compE2 E)) from V

```


have $exec: \tau Exec\text{-}mover\text{-}a\ P\ t\ (try\ E\ catch(C'\ V)\ e2)\ h\ ([Addr\ a],\ loc,\ ?pc',\ None)\ ([],\ loc[V := Addr\ a],\ Suc\ ?pc',\ None)$
by–(rule $\tau Exec1step[unfolded\ exec\text{-}move\text{-}def,\ OF\ exec\text{-}instr]$, auto simp add: nth-append intro: $\tau move2\text{-}\tau moves2.intros$)
also (rtranclp-tranclp-tranclp)
have $bisim': P, try\ E\ catch(C'\ V)\ e2,\ h \vdash (\{V:Class\ C'=None;\ e2\}, xs[V := Addr\ a]) \leftrightarrow ([], loc[V := Addr\ a], Suc\ ?pc', None)$
unfolding $\langle xs = loc \rangle$ **by**(rule $bisim1TryCatch2[OF\ bisim1\text{-}refl,\ simplified]$)
moreover have $\tau move1\ P\ h\ (try\ Throw\ a\ catch(C'\ V)\ e2)$ **by**(rule $\tau move1TryThrow$)
ultimately show $?thesis$ **by**(auto)(blast intro: tranclp-into-rtranclp)
next
case (Red1TryFail a D)
hence [simp]: $e = Throw\ a\ ta = \varepsilon\ e' = Throw\ a\ h' = h\ xs' = xs$
and $ha: typeof\text{-}addr\ h\ a = [Class\text{-}type\ D]$ **and** $sub: \neg P \vdash D \preceq^* C'$ **by** auto
have $\tau: \tau move1\ P\ h\ (try\ Throw\ a\ catch(C'\ V)\ e2)$ **by**(rule $\tau move1TryThrow$)
from $bisim1$ **have** [simp]: $xs = loc$ **and** $xcp = [a] \vee xcp = None$ **by**(auto dest: $bisim1\text{-}ThrowD$)
from $bisim1$ **have** $pc: pc \leq length\ (compE2\ E)$ **by**(rule $bisim1\text{-}pc\text{-}length\text{-}compE2$)
from $\langle xcp = [a] \vee xcp = None \rangle$ **show** $?thesis$
proof
assume [simp]: $xcp = [a]$
with $bisim1\ ha\ sub$
have $P, try\ E\ catch(C'\ V)\ e2, h \vdash (Throw\ a,\ xs) \leftrightarrow (stk,\ loc,\ pc,\ [a])$
by(auto intro: $bisim1TryFail$)
thus $?thesis$ **using** τ **by**(fastforce)
next
assume [simp]: $xcp = None$
with $bisim1$ **obtain** pc'
where $\tau Exec\text{-}mover\text{-}a\ P\ t\ E\ h\ (stk,\ loc,\ pc,\ None)\ ([Addr\ a],\ loc,\ pc',\ [a])$
and $bisim': P,\ E,\ h \vdash (Throw\ a,\ xs) \leftrightarrow ([Addr\ a],\ loc,\ pc',\ [a])$
by(auto dest: $bisim1\text{-}Throw\text{-}\tau Exec\text{-}mover$)
hence $\tau Exec\text{-}mover\text{-}a\ P\ t\ (try\ E\ catch(C'\ V)\ e2)\ h\ (stk,\ loc,\ pc,\ None)\ ([Addr\ a],\ loc,\ pc',\ [a])$
by–(rule $Try\text{-}\tau ExecrI1$)
moreover from $bisim'\ ha\ sub$
have $P, try\ E\ catch(C'\ V)\ e2, h \vdash (Throw\ a,\ xs) \leftrightarrow ([Addr\ a],\ loc,\ pc',\ [a])$
by(auto intro: $bisim1TryFail$)
ultimately show $?thesis$ **using** τ **by** auto
qed
qed
next
case ($bisim1TryCatch1\ e\ n\ a\ xs\ stk\ loc\ pc\ D\ C'\ e2\ V$)
note $bisim1 = \langle P, e, h \vdash (Throw\ a,\ xs) \leftrightarrow (stk,\ loc,\ pc,\ [a]) \rangle$
note $bisim2 = \langle \bigwedge xs. P, e2, h \vdash (e2,\ xs) \leftrightarrow ([],\ xs,\ 0,\ None) \rangle$
note $IH2 = bisim1TryCatch1.IH(6)$
note $ha = \langle typeof\text{-}addr\ h\ a = [Class\text{-}type\ D] \rangle$
note $sub = \langle P \vdash D \preceq^* C' \rangle$
note $red = \langle True, P, t \vdash 1\ \langle \{V:Class\ C'=None;\ e2\}, (h,\ xs[V := Addr\ a]) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$
note $bsok = \langle bsok\ (try\ e\ catch(C'\ V)\ e2)\ n \rangle$
from $bisim1$ **have** [simp]: $xs = loc$ **by**(auto dest: $bisim1\text{-}ThrowD$)
from red **show** $?case$
proof cases
case ($Block1Red\ E'$)
note [simp] = $\langle e' = \{V:Class\ C'=None;\ E'\} \rangle$
and $red = \langle True, P, t \vdash 1\ \langle e2,\ (h,\ xs[V := Addr\ a]) \rangle -ta \rightarrow \langle E', (h', xs') \rangle \rangle$
from red **have** $\tau: \tau move1\ P\ h\ \{V:Class\ C'=None;\ e2\} = \tau move1\ P\ h\ e2$ **by**(auto simp add:

$\tau\text{move1.simps } \tau\text{moves1.simps}$
have $\text{exec} : \tau\text{Exec-mover-a } P \ t \ (try \ e \ catch(C' \ V) \ e2) \ h \ ([Addr \ a], \ xs, \ Suc \ (length \ (compE2 \ e) \ + \ 0), \ None) \ (\ [], \ xs[V \ := \ Addr \ a], \ Suc \ (Suc \ (length \ (compE2 \ e) \ + \ 0)), \ None)$
by $-(rule \ \tau\text{Execr1step}, \ auto \ simp \ add: \ exec-move-def \ \tau\text{move2-iff} \ intro: \ exec-instr)$
moreover from $IH2[OF \ red] \ bsok$ **obtain** $pc'' \ stk'' \ loc'' \ xcp''$
where $bisim' : P, e2, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$
and $\text{exec}' : ?\text{exec} \ ta \ e2 \ e2 \ E' \ h \ \ [] \ (xs[V \ := \ Addr \ a]) \ 0 \ None \ h' \ pc'' \ stk'' \ loc'' \ xcp''$ **by** $auto$
have $?\text{exec} \ ta \ (try \ e \ catch(C' \ V) \ e2) \ \{V:Class \ C'=None; \ e2\} \ \{V:Class \ C'=None; \ E'\} \ h \ \ [] \ (xs[V \ := \ Addr \ a]) \ (Suc \ (Suc \ (length \ (compE2 \ e)))) \ None \ h' \ (Suc \ (Suc \ (length \ (compE2 \ e) \ + \ pc'')) \) \ stk'' \ loc'' \ xcp''$
proof $(cases \ \tau\text{move1} \ P \ h \ \{V:Class \ C'=None; \ e2\})$
case $True$ **with** $\tau \text{ exec}'$ **show** $?thesis$
by $(fastforce \ dest: \ Try-\tau\text{ExecrI2} \ Try-\tau\text{ExecI2} \ simp \ del: \ compE2.simps \ compEs2.simps)$
next
case $False$
with $\tau \text{ exec}'$ **obtain** $pc' \ stk' \ loc' \ xcp'$
where $e : \tau\text{Exec-mover-a } P \ t \ e2 \ h \ (\ [], \ xs[V \ := \ Addr \ a], \ 0, \ None) \ (stk', \ loc', \ pc', \ xcp')$
and $e' : \text{exec-move-a } P \ t \ e2 \ h \ (stk', \ loc', \ pc', \ xcp') \ (extTA2JVM \ (compP2 \ P) \ ta) \ h' \ (stk'', \ loc'', \ pc'', \ xcp'')$
and $\tau' : \neg \ \tau\text{move2} \ (compP2 \ P) \ h \ stk' \ e2 \ pc' \ xcp'$
and $call : call1 \ e2 = None \vee no-call2 \ e2 \ 0 \vee pc' = 0 \wedge stk' = [] \wedge loc' = xs[V \ := \ Addr \ a] \wedge xcp' = None$ **by** $auto$
from e **have** $\tau\text{Exec-mover-a } P \ t \ (try \ e \ catch(C' \ V) \ e2) \ h \ (\ [], \ xs[V \ := \ Addr \ a], \ Suc \ (Suc \ (length \ (compE2 \ e) \ + \ 0)), \ None) \ (stk', \ loc', \ Suc \ (Suc \ (length \ (compE2 \ e) \ + \ pc')), \ xcp')$
by $(rule \ Try-\tau\text{ExecrI2})$
moreover from e'
have $\text{exec-move-a } P \ t \ (try \ e \ catch(C' \ V) \ e2) \ h \ (stk', \ loc', \ Suc \ (Suc \ (length \ (compE2 \ e) \ + \ pc')), \ xcp') \ (extTA2JVM \ (compP2 \ P) \ ta) \ h' \ (stk'', \ loc'', \ Suc \ (Suc \ (length \ (compE2 \ e) \ + \ pc'')), \ xcp'')$
by $(rule \ \text{exec-move-TryI2})$
moreover from τ' **have** $\tau\text{move2} \ (compP2 \ P) \ h \ stk' \ (try \ e \ catch(C' \ V) \ e2) \ (Suc \ (Suc \ (length \ (compE2 \ e) \ + \ pc'))) \ xcp' \Longrightarrow \ False$
by $(simp \ add: \ \tau\text{move2-iff})$
moreover have $call1 \ \{V:Class \ C'=None; \ e2\} = call1 \ e2$ **by** $simp$
moreover have $no-call2 \ e2 \ 0 \Longrightarrow no-call2 \ (try \ e \ catch(C' \ V) \ e2) \ (Suc \ (Suc \ (length \ (compE2 \ e))))$
by $(auto \ simp \ add: \ no-call2-def)$
ultimately show $?thesis$ **using** $False \ call$ **by** $(auto \ simp \ del: \ split-paired-Ex \ call1.simps \ calls1.simps)$
blast
qed
moreover from $bisim'$
have $P, try \ e \ catch(C' \ V) \ e2, h' \vdash (\{V:Class \ C'=None; \ E'\}, xs') \leftrightarrow (stk'', loc'', Suc \ (Suc \ (length \ (compE2 \ e) \ + \ pc'')), xcp'')$
by $(rule \ bisim1TryCatch2)$
moreover have $no-call2 \ (try \ e \ catch(C' \ V) \ e2) \ (Suc \ (length \ (compE2 \ e)))$ **by** $(simp \ add: \ no-call2-def)$
ultimately show $?thesis$ **using** τ
by $auto(blast \ intro: \ rtranclp-trans \ rtranclp-tranclp-tranclp)+$
next
case $(Red1Block \ u)$
note $[simp] = \langle e2 = Val \ u \ \langle ta = \varepsilon \ \langle e' = Val \ u \ \langle h' = h \ \langle xs' = xs[V \ := \ Addr \ a] \rangle \rangle \rangle \rangle$
have $\tau\text{Exec-mover-a } P \ t \ (try \ e \ catch(C' \ V) \ Val \ u) \ h \ ([Addr \ a], \ xs, \ Suc \ (length \ (compE2 \ e) \ + \ 0), \ None) \ (\ [], \ xs[V \ := \ Addr \ a], \ Suc \ (Suc \ (length \ (compE2 \ e) \ + \ 0)), \ None)$
by $-(rule \ \tau\text{Execr1step}, \ auto \ simp \ add: \ exec-move-def \ \tau\text{move2-iff} \ intro: \ exec-instr)$
also have $\tau\text{Exec-mover-a } P \ t \ (try \ e \ catch(C' \ V) \ Val \ u) \ h \ (\ [], \ xs[V \ := \ Addr \ a], \ Suc \ (Suc \ (length$

$(\text{compE2 } e) + 0)), \text{None}) ([u], xs[V := \text{Addr } a], \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e) + 1)), \text{None})$
by $-(\text{rule Try-}\tau\text{ExecrI2}[OF \tau\text{Execr1step}[\text{unfolded exec-move-def}, OF \text{exec-instr}], \text{auto simp add: } \tau\text{move2-iff})$
also (rtranclp-trans)
have $P, \text{try } e \text{ catch}(C' V) \text{ Val } u, h \vdash (\text{Val } u, xs[V := \text{Addr } a]) \leftrightarrow ([u], xs[V := \text{Addr } a], \text{length } (\text{compE2 } (\text{try } e \text{ catch}(C' V) \text{ Val } u)), \text{None})$
by $(\text{rule bisim1Val2}) \text{ simp}$
moreover have $\tau\text{move1 } P h \{V:\text{Class } C'=\text{None}; \text{Val } u\}$ **by** $(\text{rule } \tau\text{move1BlockRed})$
ultimately show $?thesis$ **by** (auto)
next
case $(\text{Block1Throw } a')$
note $[simp] = \langle e2 = \text{Throw } a' \rangle \langle h' = h \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw } a' \rangle \langle xs' = xs[V := \text{Addr } a] \rangle$
have $\tau\text{move1 } P h \{V:\text{Class } C'=\text{None}; \text{Throw } a'\}$ **by** $(\text{rule } \tau\text{move1BlockThrow})$
moreover have $\tau\text{Exec-mover-a } P t (\text{try } e \text{ catch}(C' V) e2) h ([\text{Addr } a], \text{loc}, \text{Suc } (\text{length } (\text{compE2 } e)), \text{None})$
 $([\text{Addr } a'], xs', \text{Suc } (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e))))), [a']$
by $(\text{rule } \tau\text{Execr3step})(\text{auto simp add: exec-move-def exec-meth-instr } \tau\text{move2-iff})$
moreover have $P, \text{try } e \text{ catch}(C' V) \text{ Throw } a', h \vdash (\text{Throw } a', xs') \leftrightarrow ([\text{Addr } a'], xs', \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e) + \text{length } (\text{compE2 } (\text{addr } a')))), [a'])$
by $(\text{rule bisim1TryCatchThrow})(\text{rule bisim1Throw2})$
ultimately show $?thesis$ **by** auto
qed
next
case $(\text{bisim1TryCatch2 } e2 n e2' xs \text{ stk } \text{loc } \text{pc } \text{xcp } e C' V)$
note $\text{bisim2} = \langle P, e2, h \vdash (e2', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \rangle$
note $\text{bisim1} = \langle \bigwedge xs. P, e, h \vdash (e, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$
note $\text{IH2} = \text{bisim1TryCatch2.IH}(2)$
note $\text{red} = \langle \text{True}, P, t \vdash 1 \langle \{V:\text{Class } C'=\text{None}; e2'\rangle, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$
note $\text{bsok} = \langle \text{bsok } (\text{try } e \text{ catch}(C' V) e2) n \rangle$
from red show $?case$
proof cases
case $(\text{Block1Red } E')$
note $[simp] = \langle e' = \{V:\text{Class } C'=\text{None}; E'\rangle \rangle$
and $\text{red} = \langle \text{True}, P, t \vdash 1 \langle e2', (h, xs) \rangle -ta \rightarrow \langle E', (h', xs') \rangle \rangle$
from red have $\tau: \tau\text{move1 } P h \{V:\text{Class } C'=\text{None}; e2'\} = \tau\text{move1 } P h e2'$ **by** $(\text{auto simp add: } \tau\text{move1.simps } \tau\text{moves1.simps})$
from IH2 $[OF \text{red}] \text{ bsok}$ **obtain** $pc'' \text{ stk}'' \text{ loc}'' \text{ xcp}''$
where $\text{bisim}' : P, e2, h' \vdash (E', xs') \leftrightarrow (\text{stk}'', \text{loc}'', \text{pc}'', \text{xcp}'')$
and $\text{exec}' : ?\text{exec } ta e2 e2' E' h \text{ stk } \text{loc } \text{pc } \text{xcp } h' \text{ pc}'' \text{ stk}'' \text{ loc}'' \text{ xcp}''$ **by** auto
have $?\text{exec } ta (\text{try } e \text{ catch}(C' V) e2) \{V:\text{Class } C'=\text{None}; e2'\} \{V:\text{Class } C'=\text{None}; E'\} h \text{ stk } \text{loc}$
 $(\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e) + \text{pc}))) \text{ xcp } h' (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e) + \text{pc}))) \text{ stk}'' \text{ loc}'' \text{ xcp}''$
proof $(\text{cases } \tau\text{move1 } P h \{V:\text{Class } C'=\text{None}; e2'\})$
case True with $\tau \text{ exec}'$ **show** $?thesis$ **by** $(\text{auto intro: Try-}\tau\text{ExecrI2 Try-}\tau\text{ExecI2})$
next
case False
with $\tau \text{ exec}'$ **obtain** $pc' \text{ stk}' \text{ loc}' \text{ xcp}'$
where $e: \tau\text{Exec-mover-a } P t e2 h (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) (\text{stk}', \text{loc}', \text{pc}', \text{xcp}')$
and $e': \text{exec-move-a } P t e2 h (\text{stk}', \text{loc}', \text{pc}', \text{xcp}') (\text{extTA2JVM } (\text{compP2 } P) ta) h' (\text{stk}'', \text{loc}'', \text{pc}'', \text{xcp}'')$
and $\tau': \neg \tau\text{move2 } (\text{compP2 } P) h \text{ stk}' e2 \text{ pc}' \text{ xcp}'$
and $\text{call}: \text{call1 } e2' = \text{None} \vee \text{no-call2 } e2 \text{ pc} \vee \text{pc}' = \text{pc} \wedge \text{stk}' = \text{stk} \wedge \text{loc}' = \text{loc} \wedge \text{xcp}' = \text{xcp}$
by auto
from e **have** $\tau\text{Exec-mover-a } P t (\text{try } e \text{ catch}(C' V) e2) h (\text{stk}, \text{loc}, \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e) + \text{pc})), \text{xcp}) (\text{stk}', \text{loc}', \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e) + \text{pc})), \text{xcp}')$

```

    by(rule Try-τExecrI2)
  moreover from e'
  have exec-move-a P t (try e catch(C' V) e2) h (stk', loc', Suc (Suc (length (compE2 e) + pc')),
  xcp') (extTA2JVM (compP2 P) ta) h' (stk'', loc'', Suc (Suc (length (compE2 e) + pc'')), xcp'')
    by(rule exec-move-TryI2)
  moreover from τ' have τmove2 (compP2 P) h stk' (try e catch(C' V) e2) (Suc (Suc (length
  (compE2 e) + pc'))) xcp' ⇒ False
    by(simp add: τmove2-iff)
  moreover have call1 {V:Class C'=None; e2'} = call1 e2' by simp
  moreover have no-call2 e2 pc ⇒ no-call2 (try e catch(C' V) e2) (Suc (Suc (length (compE2
  e) + pc)))
    by(auto simp add: no-call2-def)
  ultimately show ?thesis using False call by(auto simp del: split-paired-Ex call1 .simps calls1 .simps)
  qed
  moreover from bisim'
  have P, try e catch(C' V) e2, h' ⊢ ({V:Class C'=None; E'}, xs') ↔ (stk'', loc'', Suc (Suc (length
  (compE2 e) + pc'')), xcp'')
    by(rule bisim1-bisims1.bisim1TryCatch2)
  ultimately show ?thesis using τ by auto blast+
next
case (Red1Block u)
note [simp] = ⟨e2' = Val u⟩ ⟨ta = ε⟩ ⟨e' = Val u⟩ ⟨h' = h⟩ ⟨xs' = xs⟩
from bisim2 have s: xcp = None xs = loc
  and τExec-mover-a P t e2 h (stk, loc, pc, xcp) ([u], loc, length (compE2 e2), None)
  by(auto dest: bisim1Val2D1)
hence τExec-mover-a P t (try e catch(C' V) e2) h (stk, loc, Suc (Suc (length (compE2 e) + pc)),
xcp) ([u], loc, Suc (Suc (length (compE2 e) + length (compE2 e2))), None)
  by -(rule Try-τExecrI2)
moreover
have P, try e catch(C' V) e2, h ⊢ (Val u, xs) ↔ ([u], xs, length (compE2 (try e catch(C' V) e2)),
None)
  by(rule bisim1Val2) simp
moreover have τmove1 P h {V:Class C'=None; Val u} by(rule τmove1BlockRed)
ultimately show ?thesis using s by auto
next
case (Block1Throw a)
note [simp] = ⟨e2' = Throw a⟩ ⟨ta = ε⟩ ⟨e' = Throw a⟩ ⟨h' = h⟩ ⟨xs' = xs⟩
have τ: τmove1 P h {V:Class C'=None; e2'} by(auto simp add: τmove1.simps τmoves1.simps)
from bisim2 have xcp = [a] ∨ xcp = None by(auto dest: bisim1-ThrowD)
thus ?thesis
proof
  assume [simp]: xcp = [a]
  with bisim2
  have P, try e catch(C' V) e2, h ⊢ (Throw a, xs) ↔ (stk, loc, Suc (Suc (length (compE2 e) +
pc)), xcp)
    by(auto intro: bisim1TryCatchThrow)
  thus ?thesis using τ by(fastforce)
next
assume [simp]: xcp = None
with bisim2 obtain pc'
  where τExec-mover-a P t e2 h (stk, loc, pc, None) ([Addr a], loc, pc', [a])
  and bisim': P, e2, h ⊢ (Throw a, xs) ↔ ([Addr a], loc, pc', [a]) and [simp]: xs = loc
  by(auto dest: bisim1-Throw-τExec-mover)
hence τExec-mover-a P t (try e catch(C' V) e2) h (stk, loc, Suc (Suc (length (compE2 e) +

```

```

pc)), None) ([Addr a], loc, Suc (Suc (length (compE2 e) + pc')), [a])
  by-(rule Try- $\tau$ ExecrI2)
  moreover from bisim'
  have P, try e catch(C' V) e2, h  $\vdash$  (Throw a, xs)  $\leftrightarrow$  ([Addr a], loc, Suc (Suc (length (compE2 e)
+ pc')), [a])
  by(rule bisim1TryCatchThrow)
  ultimately show ?thesis using  $\tau$  by auto
qed
qed
next
case bisim1TryFail thus ?case by auto
next
case bisim1TryCatchThrow thus ?case by auto
next
case bisims1Nil thus ?case by(auto elim!: reds1.cases)
next
case (bisims1List1 E n e xs stk loc pc xcp es)
note IH1 = bisims1List1.IH(2)
note IH2 = bisims1List1.IH(4)
note bisim1 =  $\langle P, E, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
note bisim2 =  $\langle \bigwedge xs. P, es, h \vdash (es, xs) [\leftrightarrow] ([], xs, 0, None) \rangle$ 
note bsok =  $\langle bsoks (E \# es) n \rangle$ 
from  $\langle True, P, t \vdash 1 \langle e \# es, (h, xs) \rangle [-ta \rightarrow] \langle es', (h', xs') \rangle \rangle$  show ?case
proof cases
case (List1Red1 E')
note [simp] =  $\langle es' = E' \# es \rangle$ 
and red =  $\langle True, P, t \vdash 1 \langle e, (h, xs) \rangle [-ta \rightarrow] \langle E', (h', xs') \rangle \rangle$ 
from red have  $\tau$ :  $\tau moves1 P h (e \# es) = \tau move1 P h e$  by(auto simp add:  $\tau move1.simps$ 
 $\tau moves1.simps$ )
moreover from IH1[OF red] bsok
obtain pc'' stk'' loc'' xcp'' where bisim:  $P, E, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$ 
and redo: ?exec ta E e E' h stk loc pc xcp h' pc'' stk'' loc'' xcp'' by auto
from bisim
have  $P, E \# es, h' \vdash (E' \# es, xs') [\leftrightarrow] (stk'', loc'', pc'', xcp'')$ 
by(rule bisim1-bisims1.bisims1List1)
moreover {
assume no-call2 E pc
hence no-calls2 (E # es) pc  $\vee$  pc = length (compE2 E) by(auto simp add: no-call2-def
no-calls2-def) }
moreover from red have calls1 (e # es) = call1 e by auto
ultimately show ?thesis using redo
apply(auto simp add: exec-move-def exec-moves-def simp del: call1.simps calls1.simps split:
if-split-asm split del: if-split)
apply(blast intro:  $\tau Exec-mover-\tau Exec-movesr \tau Exec-movet-\tau Exec-movest$  intro!: bisim1-bisims1.bisims1List1
elim:  $\tau moves2.cases$ ) +
done
next
case (List1Red2 ES' v)
note [simp] =  $\langle es' = Val v \# ES' \rangle$   $\langle e = Val v \rangle$ 
and red =  $\langle True, P, t \vdash 1 \langle es, (h, xs) \rangle [-ta \rightarrow] \langle ES', (h', xs') \rangle \rangle$ 
from bisim1 have s:  $xs = loc xcp = None$ 
and exec1:  $\tau Exec-mover-a P t E h (stk, loc, pc, xcp) ([v], loc, length (compE2 E), None)$ 
by(auto dest: bisim1Val2D1)
hence  $\tau Exec-movesr-a P t (E \# es) h (stk, loc, pc, xcp) ([v], loc, length (compE2 E), None)$ 

```

by $-(\text{rule } \tau\text{Exec-mover-}\tau\text{Exec-movesr})$
moreover from $IH2[OF \text{ red}] \text{ bsok}$ **obtain** $pc'' \text{ stk}'' \text{ loc}'' \text{ xcp}''$
where $\text{bisim}' : P, es, h' \vdash (ES', xs) [\leftrightarrow] (\text{stk}'', \text{loc}'', \text{pc}'', \text{xcp}'')$
and $\text{exec}' : ?\text{execs ta es es ES}' h \ [] \text{ xs } 0 \text{ None } h' \text{ pc}'' \text{ stk}'' \text{ loc}'' \text{ xcp}''$ **by auto**
have $\tau : \tau\text{moves1 } P h (\text{Val } v \# \text{es}) = \tau\text{moves1 } P h \text{ es}$ **by** $(\text{auto simp add: } \tau\text{move1.simps } \tau\text{moves1.simps})$
have $?\text{execs ta } (E \# \text{es}) (\text{Val } v \# \text{es}) (\text{Val } v \# \text{ES}') h [v] \text{ xs } (\text{length } (\text{compE2 } E)) \text{ None } h' (\text{length } (\text{compE2 } E) + \text{pc}'')$ $(\text{stk}'' @ [v]) \text{ loc}'' \text{ xcp}''$
proof $(\text{cases } \tau\text{moves1 } P h (\text{Val } v \# \text{es}))$
case True with $\tau \text{ exec}'$ **show** $?thesis$
using $\text{append-}\tau\text{Exec-movesr}[\text{of } [v] [E] - P t \text{ es } h \ [] \text{ xs } 0 \text{ None } \text{stk}'' \text{ loc}'' \text{ pc}'' \text{ xcp}'']$
 $\text{append-}\tau\text{Exec-movest}[\text{of } [v] [E] - P t \text{ es } h \ [] \text{ xs } 0 \text{ None } \text{stk}'' \text{ loc}'' \text{ pc}'' \text{ xcp}'']$ **by auto**
next
case False with $\tau \text{ exec}'$ **obtain** $pc' \text{ stk}' \text{ loc}' \text{ xcp}'$
where $e : \tau\text{Exec-movesr-a } P t \text{ es } h (\ [], \text{xs}, 0, \text{None}) (\text{stk}', \text{loc}', \text{pc}', \text{xcp}')$
and $e' : \text{exec-moves-a } P t \text{ es } h (\text{stk}', \text{loc}', \text{pc}', \text{xcp}') (\text{extTA2JVM } (\text{compP2 } P) \text{ ta}) h' (\text{stk}'', \text{loc}'', \text{pc}'', \text{xcp}'')$
and $\tau' : \neg \tau\text{moves2 } (\text{compP2 } P) h \text{ stk}' \text{ es } \text{pc}' \text{ xcp}'$
and $\text{call} : \text{calls1 es} = \text{None} \vee \text{no-calls2 es } 0 \vee \text{pc}' = 0 \wedge \text{stk}' = [] \wedge \text{loc}' = \text{xs} \wedge \text{xcp}' = \text{None}$
by auto
from $\text{append-}\tau\text{Exec-movesr}[OF - e, \text{ where } \text{vs}=[v] \text{ and } \text{es}' = [E]]$
have $\tau\text{Exec-movesr-a } P t (E \# \text{es}) h ([v], \text{xs}, \text{length } (\text{compE2 } E), \text{None}) (\text{stk}' @ [v], \text{loc}', \text{length } (\text{compE2 } E) + \text{pc}', \text{xcp}')$ **by simp**
moreover from $\text{append-exec-moves}[OF - e', \text{ of } [v] [E]]$
have $\text{exec-moves-a } P t (E \# \text{es}) h (\text{stk}' @ [v], \text{loc}', \text{length } (\text{compE2 } E) + \text{pc}', \text{xcp}') (\text{extTA2JVM } (\text{compP2 } P) \text{ ta}) h' (\text{stk}'' @ [v], \text{loc}'', \text{length } (\text{compE2 } E) + \text{pc}'', \text{xcp}'')$
by simp
moreover from $\tau' e'$
have $\tau\text{moves2 } (\text{compP2 } P) h (\text{stk}' @ [v]) (E \# \text{es}) (\text{length } (\text{compE2 } E) + \text{pc}') \text{ xcp}' \implies \text{False}$
by $(\text{auto simp add: } \tau\text{moves2-iff } \tau\text{instr-stk-drop-exec-moves})$
moreover have $\text{calls1 } (\text{Val } v \# \text{es}) = \text{calls1 es}$ **by simp**
moreover have $\text{no-calls2 es } 0 \implies \text{no-calls2 } (E \# \text{es}) (\text{length } (\text{compE2 } E))$
by $(\text{auto simp add: no-calls2-def})$
ultimately show $?thesis$ **using** False call **by** $(\text{auto simp del: split-paired-Ex call1.simps calls1.simps})$
blast
qed
moreover from bisim'
have $P, E \# \text{es}, h' \vdash (\text{Val } v \# \text{ES}', xs) [\leftrightarrow] (\text{stk}'' @ [v], \text{loc}'', \text{length } (\text{compE2 } E) + \text{pc}'', \text{xcp}'')$
by $(\text{rule bisim1-bisims1.bisims1List2})$
moreover from bisim1 **have** $\text{pc} \neq \text{length } (\text{compE2 } E) \longrightarrow \text{no-calls2 } (E \# \text{es}) \text{ pc}$
by $(\text{auto simp add: no-calls2-def dest: bisim-Val-pc-not-Invoke bisim1-pc-length-compE2})$
ultimately show $?thesis$ **using** $\tau \text{ exec}' s$
apply $(\text{auto simp del: split-paired-Ex call1.simps calls1.simps split: if-split-asm split del: if-split})$
apply $(\text{blast intro: } \tau\text{Exec-movesr-trans}[\text{fastforce elim!: } \tau\text{Exec-movesr-trans simp del: split-paired-Ex call1.simps calls1.simps}])$
done
qed
next
case $(\text{bisims1List2 } ES \text{ n es xs stk loc pc xcp e v})$
note $IH2 = \text{bisims1List2.IH}(2)$
note $\text{bisim1} = \langle \bigwedge \text{xs}. P, e, h \vdash (e, \text{xs}) \leftrightarrow (\ [], \text{xs}, 0, \text{None}) \rangle$
note $\text{bisim2} = \langle P, ES, h \vdash (es, \text{xs}) [\leftrightarrow] (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \rangle$
note $\text{bsok} = \langle \text{bsoks } (e \# \text{ES}) \text{ n} \rangle$
from $\langle \text{True}, P, t \vdash 1 \langle \text{Val } v \# \text{es}, (h, \text{xs}) \rangle [-\text{ta} \rightarrow] \langle \text{es}', (h', \text{xs}') \rangle \rangle$ **show** $?case$
proof cases

```

case (List1Red2 ES')
note [simp] = ⟨es' = Val v # ES'⟩
and red = ⟨True, P, t ⊢ 1 ⟨es, (h, xs)⟩ [-ta→] ⟨ES', (h', xs')⟩⟩
from IH2[OF red] bsok obtain pc'' stk'' loc'' xcp''
where bisim': P, ES, h' ⊢ (ES', xs') [↔] (stk'', loc'', pc'', xcp'')
and exec': ?execs ta ES es ES' h stk loc pc xcp h' pc'' stk'' loc'' xcp'' by auto
have τ: τmoves1 P h (Val v # es) = τmoves1 P h es by (auto simp add: τmove1.simps τmoves1.simps)
have ?execs ta (e # ES) (Val v # es) (Val v # ES') h (stk @ [v]) loc (length (compE2 e) + pc)
xcp h' (length (compE2 e) + pc'') (stk'' @ [v]) loc'' xcp''
proof (cases τmoves1 P h (Val v # es))
case True with τ exec' show ?thesis
using append-τExec-movesr[of [v] [e] - P t ES h stk]
append-τExec-movest[of [v] [e] - P t ES h stk] by auto
next
case False with τ exec' obtain pc' stk' loc' xcp'
where e: τExec-movesr-a P t ES h (stk, loc, pc, xcp) (stk', loc', pc', xcp')
and e': exec-moves-a P t ES h (stk', loc', pc', xcp') (extTA2JVM (compP2 P) ta) h' (stk'', loc'',
pc'', xcp'')
and τ': ¬ τmoves2 (compP2 P) h stk' ES pc' xcp'
and call: calls1 es = None ∨ no-calls2 ES pc ∨ pc' = pc ∧ stk' = stk ∧ loc' = loc ∧ xcp' = xcp
by auto
from append-τExec-movesr[OF - e, where vs=[v] and es' = [e]]
have τExec-movesr-a P t (e # ES) h (stk @ [v], loc, length (compE2 e) + pc, xcp) (stk' @ [v],
loc', length (compE2 e) + pc', xcp') by simp
moreover from append-exec-moves[OF - e', of [v] [e]]
have exec-moves-a P t (e # ES) h (stk' @ [v], loc', length (compE2 e) + pc', xcp') (extTA2JVM
(compP2 P) ta) h' (stk'' @ [v], loc'', length (compE2 e) + pc'', xcp'') by simp
moreover from τ' e'
have τmoves2 (compP2 P) h (stk' @ [v]) (e # ES) (length (compE2 e) + pc') xcp' ⇒ False
by (auto simp add: τmoves2-iff τinstr-stk-drop-exec-moves)
moreover have calls1 (Val v # es) = calls1 es by simp
moreover have no-calls2 ES pc ⇒ no-calls2 (e # ES) (length (compE2 e) + pc)
by (auto simp add: no-calls2-def)
ultimately show ?thesis using False call by (auto simp del: split-paired-Ex call1.simps calls1.simps)

qed
moreover from bisim'
have P, e # ES, h' ⊢ (Val v # ES', xs') [↔] (stk'' @ [v], loc'', length (compE2 e) + pc'', xcp'')
by (rule bisim1-bisims1.bisims1List2)
ultimately show ?thesis using τ by auto blast+
qed auto
qed
end

```

context J1-JVM-conf-read begin

lemma exec-1-simulates-Red1-τ:

```

assumes wf: wf-J1-prog P
and Red1: True, P, t ⊢ 1 ⟨(e, xs)/exs, h⟩ -ta→ ⟨(e', xs')/exs', h⟩
and bisim: bisim1-list1 t h (e, xs) exs xcp frs
and τ: τMove1 P h ((e, xs), exs)
shows ∃ xcp' frs'. (if sim12-size e' < sim12-size e then τExec-1-dr else τExec-1-dt) (compP2 P) t

```

$(xcp, h, frs) (xcp', h, frs') \wedge bisim1-list1\ t\ h\ (e', xs')\ exs'\ xcp'\ frs'$

proof –

from wf **have** $wt: wf-jvm-prog_{compTP\ P}\ (compP2\ P)$ **by**(rule $wt-compTP-compP2$)

from $Red1$ **show** $?thesis$

proof(cases)

case ($red1Red\ TA$)

note [$simp$] = $\langle ta = extTA2J1\ P\ TA \rangle \langle exs' = exs \rangle$

and $red = \langle True, P, t \vdash 1 \langle e, (h, xs) \rangle - TA \rightarrow \langle e', (h, xs') \rangle \rangle$

from $\tau\ red$ **have** $\tau': \tau\ move1\ P\ h\ e$ **by**(auto elim: $red1-cases$)

from $bisim$ **show** $?thesis$

proof(cases)

case ($bl1-Normal\ stk\ loc\ C\ M\ pc\ FRS\ Ts\ T\ body\ D$)

hence [$simp$]: $frs = (stk, loc, C, M, pc) \# FRS$

and $conf: compTP\ P \vdash t: (xcp, h, frs) \checkmark$

and $sees: P \vdash C\ sees\ M: Ts \rightarrow T = \lfloor body \rfloor$ in D

and $bisim: P, blocks1\ 0\ (Class\ D \# Ts)\ body, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp)$

and $bisims: list-all2\ (bisim1-fr\ P\ h)\ exs\ FRS$

and $lenxs: max-vars\ e \leq length\ xs$

by auto

from $sees\ wf$ **have** $bsok\ (blocks1\ 0\ (Class\ D \# Ts)\ body)\ 0$

by(auto dest!: $sees-wf-mdecl\ WT1-expr-locks\ simp\ add: wf-J1-mdecl-def\ wf-mdecl-def\ bsok-def$)

from $exec-instr-simulates-red1[OF\ wf\ bisim\ red\ this]$ τ' **obtain** $pc'\ stk'\ loc'\ xcp'$

where $exec: (if\ sim12-size\ e' < sim12-size\ e\ then\ \tau\ Exec-mover-a\ else\ \tau\ Exec-movet-a)\ P\ t\ body$
 $h\ (stk, loc, pc, xcp)\ (stk', loc', pc', xcp')$

and $b': P, blocks1\ 0\ (Class\ D \# Ts)\ body, h \vdash (e', xs') \leftrightarrow (stk', loc', pc', xcp')$

by(auto split: $if-split-asm\ simp\ del: blocks1.simps$)

from $exec\ sees$ **have** $(if\ sim12-size\ e' < sim12-size\ e\ then\ \tau\ Exec-1r\ else\ \tau\ Exec-1t)\ (compP2\ P)$
 $t\ (xcp, h, frs)\ (xcp', h, (stk', loc', C, M, pc') \# FRS)$

by(auto intro: $\tau\ Exec-mover-\tau\ Exec-1r\ \tau\ Exec-movet-\tau\ Exec-1t$)

from $wt\ this\ conf$ **have** $execd: (if\ sim12-size\ e' < sim12-size\ e\ then\ \tau\ Exec-1-dr\ else\ \tau\ Exec-1-dt)$
 $(compP2\ P)\ t\ (xcp, h, frs)\ (xcp', h, (stk', loc', C, M, pc') \# FRS)$

by(auto intro: $\tau\ Exec-1r-\tau\ Exec-1-dr\ \tau\ Exec-1t-\tau\ Exec-1-dt$)

moreover **from** $wt\ execd\ conf$

have $compTP\ P \vdash t: (xcp', h, (stk', loc', C, M, pc') \# FRS) \checkmark$

by(auto intro: $\tau\ Exec-1-dr-preserves-correct-state\ \tau\ Exec-1-dt-preserves-correct-state\ split: if-split-asm$)

hence $bisim1-list1\ t\ h\ (e', xs')\ exs\ xcp'\ ((stk', loc', C, M, pc') \# FRS)$

using $sees\ b'$

proof

from red **have** $max-vars\ e' \leq max-vars\ e$ **by**(rule $red1-max-vars$)

with $red1-preserves-len[OF\ red]\ lenxs$

show $max-vars\ e' \leq length\ xs'$ **by** $simp$

qed fact

hence $bisim1-list1\ t\ h\ (e', xs')\ exs'\ xcp'\ ((stk', loc', C, M, pc') \# FRS)$ **by** $simp$

ultimately **show** $?thesis$ **by** $blast$

qed(insert $red, auto\ elim: red1-cases$)

next

case ($red1Call\ a'\ M'\ vs'\ U'\ Ts'\ T'\ body'\ D'$)

hence [$simp$]: $ta = \varepsilon$

and $exs'\ [simp]: exs' = (e, xs) \# exs$

and $e': e' = blocks1\ 0\ (Class\ D' \# Ts')\ body'$

and $xs': xs' = Addr\ a' \# vs' @ replicate\ (max-vars\ body')\ undefined-value$

and $ha': typeof-addr\ h\ a' = \lfloor U' \rfloor$

and $call: call1\ e = \lfloor (a', M', vs') \rfloor$ **by** auto

note $sees' = \langle P \vdash class-type-of\ U'\ sees\ M': Ts' \rightarrow T' = \lfloor body' \rfloor$ in $D' \rangle$


```

note  $lenvs'Ts' = \langle length\ vs' = length\ Ts' \rangle$ 
from  $ha'$  sees-method-decl-above[OF sees']
have  $conf: P, h \vdash Addr\ a' : \leq\ ty\ of\ htype\ U'$  by (auto simp add: conf-def)
note  $wt = wt\ compTP\ compP2$ [OF wf]
from bisim show ?thesis
proof (cases)
  case (bl1-Normal stk loc C M pc FRS Ts T body D)
  hence [simp]:  $frs = (stk, loc, C, M, pc) \# FRS$ 
  and  $conf: compTP\ P \vdash t: (xcp, h, frs) \checkmark$ 
  and  $sees: P \vdash C\ sees\ M: Ts \rightarrow T = \lfloor body \rfloor$  in D
  and  $bisim: P, blocks1\ 0\ (Class\ D \# Ts)\ body, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp)$ 
  and  $bisims: list\ all2\ (bisim1\ fr\ P\ h)\ exs\ FRS$ 
  and  $lenxs: max\ vars\ e \leq length\ xs$  by auto
  from call bisim have [simp]:  $xcp = None$  by (cases xcp, auto dest: bisim1-call-xcpNone)
  from bisim have  $b: P, blocks1\ 0\ (Class\ D \# Ts)\ body, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, None)$  by simp
  from bisim have  $lenloc: length\ xs = length\ loc$  by (rule bisim1-length-xs)
  from sees have  $sees'': compP2\ P \vdash C\ sees\ M: Ts \rightarrow T = \lfloor (max\ stack\ body, max\ vars\ body, compE2\ body\ @\ [Return], compxE2\ body\ 0\ 0) \rfloor$  in D
  unfolding compP2-def compMb2-def Let-def by (auto dest: sees-method-compP)
  from sees wf have  $\neg\ contains\ insync\ (blocks1\ 0\ (Class\ D \# Ts)\ body)$ 
  by (auto dest!: sees-wf-mdecl WT1-expr-locks simp add: wf-J1-mdecl-def wf-mdecl-def contains-insync-conv)
  with bisim1-call- $\tau$ Exec-move[OF b call, of 0 t]  $lenxs$  obtain  $pc'\ loc'\ stk'$ 
  where  $exec: \tau Exec\ mover\ a\ P\ t\ body\ h\ (stk, loc, pc, None)\ (rev\ vs' \ @\ Addr\ a' \ #\ stk', loc', pc', None)$ 
  and  $pc': pc' < length\ (compE2\ body)$  and  $ins: compE2\ body ! pc' = Invoke\ M'\ (length\ vs')$ 
  and  $bisim': P, blocks1\ 0\ (Class\ D \# Ts)\ body, h \vdash (e, xs) \leftrightarrow (rev\ vs' \ @\ Addr\ a' \ #\ stk', loc', pc', None)$ 
  by (auto simp add: blocks1-max-vars simp del: blocks1.simps)
  let  $?f = (rev\ vs' \ @\ Addr\ a' \ #\ stk', loc', C, M, pc')$ 
  from exec sees
  have  $exec1: \tau Exec\ 1r\ (compP2\ P)\ t\ (None, h, (stk, loc, C, M, pc) \# FRS)\ (None, h, ?f \# FRS)$ 
  by (rule  $\tau Exec\ mover\ \tau Exec\ 1r$ )
  with wt have  $\tau Exec\ 1\ dr\ (compP2\ P)\ t\ (None, h, (stk, loc, C, M, pc) \# FRS)\ (None, h, ?f \# FRS)$  using conf
  by (simp) (rule  $\tau Exec\ 1r\ \tau Exec\ 1\ dr$ )
  also with wt have  $conf': compTP\ P \vdash t: (None, h, ?f \# FRS) \checkmark$  using conf
  by simp (rule  $\tau Exec\ 1\ dr\ preserves\ correct\ state$ )
  let  $?f' = (\ [], Addr\ a' \ #\ vs' \ @\ (replicate\ (max\ vars\ body')\ undefined\ value), D', M', 0)$ 
  from  $pc'\ ins\ sees\ sees'\ ha'$ 
  have  $(\varepsilon, None, h, ?f' \# ?f \# FRS) \in exec\ instr\ (instrs\ of\ (compP2\ P)\ C\ M ! pc')\ (compP2\ P)\ t\ h\ (rev\ vs' \ @\ Addr\ a' \ #\ stk')\ loc'\ C\ M\ pc'\ FRS$ 
  by (auto simp add: compP2-def compMb2-def nth-append split-beta)
  hence  $exec\ 1\ (compP2\ P)\ t\ (None, h, ?f \# FRS)\ \varepsilon\ (None, h, ?f' \# ?f \# FRS)$ 
  using exec sees by (simp add: exec-1-iff)
  with  $conf'$  have  $execd: compP2\ P, t \vdash Normal\ (None, h, ?f \# FRS) \rightarrow \varepsilon\ jvmd \rightarrow Normal\ (None, h, ?f' \# ?f \# FRS)$ 
  by (simp add: welltyped-commute[OF wt])
  hence  $check: check\ (compP2\ P)\ (None, h, ?f \# FRS)$  by (rule jvmd-NormalE)
  have  $\tau move2\ (compP2\ P)\ h\ (rev\ vs' \ @\ Addr\ a' \ #\ stk')\ body\ pc'\ None$  using  $pc'\ ins\ ha'\ sees'$ 
  by (auto simp add:  $\tau move2\ iff\ compP2\ def\ dest: sees\ method\ fun$ )
  with sees  $pc'\ ins$  have  $\tau Move2\ (compP2\ P)\ (None, h, (rev\ vs' \ @\ Addr\ a' \ #\ stk', loc', C, M, pc') \# FRS)$ 

```

unfolding τ Move2-compP2[OF sees] **by**(auto simp add: compP2-def compMb2-def)
with $\langle \text{exec-1 } (\text{compP2 } P) \ t \ (None, h, ?f \# \text{FRS}) \ \varepsilon \ (None, h, ?f' \# ?f \# \text{FRS}) \rangle$ **check**
have τ Exec-1-dt (compP2 P) t (None, h, ?f # FRS) (None, h, ?f' # ?f # FRS) **by** fastforce
also from $\text{execd sees'' sees' ins ha' pc' have compP2 } P, h \vdash vs' [\leq] Ts'$
by(auto simp add: check-def compP2-def split: if-split-asm elim!: jvmd-NormalE)
hence $\text{lenvs: length } vs' = \text{length } Ts'$ **by**(rule list-all2-lengthD)
from $wt \ \text{execd conf' have compTP } P \vdash t:(None, h, ?f' \# ?f \# \text{FRS}) \ \checkmark$
by(rule BV-correct-d-1)
hence $\text{bisim1-list1 } t \ h \ (\text{blocks1 } 0 \ (\text{Class } D' \# Ts') \ \text{body}', \ xs') \ ((e, \ xs) \# \ \text{exs}) \ \text{None} \ (?f' \# ?f \# \text{FRS})$
proof
from $\text{sees' show } P \vdash D' \ \text{sees } M': Ts' \rightarrow T' = \lfloor \text{body}' \rfloor \ \text{in } D'$ **by**(rule sees-method-idemp)
show $P, \text{blocks1 } 0 \ (\text{Class } D' \# Ts') \ \text{body}', h \vdash (\text{blocks1 } 0 \ (\text{Class } D' \# Ts') \ \text{body}', \ xs') \leftrightarrow$
 $([], \ \text{Addr } a' \# vs' \ @ \ \text{replicate } (\text{max-vars } \text{body}') \ \text{undefined-value}, \ 0, \ \text{None})$
unfolding xs' **by**(rule bisim1-refl)
show $\text{max-vars } (\text{blocks1 } 0 \ (\text{Class } D' \# Ts') \ \text{body}') \leq \text{length } xs'$
unfolding xs' **using** lenvs **by**(simp add: blocks1-max-vars)
from $\text{lenxs have } (\text{max-vars } e) \leq \text{length } xs$ **by** simp
with $\text{sees bisim' call have bisim1-fr } P \ h \ (e, \ xs) \ (\text{rev } vs' \ @ \ \text{Addr } a' \# \ \text{stk}', \ \text{loc}', \ C, \ M, \ \text{pc}')$
by(rule bisim1-fr.intros)
thus $\text{list-all2 } (\text{bisim1-fr } P \ h) \ ((e, \ xs) \# \ \text{exs})$
 $((\text{rev } vs' \ @ \ \text{Addr } a' \# \ \text{stk}', \ \text{loc}', \ C, \ M, \ \text{pc}') \# \ \text{FRS})$
using bisims **by** simp
qed
moreover have $\text{ta-bisim } \text{wbisim1 } \text{ta } \varepsilon$ **by** simp
ultimately show $?thesis$
unfolding $\langle \text{frs} = (\text{stk}, \ \text{loc}, \ C, \ M, \ \text{pc}) \# \ \text{FRS} \rangle \ \langle \text{xcp} = \text{None} \rangle \ e' \ \text{exs}'$
by auto(blast intro: tranclp-into-rtranclp)
next
case bl1-finalVal
with $\text{call show } ?thesis$ **by** simp
next
case bl1-finalThrow
with $\text{call show } ?thesis$ **by** simp
qed
next
case $(\text{red1Return } E)$
note $[\text{simp}] = \langle \text{exs} = (E, \ xs') \# \ \text{exs}' \rangle \ \langle \text{ta} = \varepsilon \rangle \ \langle e' = \text{inline-call } e \ E \rangle$
note $wt = wt\text{-compTP-compP2[OF wf]}$
from $\text{bisim have bisim: bisim1-list1 } t \ h \ (e, \ xs) \ ((E, \ xs') \# \ \text{exs}') \ \text{xcp } \text{frs}$ **by** simp
thus $?thesis$
proof cases
case $(\text{bl1-Normal } \text{stk } \text{loc } C \ M \ \text{pc } \text{FRS } Ts \ T \ \text{body } D)$
hence $[\text{simp}]: \text{frs} = (\text{stk}, \ \text{loc}, \ C, \ M, \ \text{pc}) \# \ \text{FRS}$
and $\text{conf: compTP } P \vdash t: (\text{xcp}, \ h, \ \text{frs}) \ \checkmark$
and $\text{sees: } P \vdash C \ \text{sees } M: Ts \rightarrow T = \lfloor \text{body} \rfloor \ \text{in } D$
and $\text{bisim: } P, \text{blocks1 } 0 \ (\text{Class } D \# Ts) \ \text{body}, h \vdash (e, \ xs) \leftrightarrow (\text{stk}, \ \text{loc}, \ \text{pc}, \ \text{xcp})$
and $\text{bisims: list-all2 } (\text{bisim1-fr } P \ h) \ ((E, \ xs') \# \ \text{exs}') \ \text{FRS}$
and $\text{lenxs: max-vars } e \leq \text{length } xs$ **by** auto
from $\text{bisims obtain } f \ \text{FRS}'$ **where** $[\text{simp}]: \text{FRS} = f \# \ \text{FRS}'$ **by**(fastforce simp add: list-all2-Cons1)
from $\text{bisims have bisim1-fr } P \ h \ (E, \ xs') \ f$ **by** simp
then obtain $C0 \ M0 \ Ts0 \ T0 \ \text{body0 } D0 \ \text{stk0 } \ \text{loc0 } \ \text{pc0 } \ a' \ M' \ vs'$
where $[\text{simp}]: f = (\text{stk0}, \ \text{loc0}, \ C0, \ M0, \ \text{pc0})$
and $\text{sees0: } P \vdash C0 \ \text{sees } M0: Ts0 \rightarrow T0 = \lfloor \text{body0} \rfloor \ \text{in } D0$

and $bisim0: P, blocks1\ 0\ (Class\ D0\ \#Ts0)\ body0, h \vdash (E, xs') \leftrightarrow (stk0, loc0, pc0, None)$
and $lenxs0: max\ vars\ E \leq length\ xs'$
and $call0: call1\ E = [(a', M', vs')]$
by *cases auto*

let $?ee = inline\ call\ e\ E$

from $bisim0\ call0$ **have** $pc0: pc0 < length\ (compE2\ (blocks1\ 0\ (Class\ D0\ \#Ts0)\ body0))$
by *(rule bisim1-call-pcD)*

hence $pc0: pc0 < length\ (compE2\ body0)$ **by** *simp*

with $sees\ method\ compP[OF\ sees0, \mathbf{where}\ f = \lambda C\ M\ Ts\ T. compMb2]$

$sees\ method\ compP[OF\ sees, \mathbf{where}\ f = \lambda C\ M\ Ts\ T. compMb2]$ **conf**

obtain $ST\ LT$ **where** $\Phi: compTP\ P\ C0\ M0 ! pc0 = [(ST, LT)]$

and $conff: conf\ f\ (compP\ (\lambda C\ M\ Ts\ T. compMb2)\ P)\ h\ (ST, LT)\ (compE2\ body0\ @\ [Return])$
 $(stk0, loc0, C0, M0, pc0)$

and $ins: (compE2\ body0\ @\ [Return]) ! pc0 = Invoke\ M\ (length\ Ts)$

by *(simp add: correct-state-def)(fastforce simp add: compP2-def compMb2-def dest: sees-method-fun)*

from $bisim1\ callD[OF\ bisim0\ call0, of\ M\ length\ Ts]$ $ins\ pc0$

have $[simp]: M' = M$ **by** *simp*

from $\langle final\ e \rangle$ **show** $?thesis$

proof *(cases)*

fix v

assume $[simp]: e = Val\ v$

with $bisim$ **have** $[simp]: xcp = None$ **by** *(auto dest: bisim-Val-loc-eq-xcp-None)*

from $bisim1Val2D1[OF\ bisim[unfolded\ \langle xcp = None \rangle\ \langle e = Val\ v \rangle]]$

have $\tau Exec\ mover\ a\ P\ t\ body\ h\ (stk, loc, pc, None)\ ([v], loc, length\ (compE2\ body), None)$

and $[simp]: xs = loc$ **by** *(auto simp del: blocks1.simps)*

with $sees$ **have** $\tau Exec\ 1r\ (compP2\ P)\ t\ (None, h, (stk, loc, C, M, pc) \# FRS)\ (None, h, ([v], loc, C, M, length\ (compE2\ body)) \# FRS)$

by *-(rule $\tau Exec\ mover\ \tau Exec\ 1r$)*

with $conf\ wt$ **have** $\tau Exec\ 1\ dr\ (compP2\ P)\ t\ (None, h, (stk, loc, C, M, pc) \# FRS)\ (None, h, ([v], loc, C, M, length\ (compE2\ body)) \# FRS)$

by *(simp)(rule $\tau Exec\ 1r\ \tau Exec\ 1\ dr$)*

moreover with $conf\ wt$ **have** $conf': compTP\ P \vdash t: (None, h, ([v], loc, C, M, length\ (compE2\ body)) \# FRS) \checkmark$

by *(simp)(rule $\tau Exec\ 1\ dr\ preserves\ correct\ state$)*

from $sees\ sees0$

have $exec: exec\ 1\ (compP2\ P)\ t\ (None, h, ([v], loc, C, M, length\ (compE2\ body)) \# FRS) \varepsilon$
 $(None, h, (v \# drop\ (Suc\ (length\ Ts))\ stk0, loc0, C0, M0, Suc\ pc0) \# FRS')$

by *(simp add: exec-1-iff compP2-def compMb2-def)*

moreover with $conf'\ wt$ **have** $compP2\ P, t \vdash Normal\ (None, h, ([v], loc, C, M, length\ (compE2\ body)) \# FRS) \text{---} \varepsilon \text{---} jvmd \rightarrow Normal\ (None, h, (v \# drop\ (Suc\ (length\ Ts))\ stk0, loc0, C0, M0, Suc\ pc0) \# FRS')$

by *(simp add: welltyped-commute)*

hence $check\ (compP2\ P)\ (None, h, ([v], loc, C, M, length\ (compE2\ body)) \# FRS)$

by *(rule $jvmd\ NormalE$)*

moreover have $\tau Move2\ (compP2\ P)\ (None, h, ([v], loc, C, M, length\ (compE2\ body)) \# FRS)$

unfolding $\tau Move2\ compP2[OF\ sees]$ **by** *(auto)*

ultimately have $\tau Exec\ 1\ dt\ (compP2\ P)\ t\ (None, h, (stk, loc, C, M, pc) \# FRS)\ (None, h, (v \# drop\ (Suc\ (length\ Ts))\ stk0, loc0, C0, M0, Suc\ pc0) \# FRS')$

by *-(erule $rtranclp\ into\ tranclp1, rule\ \tau exec\ 1\ dI$)*

moreover from $wt\ conf'\ exec$

have $\text{compTP } P \vdash t:(\text{None}, h, (v \# \text{drop } (\text{Suc } (\text{length } Ts)) \text{ stk0}, \text{loc0}, C0, M0, \text{Suc } \text{pc0}) \# \text{FRS}') \checkmark$
by(rule BV-correct-1)
hence $\text{bisim1-list1 } t \ h \ (\text{?ee}, xs') \ \text{exs}' \ \text{None} \ ((v \# \text{drop } (\text{Suc } (\text{length } Ts)) \ \text{stk0}, \text{loc0}, C0, M0, \text{Suc } \text{pc0}) \# \text{FRS}')$
using *sees0*
proof
from $\text{bisim1-inline-call-Val}[OF \ \text{bisim0} \ \text{call0}, \text{of } \text{length } Ts \ v] \ \text{ins } \text{pc0}$
show $P, \text{blocks1 } 0 \ (\text{Class } D0 \# Ts0) \ \text{body0}, h \vdash (\text{?ee}, xs') \leftrightarrow (v \# \text{drop } (\text{Suc } (\text{length } Ts)) \ \text{stk0}, \text{loc0}, \text{Suc } \text{pc0}, \text{None})$
by *simp*
from $\text{lenxs0} \ \text{max-vars-inline-call}[of \ e \ E]$
show $\text{max-vars } (\text{inline-call } e \ E) \leq \text{length } xs' \ \text{by } \text{simp}$
from bisims **show** $\text{list-all2 } (\text{bisim1-fr } P \ h) \ \text{exs}' \ \text{FRS}' \ \text{by } \text{simp}$
qed
ultimately show *?thesis*
by $\text{-(rule } \text{exI} \ \text{conjI} | \text{assumption} | \text{simp}) +$
next
fix *ad*
assume $[\text{simp}]: e = \text{Throw } ad$

have $\exists \text{stk}' \ \text{pc}'. \ \tau \text{Exec-mover-a } P \ t \ \text{body } h \ (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \ (\text{stk}', \text{loc}, \text{pc}', [\text{ad}]) \wedge$
 $P, \text{blocks1 } 0 \ (\text{Class } D \# Ts) \ \text{body}, h \vdash (\text{Throw } ad, \text{loc}) \leftrightarrow (\text{stk}', \text{loc}, \text{pc}', [\text{ad}])$
proof(cases *xcp*)
case $[\text{simp}]: \text{None}$
from $\text{bisim1-Throw-}\tau \text{Exec-mover}[OF \ \text{bisim}[\text{unfolded } \text{None} \ \langle e = \text{Throw } ad \rangle]]$ **obtain** pc'
where $\text{exec}: \tau \text{Exec-mover-a } P \ t \ \text{body } h \ (\text{stk}, \text{loc}, \text{pc}, \text{None}) \ ([\text{Addr } ad], \text{loc}, \text{pc}', [\text{ad}])$
and $\text{bisim}': P, \text{blocks1 } 0 \ (\text{Class } D \# Ts) \ \text{body}, h \vdash (\text{Throw } ad, \text{xs}) \leftrightarrow ([\text{Addr } ad], \text{loc}, \text{pc}', [\text{ad}])$
and $[\text{simp}]: \text{xs} = \text{loc}$ **by**(auto *simp del: blocks1.simps*)
thus *?thesis* **by** *fastforce*
next
case $(\text{Some } a')$
with bisim **have** $a' = ad \ \text{xs} = \text{loc}$ **by**(auto *dest: bisim1-ThrowD*)
thus *?thesis* **using** bisim **Some** **by**(auto)
qed
then obtain $\text{stk}' \ \text{pc}'$ **where** $\text{exec}: \tau \text{Exec-mover-a } P \ t \ \text{body } h \ (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \ (\text{stk}', \text{loc}, \text{pc}', [\text{ad}])$
and $\text{bisim}': P, \text{blocks1 } 0 \ (\text{Class } D \# Ts) \ \text{body}, h \vdash (\text{Throw } ad, \text{loc}) \leftrightarrow (\text{stk}', \text{loc}, \text{pc}', [\text{ad}])$ **by**
blast
with sees **have** $\tau \text{Exec-1r} \ (\text{compP2 } P) \ t \ (\text{xcp}, h, (\text{stk}, \text{loc}, C, M, \text{pc}) \# \text{FRS}) \ ([\text{ad}], h, (\text{stk}', \text{loc}, C, M, \text{pc}') \# \text{FRS})$
by $\text{-(rule } \tau \text{Exec-mover-}\tau \text{Exec-1r})$
with $\text{conf } wt$ **have** $\tau \text{Exec-1-dr} \ (\text{compP2 } P) \ t \ (\text{xcp}, h, (\text{stk}, \text{loc}, C, M, \text{pc}) \# \text{FRS}) \ ([\text{ad}], h, (\text{stk}', \text{loc}, C, M, \text{pc}') \# \text{FRS})$
by(*simp*)(rule $\tau \text{Exec-1r-}\tau \text{Exec-1-dr}$)
moreover with $\text{conf } wt$ **have** $\text{conf}': \text{compTP } P \vdash t: ([\text{ad}], h, (\text{stk}', \text{loc}, C, M, \text{pc}') \# \text{FRS}) \checkmark$
by(*simp*)(rule $\tau \text{Exec-1-dr-preserves-correct-state}$)
from $\text{bisim1-xcp-Some-not-caught}[OF \ \text{bisim}', \text{of } \lambda C \ M \ Ts \ T. \ \text{compMb2 } 0 \ 0] \ \text{sees}$
have $\text{match}: \text{match-ex-table } (\text{compP2 } P) \ (\text{cname-of } h \ ad) \ \text{pc}' \ (\text{ex-table-of } (\text{compP2 } P) \ C \ M) =$
 None
by(*simp add: compP2-def compMb2-def*)
hence $\text{exec}: \text{exec-1} \ (\text{compP2 } P) \ t \ ([\text{ad}], h, (\text{stk}', \text{loc}, C, M, \text{pc}') \# \text{FRS}) \ \varepsilon \ ([\text{ad}], h, \text{FRS})$
by(*simp add: exec-1-iff*)
moreover

with $conf' wt$ **have** $compP2 P, t \vdash Normal ([ad], h, (stk', loc, C, M, pc') \# FRS) -\varepsilon-jvmd \rightarrow Normal ([ad], h, FRS)$
by (*simp add: welltyped-commute*)
hence $check (compP2 P) ([ad], h, (stk', loc, C, M, pc') \# FRS)$ **by** (*rule jvmd-NormalE*)
moreover from $bisim'$ **have** $\tau Move2 (compP2 P) ([ad], h, (stk', loc, C, M, pc') \# FRS)$
unfolding $\tau Move2-compP2[OF sees]$ **by** (*auto dest: bisim1-pc-length-compE2*)
ultimately have $\tau Exec-1-dt (compP2 P) t (xcp, h, (stk, loc, C, M, pc) \# FRS) ([ad], h, FRS)$
by $-(erule rtranclp-into-tranclp1, rule \tau exec-1-dI)$
moreover from $wt conf' exec$
have $compTP P \vdash t: ([ad], h, (stk0, loc0, C0, M0, pc0) \# FRS') \checkmark$
by (*simp*) (*rule BV-correct-1*)
hence $bisim1-list1 t h (?ee, xs') exs' [ad] ((stk0, loc0, C0, M0, pc0) \# FRS')$
using *sees0*
proof
from $bisim1-inline-call-Throw[OF bisim0 call0]$ *ins pc0*
show $P, blocks1 0 (Class D0 \# Ts0) body0, h \vdash (?ee, xs') \leftrightarrow (stk0, loc0, pc0, [ad])$ **by** *simp*
from $lenxs0 max-vars-inline-call[of e E]$
show $max-vars ?ee \leq length xs'$ **by** *simp*
from $bisims Cons$ **show** $list-all2 (bisim1-fr P h) exs' FRS'$ **by** *simp*
qed
moreover from $call0$ **have** $sim12-size (inline-call (Throw ad) E) > 0$ **by** (*cases E*) *simp-all*
ultimately show *?thesis*
by $-(rule exI conjI | assumption | simp) +$
qed
qed
qed
qed

lemma *exec-1-simulates-Red1-not- τ :*

assumes $wf: wf-J1-prog P$
and $Red1: True, P, t \vdash 1 \langle (e, xs) / exs, h \rangle -ta \rightarrow \langle (e', xs') / exs', h' \rangle$
and $bisim: bisim1-list1 t h (e, xs) exs xcp frs$
and $\tau: \neg \tau Move1 P h ((e, xs), exs)$
shows $\exists xcp' frs'. \tau Exec-1-dr (compP2 P) t (xcp, h, frs) (xcp', h, frs') \wedge$
 $(\exists ta' xcp'' frs''. exec-1-d (compP2 P) t (Normal (xcp', h, frs')) ta' (Normal (xcp'', h', frs'')))$
 \wedge
 $\neg \tau Move2 (compP2 P) (xcp', h, frs') \wedge ta-bisim wbisim1 ta ta' \wedge$
 $bisim1-list1 t h' (e', xs') exs' xcp'' frs'' \wedge$
 $(call1 e = None \vee$
 $(case frs of Nil \Rightarrow False \mid (stk, loc, C, M, pc) \# FRS \Rightarrow \forall M' n. instrs-of (compP2 P) C$
 $M ! pc \neq Invoke M' n) \vee$
 $xcp' = xcp \wedge frs' = frs)$

using *Red1*

proof (*cases*)

case (*red1Red TA*)

hence [*simp*]: $ta = extTA2J1 P TA exs' = exs$

and $red: True, P, t \vdash 1 \langle e, (h, xs) \rangle -TA \rightarrow \langle e', (h', xs') \rangle$ **by** *simp-all*

from red **have** $hext: hext h h'$ **by** (*auto dest: red1-hext-incr*)

from τ **have** $\tau': \neg \tau move1 P h e$ **by** (*auto intro: \tau move1Block*)

note $wt = wt-compTP-compP2[OF wf]$

from $bisim$ **show** *?thesis*

proof (*cases*)

case (*bl1-Normal stk loc C M pc FRS Ts T body D*)

hence $[simp]: frs = (stk, loc, C, M, pc) \# FRS$
and $conf: compTP P \vdash t: (xcp, h, frs) \checkmark$
and $sees: P \vdash C \text{ sees } M: Ts \rightarrow T = [body] \text{ in } D$
and $bisim: P, blocks1\ 0\ (Class\ D \# Ts)\ body, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp)$
and $bisims: list-all2\ (bisim1-fr\ P\ h)\ exs\ FRS$
and $lenxs: max-vars\ e \leq length\ xs$ **by** *auto*
from $sees\ wf$ **have** $bsok\ (blocks1\ 0\ (Class\ D \# Ts)\ body)\ 0$
by $(auto\ dest!: sees-wf-mdecl\ WT1-expr-locks\ simp\ add: wf-J1-mdecl-def\ wf-mdecl-def\ bsok-def)$

from $exec-instr-simulates-red1\ [OF\ wf\ bisim\ red\ this]\ \tau'$
obtain $pc'\ stk'\ loc'\ xcp'\ pc''\ stk''\ loc''\ xcp''$
where $exec1: \tau Exec-mover-a\ P\ t\ body\ h\ (stk, loc, pc, xcp)\ (stk', loc', pc', xcp')$
and $exec2: exec-move-a\ P\ t\ body\ h\ (stk', loc', pc', xcp')\ (extTA2JVM\ (compP2\ P)\ TA)\ h'\ (stk'', loc'', pc'', xcp'')$
and $\tau2: \neg \tau move2\ (compP2\ P)\ h\ stk'\ body\ pc'\ xcp'$
and $b': P, blocks1\ 0\ (Class\ D \# Ts)\ body, h' \vdash (e', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$
and $call: call1\ e = None \vee no-call2\ (blocks1\ 0\ (Class\ D \# Ts)\ body)\ pc \vee pc' = pc \wedge stk' = stk \wedge loc' = loc \wedge xcp' = xcp$
by $(fastforce\ simp\ add: exec-move-def\ simp\ del: blocks1.simps)$
from $exec2$ **have** $pc'body: pc' < length\ (compE2\ body)$ **by** *(auto)*
from $exec1$ $sees$ **have** $exec1': \tau Exec-1r\ (compP2\ P)\ t\ (xcp, h, frs)\ (xcp', h, (stk', loc', C, M, pc')) \# FRS$
by $(auto\ intro: \tau Exec-mover-\tau Exec-1r)$
with wt **have** $execd: \tau Exec-1-dr\ (compP2\ P)\ t\ (xcp, h, frs)\ (xcp', h, (stk', loc', C, M, pc')) \# FRS$
using $conf$ **by** $(rule\ \tau Exec-1r-\tau Exec-1-dr)$
moreover **{** **fix** a
assume $[simp]: xcp' = [a]$
from $exec2$ $sees-method-compP\ [OF\ sees, of\ \lambda C\ M\ Ts\ T.\ compMb2]\ pc'body$
have $match-ex-table\ (compP2\ P)\ (cname-of\ h\ a)\ pc'\ (ex-table-of\ (compP2\ P)\ C\ M) \neq None$
by $(auto\ simp\ add: exec-move-def\ compP2-def\ compMb2-def\ elim!: exec-meth.cases)$ **}**
note $xt = this$
with $\tau2$ $sees\ pc'body$ **have** $\tau2': \neg \tau Move2\ (compP2\ P)\ (xcp', h, (stk', loc', C, M, pc')) \# FRS$
unfolding $\tau Move2-compP2\ [OF\ sees]$ **by** $(auto\ simp\ add: compP2-def\ compMb2-def\ \tau move2-iff)$
moreover **from** $exec2$ $sees$
have $exec2': exec-1\ (compP2\ P)\ t\ (xcp', h, (stk', loc', C, M, pc')) \# FRS$ $(extTA2JVM\ (compP2\ P)\ TA)\ (xcp'', h', (stk'', loc'', C, M, pc'')) \# FRS$
by $(rule\ exec-move-exec-1)$
from $wt\ execd\ conf$ **have** $conf': compTP\ P \vdash t: (xcp', h, (stk', loc', C, M, pc')) \# FRS$ \checkmark
by $(rule\ \tau Exec-1-dr-preserves-correct-state)$
with $exec2'\ wt$
have $exec-1-d\ (compP2\ P)\ t\ (Normal\ (xcp', h, (stk', loc', C, M, pc')) \# FRS)$ $(extTA2JVM\ (compP2\ P)\ TA)\ (Normal\ (xcp'', h', (stk'', loc'', C, M, pc'')) \# FRS)$
by $(simp\ add: welltyped-commute)$
moreover
from $\tau2$ $sees\ pc'body\ xt$ **have** $\tau2': \neg \tau Move2\ (compP2\ P)\ (xcp', h, (stk', loc', C, M, pc')) \# FRS$
unfolding $\tau Move2-compP2\ [OF\ sees]$ **by** $(auto\ simp\ add: compP2-def\ compMb2-def\ \tau move2-iff)$
moreover **from** $wt\ conf'\ exec2'$
have $conf'': compTP\ P \vdash t: (xcp'', h', (stk'', loc'', C, M, pc'')) \# FRS$ \checkmark **by** $(rule\ BV-correct-1)$
hence $bisim1-list1\ t\ h'\ (e', xs')\ exs\ xcp''\ ((stk'', loc'', C, M, pc'')) \# FRS$ **using** $sees\ b'$
proof
from $red1-preserves-len\ [OF\ red]\ red1-max-vars\ [OF\ red]\ lenxs$
show $max-vars\ e' \leq length\ xs'$ **by** *simp*

from $bisims$ **show** $list-all2\ (bisim1-fr\ P\ h')\ exs\ FRS$

```

    by(rule List.list-all2-mono)(rule bisim1-fr-heap-mono[OF - heap])
qed
moreover from conf'' have hconf h' preallocated h' by(auto simp add: correct-state-def)
with wf red
have ta-bisim wbisim1 ta (extTA2JVM (compP2 P) TA)
  by(auto intro: ta-bisim-red-extTA2J1-extTA2JVM)
moreover from call sees-method-compP[OF sees, of  $\lambda C M Ts T. compMb2$ ]
have call1 e = None  $\vee$  (case frs of []  $\Rightarrow$  False | (stk, loc, C, M, pc) # FRS  $\Rightarrow$   $\forall M' n. instrs-of$ 
(compP2 P) C M ! pc  $\neq$  Invoke M' n)  $\vee$  xcp' = xcp  $\wedge$  (stk', loc', C, M, pc') # FRS = frs
  by(auto simp add: no-call2-def compP2-def compMb2-def)
ultimately show ?thesis by  $-(rule exI conjI|assumption|simp)+$ 
next
case bl1-finalVal
with red show ?thesis by auto
next
case bl1-finalThrow
with red show ?thesis by(auto elim: red1-cases)
qed
next
case red1Call
with  $\tau$  have False
  by(auto simp add: synthesized-call-def dest!:  $\tau$  move1-not-call1 [where P=P and h=h] dest: sees-method-fun)
thus ?thesis ..
next
case red1Return
with  $\tau$  have False by auto
thus ?thesis ..
qed
end
end

```

7.18 Correctness of Stage 2: From JVM to intermediate language

theory JVMJ1 imports

J1JVMBisim

begin

declare split-paired-Ex[simp del]

lemma rec-option-is-case-option: rec-option = case-option

apply (rule ext)+

apply (rename-tac y)

apply (case-tac y)

apply auto

done

context J1-JVM-heap-base begin

lemma assumes ha: typeof-addr h a = [Class-type D]

and subclsObj: $P \vdash D \preceq^* Throwable$

shows *bisim1-xcp-τRed*:
 $\llbracket P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, [a]);$
match-ex-table (*compP f P*) (*cname-of h a*) *pc* (*compxE2 e 0 0*) = *None*;
 $\exists n. n + \text{max-vars } e' \leq \text{length } xs \wedge \mathcal{B} e n \rrbracket$
 $\implies \tau\text{red1r } P \text{ t h } (e', xs) (\text{Throw } a, loc) \wedge P, e, h \vdash (\text{Throw } a, loc) \leftrightarrow (stk, loc, pc, [a])$

and *bisims1-xcp-τReds*:
 $\llbracket P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, [a]);$
match-ex-table (*compP f P*) (*cname-of h a*) *pc* (*compxEs2 es 0 0*) = *None*;
 $\exists n. n + \text{max-varss } es' \leq \text{length } xs \wedge \mathcal{B} s \text{ es } n \rrbracket$
 $\implies \exists vs \ es''. \tau\text{reds1r } P \text{ t h } (es', xs) (\text{map Val vs } @ \text{ Throw } a \# es'', loc) \wedge$
 $P, es, h \vdash (\text{map Val vs } @ \text{ Throw } a \# es'', loc) [\leftrightarrow] (stk, loc, pc, [a])$

proof(*induct* (*e', xs*) (*stk, loc, pc, [a :: 'addr]*)
and (*es', xs*) (*stk, loc, pc, [a :: 'addr]*)
arbitrary: *e' xs stk loc pc* **and** *es' xs stk loc pc* *rule*: *bisim1-bisims1.inducts*)
case *bisim1NewThrow* **thus** ?*case*
by(*fastforce* *intro*: *bisim1-bisims1.intros*)
next
case *bisim1NewArray* **thus** ?*case*
by(*auto* *intro*: *rtranclp.rtrancl-into-rtrancl New1ArrayThrow bisim1-bisims1.intros* *dest*: *bisim1-ThrowD*
elim!: *NewArray-τred1r-xt*)
next
case *bisim1NewArrayThrow* **thus** ?*case*
by(*fastforce* *intro*: *bisim1-bisims1.intros*)
next
case *bisim1NewArrayFail* **thus** ?*case*
by(*fastforce* *intro*: *bisim1-bisims1.intros*)
next
case *bisim1Cast* **thus** ?*case*
by(*fastforce* *intro*: *rtranclp.rtrancl-into-rtrancl Cast1Throw bisim1-bisims1.intros* *dest*: *bisim1-ThrowD*
elim!: *Cast-τred1r-xt*)
next
case *bisim1CastThrow* **thus** ?*case*
by(*fastforce* *intro*: *bisim1-bisims1.intros*)
next
case *bisim1CastFail* **thus** ?*case*
by(*fastforce* *intro*: *bisim1-bisims1.intros*)
next
case *bisim1InstanceOf* **thus** ?*case*
by(*fastforce* *intro*: *rtranclp.rtrancl-into-rtrancl InstanceOf1Throw bisim1-bisims1.intros* *dest*: *bisim1-ThrowD*
elim!: *InstanceOf-τred1r-xt*)
next
case *bisim1InstanceOfThrow* **thus** ?*case*
by(*fastforce* *intro*: *bisim1-bisims1.intros*)
next
case *bisim1BinOp1* **thus** ?*case*
by(*fastforce* *intro*: *rtranclp.rtrancl-into-rtrancl Bin1OpThrow1 bisim1-bisims1.intros* *simp* *add*:
match-ex-table-append *dest*: *bisim1-ThrowD* *elim!*: *BinOp-τred1r-xt1*)
next
case *bisim1BinOp2* **thus** ?*case*
by(*clarsimp* *simp* *add*: *match-ex-table-append-not-pcs compxE2-size-convs compxE2-stack-xlift-convs*
match-ex-table-shift-pc-None)
(*fastforce* *intro*: *rtranclp.rtrancl-into-rtrancl red1-reds1.intros bisim1BinOpThrow2* *elim!*: *BinOp-τred1r-xt2*)
next


```

case bisim1BinOpThrow1 thus ?case
  by(auto simp add: match-ex-table-append intro: bisim1-bisims1.intros)
next
case (bisim1BinOpThrow2 e xs stk loc pc e1 bop)
note bisim = ⟨P,e,h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
hence xs = loc by(auto dest: bisim1-ThrowD)
with bisim show ?case
  by(auto intro: bisim1-bisims1.bisim1BinOpThrow2)
next
case bisim1BinOpThrow thus ?case
  by(fastforce intro: bisim1-bisims1.intros)
next
case bisim1LAss1 thus ?case
  by(fastforce intro: rtranclp.rtrancl-into-rtrancl LAss1Throw bisim1-bisims1.intros dest: bisim1-ThrowD
elim!: LAss-τred1r)
next
case bisim1LAssThrow thus ?case
  by(fastforce intro: bisim1-bisims1.intros)
next
case bisim1AAcc1 thus ?case
  by(fastforce intro: rtranclp.rtrancl-into-rtrancl AAcc1Throw1 bisim1-bisims1.intros simp add: match-ex-table-append
dest: bisim1-ThrowD elim!: AAcc-τred1r-xt1)
next
case bisim1AAcc2 thus ?case
  by(clarsimp simp add: match-ex-table-append-not-pcs compxE2-size-convs compxE2-stack-xlift-convs
match-ex-table-shift-pc-None)
  (fastforce intro: rtranclp.rtrancl-into-rtrancl red1-reds1.intros bisim1AAccThrow2 elim!: AAcc-τred1r-xt2)
next
case bisim1AAccThrow1 thus ?case
  by(auto simp add: match-ex-table-append intro: bisim1-bisims1.intros)
next
case bisim1AAccThrow2 thus ?case
  by(auto simp add: match-ex-table-append intro: bisim1-bisims1.intros dest: bisim1-ThrowD)
next
case bisim1AAccFail thus ?case
  by(fastforce intro: bisim1-bisims1.intros)
next
case bisim1AAss1 thus ?case
  by(fastforce intro: rtranclp.rtrancl-into-rtrancl AAss1Throw1 bisim1-bisims1.intros simp add: match-ex-table-append
dest: bisim1-ThrowD elim!: AAss-τred1r-xt1)
next
case bisim1AAss2 thus ?case
  by(clarsimp simp add: compxE2-size-convs compxE2-stack-xlift-convs)
  (fastforce simp add: match-ex-table-append intro: rtranclp.rtrancl-into-rtrancl red1-reds1.intros
bisim1AAssThrow2 elim!: AAss-τred1r-xt2)
next
case bisim1AAss3 thus ?case
  by(clarsimp simp add: compxE2-size-convs compxE2-stack-xlift-convs)
  (fastforce simp add: match-ex-table-append intro: rtranclp.rtrancl-into-rtrancl red1-reds1.intros
bisim1AAssThrow3 elim!: AAss-τred1r-xt3)
next
case bisim1AAssThrow1 thus ?case
  by(auto simp add: match-ex-table-append intro: bisim1-bisims1.intros)
next

```

```

case (bisim1AAssThrow2 e xs stk loc pc i e2)
note bisim = ⟨P,e,h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
hence xs = loc by(auto dest: bisim1-ThrowD)
with bisim show ?case
  by(auto intro: bisim1-bisims1.bisim1AAssThrow2)
next
case (bisim1AAssThrow3 e xs stk loc pc A i)
note bisim = ⟨P,e,h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
hence xs = loc by(auto dest: bisim1-ThrowD)
with bisim show ?case
  by(auto intro: bisim1-bisims1.bisim1AAssThrow3)
next
case bisim1AAssFail thus ?case
  by(fastforce intro: bisim1-bisims1.intros)
next
case bisim1ALength thus ?case
  by(fastforce intro: rtranclp.rtrancl-into-rtrancl ALength1Throw bisim1-bisims1.intros dest: bisim1-ThrowD
elim!: ALength-τred1r-xt)
next
case bisim1ALengthThrow thus ?case
  by(fastforce intro: bisim1-bisims1.intros)
next
case bisim1ALengthNull thus ?case
  by(fastforce intro: bisim1-bisims1.intros)
next
case bisim1FAcc thus ?case
  by(fastforce intro: rtranclp.rtrancl-into-rtrancl FAcc1Throw bisim1-bisims1.intros dest: bisim1-ThrowD
elim!: FAcc-τred1r-xt)
next
case bisim1FAccThrow thus ?case
  by(fastforce intro: bisim1-bisims1.intros)
next
case bisim1FAccNull thus ?case
  by(fastforce intro: bisim1-bisims1.intros)
next
case bisim1FAss1 thus ?case
  by(fastforce intro: rtranclp.rtrancl-into-rtrancl FAss1Throw1 bisim1-bisims1.intros simp add: match-ex-table-append
dest: bisim1-ThrowD elim!: FAss-τred1r-xt1)
next
case bisim1FAss2 thus ?case
  by(clarsimp simp add: match-ex-table-append-not-pcs compxE2-size-convs compxE2-stack-xlift-convs)
(fastforce intro: rtranclp.rtrancl-into-rtrancl red1-reds1.intros bisim1FAssThrow2 elim!: FAss-τred1r-xt2)
next
case bisim1FAssThrow1 thus ?case
  by(auto simp add: match-ex-table-append intro: bisim1-bisims1.intros)
next
case (bisim1FAssThrow2 e2 xs stk loc pc e)
note bisim = ⟨P,e2,h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
hence xs = loc by(auto dest: bisim1-ThrowD)
with bisim show ?case
  by(auto intro: bisim1-bisims1.bisim1FAssThrow2)
next
case bisim1FAssNull thus ?case
  by(fastforce intro: bisim1-bisims1.intros)

```

```

next
  case bisim1CAS1 thus ?case
    by(fastforce intro: rtranclp.rtrancl-into-rtrancl CAS1Throw bisim1-bisims1.intros simp add: match-ex-table-append
dest: bisim1-ThrowD elim!: CAS-τred1r-xt1)
next
  case bisim1CAS2 thus ?case
    by(clarsimp simp add: compxE2-size-convs compxE2-stack-xlift-convs)
    (fastforce simp add: match-ex-table-append intro: rtranclp.rtrancl-into-rtrancl red1-reds1.intros
bisim1CASThrow2 elim!: CAS-τred1r-xt2)
next
  case bisim1CAS3 thus ?case
    by(clarsimp simp add: compxE2-size-convs compxE2-stack-xlift-convs)
    (fastforce simp add: match-ex-table-append intro: rtranclp.rtrancl-into-rtrancl red1-reds1.intros
bisim1CASThrow3 elim!: CAS-τred1r-xt3)
next
  case bisim1CASThrow1 thus ?case
    by(auto simp add: match-ex-table-append intro: bisim1-bisims1.intros)
next
  case (bisim1CASThrow2 e xs stk loc pc i e2)
  note bisim =  $\langle P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$ 
  hence xs = loc by(auto dest: bisim1-ThrowD)
  with bisim show ?case
    by(auto intro: bisim1-bisims1.bisim1CASThrow2)
next
  case (bisim1CASThrow3 e xs stk loc pc A i)
  note bisim =  $\langle P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$ 
  hence xs = loc by(auto dest: bisim1-ThrowD)
  with bisim show ?case
    by(auto intro: bisim1-bisims1.bisim1CASThrow3)
next
  case bisim1CASFail thus ?case
    by(fastforce intro: bisim1-bisims1.intros)
next
  case bisim1Call1 thus ?case
    by(fastforce intro: rtranclp.rtrancl-into-rtrancl Call1ThrowObj bisim1-bisims1.intros simp add: match-ex-table-append
dest: bisim1-ThrowD elim!: Call-τred1r-obj)
next
  case bisim1CallParams thus ?case
    by(clarsimp simp add: match-ex-table-append-not-pcs compxE2-size-convs compxEs2-size-convs compxE2-stack-xlift-convs compxEs2-stack-xlift-convs match-ex-table-shift-pc-None)
    (fastforce intro: rtranclp.rtrancl-into-rtrancl Call1ThrowParams[OF refl] bisim1CallThrowParams
elim!: Call-τred1r-param)
next
  case bisim1CallThrowObj thus ?case
    by(auto simp add: match-ex-table-append intro: bisim1-bisims1.intros)
next
  case (bisim1CallThrowParams es vs es' xs stk loc pc obj M)
  note bisim =  $\langle P, es, h \vdash (\text{map Val } vs @ \text{Throw } a \# es', xs) [\leftrightarrow] (stk, loc, pc, [a]) \rangle$ 
  hence xs = loc by(auto dest: bisims1-ThrowD)
  with bisim show ?case
    by(auto intro: bisim1-bisims1.bisim1CallThrowParams)
next
  case bisim1CallThrow thus ?case
    by(auto intro: bisim1-bisims1.intros)

```

next

case (*bisim1BlockSome4* $e\ e'\ xs\ stk\ loc\ pc\ V\ Ty\ v$)
from $\langle \exists n. n + \text{max-vars } \{V:Ty=None; e'\} \leq \text{length } xs \wedge \mathcal{B} \{V:Ty=[v]; e\} n \rangle$
have $V: V < \text{length } xs$ **by** *simp*
from *bisim1BlockSome4* **have** *Red*: $\tau\text{red1r } P\ t\ h\ (e',\ xs)\ (Throw\ a,\ loc)$
and *bisim*: $P, e, h \vdash (Throw\ a,\ loc) \leftrightarrow (stk,\ loc,\ pc,\ [a])$
by(*auto simp add: match-ex-table-append-not-pcs compxE2-size-convs compxEs2-size-convs compxE2-stack-xlift-convs compxEs2-stack-xlift-convs match-ex-table-shift-pc-None intro!: exI[where x=Suc V] elim: meta-impE*)
note $len = \tau\text{red1r-preserves-len}[OF\ Red]$
from *Red* **have** $\tau\text{red1r } P\ t\ h\ (\{V:Ty=None; e'\},\ xs)\ (\{V:Ty=None; Throw\ a\},\ loc)$ **by**(*auto intro: Block-None- $\tau\text{red1r-xt}$*)
thus ?*case* **using** $V\ len\ bisim$
by(*auto intro: $\tau\text{move1BlockThrow Block1Throw bisim1BlockThrowSome elim!: rtranclp.rtrancl-into-rtrancl}$*)

next

case *bisim1BlockThrowSome* **thus** ?*case*
by(*auto dest: bisim1-ThrowD intro: bisim1-bisims1.bisim1BlockThrowSome*)

next

case (*bisim1BlockNone* $e\ e'\ xs\ stk\ loc\ pc\ V\ Ty$)
hence *Red*: $\tau\text{red1r } P\ t\ h\ (e',\ xs)\ (Throw\ a,\ loc)$
and *bisim*: $P, e, h \vdash (Throw\ a,\ loc) \leftrightarrow (stk,\ loc,\ pc,\ [a])$
by(*auto elim: meta-impE intro!: exI[where x=Suc V]*)
from *Red* **have** $len: \text{length } loc = \text{length } xs$ **by**(*rule $\tau\text{red1r-preserves-len}$*)
from $\langle \exists n. n + \text{max-vars } \{V:Ty=None; e'\} \leq \text{length } xs \wedge \mathcal{B} \{V:Ty=None; e\} n \rangle$
have $V: V < \text{length } xs$ **by** *simp*
from *Red* **have** $\tau\text{red1r } P\ t\ h\ (\{V:Ty=None; e'\},\ xs)\ (\{V:Ty=None; Throw\ a\},\ loc)$ **by**(*rule Block-None- $\tau\text{red1r-xt}$*)
thus ?*case* **using** $V\ len\ bisim$
by(*auto intro: $\tau\text{move1BlockThrow Block1Throw bisim1BlockThrowNone elim!: rtranclp.rtrancl-into-rtrancl}$*)

next

case *bisim1BlockThrowNone* **thus** ?*case*
by(*auto dest: bisim1-ThrowD intro: bisim1-bisims1.bisim1BlockThrowNone*)

next

case *bisim1Sync1* **thus** ?*case*
by(*fastforce intro: rtranclp.rtrancl-into-rtrancl Synchronized1Throw1 bisim1-bisims1.intros simp add: match-ex-table-append dest: bisim1-ThrowD elim!: Sync- $\tau\text{red1r-xt}$*)

next

case (*bisim1Sync4* $e2\ e'\ xs\ stk\ loc\ pc\ V\ e1\ a'$)
from $\langle P, e2, h \vdash (e',\ xs) \leftrightarrow (stk,\ loc,\ pc,\ [a]) \rangle$
have $pc < \text{length } (compE2\ e2)$ **by**(*auto dest!: bisim1-xcp-pcD*)
with $\langle \text{match-ex-table } (compP\ f\ P)\ (cname-of\ h\ a)\ (Suc\ (Suc\ (Suc\ (\text{length } (compE2\ e1) + pc)))) \rangle$
 $(compxE2\ (sync_V\ (e1)\ e2)\ 0\ 0) = None \rangle \text{subclsObj } ha$
have *False* **by**(*simp add: match-ex-table-append matches-ex-entry-def split: if-split-asm*)
thus ?*case* ..

next

case *bisim1Sync10* **thus** ?*case*
by(*fastforce intro: bisim1-bisims1.intros*)

next

case *bisim1Sync11* **thus** ?*case*
by(*fastforce intro: bisim1-bisims1.intros*)

next

case *bisim1Sync12* **thus** ?*case*
by(*fastforce intro: bisim1-bisims1.intros*)

next

case *bisim1Sync14* **thus** ?*case*

```

    by(fastforce intro: bisim1-bisims1.intros)
next
  case bisim1SyncThrow thus ?case
    by(auto simp add: match-ex-table-append intro: bisim1-bisims1.intros)
next
  case bisim1Seq1 thus ?case
    by(fastforce intro: rtranclp.rtrancl-into-rtrancl Seq1Throw bisim1-bisims1.intros dest: bisim1-ThrowD
elim!: Seq- $\tau$ red1r-xt simp add: match-ex-table-append)
next
  case bisim1SeqThrow1 thus ?case
    by(auto simp add: match-ex-table-append intro: bisim1-bisims1.intros)
next
  case bisim1Seq2 thus ?case
    by(auto simp add: match-ex-table-append-not-pcs compxE2-size-convs compxE2-stack-xlift-convs
match-ex-table-shift-pc-None intro: bisim1-bisims1.bisim1Seq2)
next
  case bisim1Cond1 thus ?case
    by(fastforce intro: rtranclp.rtrancl-into-rtrancl Cond1Throw bisim1-bisims1.intros dest: bisim1-ThrowD
elim!: Cond- $\tau$ red1r-xt simp add: match-ex-table-append)
next
  case bisim1CondThen thus ?case
    by(clarsimp simp add: match-ex-table-append)
    (auto simp add: match-ex-table-append-not-pcs compxE2-size-convs compxE2-stack-xlift-convs
match-ex-table-shift-pc-None intro: bisim1-bisims1.bisim1CondThen)
next
  case bisim1CondElse thus ?case
    by(clarsimp simp add: match-ex-table-append)
    (auto simp add: match-ex-table-append-not-pcs compxE2-size-convs compxE2-stack-xlift-convs
match-ex-table-shift-pc-None intro: bisim1-bisims1.bisim1CondElse)
next
  case bisim1CondThrow thus ?case
    by(auto simp add: match-ex-table-append intro: bisim1-bisims1.intros)
next
  case bisim1While3 thus ?case
    by(fastforce intro: rtranclp.rtrancl-into-rtrancl Cond1Throw bisim1-bisims1.intros dest: bisim1-ThrowD
elim!: Cond- $\tau$ red1r-xt simp add: match-ex-table-append)
next
  case (bisim1While4 e e' xs stk loc pc c)
    hence  $\tau$ red1r P t h (e', xs) (Throw a, loc)  $\wedge$  P,e,h  $\vdash$  (Throw a, loc)  $\leftrightarrow$  (stk, loc, pc, [a])
      by(auto simp add: match-ex-table-append-not-pcs compxE2-size-convs compxEs2-size-convs com-
pxE2-stack-xlift-convs compxEs2-stack-xlift-convs match-ex-table-shift-pc-None)
    hence  $\tau$ red1r P t h (e';while (c) e, xs) (Throw a, loc)
      P,while (c) e,h  $\vdash$  (Throw a, loc)  $\leftrightarrow$  (stk, loc, Suc (length (compE2 c) + pc), [a])
      by(auto intro: rtranclp.rtrancl-into-rtrancl Seq1Throw  $\tau$ move1SeqThrow bisim1WhileThrow2 elim!:
Seq- $\tau$ red1r-xt)
    thus ?case ..
next
  case bisim1WhileThrow1 thus ?case
    by(auto simp add: match-ex-table-append intro: bisim1-bisims1.intros)
next
  case bisim1WhileThrow2 thus ?case
    by(auto simp add: match-ex-table-append intro: bisim1-bisims1.intros dest: bisim1-ThrowD)
next
  case bisim1Throw1 thus ?case

```

```

  by(fastforce intro: rtranclp.rtrancl-into-rtrancl Throw1Throw bisim1-bisims1.intros dest: bisim1-ThrowD
elim!: Throw-τred1r-xt)
next
  case bisim1Throw2 thus ?case
    by(fastforce intro: bisim1-bisims1.intros)
next
  case bisim1ThrowNull thus ?case
    by(fastforce intro: bisim1-bisims1.intros)
next
  case bisim1ThrowThrow thus ?case
    by(fastforce intro: bisim1-bisims1.intros)
next
  case (bisim1Try e e' xs stk loc pc C V e2)
  hence red: τred1r P t h (e', xs) (Throw a, loc)
  and bisim: P, e, h ⊢ (Throw a, loc) ↔ (stk, loc, pc, [a])
  by(auto simp add: match-ex-table-append)
  from red have Red: τred1r P t h (try e' catch(C V) e2, xs) (try Throw a catch(C V) e2, loc)
by(rule Try-τred1r-xt)
  from ⟨match-ex-table (compP f P) (cname-of h a) pc (compxE2 (try e catch(C V) e2) 0 0) = None⟩
  have ¬ matches-ex-entry (compP f P) (cname-of h a) pc (0, length (compE2 e), [C], Suc (length
(compE2 e)), 0)
  by(auto simp add: match-ex-table-append split: if-split-asm)
  moreover from ⟨P, e, h ⊢ (e', xs) ↔ (stk, loc, pc, [a])⟩
  have pc < length (compE2 e) by(auto dest: bisim1-xcp-pcD)
  ultimately have subcls: ¬ P ⊢ D ≲* C using ha by(simp add: matches-ex-entry-def cname-of-def)
  with ha have True, P, t ⊢ 1 ⟨try Throw a catch(C V) e2, (h, loc)⟩ -ε→ ⟨Throw a, (h, loc)⟩
  by -(rule Red1TryFail, auto)
  moreover from bisim ha subcls
  have P, try e catch(C V) e2, h ⊢ (Throw a, loc) ↔ (stk, loc, pc, [a])
  by(rule bisim1TryFail)
  ultimately show ?case using Red by(blast intro: rtranclp.rtrancl-into-rtrancl τmove1TryThrow)
next
  case (bisim1TryCatch2 e2 e' xs stk loc pc e1 C V)
  hence *: τred1r P t h (e', xs) (Throw a, loc) ∧ P, e2, h ⊢ (Throw a, loc) ↔ (stk, loc, pc, [a])
  by(clarsimp simp add: match-ex-table-append matches-ex-entry-def split: if-split-asm)
  (auto simp add: match-ex-table-append compxE2-size-convns compxE2-stack-xlift-convns match-ex-table-shift-pc-N
elim: meta-impE intro!: exI[where x=Suc V])
  moreover note τred1r-preserves-len[OF * [THEN conjunct1]]
  moreover from ⟨∃ n. n + max-vars {V:Class C=None; e'} ≤ length xs ∧ B (try e1 catch(C V)
e2) n⟩
  have V < length xs by simp
  ultimately show ?case
  by(fastforce intro: rtranclp.rtrancl-into-rtrancl Block1Throw τmove1BlockThrow bisim1TryCatchThrow
elim!: Block-None-τred1r-xt)
next
  case (bisim1TryFail e xs stk loc pc C'' C' V e2)
  note bisim = ⟨P, e, h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
  hence xs = loc by(auto dest: bisim1-ThrowD)
  with bisim ⟨typeof-addr h a = [Class-type C'']⟩ ⟨¬ P ⊢ C'' ≲* C'⟩
  show ?case by(auto intro: bisim1-bisims1.bisim1TryFail)
next
  case (bisim1TryCatchThrow e2 xs stk loc pc e C' V)
  from ⟨P, e2, h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩ have xs = loc
  by(auto dest: bisim1-ThrowD)

```

with $\langle P, e2, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$ **show** $?case$
by(*auto intro: bisim1-bisims1.bisim1TryCatchThrow*)

next
case (*bisims1List1 e e' xs stk loc pc es*)
hence $\tau red1r P t h (e', xs) (\text{Throw } a, loc)$
and *bisim: P, e, h* $\vdash (\text{Throw } a, loc) \leftrightarrow (stk, loc, pc, [a])$ **by**(*auto simp add: match-ex-table-append*)
hence $\tau reds1r P t h (e' \# es, xs) (\text{map Val []} @ \text{Throw } a \# es, loc)$ **by**(*auto intro: \tau red1r-inj-\tau reds1r*)
moreover from *bisim*
have $P, e \# es, h \vdash (\text{Throw } a \# es, loc) [\leftrightarrow] (stk, loc, pc, [a])$
by(*rule bisim1-bisims1.bisims1List1*)
ultimately show $?case$ **by** *fastforce*

next
case (*bisims1List2 es es' xs stk loc pc e v*)
hence $\exists vs es''. \tau reds1r P t h (es', xs) (\text{map Val vs} @ \text{Throw } a \# es'', loc) \wedge P, es, h \vdash (\text{map Val vs} @ \text{Throw } a \# es'', loc) [\leftrightarrow] (stk, loc, pc, [a])$
by(*auto simp add: match-ex-table-append-not-pcs compxEs2-size-convs compxEs2-stack-xlift-convs match-ex-table-shift-pc-None*)
then obtain $vs es''$ **where** *red: \tau reds1r P t h (es', xs) (\text{map Val vs} @ \text{Throw } a \# es'', loc)*
and *bisim: P, es, h* $\vdash (\text{map Val vs} @ \text{Throw } a \# es'', loc) [\leftrightarrow] (stk, loc, pc, [a])$ **by** *blast*
from *red* **have** $\tau reds1r P t h (Val v \# es', xs) (\text{map Val } (v \# vs) @ \text{Throw } a \# es'', loc)$
by(*auto intro: \tau reds1r-cons-\tau reds1r*)
moreover from *bisim*
have $P, e \# es, h \vdash (\text{map Val } (v \# vs) @ \text{Throw } a \# es'', loc) [\leftrightarrow] (stk @ [v], loc, length (compE2 e) + pc, [a])$
by(*auto intro: bisim1-bisims1.bisims1List2*)
ultimately show $?case$ **by** *fastforce*

qed

primrec *conf-xcp' :: 'm prog* \Rightarrow *'heap* \Rightarrow *'addr option* \Rightarrow *bool* **where**
conf-xcp' P h None = True
 $| \text{conf-xcp' } P h [a] = (\exists D. \text{typeof-addr } h a = [\text{Class-type } D] \wedge P \vdash D \preceq^* \text{Throwable})$

lemma *conf-xcp-conf-xcp'*:
 $\text{conf-xcp } P h \text{ xcp } i \Longrightarrow \text{conf-xcp' } P h \text{ xcp}$
by(*cases xcp*) *auto*

lemma *conf-xcp'-compP [simp]*: $\text{conf-xcp' } (\text{compP } f P) = \text{conf-xcp' } P$
by(*clarsimp simp add: fun-eq-iff conf-xcp'-def rec-option-is-case-option*)

end

context *J1-heap-base* **begin**

lemmas $\tau red1\text{-Val-simps [simp]} =$
 $\tau red1r\text{-Val } \tau red1t\text{-Val } \tau reds1r\text{-map-Val } \tau reds1t\text{-map-Val}$

end

context *J1-JVM-conf-read* **begin**

lemma **assumes** *wf: wf-J1-prog P*
and *hconf: hconf h preallocated h*
and *tconf: P, h* \vdash $t \sqrt{t}$
shows *red1-simulates-exec-instr:*

$\llbracket P, E, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp);$
 $exec-move-d P t E h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp');$
 $n + max-vars e \leq length xs; bsok E n; P, h \vdash stk [:\leq] ST; conf-xcp' (compP2 P) h xcp \rrbracket$
 $\implies \exists e'' xs''. P, E, h' \vdash (e'', xs'') \leftrightarrow (stk', loc', pc', xcp') \wedge$
 $(if \tau move2 (compP2 P) h stk E pc xcp$
 $then h' = h \wedge (if xcp' = None \longrightarrow pc < pc' then \tau red1r else \tau red1t) P t h (e, xs) (e'', xs'')$
 $else \exists ta' e' xs'. \tau red1r P t h (e, xs) (e', xs') \wedge True, P, t \vdash 1 \langle e', (h, xs') \rangle -ta' \rightarrow \langle e'', (h', xs'') \rangle$
 $\wedge ta-bisim wbisim1 (extTA2J1 P ta') ta \wedge \neg \tau move1 P h e' \wedge (call1 e = None \vee no-call2 E pc \vee e'$
 $= e \wedge xs' = xs))$
 $(is \llbracket -; ?exec E stk loc pc xcp stk' loc' pc' xcp'; -; -; - \rrbracket$
 $\implies \exists e'' xs''. P, E, h' \vdash (e'', xs'') \leftrightarrow (stk', loc', pc', xcp') \wedge ?red e xs e'' xs'' E stk pc pc' xcp'$
 $xcp')$

and *reds1-simulates-exec-instr*:

$\llbracket P, Es, h \vdash (es, xs) [\leftrightarrow] (stk, loc, pc, xcp);$
 $exec-moves-d P t Es h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp');$
 $n + max-varss es \leq length xs; bsoks Es n; P, h \vdash stk [:\leq] ST; conf-xcp' (compP2 P) h xcp \rrbracket$
 $\implies \exists es'' xs''. P, Es, h' \vdash (es'', xs'') [\leftrightarrow] (stk', loc', pc', xcp') \wedge$
 $(if \tau moves2 (compP2 P) h stk Es pc xcp$
 $then h' = h \wedge (if xcp' = None \longrightarrow pc < pc' then \tau reds1r else \tau reds1t) P t h (es, xs) (es'', xs'')$
 $else \exists ta' es' xs'. \tau reds1r P t h (es, xs) (es', xs') \wedge True, P, t \vdash 1 \langle es', (h, xs') \rangle [-ta' \rightarrow] \langle es'', (h',$
 $xs'') \rangle \wedge ta-bisim wbisim1 (extTA2J1 P ta') ta \wedge \neg \tau moves1 P h es' \wedge (calls1 es = None \vee no-calls2$
 $Es pc \vee es' = es \wedge xs' = xs))$
 $(is \llbracket -; ?execs Es stk loc pc xcp stk' loc' pc' xcp'; -; -; - \rrbracket$
 $\implies \exists es'' xs''. P, Es, h' \vdash (es'', xs'') [\leftrightarrow] (stk', loc', pc', xcp') \wedge ?reds es xs es'' xs'' Es stk pc$
 $pc' xcp xcp')$

proof(*induction* $E n e xs stk loc pc xcp$ **and** $Es n es xs stk loc pc xcp$)

arbitrary: $stk' loc' pc' xcp' ST$ **and** $stk' loc' pc' xcp' ST$ *rule*: *bisim1-bisims1-inducts-split*)

case (*bisim1Val2* $e n v xs$)

from $\langle ?exec e [v] xs (length (compE2 e)) None stk' loc' pc' xcp' \rangle$

have *False* **by**(*auto dest: exec-meth-length-compE2D simp add: exec-move-def*)

thus *?case ..*

next

case (*bisim1New* $C' n xs$)

note $exec = \langle exec-move-d P t (new C') h ([], xs, 0, None) ta h' (stk', loc', pc', xcp') \rangle$

have $\tau: \neg \tau move2 (compP2 P) h [] (new C') 0 None \neg \tau move1 P h (new C') \mathbf{by}$ (*auto simp add: $\tau move2$ -iff*)

show *?case*

proof(*cases allocate h (Class-type C') = {}*)

case *True*

have $P, new C', h' \vdash (THROW OutOfMemory, xs) \leftrightarrow ([], xs, 0, [addr-of-sys-xcpt OutOfMemory])$

by(*rule bisim1NewThrow*)

with $exec \tau True$ **show** *?thesis*

by(*fastforce intro: Red1NewFail elim!: exec-meth.cases simp add: exec-move-def*)

next

case *False*

have $\bigwedge a h'. P, new C', h' \vdash (addr a, xs) \leftrightarrow ([Addr a], xs, length (compE2 (new C')), None)$

by(*rule bisim1Val2*) *auto*

thus *?thesis using exec False τ*

apply(*simp add: exec-move-def*)

apply(*erule exec-meth.cases*)

apply *simp-all*

apply *clarsimp*

apply(*auto intro!: Red1New exI simp add: ta-bisim-def*)


```

done
qed
next
case (bisim1NewThrow C' n xs)
from ⟨?exec (new C') [] xs 0 [addr-of-sys-xcpt OutOfMemory] stk' loc' pc' xcp'⟩
have False by(auto elim: exec-meth.cases simp add: exec-move-def)
thus ?case ..
next
case (bisim1NewArray e n e' xs stk loc pc xcp U)
note IH = bisim1NewArray.IH(2)
note exec = ⟨?exec (newA U[e]) stk loc pc xcp stk' loc' pc' xcp'⟩
note bisim = ⟨P,e,h ⊢ (e', xs) ↔ (stk, loc, pc, xcp)⟩
note len = ⟨n + max-vars (newA U[e]) ≤ length xs⟩
note bsok = bsok (newA U[e]) n
from bisim have pc: pc ≤ length (compE2 e) by(rule bisim1-pc-length-compE2)
show ?case
proof(cases pc < length (compE2 e))
case True
with exec have exec': ?exec e stk loc pc xcp stk' loc' pc' xcp'
by(auto simp add: exec-move-newArray)
from True have τmove2 (compP2 P) h stk (newA U[e]) pc xcp = τmove2 (compP2 P) h stk e
pc xcp by(simp add: τmove2-iff)
moreover have no-call2 e pc ⇒ no-call2 (newA U[e]) pc by(simp add: no-call2-def)
ultimately show ?thesis using IH[OF exec' - - ⟨P,h ⊢ stk [≤] ST⟩ ⟨conf-xcp' (compP2 P) h
xcp⟩] len bsok
by(fastforce intro: bisim1-bisims1.bisim1NewArray New1ArrayRed elim!: NewArray-τred1r-xt
NewArray-τred1t-xt)
next
case False
with pc have [simp]: pc = length (compE2 e) by simp
with bisim obtain v where stk: stk = [v] and xcp: xcp = None
by(auto dest: bisim1-pc-length-compE2D)
with bisim pc len bsok have red: τred1r P t h (e', xs) (Val v, loc)
by(auto intro: bisim1-Val-τred1r simp add: bsok-def)
hence τred1r P t h (newA U[e'], xs) (newA U[Val v], loc) by(rule NewArray-τred1r-xt)
moreover have τ: ¬ τmove2 (compP2 P) h [v] (newA U[e]) pc None by(simp add: τmove2-iff)
moreover have ¬ τmove1 P h (newA U[Val v]) by auto
moreover from exec stk xcp obtain I
where [simp]: v = Intg I by(auto elim!: exec-meth.cases simp add: exec-move-def)
have ∃ ta' e''. P,newA U[e],h' ⊢ (e'',loc) ↔ (stk', loc', pc', xcp') ∧ True,P,t ⊢ 1 ⟨newA U[Val
v],(h, loc)⟩ -ta'→ ⟨e'',(h', loc)⟩ ∧ ta-bisim wbisim1 (extTA2J1 P ta') ta
proof(cases I < s 0)
case True with exec stk xcp show ?thesis
by(fastforce elim!: exec-meth.cases intro: bisim1NewArrayFail Red1NewArrayNegative simp add:
exec-move-def)
next
case False
show ?thesis
proof(cases allocate h (Array-type U (nat (sint I))) = {})
case True
with False exec stk xcp show ?thesis
by(fastforce elim!: exec-meth.cases intro: bisim1NewArrayFail Red1NewArrayFail simp add:
exec-move-def)
next

```

```

    case False
    have  $\wedge a h'. P, newA U[e], h' \vdash (addr a, loc) \leftrightarrow ([Addr a], loc, length (compE2 (newA U[e])),$ 
None)
    by(rule bisim1Val2) simp
    with False  $\langle \neg I < s 0 \rangle exec stk xcp$  show ?thesis
    apply(simp add: exec-move-def)
    apply(erule exec-meth.cases)
    apply simp-all
    apply clarsimp
    apply(auto intro!: Red1NewArray exI simp add: ta-bisim-def)
    done
  qed
  qed
  moreover have no-call2 (newA U[e]) pc by(simp add: no-call2-def)
  ultimately show ?thesis using exec stk xcp by fastforce
  qed
next
case (bisim1NewArrayThrow e n a xs stk loc pc U)
note exec =  $\langle ?exec (newA U[e]) stk loc pc [a] stk' loc' pc' xcp' \rangle$ 
note bisim =  $\langle P, e, h \vdash (Throw a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$ 
from bisim have pc:  $pc < length (compE2 e)$  by(auto dest: bisim1-ThrowD)
from bisim have match-ex-table (compP2 P) (cname-of h a) (0 + pc) (compxE2 e 0 0) = None
  unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
with exec pc have False by (auto elim!: exec-meth.cases simp add: exec-move-def)
thus ?case ..
next
case (bisim1NewArrayFail e n U a xs v)
note exec =  $\langle ?exec (newA U[e]) [v] xs (length (compE2 e)) [a] stk' loc' pc' xcp' \rangle$ 
hence False by(auto elim!: exec-meth.cases dest: match-ex-table-pc-length-compE2 simp add: exec-move-def)
thus ?case ..
next
case (bisim1Cast e n e' xs stk loc pc xcp U)
note IH = bisim1Cast.IH(2)
note exec =  $\langle ?exec (Cast U e) stk loc pc xcp stk' loc' pc' xcp' \rangle$ 
note bisim =  $\langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
note len =  $\langle n + max-vars (Cast U e') \leq length xs \rangle$ 
note ST =  $\langle P, h \vdash stk [:\leq] ST \rangle$ 
note bsok =  $\langle bsok (Cast U e) n \rangle$ 
from bisim have pc:  $pc \leq length (compE2 e)$  by(rule bisim1-pc-length-compE2)
show ?case
proof(cases pc < length (compE2 e))
  case True
  with exec have exec':  $?exec e stk loc pc xcp stk' loc' pc' xcp'$  by(auto simp add: exec-move-Cast)
  from True have  $\tau move2 (compP2 P) h stk (Cast U e) pc xcp = \tau move2 (compP2 P) h stk e pc$ 
  xcp by(simp add:  $\tau move2$ -iff)
  moreover have no-call2 e pc  $\implies$  no-call2 (Cast U e) pc by(simp add: no-call2-def)
  ultimately show ?thesis using IH[OF exec' - - ST  $\langle conf-xcp' (compP2 P) h xcp \rangle$  len bsok
  by(fastforce intro: bisim1-bisims1.bisim1Cast Cast1Red elim!: Cast- $\tau red1r$ -xt Cast- $\tau red1t$ -xt)
next
case False
  with pc have [simp]:  $pc = length (compE2 e)$  by simp
  with bisim obtain v where stk:  $stk = [v]$  and xcp:  $xcp = None$ 
  by(auto dest: bisim1-pc-length-compE2D)
  with bisim pc len bsok have red:  $\tau red1r P t h (e', xs) (Val v, loc)$ 

```

by(*auto intro: bisim1-Val- τ red1r simp add: bsok-def*)
hence $\tau\text{red1r } P \ t \ h \ (Cast \ U \ e', \ xs) \ (Cast \ U \ (Val \ v), \ loc) \ \mathbf{by}$ (*rule Cast- τ red1r-xt*)
also from *exec* **have** [*simp*]: $h' = h \ ta = \varepsilon \ \mathbf{by}$ (*auto simp add: exec-move-def elim!: exec-meth.cases split: if-split-asm*)
have $\exists e''. \ P, Cast \ U \ e, h \vdash (e'', loc) \leftrightarrow (stk', loc', pc', xcp') \wedge True, P, t \vdash 1 \langle Cast \ U \ (Val \ v), (h, loc) \rangle$
 $-\varepsilon \rightarrow \langle e'', (h, loc) \rangle$
proof(*cases P \vdash the (typeof_h v) \leq U*)
case *False* **with** *exec stk xcp bsok ST* **show** *?thesis*
by(*fastforce simp add: compP2-def exec-move-def exec-meth-instr list-all2-Cons1 conf-def intro: bisim1CastFail Red1CastFail*)
next
case *True*
have $P, Cast \ U \ e, h \vdash (Val \ v, loc) \leftrightarrow ([v], loc, length \ (compE2 \ (Cast \ U \ e)), None)$
by(*rule bisim1Val2*) *simp*
with *exec stk xcp ST True* **show** *?thesis*
by(*fastforce simp add: compP2-def exec-move-def exec-meth-instr list-all2-Cons1 conf-def intro: Red1Cast*)
qed
then obtain e'' **where** $bisim': P, Cast \ U \ e, h \vdash (e'', loc) \leftrightarrow (stk', loc', pc', xcp')$
and $red: True, P, t \vdash 1 \langle Cast \ U \ (Val \ v), (h, loc) \rangle -\varepsilon \rightarrow \langle e'', (h, loc) \rangle$ **by** *blast*
have $\tau\text{move1 } P \ h \ (Cast \ U \ (Val \ v)) \ \mathbf{by}$ (*rule $\tau\text{move1CastRed}$*)
with *red* **have** $\tau\text{red1t } P \ t \ h \ (Cast \ U \ (Val \ v), \ loc) \ (e'', \ loc) \ \mathbf{by}$ (*auto intro: $\tau\text{red1t-1step}$*)
also have $\tau: \tau\text{move2} \ (compP2 \ P) \ h \ [v] \ (Cast \ U \ e) \ pc \ None \ \mathbf{by}$ (*simp add: $\tau\text{move2-iff}$*)
moreover have *no-call2* $(Cast \ U \ e) \ pc \ \mathbf{by}$ (*simp add: no-call2-def*)
ultimately show *?thesis* **using** *exec stk xcp bisim'* **by** *fastforce*
qed
next
case (*bisim1CastThrow e n a xs stk loc pc U*)
note $exec = \langle ?exec \ (Cast \ U \ e) \ stk \ loc \ pc \ [a] \ stk' \ loc' \ pc' \ xcp' \rangle$
note $bisim = \langle P, e, h \vdash (Throw \ a, \ xs) \leftrightarrow (stk, \ loc, \ pc, \ [a]) \rangle$
from *bisim* **have** $pc: pc < length \ (compE2 \ e) \ \mathbf{by}$ (*auto dest: bisim1-ThrowD*)
from *bisim* **have** $match\text{-ex}\text{-table} \ (compP2 \ P) \ (cname\text{-of} \ h \ a) \ (0 + pc) \ (compE2 \ e \ 0 \ 0) = None$
unfolding *compP2-def* **by**(*rule bisim1-xcp-Some-not-caught*)
with *exec pc* **have** *False* **by** (*auto elim!: exec-meth.cases simp add: exec-move-def*)
thus *?case ..*
next
case (*bisim1CastFail e n U xs v*)
note $exec = \langle ?exec \ (Cast \ U \ e) \ [v] \ xs \ (length \ (compE2 \ e)) \ [addr\text{-of}\text{-sys}\text{-xcp} \ ClassCast] \ stk' \ loc' \ pc' \ xcp' \rangle$
hence *False* **by**(*auto elim!: exec-meth.cases dest: match-ex-table-pc-length-compE2 simp add: exec-move-def*)
thus *?case ..*
next
case (*bisim1InstanceOf e n e' xs stk loc pc xcp U*)
note $IH = bisim1InstanceOf.IH(2)$
note $exec = \langle ?exec \ (e \ instanceof \ U) \ stk \ loc \ pc \ xcp \ stk' \ loc' \ pc' \ xcp' \rangle$
note $bisim = \langle P, e, h \vdash (e', \ xs) \leftrightarrow (stk, \ loc, \ pc, \ xcp) \rangle$
note $len = \langle n + max\text{-vars} \ (e' \ instanceof \ U) \leq length \ xs \rangle$
note $ST = \langle P, h \vdash stk \ [:\leq] \ ST \rangle$
note $bsok = \langle bsok \ (e \ instanceof \ U) \ n \rangle$
from *bisim* **have** $pc: pc \leq length \ (compE2 \ e) \ \mathbf{by}$ (*rule bisim1-pc-length-compE2*)
show *?case*
proof(*cases pc < length (compE2 e)*)
case *True*
with *exec* **have** $exec': ?exec \ e \ stk \ loc \ pc \ xcp \ stk' \ loc' \ pc' \ xcp' \ \mathbf{by}$ (*auto simp add: exec-move-InstanceOf*)

```

from True have  $\tau\text{move2}$  (compP2 P) h stk (e instanceof U) pc xcp =  $\tau\text{move2}$  (compP2 P) h stk
e pc xcp
  by(simp add:  $\tau\text{move2}\text{-iff}$ )
  moreover have no-call2 e pc  $\implies$  no-call2 (e instanceof U) pc by(simp add: no-call2-def)
  ultimately show ?thesis using IH[OF exec' - - ST  $\langle$ conf-xcp' (compP2 P) h xcp $\rangle$ ] len bsok
    by(fastforce intro: bisim1-bisims1.bisim1InstanceOf InstanceOf1Red elim!: InstanceOf- $\tau$ red1r-xt
InstanceOf- $\tau$ red1t-xt)
  next
  case False
  with pc have [simp]: pc = length (compE2 e) by simp
  with bisim obtain v where stk: stk = [v] and xcp: xcp = None
    by(auto dest: bisim1-pc-length-compE2D)
  with bisim pc len bsok have red:  $\tau\text{red1r}$  P t h (e', xs) (Val v, loc)
    by(auto intro: bisim1-Val- $\tau$ red1r simp add: bsok-def)
  hence  $\tau\text{red1r}$  P t h (e' instanceof U, xs) ((Val v) instanceof U, loc) by(rule InstanceOf- $\tau$ red1r-xt)
  also let ?v = Bool (v  $\neq$  Null  $\wedge$  P  $\vdash$  the (typeofh v)  $\leq$  U)
  from exec ST stk xcp have [simp]: h' = h ta =  $\varepsilon$  xcp' = None loc' = loc stk' = [?v] pc' = Suc pc
    by(auto simp add: exec-move-def list-all2-Cons1 conf-def compP2-def elim!: exec-meth.cases split:
if-split-asm)
  have bisim': P, e instanceof U, h  $\vdash$  (Val ?v, loc)  $\leftrightarrow$  ([?v], loc, length (compE2 (e instanceof U)),
None)
    by(rule bisim1Val2) simp
  from exec stk xcp ST
  have red: True, P, t  $\vdash$  1  $\langle$ (Val v) instanceof U, (h, loc) $\rangle$   $\text{-}\varepsilon\text{-}\rightarrow$   $\langle$ Val ?v, (h, loc) $\rangle$ 
    by(auto simp add: compP2-def exec-move-def exec-meth-instr list-all2-Cons1 conf-def intro:
Red1InstanceOf)
  have  $\tau\text{move1}$  P h ((Val v) instanceof U) by(rule  $\tau\text{move1}$ InstanceOfRed)
  with red have  $\tau\text{red1t}$  P t h ((Val v) instanceof U, loc) (Val ?v, loc) by(auto intro:  $\tau\text{red1t}\text{-1step}$ )
  also have  $\tau$ :  $\tau\text{move2}$  (compP2 P) h [v] (e instanceof U) pc None by(simp add:  $\tau\text{move2}\text{-iff}$ )
  moreover have no-call2 (e instanceof U) pc by(simp add: no-call2-def)
  ultimately show ?thesis using exec stk xcp bisim' by(fastforce)
  qed
next
  case (bisim1InstanceOfThrow e n a xs stk loc pc U)
  note exec =  $\langle$ ?exec (e instanceof U) stk loc pc [a] stk' loc' pc' xcp $\rangle$ 
  note bisim =  $\langle$ P, e, h  $\vdash$  (Throw a, xs)  $\leftrightarrow$  (stk, loc, pc, [a]) $\rangle$ 
  from bisim have pc: pc < length (compE2 e) by(auto dest: bisim1-ThrowD)
  from bisim have match-ex-table (compP2 P) (cname-of h a) (0 + pc) (compE2 e 0 0) = None
    unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
  with exec pc have False by (auto elim!: exec-meth.cases simp add: exec-move-def)
  thus ?case ..
next
  case (bisim1Val v n xs)
  from  $\langle$ ?exec (Val v) [] xs 0 None stk' loc' pc' xcp $\rangle$ 
  have stk' = [v] loc' = xs h' = h pc' = length (compE2 (Val v)) xcp' = None
    by(auto elim: exec-meth.cases simp add: exec-move-def)
  moreover have P, Val v, h  $\vdash$  (Val v, xs)  $\leftrightarrow$  ([v], xs, length (compE2 (Val v)), None)
    by(rule bisim1Val2) simp
  moreover have  $\tau\text{move2}$  (compP2 P) h [] (Val v) 0 None by(rule  $\tau\text{move2}$ Val)
  ultimately show ?case by(auto)
next
  case (bisim1Var V n xs)
  note exec =  $\langle$ ?exec (Var V) [] xs 0 None stk' loc' pc' xcp $\rangle$ 
  moreover note len =  $\langle$ n + max-vars (Var V)  $\leq$  length xs $\rangle$ 

```

moreover have $\tau_{move2} (compP2\ P)\ h\ []\ (Var\ V)\ 0\ None\ \tau_{move1}\ P\ h\ (Var\ V)$
by(*auto intro: $\tau_{move1}Var$ simp add: τ_{move2} -iff*)
moreover have $P, Var\ V, h \vdash (Val\ (xs\ !\ V),\ xs) \leftrightarrow ([xs\ !\ V],\ xs,\ length\ (compE2\ (Var\ V)),\ None)$
by(*rule bisim1Val2) simp*)
ultimately show *?case by(fastforce elim!: exec-meth.cases intro: Red1Var r-into-rtranclp simp add: exec-move-def)*
next
case (*bisim1BinOp1 e1 n e1' xs stk loc pc xcp e2 bop*)
note $IH1 = bisim1BinOp1.IH(2)$
note $IH2 = bisim1BinOp1.IH(4)$
note $exec = \langle ?exec\ (e1\ \llbracket bop \rrbracket\ e2)\ stk\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$
note $bisim1 = \langle P, e1, h \vdash (e1', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
note $bisim2 = \langle P, e2, h \vdash (e2, loc) \leftrightarrow ([], loc, 0, None) \rangle$
note $len = \langle n + max-vars\ (e1'\ \llbracket bop \rrbracket\ e2) \leq length\ xs \rangle$
note $ST = \langle P, h \vdash stk\ [:\leq]\ ST \rangle$
note $bsok = \langle bsok\ (e1\ \llbracket bop \rrbracket\ e2)\ n \rangle$
from *bisim1 have pc: pc \leq length (compE2 e1) by(rule bisim1-pc-length-compE2)*
show *?case*
proof(*cases pc < length (compE2 e1)*)
case *True*
with *exec have exec': ?exec e1 stk loc pc xcp stk' loc' pc' xcp' by(auto simp add: exec-move-BinOp1)*
from *True have τ : $\tau_{move2} (compP2\ P)\ h\ stk\ (e1\ \llbracket bop \rrbracket\ e2)\ pc\ xcp = \tau_{move2} (compP2\ P)\ h\ stk\ e1\ pc\ xcp$*
by(*simp add: τ_{move2} -iff*)
with $IH1[OF\ exec'\ -\ ST\ \langle conf-xcp'\ (compP2\ P)\ h\ xcp \rangle]\ bisim2\ len\ bsok$ **obtain** $e''\ xs''$
where $bisim': P, e1, h' \vdash (e'', xs'') \leftrightarrow (stk', loc', pc', xcp')$
and $red: ?red\ e1'\ xs\ e''\ xs''\ e1\ stk\ pc\ pc'\ xcp\ xcp'$ **by** *auto*
from $bisim'\ have\ P, e1\ \llbracket bop \rrbracket\ e2, h' \vdash (e''\ \llbracket bop \rrbracket\ e2, xs'') \leftrightarrow (stk', loc', pc', xcp')$
by(*rule bisim1-bisims1.bisim1BinOp1*)
moreover from *True have no-call2 (e1 $\llbracket bop \rrbracket$ e2) pc = no-call2 e1 pc by(simp add: no-call2-def)*
ultimately show *?thesis using red τ*
by(*fastforce intro: Bin1OpRed1 elim!: BinOp- τ red1r-xt1 BinOp- τ red1t-xt1*)
next
case *False*
with *pc have pc: pc = length (compE2 e1) by auto*
with *bisim1 obtain v where e1': is-val e1' \longrightarrow e1' = Val v*
and $stk: stk = [v]$ **and** $xcp: xcp = None$ **and** $call: call1\ e1' = None$
by(*auto dest: bisim1-pc-length-compE2D*)
with *bisim1 pc len bsok have rede1': $\tau_{red1r}\ P\ t\ h\ (e1',\ xs)\ (Val\ v,\ loc)$*
by(*auto intro: bisim1-Val- τ red1r simp add: bsok-def*)
hence $rede1'': \tau_{red1r}\ P\ t\ h\ (e1'\ \llbracket bop \rrbracket\ e2,\ xs)\ (Val\ v\ \llbracket bop \rrbracket\ e2,\ loc)$ **by**(*rule BinOp- τ red1r-xt1*)
moreover from *pc exec stk xcp*
have $exec\text{-meth-d}\ (compP2\ P)\ (compE2\ (e1\ \llbracket bop \rrbracket\ e2))\ (compxE2\ (e1\ \llbracket bop \rrbracket\ e2)\ 0\ 0)\ t\ h\ ([[]\ @\ [v],\ loc,\ length\ (compE2\ e1)\ +\ 0,\ None])\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$
by(*simp add: compxE2-size-convs compxE2-stack-xlift-convs exec-move-def*)
hence $exec': exec\text{-meth-d}\ (compP2\ P)\ (compE2\ e2)\ (stack\text{-xlift}\ (length\ [v])\ (compxE2\ e2\ 0\ 0))\ t\ h\ ([[]\ @\ [v],\ loc,\ 0,\ None])\ ta\ h'\ (stk',\ loc',\ pc' - length\ (compE2\ e1),\ xcp')$
and $pc': pc' \geq length\ (compE2\ e1)$ **by**(*safe dest!: BinOp-exec2D) simp-all*)
then obtain PC' **where** $PC': pc' = length\ (compE2\ e1) + PC'$
by $-(rule\ that[where\ PC' = pc' - length\ (compE2\ e1)],\ simp)$
from $exec'\ bisim2$ **obtain** stk'' **where** $stk': stk' = stk''\ @\ [v]$
and $exec'': exec\text{-move-d}\ P\ t\ e2\ h\ ([[]],\ loc,\ 0,\ None)\ ta\ h'\ (stk'',\ loc',\ pc' - length\ (compE2\ e1),\ xcp')$
by(*unfold exec-move-def)(drule (1) exec-meth-stk-split, auto)*

with pc xcp **have** $\tau: \tau move2 (compP2 P) h [v] (e1 \ll bop \gg e2) (length (compE2 e1)) None = \tau move2 (compP2 P) h [] e2 0 None$
using $\tau instr-stk-drop-exec-move$ **[where** $stk = []$ **and** $vs = [v]$
by $(simp\ add: \tau move2-iff\ \tau instr-stk-drop-exec-move)$
from $bisim1$ **have** $length\ xs = length\ loc$ **by** $(rule\ bisim1-length-xs)$
with $IH2[OF\ exec'',\ of\ []\ len\ bsok]$ **obtain** $e''\ xs''$
where $bisim': P, e2, h' \vdash (e'', xs'') \leftrightarrow (stk'', loc', pc' - length (compE2 e1), xcp')$
and $red: ?red\ e2\ loc\ e''\ xs''\ e2\ []\ 0\ (pc' - length (compE2 e1))\ None\ xcp'$ **by** $auto$
from $bisim'$
have $P, e1 \ll bop \gg e2, h' \vdash (Val\ v \ll bop \gg e'', xs'') \leftrightarrow (stk'' @ [v], loc', length (compE2 e1) + (pc' - length (compE2 e1)), xcp')$
by $(rule\ bisim1-bisims1.bisim1BinOp2)$
moreover from $red\ \tau$
have $?red (Val\ v \ll bop \gg e2) loc (Val\ v \ll bop \gg e'') xs'' (e1 \ll bop \gg e2) [v] (length (compE2 e1)) pc' None\ xcp'$
by $(fastforce\ intro: Bin1OpRed2\ elim!: BinOp-\tau red1r-xt2\ BinOp-\tau red1t-xt2\ simp\ add: no-call2-def)$
moreover have $no-call2 (e1 \ll bop \gg e2) (length (compE2 e1))$ **by** $(simp\ add: no-call2-def)$
ultimately show $?thesis$ **using** $\tau\ stk'\ pc\ xcp\ pc'\ PC'\ bisim1\ bisim2\ e1'\ stk\ call$ **by** $(fastforce\ elim!: rtranclp-trans)$
qed
next
case $(bisim1BinOp2\ e2\ n\ e2'\ xs\ stk\ loc\ pc\ xcp\ e1\ bop\ v1)$
note $IH2 = bisim1BinOp2.IH(2)$
note $exec = \langle ?exec (e1 \ll bop \gg e2) (stk @ [v1]) loc (length (compE2 e1) + pc) xcp\ stk'\ loc'\ pc'\ xcp' \rangle$
note $bisim1 = \langle P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, None) \rangle$
note $bisim2 = \langle P, e2, h \vdash (e2', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
note $len = \langle n + max-vars (Val\ v1 \ll bop \gg e2') \leq length\ xs \rangle$
note $bsok = \langle bsok (e1 \ll bop \gg e2) n \rangle$
note $ST = \langle P, h \vdash stk @ [v1] [:\leq] ST \rangle$
then obtain $ST2\ T$ **where** $ST = ST2 @ [T] P, h \vdash stk [:\leq] ST2\ P, h \vdash v1 : \leq T$
by $(auto\ simp\ add: list-all2-append1\ length-Suc-conv)$
from $bisim2$ **have** $pc: pc \leq length (compE2 e2)$ **by** $(rule\ bisim1-pc-length-compE2)$
show $?case$
proof $(cases\ pc < length (compE2 e2))$
case $True$
with $exec$ **have** $exec': exec-meth-d (compP2 P) (compE2 e2) (stack-xlift (length [v1]) (compxE2 e2 0 0)) t\ h (stk @ [v1], loc, pc, xcp) ta\ h' (stk', loc', pc' - length (compE2 e1), xcp')$
and $pc': pc' \geq length (compE2 e1)$
by $(unfold\ exec-move-def)(safe\ dest!: BinOp-exec2D)$
from $exec'\ bisim2$ **obtain** stk'' **where** $stk': stk' = stk'' @ [v1]$
and $exec'': exec-move-d P\ t\ e2\ h (stk, loc, pc, xcp) ta\ h' (stk'', loc', pc' - length (compE2 e1), xcp')$
by $-(drule (1)\ exec-meth-stk-split, auto\ simp\ add: exec-move-def)$
with $True$ **have** $\tau: \tau move2 (compP2 P) h (stk @ [v1]) (e1 \ll bop \gg e2) (length (compE2 e1) + pc)$
 $xcp = \tau move2 (compP2 P) h\ stk\ e2\ pc\ xcp$
by $(auto\ simp\ add: \tau move2-iff\ \tau instr-stk-drop-exec-move)$
from $IH2[OF\ exec'' - - \langle P, h \vdash stk [:\leq] ST2 \rangle \langle conf-xcp' (compP2 P) h\ xcp \rangle len\ bsok]$ **obtain** $e''\ xs''$
where $bisim': P, e2, h' \vdash (e'', xs'') \leftrightarrow (stk'', loc', pc' - length (compE2 e1), xcp')$
and $red: ?red\ e2'\ xs\ e''\ xs''\ e2\ stk\ pc (pc' - length (compE2 e1))\ xcp\ xcp'$ **by** $auto$
from $bisim'$ **have** $P, e1 \ll bop \gg e2, h' \vdash (Val\ v1 \ll bop \gg e'', xs'') \leftrightarrow (stk'' @ [v1], loc', length (compE2 e1) + (pc' - length (compE2 e1)), xcp')$
by $(rule\ bisim1-bisims1.bisim1BinOp2)$
with $red\ \tau\ stk'\ pc'\ True$ **show** $?thesis$

```

  by(fastforce intro: Bin1OpRed2 elim!: BinOp- $\tau$ red1r-xt2 BinOp- $\tau$ red1t-xt2 split: if-split-asm simp
add: no-call2-def)
next
  case False
  with pc have [simp]: pc = length (compE2 e2) by simp
  with bisim2 obtain v2 where e2': is-val e2'  $\longrightarrow$  e2' = Val v2
    and stk: stk = [v2] and xcp: xcp = None and call: call1 e2' = None
    by(auto dest: bisim1-pc-length-compE2D)
  with bisim2 pc len bsok have red:  $\tau$ red1r P t h (e2', xs) (Val v2, loc)
    by(auto intro: bisim1-Val- $\tau$ red1r simp add: bsok-def)
  hence red1:  $\tau$ red1r P t h (Val v1 «bop» e2', xs) (Val v1 «bop» Val v2, loc) by(rule BinOp- $\tau$ red1r-xt2)
  show ?thesis
  proof(cases the (binop bop v1 v2))
    case (Inl v)
    note red1
    also from exec xcp ST stk Inl
    have  $\tau$ red1r P t h (Val v1 «bop» Val v2, loc) (Val v, loc)
      by(force simp add: exec-move-def exec-meth-instr list-all2-Cons1 conf-def compP2-def dest:
binop-progress intro: r-into-rtranclp Red1BinOp  $\tau$ move1BinOp)
    also have  $\tau$ :  $\tau$ move2 (compP2 P) h [v2, v1] (e1 «bop» e2) (length (compE2 e1) + length
(compE2 e2)) None
      by(simp add:  $\tau$ move2-iff)
    moreover have P, e1 «bop» e2, h  $\vdash$  (Val v, loc)  $\leftrightarrow$  ([v], loc, length (compE2 (e1 «bop» e2)),
None)
      by(rule bisim1Val2) simp
    ultimately show ?thesis using exec xcp stk call Inl by(auto simp add: exec-move-def exec-meth-instr)
  next
    case (Inr a)
    note red1
    also from exec xcp ST stk Inr
    have  $\tau$ red1r P t h (Val v1 «bop» Val v2, loc) (Throw a, loc)
      by(force simp add: exec-move-def exec-meth-instr list-all2-Cons1 conf-def compP2-def dest:
binop-progress intro: r-into-rtranclp Red1BinOpFail  $\tau$ move1BinOp)
    also have  $\tau$ :  $\tau$ move2 (compP2 P) h [v2, v1] (e1 «bop» e2) (length (compE2 e1) + length
(compE2 e2)) None
      by(simp add:  $\tau$ move2-iff)
    moreover
    have P, e1 «bop» e2, h  $\vdash$  (Throw a, loc)  $\leftrightarrow$  ([v2, v1], loc, length (compE2 e1) + length (compE2
e2), [a])
      by(rule bisim1BinOpThrow)
    ultimately show ?thesis using exec xcp stk call Inr by(auto simp add: exec-move-def exec-meth-instr)
  qed
qed
next
  case (bisim1BinOpThrow1 e1 n a xs stk loc pc e2 bop)
  note exec = ⟨?exec (e1 «bop» e2) stk loc pc [a] stk' loc' pc' xcp'⟩
  note bisim1 = ⟨P, e1, h  $\vdash$  (Throw a, xs)  $\leftrightarrow$  (stk, loc, pc, [a])⟩
  from bisim1 have pc: pc < length (compE2 e1) by(auto dest: bisim1-ThrowD)
  from bisim1 have match-ex-table (compP2 P) (cname-of h a) (0 + pc) (compE2 e1 0 0) = None
    unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
  with exec pc have False by(auto elim!: exec-meth.cases simp add: match-ex-table-not-pcs-None
exec-move-def)
  thus ?case ..
next

```

```

case (bisim1BinOpThrow2 e2 n a xs stk loc pc e1 bop v1)
note exec = ⟨?exec (e1 «bop» e2) (stk @ [v1]) loc (length (compE2 e1) + pc) [a] stk' loc' pc' xcp'⟩
note bisim2 = ⟨P, e2, h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
hence match-ex-table (compP2 P) (cname-of h a) (length (compE2 e1) + pc) (compE2 e2 (length
(compE2 e1)) 0) = None
  unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
  with exec have False
  apply(auto elim!: exec-meth.cases simp add: match-ex-table-append-not-pcs exec-move-def)
  apply(auto simp only: compE2-size-convs compE2-stack-xlift-convs match-ex-table-stack-xlift-eq-Some-conv)
  done
  thus ?case ..
next
case (bisim1BinOpThrow e1 n e2 bop a xs v1 v2)
note ⟨?exec (e1 «bop» e2) [v1, v2] xs (length (compE2 e1) + length (compE2 e2)) [a] stk' loc' pc'
xcp'⟩
  hence False
  by(auto elim!: exec-meth.cases simp add: match-ex-table-append-not-pcs compE2-size-convs exec-move-def
dest!: match-ex-table-shift-pcD match-ex-table-pc-length-compE2)
  thus ?case ..
next
case (bisim1LAss1 e n e' xs stk loc pc xcp V)
note IH = bisim1LAss1.IH(2)
note exec = ⟨?exec (V := e) stk loc pc xcp stk' loc' pc' xcp'⟩
note bisim = ⟨P, e, h ⊢ (e', xs) ↔ (stk, loc, pc, xcp)⟩
note len = ⟨n + max-vars (V := e') ≤ length xs⟩
note bsok = ⟨bsok (V := e) n⟩
from bisim have pc: pc ≤ length (compE2 e) by(rule bisim1-pc-length-compE2)
show ?case
proof(cases pc < length (compE2 e))
  case True
    with exec have exec': ?exec e stk loc pc xcp stk' loc' pc' xcp' by(auto simp add: exec-move-LAss)
    from True have τmove2 (compP2 P) h stk (V := e) pc xcp = τmove2 (compP2 P) h stk e pc xcp
by(simp add: τmove2-iff)
    with IH[OF exec' - - ⟨P, h ⊢ stk [:≤] ST⟩ ⟨conf-xcp' (compP2 P) h xcp⟩ len bsok] show ?thesis
    by(fastforce intro: bisim1-bisims1.bisim1LAss1 LAss1Red elim!: LAss-τred1r LAss-τred1t simp
add: no-call2-def)
  case False
    with pc have [simp]: pc = length (compE2 e) by simp
    with bisim obtain v where stk: stk = [v] and xcp: xcp = None
    by(auto dest: bisim1-pc-length-compE2D)
    with bisim pc len bsok have red: τred1r P t h (e', xs) (Val v, loc)
    by(auto intro: bisim1-Val-τred1r simp add: bsok-def)
    hence τred1r P t h (V := e', xs) (V := Val v, loc) by(rule LAss-τred1r)
    also have τmove1 P h (V := Val v) by(rule τmove1LAssRed)
    with exec stk xcp have τred1r P t h (V := Val v, loc) (unit, loc[V := v])
    by(auto intro!: r-into-rtranclp Red1LAss simp add: exec-move-def elim!: exec-meth.cases)
    also have τ: τmove2 (compP2 P) h [v] (V := e) pc None by(simp add: τmove2-iff)
    moreover have P, (V := e), h ⊢ (unit, loc[V := v]) ↔ ([], loc[V := v], Suc (length (compE2 e)),
None)
    by(rule bisim1LAss2)
    ultimately show ?thesis using exec stk xcp
    by(fastforce elim!: exec-meth.cases simp add: exec-move-def)
  qed

```


next
case (*bisim1LAss2 e n V xs*)
note *bisim* = $\langle P, e, h \vdash (e, xs) \leftrightarrow ([], xs, 0, None) \rangle$
note *exec* = $\langle ?exec (V := e) [] xs (Suc (length (compE2 e))) None stk' loc' pc' xcp' \rangle$
hence *stk'* = *[Unit]* *loc'* = *xs* *pc'* = *length (compE2 (V := e))* *xcp'* = *None* *h'* = *h*
by(*auto elim!*: *exec-meth.cases simp add: exec-move-def*)
moreover have $\tau move2 (compP2 P) h [] (V := e) (Suc (length (compE2 e))) None$ **by**(*simp add: $\tau move2$ -iff*)
moreover have $P, V := e, h' \vdash (unit, xs) \leftrightarrow ([Unit], xs, length (compE2 (V := e)), None)$
by(*rule bisim1Val2*) *simp*
ultimately show *?case by*(*auto*)
next
case (*bisim1LAssThrow e n a xs stk loc pc V*)
note *exec* = $\langle ?exec (V := e) stk loc pc [a] stk' loc' pc' xcp' \rangle$
note *bisim* = $\langle P, e, h \vdash (Throw a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$
from *bisim* **have** *pc*: *pc* < *length (compE2 e)* **by**(*auto dest: bisim1-ThrowD*)
from *bisim* **have** *match-ex-table (compP2 P) (cname-of h a) (0 + pc) (compxE2 e 0 0) = None*
unfolding *compP2-def* **by**(*rule bisim1-xcp-Some-not-caught*)
with *exec pc* **have** *False* **by** (*auto elim!*: *exec-meth.cases simp add: exec-move-def*)
thus *?case ..*
next
case (*bisim1AAcc1 a n a' xs stk loc pc xcp i*)
note *IH1* = *bisim1AAcc1.IH(2)*
note *IH2* = *bisim1AAcc1.IH(4)*
note *exec* = $\langle ?exec (a[i]) stk loc pc xcp stk' loc' pc' xcp' \rangle$
note *bisim1* = $\langle P, a, h \vdash (a', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
note *bisim2* = $\langle P, i, h \vdash (i, loc) \leftrightarrow ([], loc, 0, None) \rangle$
note *len* = $\langle n + max\text{-vars } (a[i]) \leq length\ xs \rangle$
note *bsok* = $\langle bsok (a[i]) n \rangle$
from *bisim1* **have** *pc*: *pc* $\leq length (compE2 a)$ **by**(*rule bisim1-pc-length-compE2*)
show *?case*
proof(*cases pc < length (compE2 a)*)
case *True*
with *exec* **have** *exec'*: *?exec a stk loc pc xcp stk' loc' pc' xcp'* **by**(*auto simp add: exec-move-AAcc1*)
from *True* **have** τ : $\tau move2 (compP2 P) h stk (a[i]) pc xcp = \tau move2 (compP2 P) h stk a pc xcp$
by(*auto intro: $\tau move2$ AAcc1*)
with *IH1*[*OF exec' - -* $\langle P, h \vdash stk [:\leq] ST \rangle \langle conf\text{-xcp}' (compP2 P) h xcp \rangle$ *len bsok*] **obtain** *e'' xs''*
where *bisim'*: $P, a, h' \vdash (e'', xs'') \leftrightarrow (stk', loc', pc', xcp')$
and *red*: *?red a' xs e'' xs'' a stk pc pc' xcp xcp'* **by** *auto*
from *bisim'* **have** $P, a[i], h' \vdash (e''[i], xs'') \leftrightarrow (stk', loc', pc', xcp')$ **by**(*rule bisim1-bisims1.bisim1AAcc1*)
moreover from *True* **have** *no-call2 (a[i]) pc = no-call2 a pc* **by**(*simp add: no-call2-def*)
ultimately show *?thesis using red τ* **by**(*fastforce intro: AAcc1Red1 elim!: AAcc1-red1r-xt1*
AAcc1-red1t-xt1)
next
case *False*
with *pc* **have** *pc*: *pc* = *length (compE2 a)* **by** *auto*
with *bisim1* **obtain** *v* **where** *a'*: *is-val a' $\longrightarrow a' = Val v$*
and *stk*: *stk* = *[v]* **and** *xcp*: *xcp* = *None* **and** *call*: *call1 a' = None*
by(*auto dest: bisim1-pc-length-compE2D*)
with *bisim1 pc len bsok* **have** *rede1'*: $\tau red1r P t h (a', xs) (Val v, loc)$
by(*auto intro: bisim1-Val- $\tau red1r$ simp add: bsok-def*)
hence $\tau red1r P t h (a'[i], xs) (Val v[i], loc)$ **by**(*rule AAcc1-red1r-xt1*)
moreover from *pc exec stk xcp*
have *exec'*: *exec-meth-d (compP2 P) (compE2 a @ compE2 i @ [ALoad]) (compxE2 a 0 0 @ shift*

$(length (compE2 a)) (stack-xlift (length [v]) (compxE2 i 0 0)) t h ([v] @ [v], loc, length (compE2 a) + 0, None) ta h' (stk', loc', pc', xcp')$
by(simp add: compxE2-size-convs compxE2-stack-xlift-convs exec-move-def)
hence exec-meth-d (compP2 P) (compE2 i @ [ALoad]) (stack-xlift (length [v]) (compxE2 i 0 0)) t h ([v] @ [v], loc, 0, None) ta h' (stk', loc', pc' - length (compE2 a), xcp')
by(rule exec-meth-drop-xt) auto
hence exec-meth-d (compP2 P) (compE2 i) (stack-xlift (length [v]) (compxE2 i 0 0)) t h ([v] @ [v], loc, 0, None) ta h' (stk', loc', pc' - length (compE2 a), xcp')
by(rule exec-meth-take) simp
with bisim2 **obtain** stk'' **where** stk': stk' = stk'' @ [v]
and exec'': exec-move-d P t i h ([v], loc, 0, None) ta h' (stk'', loc', pc' - length (compE2 a), xcp')
unfolding exec-move-def **by**(blast dest: exec-meth-stk-split)
with pc xcp **have** $\tau: \tau move2 (compP2 P) h ([v] @ [v]) (a[i]) (length (compE2 a)) None = \tau move2 (compP2 P) h [v] i 0 None$
using $\tau instr-stk-drop-exec-move$ **where** $stk = []$ **and** $vs = [v]$
by(auto simp add: $\tau move2-iff \tau instr-stk-drop-exec-move$)
from bisim1 **have** $length xs = length loc$ **by**(rule bisim1-length-xs)
with IH2[OF exec''] len bsok **obtain** e'' xs''
where bisim': $P, i, h' \vdash (e'', xs'') \leftrightarrow (stk'', loc', pc' - length (compE2 a), xcp')$
and red: $?red i loc e'' xs'' i [v] 0 (pc' - length (compE2 a)) None xcp'$ **by**(fastforce)
from bisim'
have $P, a[i], h' \vdash (Val v[e''], xs'') \leftrightarrow (stk'' @ [v], loc', length (compE2 a) + (pc' - length (compE2 a)), xcp')$
by(rule bisim1-bisims1.bisim1AAcc2)
moreover from red τ **have** $?red (Val v[i]) loc (Val v[e'']) xs'' (a[i]) [v] (length (compE2 a)) pc' None xcp'$
by(fastforce intro: AAcc1Red2 elim!: AAcc- $\tau red1r-xt2$ AAcc- $\tau red1t-xt2$ split: if-split-asm simp add: no-call2-def)
moreover from exec' **have** $pc' \geq length (compE2 a)$
by(rule exec-meth-drop-xt-pc) auto
moreover have no-call2 (a[i]) pc **using** pc **by**(simp add: no-call2-def)
ultimately show ?thesis **using** τ stk' pc xcp stk call
by(fastforce elim!: rtranclp-trans)+
qed
next
case (bisim1AAcc2 i n i' xs stk loc pc xcp a v)
note IH2 = bisim1AAcc2.IH(2)
note exec = $\langle ?exec (a[i]) (stk @ [v]) loc (length (compE2 a) + pc) xcp stk' loc' pc' xcp' \rangle$
note bisim2 = $\langle P, i, h \vdash (i', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
note len = $\langle n + max-vars (Val v[i']) \leq length xs \rangle$
note bsok = $\langle bsok (a[i]) n \rangle$
from bisim2 **have** $pc: pc \leq length (compE2 i)$ **by**(rule bisim1-pc-length-compE2)
show ?case
proof(cases $pc < length (compE2 i)$)
case True
from exec **have** exec': exec-meth-d (compP2 P) (compE2 a @ compE2 i @ [ALoad]) (compxE2 a 0 0 @ shift (length (compE2 a)) (stack-xlift (length [v]) (compxE2 i 0 0))) t h (stk @ [v], loc, length (compE2 a) + pc, xcp) ta h' (stk', loc', pc', xcp')
by(simp add: shift-compxE2 stack-xlift-compxE2 exec-move-def)
hence exec-meth-d (compP2 P) (compE2 i @ [ALoad]) (stack-xlift (length [v]) (compxE2 i 0 0)) t h (stk @ [v], loc, pc, xcp) ta h' (stk', loc', pc' - length (compE2 a), xcp')
by(rule exec-meth-drop-xt) auto
hence exec-meth-d (compP2 P) (compE2 i) (stack-xlift (length [v]) (compxE2 i 0 0)) t h (stk @ [v], loc, pc, xcp) ta h' (stk', loc', pc' - length (compE2 a), xcp')

using *True* **by**(*rule exec-meth-take*)
with *bisim2* **obtain** *stk''* **where** *stk'*: $stk' = stk'' @ [v]$
and *exec''*: *exec-move-d* *P t i h* (*stk*, *loc*, *pc*, *xcp*) *ta* *h'* (*stk''*, *loc'*, $pc' - \text{length}(\text{compE2 } a)$, *xcp'*)
unfolding *exec-move-def* **by**(*blast dest: exec-meth-stk-split*)
with *True* **have** τ : $\tau\text{move2}(\text{compP2 } P) h (stk @ [v]) (a[i]) (\text{length}(\text{compE2 } a) + pc) xcp =$
 $\tau\text{move2}(\text{compP2 } P) h stk i pc xcp$
by(*auto simp add: \tau move2-iff \tau instr-stk-drop-exec-move*)
moreover from $\langle P, h \vdash stk @ [v] [:\leq] ST \rangle$ **obtain** *ST2*
where $P, h \vdash stk [:\leq] ST2$ **by**(*auto simp add: list-all2-append1*)
from *IH2*[*OF exec'' - - this \langle conf-xcp' (compP2 P) h xcp \rangle len bsok*] **obtain** *e'' xs''*
where *bisim'*: $P, i, h' \vdash (e'', xs'') \leftrightarrow (stk'', loc', pc' - \text{length}(\text{compE2 } a), xcp')$
and *red*: $?red i' xs e'' xs'' i stk pc (pc' - \text{length}(\text{compE2 } a)) xcp xcp'$ **by** *auto*
from *bisim'* **have** $P, a[i], h' \vdash (\text{Val } v[e''], xs'') \leftrightarrow (stk'' @ [v], loc', \text{length}(\text{compE2 } a) + (pc' -$
 $\text{length}(\text{compE2 } a)), xcp')$
by(*rule bisim1-bisims1.bisim1AAcc2*)
moreover from *exec'* **have** $pc' \geq \text{length}(\text{compE2 } a)$
by(*rule exec-meth-drop-xt-pc*) *auto*
moreover have *no-call2* *i pc* \implies *no-call2* (*a*[*i*]) ($\text{length}(\text{compE2 } a) + pc$)
by(*simp add: no-call2-def*)
ultimately show *?thesis* **using** *red \tau stk' True*
by(*fastforce intro: AAacc1Red2 elim!: AAacc-\tau red1r-xt2 AAacc-\tau red1t-xt2 split: if-split-asm*)
next
case *False*
with *pc* **have** [*simp*]: $pc = \text{length}(\text{compE2 } i)$ **by** *simp*
with *bisim2* **obtain** *v2* **where** *i'*: *is-val* *i'* $\longrightarrow i' = \text{Val } v2$
and *stk*: $stk = [v2]$ **and** *xcp*: $xcp = \text{None}$ **and** *call*: $call1 i' = \text{None}$
by(*auto dest: bisim1-pc-length-compE2D*)
with *bisim2* *pc len bsok* **have** *red*: $\tau\text{red1r } P t h (i', xs) (\text{Val } v2, loc)$
by(*auto intro: bisim1-Val-\tau red1r simp add: bsok-def*)
hence $\tau\text{red1r } P t h (\text{Val } v[i], xs) (\text{Val } v[\text{Val } v2], loc)$ **by**(*rule AAacc-\tau red1r-xt2*)
moreover have τ : $\neg \tau\text{move2}(\text{compP2 } P) h [v2, v] (a[i]) (\text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i)) \text{None}$
by(*simp add: \tau move2-iff*)
moreover
have $\exists ta' e''. P, a[i], h' \vdash (e'', loc) \leftrightarrow (stk', loc', pc', xcp') \wedge \text{True}, P, t \vdash 1 \langle \text{Val } v[\text{Val } v2], (h, loc) \rangle$
 $-ta' \rightarrow \langle e'', (h', loc) \rangle \wedge ta\text{-bisim } wbisim1 (extTA2J1 P ta')$ *ta*
proof(*cases v = Null*)
case *True* **with** *exec stk xcp* **show** *?thesis*
by(*fastforce elim!: exec-meth.cases simp add: exec-move-def intro: bisim1AAccFail Red1AAccNull*)
next
case *False*
with *exec xcp stk* **obtain** *U el A len I* **where** [*simp*]: $v = \text{Addr } A$ **and** *hA*: *typeof-addr* $h A =$
 $[\text{Array-type } U \text{ len}]$
and [*simp*]: $v2 = \text{Intg } I$ **by**(*auto simp add: exec-move-def exec-meth-instr is-Ref-def conf-def*
split: if-split-asm)
show *?thesis*
proof(*cases 0 <= s I \wedge sint I < int len*)
case *True*
hence $\neg I < s 0$ **by** *auto*
moreover
with *exec xcp stk True hA* **obtain** *v3* **where** $stk' = [v3] \text{heap-read } h A (A\text{Cell } (\text{nat } (\text{sint } I)))$
v3
by(*auto simp add: exec-move-def exec-meth-instr is-Ref-def*)
moreover

```

have  $P, a[i], h' \vdash (Val\ v3, loc) \leftrightarrow ([v3], loc, length\ (compE2\ (a[i])), None)$ 
  by(rule bisim1Val2) simp
ultimately show ?thesis using exec stk xcp True hA
  by(fastforce elim!: exec-meth.cases intro: Red1AAcc simp add: exec-move-def ta-upd-simps
ta-bisim-def split: if-split-asm)
next
  case False
  with exec stk xcp hA show ?thesis
  by(fastforce elim!: exec-meth.cases simp add: is-Ref-def exec-move-def intro: bisim1AAccFail
Red1AAccBounds split: if-split-asm)
  qed
  qed
  ultimately show ?thesis using exec xcp stk call by fastforce
  qed
next
  case (bisim1AAccThrow1 A n a xs stk loc pc i)
  note exec =  $\langle ?exec\ (A[i])\ stk\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$ 
  note bisim1 =  $\langle P, A, h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$ 
  from bisim1 have pc:  $pc < length\ (compE2\ A)$  by(auto dest: bisim1-ThrowD)
  from bisim1 have match-ex-table (compP2 P) (cname-of h a) (0 + pc) (compxE2 A 0 0) = None
  unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
  with exec pc have False
  by(auto elim!: exec-meth.cases simp add: match-ex-table-not-pcs-None exec-move-def)
  thus ?case ..
next
  case (bisim1AAccThrow2 i n a xs stk loc pc A v)
  note exec =  $\langle ?exec\ (A[i])\ (stk\ @\ [v])\ loc\ (length\ (compE2\ A) + pc)\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$ 
  note bisim2 =  $\langle P, i, h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$ 
  hence match-ex-table (compP2 P) (cname-of h a) (length (compE2 A) + pc) (compxE2 i (length
(compE2 A) 0) 0) = None
  unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
  with exec have False
  apply(auto elim!: exec-meth.cases simp add: match-ex-table-append-not-pcs exec-move-def)
  apply(auto simp only: compxE2-size-convs compxE2-stack-xlift-convs match-ex-table-stack-xlift-eq-Some-conv)
  done
  thus ?case ..
next
  case (bisim1AAccFail a n i ad xs v v')
  note  $\langle ?exec\ (a[i])\ [v, v']\ xs\ (length\ (compE2\ a) + length\ (compE2\ i))\ [ad]\ stk'\ loc'\ pc'\ xcp' \rangle$ 
  hence False
  by(auto elim!: exec-meth.cases simp add: match-ex-table-append-not-pcs compxE2-size-convs exec-move-def
dest!: match-ex-table-shift-pcD match-ex-table-pc-length-compE2)
  thus ?case ..
next
  case (bisim1AAss1 a n a' xs stk loc pc xcp i e)
  note IH1 = bisim1AAss1.IH(2)
  note IH2 = bisim1AAss1.IH(4)
  note exec =  $\langle ?exec\ (a[i]\ :=\ e)\ stk\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$ 
  note bisim1 =  $\langle P, a, h \vdash (a', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
  note bisim2 =  $\langle P, i, h \vdash (i, loc) \leftrightarrow ([], loc, 0, None) \rangle$ 
  note len =  $\langle n + max-vars\ (a'[i]\ :=\ e) \leq length\ xs \rangle$ 
  note bsok =  $\langle bsok\ (a[i]\ :=\ e)\ n \rangle$ 
  from bisim1 have pc:  $pc \leq length\ (compE2\ a)$  by(rule bisim1-pc-length-compE2)
  show ?case

```

proof(cases $pc < \text{length}(\text{compE2 } a)$)
case *True*
with *exec* **have** $\text{exec}' : ?\text{exec } a \text{ stk } \text{loc } pc \text{ xcp } \text{stk}' \text{ loc}' \text{ pc}' \text{ xcp}'$ **by**(*simp add: exec-move-AAss1*)
from *True* **have** $\tau : \tau\text{move2}(\text{compP2 } P) h \text{ stk } (a[i] := e) \text{ pc } \text{ xcp} = \tau\text{move2}(\text{compP2 } P) h \text{ stk } a \text{ pc } \text{ xcp}$ **by**(*simp add: \tau\text{move2-iff}*)
with *IH1*[*OF exec' - -* $\langle P, h \vdash \text{stk } [:\leq] ST \rangle \langle \text{conf-xcp}'(\text{compP2 } P) h \text{ xcp} \rangle$ *len bsok* **obtain** $e'' \text{ xs}''$
where $\text{bisim}' : P, a, h' \vdash (e'', \text{xs}'') \leftrightarrow (\text{stk}', \text{loc}', \text{pc}', \text{xcp}')$
and $\text{red} : ?\text{red } a' \text{ xs } e'' \text{ xs}'' a \text{ stk } pc \text{ pc}' \text{ xcp } \text{xcp}'$ **by** *auto*
from bisim' **have** $P, a[i] := e, h' \vdash (e''[i] := e, \text{xs}'') \leftrightarrow (\text{stk}', \text{loc}', \text{pc}', \text{xcp}')$
by(*rule bisim1-bisims1.bisim1AAss1*)
moreover from *True* **have** $\text{no-call2}(a[i] := e) \text{ pc} = \text{no-call2 } a \text{ pc}$ **by**(*simp add: no-call2-def*)
ultimately show $?thesis$ **using** *red \tau* **by**(*fastforce intro: AAAss1Red1 elim!: AAAss-\tau\text{red1r-xt1} AAAss-\tau\text{red1t-xt1}*)
next
case *False*
with *pc* **have** $pc : pc = \text{length}(\text{compE2 } a)$ **by** *auto*
with *bisim1* **obtain** v **where** $a' : \text{is-val } a' \longrightarrow a' = \text{Val } v$
and $\text{stk} : \text{stk} = [v]$ **and** $\text{xcp} : \text{xcp} = \text{None}$ **and** $\text{call} : \text{call1 } a' = \text{None}$
by(*auto dest: bisim1-pc-length-compE2D*)
with *bisim1* pc *len bsok* **have** $\text{rede1}' : \tau\text{red1r } P \text{ t h } (a', \text{xs}) (\text{Val } v, \text{loc})$
by(*auto intro: bisim1-Val-\tau\text{red1r simp add: bsok-def}*)
hence $\tau\text{red1r } P \text{ t h } (a'[i] := e, \text{xs}) (\text{Val } v[i] := e, \text{loc})$ **by**(*rule AAAss-\tau\text{red1r-xt1}*)
moreover from *pc exec stk xcp*
have $\text{exec}' : \text{exec-meth-d}(\text{compP2 } P) (\text{compE2 } a @ \text{compE2 } i @ \text{compE2 } e @ [\text{AStore}, \text{Push Unit}])$
 $(\text{compxE2 } a \text{ } 0 \text{ } 0 @ \text{shift}(\text{length}(\text{compE2 } a)) (\text{stack-xlift}(\text{length } [v]) (\text{compxE2 } i \text{ } 0 \text{ } 0) @ \text{shift}(\text{length}(\text{compE2 } i)) (\text{compxE2 } e \text{ } 0 (\text{Suc } (\text{Suc } 0)))) \text{ t h } ([@ [v], \text{loc}, \text{length}(\text{compE2 } a) + 0, \text{None}) \text{ ta } h'$
 $(\text{stk}', \text{loc}', \text{pc}', \text{xcp}')$
by(*simp add: compxE2-size-convvs compxE2-stack-xlift-convvs exec-move-def*)
hence $\text{exec-meth-d}(\text{compP2 } P) (\text{compE2 } i @ \text{compE2 } e @ [\text{AStore}, \text{Push Unit}]) (\text{stack-xlift}(\text{length } [v]) (\text{compxE2 } i \text{ } 0 \text{ } 0) @ \text{shift}(\text{length}(\text{compE2 } i)) (\text{compxE2 } e \text{ } 0 (\text{Suc } (\text{Suc } 0)))) \text{ t h } ([@ [v], \text{loc}, 0, \text{None}) \text{ ta } h'$
 $(\text{stk}', \text{loc}', \text{pc}' - \text{length}(\text{compE2 } a), \text{xcp}')$
by(*rule exec-meth-drop-xt auto*)
hence $\text{exec-meth-d}(\text{compP2 } P) (\text{compE2 } i) (\text{stack-xlift}(\text{length } [v]) (\text{compxE2 } i \text{ } 0 \text{ } 0)) \text{ t h } ([@ [v], \text{loc}, 0, \text{None}) \text{ ta } h'$
 $(\text{stk}', \text{loc}', \text{pc}' - \text{length}(\text{compE2 } a), \text{xcp}')$
by(*rule exec-meth-take-xt simp*)
with *bisim2* **obtain** stk'' **where** $\text{stk}' : \text{stk}' = \text{stk}'' @ [v]$
and $\text{exec}'' : \text{exec-move-d } P \text{ t h } ([, \text{loc}, 0, \text{None}) \text{ ta } h'(\text{stk}'', \text{loc}', \text{pc}' - \text{length}(\text{compE2 } a), \text{xcp}')$
unfolding *exec-move-def* **by**(*blast dest: exec-meth-stk-split*)
with *pc xcp* **have** $\tau : \tau\text{move2}(\text{compP2 } P) h [v] (a[i] := e) (\text{length}(\text{compE2 } a)) \text{None} = \tau\text{move2}(\text{compP2 } P) h [] i \text{None}$
using $\tau\text{instr-stk-drop-exec-move}$ **where** $\text{stk} = []$ **and** $\text{vs} = [v]$
by(*auto simp add: \tau\text{move2-iff}*)
from *bisim1* **have** $\text{length } \text{xs} = \text{length } \text{loc}$ **by**(*rule bisim1-length-xs*)
with *IH2*[*OF exec''*] *len bsok* **obtain** $e'' \text{ xs}''$
where $\text{bisim}' : P, i, h' \vdash (e'', \text{xs}'') \leftrightarrow (\text{stk}'', \text{loc}', \text{pc}' - \text{length}(\text{compE2 } a), \text{xcp}')$
and $\text{red} : ?\text{red } i \text{ loc } e'' \text{ xs}'' i [] 0 (\text{pc}' - \text{length}(\text{compE2 } a)) \text{None } \text{xcp}'$ **by** *fastforce*
from bisim'
have $P, a[i] := e, h' \vdash (\text{Val } v[e''] := e, \text{xs}'') \leftrightarrow (\text{stk}'' @ [v], \text{loc}', \text{length}(\text{compE2 } a) + (\text{pc}' - \text{length}(\text{compE2 } a)), \text{xcp}')$
by(*rule bisim1-bisims1.bisim1AAss2*)
moreover from *red \tau* **have** $?red(\text{Val } v[i] := e) \text{ loc } (\text{Val } v[e''] := e) \text{ xs}'' (a[i] := e) [v] (\text{length}(\text{compE2 } a)) \text{ pc}' \text{None } \text{xcp}'$
by(*fastforce intro: AAAss1Red2 elim!: AAAss-\tau\text{red1r-xt2 AAAss-\tau\text{red1t-xt2 split: if-split-asm simp add: no-call2-def}*)

moreover from $exec'$ **have** $pc' \geq \text{length}(\text{compE2 } a)$
by(rule $exec\text{-meth-drop-xt-pc}$) *auto*
moreover have $no\text{-call2 } (a[i] := e) \text{ pc}$ **using** pc **by**(simp add: $no\text{-call2-def}$)
ultimately show $?thesis$ **using** $\tau \text{ stk}' \text{ pc } \text{ xcp } \text{ stk}$ **by**(fastforce elim!: $rtranclp\text{-trans}$)
qed
next
case ($bisim1AAss2 \ i \ n \ i' \ xs \ \text{stk} \ \text{loc} \ \text{pc} \ \text{xcp} \ a \ e \ v$)
note $IH2 = bisim1AAss2.IH(2)$
note $IH3 = bisim1AAss2.IH(6)$
note $exec = \langle ?exec \ (a[i] := e) \ (\text{stk} \ @ \ [v]) \ \text{loc} \ (\text{length} \ (\text{compE2} \ a) \ + \ \text{pc}) \ \text{xcp} \ \text{stk}' \ \text{loc}' \ \text{pc}' \ \text{xcp}' \rangle$
note $bisim2 = \langle P, i, h \vdash (i', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \rangle$
note $bisim3 = \langle P, e, h \vdash (e, \text{loc}) \leftrightarrow ([], \text{loc}, 0, \text{None}) \rangle$
note $len = \langle n + \text{max-vars} \ (\text{Val } v[i'] := e) \leq \text{length} \ xs \rangle$
note $bsok = \langle bsok \ (a[i] := e) \ n \rangle$
from $bisim2$ **have** $pc: \text{pc} \leq \text{length} \ (\text{compE2} \ i)$ **by**(rule $bisim1\text{-pc-length-compE2}$)
show $?case$
proof(cases $pc < \text{length} \ (\text{compE2} \ i)$)
case *True*
from $exec$ **have** $exec'$: $exec\text{-meth-d} \ (\text{compP2} \ P) \ (\text{compE2} \ a \ @ \ \text{compE2} \ i \ @ \ \text{compE2} \ e \ @ \ [AStore, Push \ Unit]) \ (\text{compxE2} \ a \ 0 \ 0 \ @ \ \text{shift} \ (\text{length} \ (\text{compE2} \ a)) \ (\text{stack-xlift} \ (\text{length} \ [v]) \ (\text{compxE2} \ i \ 0 \ 0) \ @ \ \text{shift} \ (\text{length} \ (\text{compE2} \ i)) \ (\text{compxE2} \ e \ 0 \ (\text{Suc} \ (\text{Suc} \ 0)))) \ t \ h \ (\text{stk} \ @ \ [v], \ \text{loc}, \ \text{length} \ (\text{compE2} \ a) \ + \ \text{pc}, \ \text{xcp}) \ \text{ta} \ h' \ (\text{stk}', \ \text{loc}', \ \text{pc}', \ \text{xcp}')$
by(simp add: $shift\text{-compxE2} \ \text{stack-xlift-compxE2} \ \text{ac-simps} \ \text{exec-move-def}$)
hence $exec\text{-meth-d} \ (\text{compP2} \ P) \ (\text{compE2} \ i \ @ \ \text{compE2} \ e \ @ \ [AStore, Push \ Unit]) \ (\text{stack-xlift} \ (\text{length} \ [v]) \ (\text{compxE2} \ i \ 0 \ 0) \ @ \ \text{shift} \ (\text{length} \ (\text{compE2} \ i)) \ (\text{compxE2} \ e \ 0 \ (\text{Suc} \ (\text{Suc} \ 0)))) \ t \ h \ (\text{stk} \ @ \ [v], \ \text{loc}, \ \text{pc}, \ \text{xcp}) \ \text{ta} \ h' \ (\text{stk}', \ \text{loc}', \ \text{pc}' - \ \text{length} \ (\text{compE2} \ a), \ \text{xcp}')$
by(rule $exec\text{-meth-drop-xt}$) *auto*
hence $exec\text{-meth-d} \ (\text{compP2} \ P) \ (\text{compE2} \ i) \ (\text{stack-xlift} \ (\text{length} \ [v]) \ (\text{compxE2} \ i \ 0 \ 0)) \ t \ h \ (\text{stk} \ @ \ [v], \ \text{loc}, \ \text{pc}, \ \text{xcp}) \ \text{ta} \ h' \ (\text{stk}', \ \text{loc}', \ \text{pc}' - \ \text{length} \ (\text{compE2} \ a), \ \text{xcp}')$
using *True* **by**(rule $exec\text{-meth-take-xt}$)
with $bisim2$ **obtain** stk'' **where** $stk': \text{stk}' = \text{stk}'' \ @ \ [v]$
and $exec''$: $exec\text{-move-d} \ P \ t \ i \ h \ (\text{stk}, \ \text{loc}, \ \text{pc}, \ \text{xcp}) \ \text{ta} \ h' \ (\text{stk}'', \ \text{loc}', \ \text{pc}' - \ \text{length} \ (\text{compE2} \ a), \ \text{xcp}')$
unfolding $exec\text{-move-def}$ **by**(blast dest: $exec\text{-meth-stk-split}$)
with *True* **have** $\tau: \tau\text{move2} \ (\text{compP2} \ P) \ h \ (\text{stk} \ @ \ [v]) \ (a[i] := e) \ (\text{length} \ (\text{compE2} \ a) \ + \ \text{pc}) \ \text{xcp} = \tau\text{move2} \ (\text{compP2} \ P) \ h \ \text{stk} \ i \ \text{pc} \ \text{xcp}$
by(auto simp add: $\tau\text{move2-iff} \ \tau\text{instr-stk-drop-exec-move}$)
moreover from $\langle P, h \vdash \text{stk} \ @ \ [v] \ [:\leq] \ ST \rangle$ **obtain** $ST2$ **where** $P, h \vdash \text{stk} \ [:\leq] \ ST2$ **by**(auto simp add: $list\text{-all2-append1}$)
from $IH2[OF \ exec'' - - \text{this} \ \langle \text{conf-xcp}' \ (\text{compP2} \ P) \ h \ \text{xcp} \rangle] \ len \ bsok$ **obtain** $e'' \ xs''$
where $bisim': P, i, h' \vdash (e'', xs'') \leftrightarrow (\text{stk}'', \text{loc}', \text{pc}' - \text{length} \ (\text{compE2} \ a), \ \text{xcp}')$
and $red: ?red \ i' \ xs \ e'' \ xs'' \ i \ \text{stk} \ \text{pc} \ (\text{pc}' - \text{length} \ (\text{compE2} \ a)) \ \text{xcp} \ \text{xcp}'$ **by** fastforce
from $bisim'$
have $P, a[i] := e, h' \vdash (\text{Val } v[e''] := e, xs'') \leftrightarrow (\text{stk}'' \ @ \ [v], \ \text{loc}', \ \text{length} \ (\text{compE2} \ a) \ + \ (\text{pc}' - \text{length} \ (\text{compE2} \ a)), \ \text{xcp}')$
by(rule $bisim1\text{-bisims1.bisim1AAss2}$)
moreover from $exec'$ **have** $pc' \geq \text{length} \ (\text{compE2} \ a)$
by(rule $exec\text{-meth-drop-xt-pc}$) *auto*
moreover have $no\text{-call2} \ i \ \text{pc} \implies no\text{-call2} \ (a[i] := e) \ (\text{length} \ (\text{compE2} \ a) \ + \ \text{pc})$ **by**(simp add: $no\text{-call2-def}$)
ultimately show $?thesis$ **using** $red \ \tau \ \text{stk}' \ \text{True}$
by(fastforce intro: $AAss1Red2 \ elim!$: $AAss\text{-}\tau\text{red1r-xt2} \ AAss\text{-}\tau\text{red1t-xt2} \ \text{split: if-split-asm}$)
next
case *False*
with pc **have** $[simp]: \text{pc} = \text{length} \ (\text{compE2} \ i)$ **by** simp

with *bisim2* **obtain** *v2* **where** *i'*: *is-val i' → i' = Val v2*
and *stk*: *stk = [v2]* **and** *xcp*: *xcp = None* **and** *call*: *call1 i' = None*
by(*auto dest: bisim1-pc-length-compE2D*)
with *bisim2* *pc len bsok* **have** *red*: $\tau\text{red1r } P \ t \ h \ (i', \ xs) \ (Val \ v2, \ loc)$
by(*auto intro: bisim1-Val- τ red1r simp add: bsok-def*)
hence $\tau\text{red1r } P \ t \ h \ (Val \ v \ [i'] := e, \ xs) \ (Val \ v \ [Val \ v2] := e, \ loc)$ **by**(*rule AAss- τ red1r-xt2*)
moreover from *pc exec stk xcp*
have *exec'*: *exec-meth-d (compP2 P) ((compE2 a @ compE2 i) @ compE2 e @ [AStore, Push Unit])*
((compxE2 a 0 0 @ compxE2 i (length (compE2 a)) (Suc 0)) @ shift (length (compE2 a @ compE2
i)) (stack-xlift (length [v2, v]) (compxE2 e 0 0))) t h ([@ [v2, v], loc, length (compE2 a @ compE2
i) + 0, None) ta h' (stk', loc', pc', xcp')
by(*simp add: compxE2-size-convs compxE2-stack-xlift-convs exec-move-def*)
hence *exec-meth-d (compP2 P) (compE2 e @ [AStore, Push Unit]) (stack-xlift (length [v2, v])*
(compxE2 e 0 0)) t h ([@ [v2, v], loc, 0, None) ta h' (stk', loc', pc' - length (compE2 a @ compE2
i), xcp')
by(*rule exec-meth-drop-xt*) *auto*
hence *exec-meth-d (compP2 P) (compE2 e) (stack-xlift (length [v2, v]) (compxE2 e 0 0)) t h ([*
@ [v2, v], loc, 0, None) ta h' (stk', loc', pc' - length (compE2 a @ compE2 i), xcp')
by(*rule exec-meth-take*) *simp*
with *bisim3* **obtain** *stk''* **where** *stk'*: *stk' = stk'' @ [v2, v]*
and *exec''*: *exec-move-d P t e h ([, loc, 0, None) ta h' (stk'', loc', pc' - length (compE2 a @*
compE2 i), xcp')
unfolding *exec-move-def* **by**(*blast dest: exec-meth-stk-split*)
with *pc xcp* **have** τ : $\tau\text{move2 (compP2 P) h [v2, v] (a[i] := e) (length (compE2 a) + length (compE2$
i)) None = $\tau\text{move2 (compP2 P) h [] e 0 None}$
using $\tau\text{instr-stk-drop-exec-move}$ **where** *stk* = $[]$ **and** *vs* = $[v2, v]$ **by**(*simp add: $\tau\text{move2-iff}$*)
from *bisim2* **have** *length xs = length loc* **by**(*rule bisim1-length-xs*)
with *IH3[OF exec'', of []] len bsok* **obtain** *e'' xs''*
where *bisim'*: $P, e, h \vdash (e'', xs'') \leftrightarrow (stk'', loc', pc' - length (compE2 a) - length (compE2 i),$
xcp')
and *red*: $?red \ e \ loc \ e'' \ xs'' \ e \ [] \ 0 \ (pc' - length (compE2 a) - length (compE2 i)) \ None \ xcp'$
by *auto (fastforce simp only: length-append diff-diff-left)*
from *bisim'*
have $P, a[i] := e, h \vdash (Val \ v \ [Val \ v2] := e'', xs'') \leftrightarrow (stk'' @ [v2, v], loc', length (compE2 a) +$
length (compE2 i) + (pc' - length (compE2 a) - length (compE2 i)), xcp')
by(*rule bisim1-bisims1.bisim1AAss3*)
moreover from *red τ*
have $?red \ (Val \ v \ [Val \ v2] := e) \ loc \ (Val \ v \ [Val \ v2] := e'') \ xs'' \ (a[i] := e) \ [v2, v] \ (length (compE2$
a) + length (compE2 i)) \ pc' \ None \ xcp'
by(*fastforce intro: AAss1Red3 elim!: AAss- τ red1r-xt3 AAss- τ red1t-xt3 split: if-split-asm simp add:*
no-call2-def)
moreover from *exec'* **have** $pc' \geq length (compE2 a @ compE2 i)$
by(*rule exec-meth-drop-xt-pc*) *auto*
moreover have *no-call2 (a[i] := e) (length (compE2 a) + pc)* **by**(*simp add: no-call2-def*)
ultimately show *?thesis* **using** $\tau \ stk' \ pc \ xcp \ stk$ **by**(*fastforce elim!: rtranclp-trans*)
qed
next
case (*bisim1AAss3 e n e' xs stk loc pc xcp a i v v'*)
note *IH3 = bisim1AAss3.IH(2)*
note *exec = ⟨?exec (a[i] := e) (stk @ [v', v]) loc (length (compE2 a) + length (compE2 i) + pc)*
xcp stk' loc' pc' xcp'⟩
note *bisim3 = ⟨P, e, h ⊢ (e', xs) ↔ (stk, loc, pc, xcp)⟩*
note *len = ⟨n + max-vars (Val v [Val v'] := e') ≤ length xs⟩*
note *bsok = ⟨bsok (a[i] := e) n⟩*

from $\langle P, h \vdash stk \ @ \ [v', v] \ [:\leq] \ ST \rangle$ **obtain** $T \ T' \ ST'$
where $[simp]: ST = ST' \ @ \ [T', T]$
and $wtv: P, h \vdash v \ ::\leq \ T$ **and** $wtv': P, h \vdash v' \ ::\leq \ T'$ **and** $ST': P, h \vdash stk \ [:\leq] \ ST'$
by(*auto simp add: list-all2-Cons1 list-all2-append1*)
from *bisim3* **have** $pc: pc \leq \text{length} \ (\text{compE2} \ e)$ **by**(*rule bisim1-pc-length-compE2*)
show *?case*
proof(*cases pc < length (compE2 e)*)
case *True*
from *exec* **have** $exec': \text{exec-meth-d} \ (\text{compP2} \ P) \ ((\text{compE2} \ a \ @ \ \text{compE2} \ i) \ @ \ \text{compE2} \ e \ @ \ [\text{AStore}, \text{Push} \ \text{Unit}]) \ ((\text{compxE2} \ a \ 0 \ 0 \ @ \ \text{compxE2} \ i \ (\text{length} \ (\text{compE2} \ a)) \ (\text{Suc} \ 0)) \ @ \ \text{shift} \ (\text{length} \ (\text{compE2} \ a \ @ \ \text{compE2} \ i)) \ (\text{stack-xlift} \ (\text{length} \ [v', v]) \ (\text{compxE2} \ e \ 0 \ 0))) \ t \ h \ (stk \ @ \ [v', v], \text{loc}, \text{length} \ (\text{compE2} \ a \ @ \ \text{compE2} \ i) + pc, \text{xcp}) \ ta \ h' \ (stk', \text{loc}', pc', \text{xcp}')$
by(*simp add: shift-compxE2 stack-xlift-compxE2 exec-move-def*)
hence $\text{exec-meth-d} \ (\text{compP2} \ P) \ (\text{compE2} \ e \ @ \ [\text{AStore}, \text{Push} \ \text{Unit}]) \ (\text{stack-xlift} \ (\text{length} \ [v', v]) \ (\text{compxE2} \ e \ 0 \ 0)) \ t \ h \ (stk \ @ \ [v', v], \text{loc}, pc, \text{xcp}) \ ta \ h' \ (stk', \text{loc}', pc' - \text{length} \ (\text{compE2} \ a \ @ \ \text{compE2} \ i), \text{xcp}')$
by(*rule exec-meth-drop-xt*) *auto*
hence $\text{exec-meth-d} \ (\text{compP2} \ P) \ (\text{compE2} \ e) \ (\text{stack-xlift} \ (\text{length} \ [v', v]) \ (\text{compxE2} \ e \ 0 \ 0)) \ t \ h \ (stk \ @ \ [v', v], \text{loc}, pc, \text{xcp}) \ ta \ h' \ (stk', \text{loc}', pc' - \text{length} \ (\text{compE2} \ a \ @ \ \text{compE2} \ i), \text{xcp}')$
using *True* **by**(*rule exec-meth-take*)
with *bisim3* **obtain** stk'' **where** $stk': stk' = stk'' \ @ \ [v', v]$
and $exec'': \text{exec-move-d} \ P \ t \ e \ h \ (stk, \text{loc}, pc, \text{xcp}) \ ta \ h' \ (stk'', \text{loc}', pc' - \text{length} \ (\text{compE2} \ a \ @ \ \text{compE2} \ i), \text{xcp}')$
unfolding *exec-move-def* **by**(*blast dest: exec-meth-stk-split*)
with *True* **have** $\tau: \tau \text{move2} \ (\text{compP2} \ P) \ h \ (stk \ @ \ [v', v]) \ (a[i] := e) \ (\text{length} \ (\text{compE2} \ a) + \text{length} \ (\text{compE2} \ i) + pc) \ \text{xcp} = \tau \text{move2} \ (\text{compP2} \ P) \ h \ stk \ e \ pc \ \text{xcp}$
by(*auto simp add: \tau move2-iff \tau instr-stk-drop-exec-move*)
moreover from *IH3[OF exec'' - - ST' \conf-xcp' (compP2 P) h xcp]* *len bsok* **obtain** $e'' \ xs''$
where $\text{bisim}': P, e, h' \vdash (e'', xs'') \leftrightarrow (stk'', \text{loc}', pc' - \text{length} \ (\text{compE2} \ a) - \text{length} \ (\text{compE2} \ i), \text{xcp}')$
and $\text{red}: ?\text{red} \ e' \ xs \ e'' \ xs'' \ e \ stk \ pc \ (pc' - \text{length} \ (\text{compE2} \ a) - \text{length} \ (\text{compE2} \ i)) \ \text{xcp} \ \text{xcp}'$
by *auto(fastforce simp only: length-append diff-diff-left)*
from *bisim'*
have $P, a[i] := e, h' \vdash (\text{Val} \ v \ [\ \text{Val} \ v \] := e'', xs'') \leftrightarrow (stk'' \ @ \ [v', v], \text{loc}', \text{length} \ (\text{compE2} \ a) + \text{length} \ (\text{compE2} \ i) + (pc' - \text{length} \ (\text{compE2} \ a) - \text{length} \ (\text{compE2} \ i)), \text{xcp}')$
by(*rule bisim1-bisims1.bisim1AAss3*)
moreover from *exec'* **have** $pc' \geq \text{length} \ (\text{compE2} \ a \ @ \ \text{compE2} \ i)$
by(*rule exec-meth-drop-xt-pc*) *auto*
moreover have $\text{no-call2} \ e \ pc \implies \text{no-call2} \ (a[i] := e) \ (\text{length} \ (\text{compE2} \ a) + \text{length} \ (\text{compE2} \ i) + pc)$
by(*simp add: no-call2-def*)
ultimately show *?thesis* **using** $\text{red} \ \tau \ \text{stk}' \ \text{True}$
by(*fastforce intro: AAAss1Red3 elim!: AAAss-\tau red1r-xt3 AAAss-\tau red1t-xt3 split: if-split-asm*)
next
case *False*
with *pc* **have** $[simp]: pc = \text{length} \ (\text{compE2} \ e)$ **by** *simp*
with *bisim3* **obtain** $v2$ **where** $stk: stk = [v2]$ **and** $\text{xcp}: \text{xcp} = \text{None}$
by(*auto dest: bisim1-pc-length-compE2D*)
with *bisim3* *pc len bsok* **have** $\text{red}: \tau \text{red1r} \ P \ t \ h \ (e', xs) \ (\text{Val} \ v2, \text{loc})$
by(*auto intro: bisim1-Val-\tau red1r simp add: bsok-def*)
hence $\tau \text{red1r} \ P \ t \ h \ (\text{Val} \ v \ [\ \text{Val} \ v \] := e', xs) \ (\text{Val} \ v \ [\ \text{Val} \ v \] := \text{Val} \ v2, \text{loc})$ **by**(*rule AAAss-\tau red1r-xt3*)
moreover have $\tau: \neg \tau \text{move2} \ (\text{compP2} \ P) \ h \ [v2, v', v] \ (a[i] := e) \ (\text{length} \ (\text{compE2} \ a) + \text{length} \ (\text{compE2} \ i) + \text{length} \ (\text{compE2} \ e)) \ \text{None}$
by(*simp add: \tau move2-iff*)


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moreover
  have  $\exists ta' e''. P, a[i] := e, h' \vdash (e'', loc) \leftrightarrow (stk', loc', pc', xcp') \wedge True, P, t \vdash 1 \langle Val v [Val v'] := Val v2, (h, loc) \rangle -ta' \rightarrow \langle e'', (h', loc) \rangle \wedge ta\text{-bisim } w\text{bisim1 } (extTA2J1 P ta') ta$ 
  proof(cases v = Null)
    case True with exec stk xcp show ?thesis
    by(fastforce elim!: exec-meth.cases simp add: exec-move-def intro: bisim1AAssFail Red1AAssNull)
  next
  case False
  with exec stk xcp obtain U A len I where [simp]: v = Addr A v' = Intg I
    and hA: typeof-addr h A = [Array-type U len]
    by(fastforce simp add: exec-move-def exec-meth-instr is-Ref-def)
  from ST' stk obtain T3 where wt3': typeofh v2 = [T3] by(auto simp add: list-all2-Cons1 conf-def)
  show ?thesis
  proof(cases 0 <= s I  $\wedge$  sint I < int len)
    case True
    note I = True
    show ?thesis
    proof(cases P  $\vdash$  T3  $\leq$  U)
      case True
      with exec stk xcp True hA I wt3' show ?thesis
      by(fastforce elim!: exec-meth.cases simp add: compP2-def exec-move-def ta-bisim-def ta-upd-simps intro: Red1AAss bisim1AAss4 split: if-split-asm)
    next
    case False
    with exec stk xcp True hA I wt3' show ?thesis
    by(fastforce elim!: exec-meth.cases simp add: compP2-def exec-move-def intro: Red1AAssStore bisim1AAssFail split: if-split-asm)
  qed
  next
  case False
  with exec stk xcp hA show ?thesis
  by(fastforce elim!: exec-meth.cases intro: bisim1AAssFail Red1AAssBounds simp add: exec-move-def split: if-split-asm)
  qed
  ultimately show ?thesis using exec xcp stk by(fastforce simp add: no-call2-def)
  qed
next
  case (bisim1AAssThrow1 A n a xs stk loc pc i e)
  note exec =  $\langle ?exec (A[i] := e) stk loc pc [a] stk' loc' pc' xcp' \rangle$ 
  note bisim1 =  $\langle P, A, h \vdash (Throw a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$ 
  from bisim1 have pc: pc < length (compE2 A) by(auto dest: bisim1-ThrowD)
  from bisim1 have match-ex-table (compP2 P) (cname-of h a) (0 + pc) (compxE2 A 0 0) = None
    unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
  with exec pc have False
  by(auto elim!: exec-meth.cases simp add: match-ex-table-not-pcs-None exec-move-def)
  thus ?case ..
next
  case (bisim1AAssThrow2 i n a xs stk loc pc A e v)
  note exec =  $\langle ?exec (A[i] := e) (stk @ [v]) loc (length (compE2 A) + pc) [a] stk' loc' pc' xcp' \rangle$ 
  note bisim2 =  $\langle P, i, h \vdash (Throw a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$ 
  from bisim2 have pc: pc < length (compE2 i) by(auto dest: bisim1-ThrowD)
  from bisim2 have match-ex-table (compP2 P) (cname-of h a) (length (compE2 A) + pc) (compxE2

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i (length (compE2 A)) 0) = None
  unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
  with exec pc have False
  apply(auto elim!: exec-meth.cases simp add: compxE2-stack-xlift-convs compxE2-size-convs exec-move-def)
  apply(auto simp add: match-ex-table-append-not-pcs)
  done
  thus ?case ..
next
  case (bisim1AAssThrow3 e n a xs stk loc pc A i v' v)
  note exec = ⟨?exec (A[i] := e) (stk @ [v', v]) loc (length (compE2 A) + length (compE2 i) + pc)
[a] stk' loc' pc' xcp'⟩
  note bisim2 = ⟨P,e,h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
  from bisim2 have match-ex-table (compP2 P) (cname-of h a) (length (compE2 A) + length (compE2
i) + pc) (compxE2 e (length (compE2 A) + length (compE2 i)) 0) = None
  unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
  with exec have False
  apply(auto elim!: exec-meth.cases simp add: compxE2-stack-xlift-convs compxE2-size-convs exec-move-def)
  apply(auto dest!: match-ex-table-stack-xliftD match-ex-table-shift-pcD dest: match-ex-table-pcsD
simp add: match-ex-table-append match-ex-table-shift-pc-None)
  done
  thus ?case ..
next
  case (bisim1AAssFail a n i e ad xs v' v v'')
  note exec = ⟨?exec (a[i] := e) [v', v, v''] xs (length (compE2 a) + length (compE2 i) + length
(compE2 e)) [ad] stk' loc' pc' xcp'⟩
  hence False
  by(auto elim!: exec-meth.cases simp add: match-ex-table-append exec-move-def
dest!: match-ex-table-shift-pcD match-ex-table-pc-length-compE2)
  thus ?case ..
next
  case (bisim1AAss4 a n i e xs)
  have P,a[i] := e,h ⊢ (unit, xs) ↔ ([Unit], xs, length (compE2 (a[i] := e)), None) by(rule
bisim1Val2) simp
  moreover have τmove2 (compP2 P) h [] (a[i] := e) (Suc (length (compE2 a) + length (compE2
i) + length (compE2 e))) None
  by(simp add: τmove2-iff)
  moreover note ⟨?exec (a[i] := e) [] xs (Suc (length (compE2 a) + length (compE2 i) + length
(compE2 e))) None stk' loc' pc' xcp'⟩
  ultimately show ?case
  by(fastforce elim!: exec-meth.cases simp add: ac-simps exec-move-def)
next
  case (bisim1ALength a n a' xs stk loc pc xcp)
  note IH = bisim1ALength.IH(2)
  note exec = ⟨?exec (a·length) stk loc pc xcp stk' loc' pc' xcp'⟩
  note bisim = ⟨P,a,h ⊢ (a', xs) ↔ (stk, loc, pc, xcp)⟩
  note len = ⟨n + max-vars (a'·length) ≤ length xs⟩
  note bsok = ⟨bsok (a·length) n⟩
  from bisim have pc: pc ≤ length (compE2 a) by(rule bisim1-pc-length-compE2)
  show ?case
  proof(cases pc < length (compE2 a))
  case True
  with exec have exec': ?exec a stk loc pc xcp stk' loc' pc' xcp' by(auto simp add: exec-move-ALength)
  from True have τ: τmove2 (compP2 P) h stk (a·length) pc xcp = τmove2 (compP2 P) h stk a pc
xcp by(simp add: τmove2-iff)

```

with $IH[OF\ exec' - - \langle P, h \vdash stk \[:\leq] ST \rangle \langle conf\text{-}xcp' (compP2\ P)\ h\ xcp \rangle]$ *len bsok* **obtain** $e''\ xs''$
where $bisim': P, a, h' \vdash (e'', xs'') \leftrightarrow (stk', loc', pc', xcp')$
and $red: ?red\ a'\ xs\ e''\ xs''\ a\ stk\ pc\ pc'\ xcp\ xcp'$ **by** *auto*
from $bisim'$ **have** $P, a \cdot length, h' \vdash (e'' \cdot length, xs'') \leftrightarrow (stk', loc', pc', xcp')$
by(*rule bisim1-bisims1.bisim1ALength*)
with $red\ \tau$ **show** $?thesis$ **by**(*fastforce intro: ALength1Red elim!: ALength- τ red1r-xt ALength- τ red1t-xt simp add: no-call2-def*)
next
case *False*
with pc **have** $pc: pc = length (compE2\ a)$ **by** *auto*
with $bisim$ **obtain** v **where** $stk: stk = [v]$ **and** $xcp: xcp = None$
by(*auto dest: bisim1-pc-length-compE2D*)
with $bisim\ pc\ len\ bsok$ **have** $\tau red1r\ P\ t\ h\ (a', xs)\ (Val\ v,\ loc)$
by(*auto intro: bisim1-Val- τ red1r simp add: bsok-def*)
hence $\tau red1r\ P\ t\ h\ (a' \cdot length, xs)\ (Val\ v \cdot length, loc)$ **by**(*rule ALength- τ red1r-xt*)
moreover
moreover **have** $\tau: \neg\ \tau move2\ (compP2\ P)\ h\ [v]\ (a \cdot length)\ (length\ (compE2\ a))\ None$ **by**(*simp add: τ move2-iff*)
moreover **have** $\exists ta'\ e''. P, a \cdot length, h' \vdash (e'', loc) \leftrightarrow (stk', loc', pc', xcp') \wedge True, P, t \vdash 1 \langle Val\ v \cdot length, (h, loc) \rangle - ta' \rightarrow \langle e'', (h', loc) \rangle \wedge ta\text{-}bisim\ wbisim1\ (extTA2J1\ P\ ta')\ ta$
proof(*cases v = Null*)
case *True* **with** $exec\ stk\ xcp\ pc$ **show** $?thesis$
by(*fastforce elim!: exec-meth.cases simp add: exec-move-def intro: bisim1ALengthNull Red1ALength-Null*)
next
case *False*
with $exec\ stk\ xcp\ pc\ \langle P, h \vdash stk \[:\leq] ST \rangle$
obtain $U\ A\ len$ **where** [*simp*]: $v = Addr\ A$
and $hA: typeof\ addr\ h\ A = [Array\text{-}type\ U\ len]$
by(*fastforce simp add: exec-move-def exec-meth-instr is-Ref-def list-all2-Cons1*)
have $P, a \cdot length, h' \vdash (Val\ (Intg\ (word\ of\ int\ (int\ len))), loc) \leftrightarrow ([Intg\ (word\ of\ int\ (int\ len))], loc, length\ (compE2\ (a \cdot length)), None)$
by(*rule bisim1Val2*) *simp*
thus $?thesis$ **using** $exec\ stk\ xcp\ hA\ pc$
by(*fastforce elim!: exec-meth.cases intro: Red1ALength simp add: exec-move-def*)
qed
ultimately **show** $?thesis$ **using** $\tau\ pc\ xcp\ stk$ **by**(*fastforce elim!: rtranclp-trans simp add: no-call2-def*)
qed
next
case (*bisim1ALengthThrow A n a xs stk loc pc*)
note $exec = \langle ?exec\ (A \cdot length)\ stk\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$
note $bisim1 = \langle P, A, h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$
from $bisim1$ **have** $pc: pc < length\ (compE2\ A)$ **by**(*auto dest: bisim1-ThrowD*)
from $bisim1$ **have** $match\ ex\ table\ (compP2\ P)\ (cname\ of\ h\ a)\ (0 + pc)\ (compxE2\ A\ 0\ 0) = None$
unfolding *compP2-def* **by**(*rule bisim1-xcp-Some-not-caught*)
with $exec\ pc$ **have** *False* **by**(*auto elim!: exec-meth.cases simp add: exec-move-def*)
thus $?case\ ..$
next
case (*bisim1ALengthNull a n xs*)
note $exec = \langle ?exec\ (a \cdot length)\ [Null]\ xs\ (length\ (compE2\ a))\ [addr\ of\ sys\ xcpt\ NullPointer]\ stk'\ loc'\ pc'\ xcp' \rangle$
hence *False* **by**(*auto elim!: exec-meth.cases dest!: match-ex-table-pc-length-compE2 simp add: exec-move-def*)
thus $?case\ ..$
next

case (*bisim1FAcc* *e n e' xs stk loc pc xcp F D*)
note *IH* = *bisim1FAcc.IH*(2)
note *exec* = $\langle ?exec (e \cdot F\{D\}) \text{ stk loc pc xcp stk' loc' pc' xcp' \rangle$
note *bisim* = $\langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
note *len* = $\langle n + \text{max-vars} (e \cdot F\{D\}) \leq \text{length } xs \rangle$
note *bsok* = $\langle bsok (e \cdot F\{D\}) \ n \rangle$
from *bisim* **have** *pc*: *pc* \leq *length* (*compE2* *e*) **by**(*rule bisim1-pc-length-compE2*)
show ?*case*
proof(*cases pc < length (compE2 e)*)
case *True*
with *exec* **have** *exec'*: ?*exec* *e stk loc pc xcp stk' loc' pc' xcp'* **by**(*simp add: exec-move-FAcc*)
from *True* **have** τ : $\tau \text{move2} (\text{compP2 } P) \ h \ \text{stk} \ (e \cdot F\{D\}) \ pc \ xcp = \tau \text{move2} (\text{compP2 } P) \ h \ \text{stk} \ e \ pc$
xcp **by**(*simp add: \tau move2-iff*)
with *IH*[*OF exec' - - \langle P, h \vdash stk [:\leq] ST \rangle \langle conf-xcp' (compP2 P) h xcp \rangle*] *len bsok* **obtain** *e'' xs''*
where *bisim'*: $P, e, h' \vdash (e'', xs'') \leftrightarrow (stk', loc', pc', xcp')$
and *red*: ?*red* *e' xs e'' xs'' e stk pc pc' xcp xcp'* **by** *auto*
from *bisim'* **have** $P, e \cdot F\{D\}, h' \vdash (e'' \cdot F\{D\}, xs'') \leftrightarrow (stk', loc', pc', xcp')$
by(*rule bisim1-bisims1.bisim1FAcc*)
with *red* τ **show** ?*thesis* **by**(*fastforce intro: FAcc1Red elim!: FAcc-\tau red1r-xt FAcc-\tau red1t-xt simp add: no-call2-def*)
next
case *False*
with *pc* **have** *pc*: *pc* = *length* (*compE2* *e*) **by** *auto*
with *bisim* **obtain** *v* **where** *stk*: *stk* = [*v*] **and** *xcp*: *xcp* = *None*
by(*auto dest: bisim1-pc-length-compE2D*)
with *bisim pc len bsok* **have** $\tau \text{red1r } P \ t \ h \ (e', xs) \ (Val \ v, \ loc)$
by(*auto intro: bisim1-Val-\tau red1r simp add: bsok-def*)
hence $\tau \text{red1r } P \ t \ h \ (e' \cdot F\{D\}, xs) \ (Val \ v \cdot F\{D\}, loc)$ **by**(*rule FAcc-\tau red1r-xt*)
moreover **have** τ : $\neg \tau \text{move2} (\text{compP2 } P) \ h \ [v] \ (e \cdot F\{D\}) \ (\text{length} (\text{compE2 } e)) \ None$ **by**(*simp add: \tau move2-iff*)
moreover **have** $\exists ta' e''. P, e \cdot F\{D\}, h' \vdash (e'', loc) \leftrightarrow (stk', loc', pc', xcp') \wedge True, P, t \vdash 1 \langle Val \ v \cdot F\{D\}, (h, loc) \rangle - ta' \rightarrow \langle e'', (h', loc) \rangle \wedge ta \text{-bisim } wbisim1 \ (extTA2J1 \ P \ ta')$
proof(*cases v = Null*)
case *True* **with** *exec stk xcp pc* **show** ?*thesis*
by(*fastforce elim!: exec-meth.cases simp add: exec-move-def intro: bisim1FAccNull Red1FAccNull*)
next
case *False*
with *exec stk xcp pc* $\langle P, h \vdash stk [:\leq] ST \rangle$
obtain *A* **where** [*simp*]: *v* = *Addr A*
by(*fastforce simp add: exec-move-def exec-meth-instr is-Ref-def compP2-def*)
from *exec False pc stk xcp* **obtain** *v'* **where** *v'*: *heap-read* *h A (CField D F) v' stk' = [v']*
by(*auto simp add: exec-move-def exec-meth-instr*)
have $P, e \cdot F\{D\}, h' \vdash (Val \ v', loc) \leftrightarrow ([v'], loc, \text{length} (\text{compE2} (e \cdot F\{D\})), None)$
by(*rule bisim1Val2*) *simp*
thus ?*thesis* **using** *exec stk xcp pc v'*
by(*fastforce elim!: exec-meth.cases intro: Red1FAcc simp add: exec-move-def ta-upd-simps ta-bisim-def*)
qed
ultimately show ?*thesis* **using** $\tau \ pc \ xcp \ stk$ **by**(*fastforce elim!: rtranclp-trans simp add: no-call2-def*)
qed
next
case (*bisim1FAccThrow* *e n a xs stk loc pc F D*)
note *exec* = $\langle ?exec (e \cdot F\{D\}) \ \text{stk loc pc } [a] \ \text{stk' loc' pc' xcp' \rangle$
note *bisim1* = $\langle P, e, h \vdash (Throw \ a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$

```

from bisim1 have pc: pc < length (compE2 e) by(auto dest: bisim1-ThrowD)
from bisim1 have match-ex-table (compP2 P) (cname-of h a) (0 + pc) (compxE2 e 0 0) = None
  unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
with exec pc have False by(auto elim!: exec-meth.cases simp add: exec-move-def)
thus ?case ..
next
  case (bisim1FAccNull e n F D xs)
  note exec = ⟨?exec (e.F{D}) [Null] xs (length (compE2 e)) [addr-of-sys-xcpt NullPointer] stk' loc'
pc' xcp'⟩
  hence False by(auto elim!: exec-meth.cases dest!: match-ex-table-pc-length-compE2 simp add: exec-move-def)
  thus ?case ..
next
  case (bisim1FAss1 e n e' xs stk loc pc xcp e2 F D)
  note IH1 = bisim1FAss1.IH(2)
  note IH2 = bisim1FAss1.IH(4)
  note exec = ⟨?exec (e.F{D} := e2) stk loc pc xcp stk' loc' pc' xcp'⟩
  note bisim1 = ⟨P, e, h ⊢ (e', xs) ↔ (stk, loc, pc, xcp)⟩
  note bisim2 = ⟨P, e2, h ⊢ (e2, loc) ↔ ([] , loc, 0, None)⟩
  note len = ⟨n + max-vars (e'.F{D} := e2) ≤ length xs⟩
  note bsok = ⟨bsok (e.F{D} := e2) n⟩
  from bisim1 have pc: pc ≤ length (compE2 e) by(rule bisim1-pc-length-compE2)
  show ?case
  proof(cases pc < length (compE2 e))
    case True
    with exec have exec': ?exec e stk loc pc xcp stk' loc' pc' xcp' by(simp add: exec-move-FAss1)
    from True have τ: τmove2 (compP2 P) h stk (e.F{D} := e2) pc xcp = τmove2 (compP2 P) h
stk e pc xcp
      by(simp add: τmove2-iff)
    with IH1[OF exec' - - ⟨P, h ⊢ stk [:≤] ST⟩ ⟨conf-xcp' (compP2 P) h xcp⟩] len bsok obtain e'' xs''
      where bisim': P, e, h' ⊢ (e'', xs'') ↔ (stk', loc', pc', xcp')
      and red: ?red e' xs e'' xs'' e stk pc pc' xcp xcp' by auto
    from bisim' have P, e.F{D} := e2, h' ⊢ (e''.F{D} := e2, xs'') ↔ (stk', loc', pc', xcp')
      by(rule bisim1-bisims1.bisim1FAss1)
    with red τ show ?thesis by(fastforce intro: FAss1Red1 elim!: FAss-τred1r-xt1 FAss-τred1t-xt1 simp
add: no-call2-def)
    next
    case False
    with pc have pc: pc = length (compE2 e) by auto
    with bisim1 obtain v where stk: stk = [v] and xcp: xcp = None
      by(auto dest: bisim1-pc-length-compE2D)
    with bisim1 pc len bsok have rede1': τred1r P t h (e', xs) (Val v, loc)
      by(auto intro: bisim1-Val-τred1r simp add: bsok-def)
    hence τred1r P t h (e'.F{D} := e2, xs) (Val v.F{D} := e2, loc) by(rule FAss-τred1r-xt1)
    moreover from pc exec stk xcp
      have exec': exec-meth-d (compP2 P) (compE2 e @ compE2 e2 @ [Putfield F D, Push Unit])
(compxE2 e 0 0 @ shift (length (compE2 e)) (stack-xlift (length [v]) (compxE2 e2 0 0))) t h ([] @ [v],
loc, length (compE2 e) + 0, None) ta h' (stk', loc', pc', xcp')
      by(simp add: compxE2-size-conv s compxE2-stack-xlift-conv s exec-move-def)
    hence exec-meth-d (compP2 P) (compE2 e2 @ [Putfield F D, Push Unit]) (stack-xlift (length [v])
(compxE2 e2 0 0)) t h ([] @ [v], loc, 0, None) ta h' (stk', loc', pc' - length (compE2 e), xcp')
      by(rule exec-meth-drop-xt) auto
    hence exec-meth-d (compP2 P) (compE2 e2) (stack-xlift (length [v]) (compxE2 e2 0 0)) t h ([] @
[v], loc, 0, None) ta h' (stk', loc', pc' - length (compE2 e), xcp')
      by(rule exec-meth-take) simp

```

with *bisim2* **obtain** *stk''* **where** *stk'*: *stk'* = *stk''* @ [*v*]
and *exec''*: *exec-move-d* *P t e2 h* ([], *loc*, 0, *None*) *ta h'* (*stk''*, *loc'*, *pc'* - *length* (*compE2* *e*), *xcp'*)
unfolding *exec-move-def* **by**(*blast dest: exec-meth-stk-split*)
with *pc xcp* **have** τ : τmove2 (*compP2* *P*) *h* [*v*] (*e*·*F*{*D*} := *e2*) (*length* (*compE2* *e*)) *None* = τmove2 (*compP2* *P*) *h* [] *e2* 0 *None*
using $\tau\text{instr-stk-drop-exec-move}$ [**where** *stk*=[] **and** *vs* = [*v*]] **by**(*simp add: \tau move2-iff*)
from *bisim1* **have** *length xs* = *length loc* **by**(*rule bisim1-length-xs*)
with *IH2*[*OF exec''*, *of* []] *len bsok* **obtain** *e'' xs''*
where *bisim'*: *P, e2, h' ⊢ (e'', xs'') ↔ (stk'', loc', pc' - length (compE2 e), xcp')*
and *red*: $?red$ *e2 loc e'' xs'' e2* [] 0 (*pc'* - *length* (*compE2* *e*)) *None xcp'* **by** *auto*
from *bisim'*
have *P, e·F{D} := e2, h' ⊢ (Val v·F{D} := e'', xs'') ↔ (stk'' @ [v], loc', length (compE2 e) + (pc' - length (compE2 e)), xcp')*
by(*rule bisim1-bisims1.bisim1FAss2*)
moreover from *red* τ
have $?red$ (*Val v·F{D} := e2*) *loc* (*Val v·F{D} := e''*) *xs''* (*e·F{D} := e2*) [*v*] (*length* (*compE2* *e*)) *pc' None xcp'*
by(*fastforce intro: FAss1Red2 elim!: FAss-τred1r-xt2 FAss-τred1t-xt2 split: if-split-asm simp add: no-call2-def*)
moreover from *exec'* **have** *pc' ≥ length (compE2 e)*
by(*rule exec-meth-drop-xt-pc*) *auto*
moreover have *no-call2* (*e·F{D} := e2*) *pc* **using** *pc* **by**(*simp add: no-call2-def*)
ultimately show $?thesis$ **using** τ *stk' pc xcp* *stk* **by**(*fastforce elim!: rtranclp-trans*)
qed
next
case (*bisim1FAss2 e2 n e' xs stk loc pc xcp e F D v*)
note *IH2* = *bisim1FAss2.IH*(2)
note *exec* = $\langle ?exec$ (*e·F{D} := e2*) (*stk* @ [*v*]) *loc* (*length* (*compE2* *e*) + *pc*) *xcp* *stk' loc' pc' xcp'* \rangle
note *bisim2* = $\langle P, e2, h ⊢ (e', xs) ↔ (stk, loc, pc, xcp) \rangle$
note *len* = $\langle n + \text{max-vars} (Val v·F{D} := e') \leq \text{length } xs \rangle$
note *bsok* = $\langle bsok (e·F{D} := e2) n \rangle$
note *ST* = $\langle P, h ⊢ stk @ [v] [:\leq] ST \rangle$
then obtain *T ST'* **where** *ST'*: *P, h ⊢ stk [:\leq] ST'* **and** *T*: *typeof_h v* = [*T*]
by(*auto simp add: list-all2-append1 list-all2-Cons1 conf-def*)

from *bisim2* **have** *pc*: *pc* ≤ *length (compE2 e2)* **by**(*rule bisim1-pc-length-compE2*)
show $?case$
proof(*cases pc < length (compE2 e2)*)
case *True*
from *exec* **have** *exec'*: *exec-meth-d* (*compP2* *P*) (*compE2* *e* @ *compE2* *e2* @ [*Putfield F D, Push Unit*]) (*compxE2* *e* 0 0 @ *shift* (*length* (*compE2* *e*)) (*stack-xlift* (*length* [*v*]) (*compxE2* *e2* 0 0))) *t h* (*stk* @ [*v*], *loc*, *length* (*compE2* *e*) + *pc*, *xcp*) *ta h'* (*stk'*, *loc'*, *pc'*, *xcp'*)
by(*simp add: shift-compxE2 stack-xlift-compxE2 exec-move-def*)
hence *exec-meth-d* (*compP2* *P*) (*compE2* *e2* @ [*Putfield F D, Push Unit*]) (*stack-xlift* (*length* [*v*]) (*compxE2* *e2* 0 0)) *t h* (*stk* @ [*v*], *loc*, *pc*, *xcp*) *ta h'* (*stk'*, *loc'*, *pc'* - *length* (*compE2* *e*), *xcp'*)
by(*rule exec-meth-drop-xt*) *auto*
hence *exec-meth-d* (*compP2* *P*) (*compE2* *e2*) (*stack-xlift* (*length* [*v*]) (*compxE2* *e2* 0 0)) *t h* (*stk* @ [*v*], *loc*, *pc*, *xcp*) *ta h'* (*stk'*, *loc'*, *pc'* - *length* (*compE2* *e*), *xcp'*)
using *True* **by**(*rule exec-meth-take*)
with *bisim2* **obtain** *stk''* **where** *stk'*: *stk'* = *stk''* @ [*v*]
and *exec''*: *exec-move-d* *P t e2 h* (*stk*, *loc*, *pc*, *xcp*) *ta h'* (*stk''*, *loc'*, *pc'* - *length* (*compE2* *e*), *xcp'*)
unfolding *exec-move-def* **by**(*blast dest: exec-meth-stk-split*)

with *True* **have** $\tau: \tau \text{move2} (\text{compP2 } P) h (\text{stk} @ [v]) (e \cdot F\{D\} := e2) (\text{length} (\text{compE2 } e) + pc)$
 $xcp = \tau \text{move2} (\text{compP2 } P) h \text{stk } e2 \text{pc } xcp$
by(*auto simp add: $\tau \text{move2-iff } \tau \text{instr-stk-drop-exec-move}$*)
moreover from $IH2[OF \text{exec}'' - - ST' \langle \text{conf-xcp}' (\text{compP2 } P) h xcp \rangle] \text{len } \text{bsok}$ **obtain** $e'' \text{xs}''$
where $\text{bisim}' : P, e2, h' \vdash (e'', \text{xs}'') \leftrightarrow (\text{stk}'', \text{loc}', \text{pc}' - \text{length} (\text{compE2 } e), xcp')$
and $\text{red} : ?\text{red } e' \text{xs } e'' \text{xs}'' e2 \text{stk } pc (\text{pc}' - \text{length} (\text{compE2 } e)) xcp xcp'$ **by** *auto*
from bisim' **have** $P, e \cdot F\{D\} := e2, h' \vdash (\text{Val } v \cdot F\{D\} := e'', \text{xs}'') \leftrightarrow (\text{stk}'' @ [v], \text{loc}', \text{length} (\text{compE2 } e) + (\text{pc}' - \text{length} (\text{compE2 } e)), xcp')$
by(*rule bisim1-bisims1.bisim1FAss2*)
moreover from exec' **have** $\text{pc}' \geq \text{length} (\text{compE2 } e)$
by(*rule exec-meth-drop-xt-pc*) *auto*
ultimately show $?thesis$ **using** $\text{red } \tau \text{stk}' \text{True}$
by(*fastforce intro: FAss1Red2 elim!: FAss- τ red1r-xt2 FAss- τ red1t-xt2 split: if-split-asm simp add: no-call2-def*)
next
case *False*
with pc **have** $[simp]: pc = \text{length} (\text{compE2 } e2)$ **by** *simp*
with $\text{bisim}2$ **obtain** $v2$ **where** $\text{stk} : \text{stk} = [v2]$ **and** $xcp : xcp = \text{None}$
by(*auto dest: bisim1-pc-length-compE2D*)
with $\text{bisim}2$ pc $\text{len } \text{bsok}$ **have** $\text{red} : \tau \text{red1r } P t h (e', \text{xs}) (\text{Val } v2, \text{loc})$
by(*auto intro: bisim1-Val- τ red1r simp add: bsok-def*)
hence $\tau \text{red1r } P t h (\text{Val } v \cdot F\{D\} := e', \text{xs}) (\text{Val } v \cdot F\{D\} := \text{Val } v2, \text{loc})$ **by**(*rule FAss- τ red1r-xt2*)
moreover have $\tau: \neg \tau \text{move2} (\text{compP2 } P) h [v2, v] (e \cdot F\{D\} := e2) (\text{length} (\text{compE2 } e) + \text{length} (\text{compE2 } e2)) \text{None}$ **by**(*simp add: $\tau \text{move2-iff}$*)
moreover
have $\exists ta' e''. P, e \cdot F\{D\} := e2, h' \vdash (e'', \text{loc}) \leftrightarrow (\text{stk}', \text{loc}', \text{pc}', xcp') \wedge \text{True}, P, t \vdash 1 \langle \text{Val } v \cdot F\{D\} := \text{Val } v2, (h, \text{loc}) \rangle - ta' \rightarrow \langle e'', (h', \text{loc}) \rangle \wedge ta\text{-bisim } \text{wbisim1} (\text{extTA2J1 } P ta') ta$
proof(*cases v = Null*)
case *True* **with** $\text{exec } \text{stk } xcp$ **show** $?thesis$
by(*fastforce elim!: exec-meth.cases simp add: exec-move-def intro: bisim1FAssNull Red1FAssNull*)
next
case *False* **with** $\text{exec } \text{stk } xcp T$ **show** $?thesis$
by(*fastforce simp add: exec-move-def compP2-def exec-meth-instr is-Ref-def ta-upd-simps ta-bisim-def intro: bisim1FAss3 Red1FAss*)
qed
ultimately show $?thesis$ **using** $\text{exec } xcp \text{stk}$ **by**(*fastforce simp add: no-call2-def*)
qed
next
case ($\text{bisim1FAssThrow1 } e n a \text{xs } \text{stk } \text{loc } pc e2 F D$)
note $\text{exec} = \langle ?\text{exec } (e \cdot F\{D\} := e2) \text{stk } \text{loc } pc [a] \text{stk}' \text{loc}' \text{pc}' xcp' \rangle$
note $\text{bisim1} = \langle P, e, h \vdash (\text{Throw } a, \text{xs}) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a]) \rangle$
from bisim1 **have** $pc : pc < \text{length} (\text{compE2 } e)$ **by**(*auto dest: bisim1-ThrowD*)
from bisim1 **have** $\text{match-ex-table} (\text{compP2 } P) (\text{cname-of } h a) (0 + pc) (\text{compxE2 } e 0 0) = \text{None}$
unfolding compP2-def **by**(*rule bisim1-xcp-Some-not-caught*)
with $\text{exec } pc$ **have** *False*
by(*auto elim!: exec-meth.cases simp add: exec-move-def match-ex-table-not-pcs-None*)
thus $?case \dots$
next
case ($\text{bisim1FAssThrow2 } e2 n a \text{xs } \text{stk } \text{loc } pc e F D v$)
note $\text{exec} = \langle ?\text{exec } (e \cdot F\{D\} := e2) (\text{stk} @ [v]) \text{loc} (\text{length} (\text{compE2 } e) + pc) [a] \text{stk}' \text{loc}' \text{pc}' xcp' \rangle$
note $\text{bisim2} = \langle P, e2, h \vdash (\text{Throw } a, \text{xs}) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a]) \rangle$
hence $\text{match-ex-table} (\text{compP2 } P) (\text{cname-of } h a) (\text{length} (\text{compE2 } e) + pc) (\text{compxE2 } e2 (\text{length} (\text{compE2 } e)) 0) = \text{None}$
unfolding compP2-def **by**(*rule bisim1-xcp-Some-not-caught*)

with *exec* **have** *False*
by(*auto elim!*: *exec-meth.cases simp add: compxE2-stack-xlift-conv* *exec-move-def*)(*auto dest!*:
match-ex-table-stack-xliftD simp add: match-ex-table-append-not-pcs)
thus *?case ..*
next
case (*bisim1FAssNull e n e2 F D xs v*)
note *exec = ⟨?exec (e·F{D} := e2) [v, Null] xs (length (compE2 e) + length (compE2 e2))*
[addr-of-sys-xcpt NullPointer] stk' loc' pc' xcp'⟩
hence *False*
by(*auto elim!*: *exec-meth.cases simp add: match-ex-table-append-not-pcs compxE2-size-conv* *exec-move-def*
dest!: *match-ex-table-shift-pcD match-ex-table-pc-length-compE2*)
thus *?case ..*
next
case (*bisim1FAss3 e n e2 F D xs*)
have *P, e·F{D} := e2, h ⊢ (unit, xs) ↔ ([Unit], xs, length (compE2 (e·F{D} := e2)), None)* **by**(*rule*
bisim1Val2) *simp*
moreover **have** *τmove2 (compP2 P) h [] (e·F{D} := e2) (Suc (length (compE2 e) + length (compE2*
e2))) None **by**(*simp add: τmove2-iff*)
moreover **note** *⟨?exec (e·F{D} := e2) [] xs (Suc (length (compE2 e) + length (compE2 e2))) None*
stk' loc' pc' xcp'⟩
ultimately **show** *?case*
by(*fastforce elim!*: *exec-meth.cases simp add: ac-simps exec-move-def*)
next
case (*bisim1CAS1 a n a' xs stk loc pc xcp i e D F*)
note *IH1 = bisim1CAS1.IH(2)*
note *IH2 = bisim1CAS1.IH(4)*
note *exec = ⟨?exec (a·compareAndSwap(D·F, i, e)) stk loc pc xcp stk' loc' pc' xcp'⟩*
note *bisim1 = ⟨P, a, h ⊢ (a', xs) ↔ (stk, loc, pc, xcp)⟩*
note *bisim2 = ⟨P, i, h ⊢ (i, loc) ↔ ([], loc, 0, None)⟩*
note *len = ⟨n + max-vars - ≤ length xs⟩*
note *bsok = ⟨bsok (a·compareAndSwap(D·F, i, e)) n⟩*
from *bisim1* **have** *pc: pc ≤ length (compE2 a)* **by**(*rule bisim1-pc-length-compE2*)
show *?case*
proof(*cases pc < length (compE2 a)*)
case *True*
with *exec* **have** *exec': ?exec a stk loc pc xcp stk' loc' pc' xcp'* **by**(*simp add: exec-move-CAS1*)
from *True* **have** *τ: τmove2 (compP2 P) h stk (a·compareAndSwap(D·F, i, e)) pc xcp = τmove2*
(compP2 P) h stk a pc xcp **by**(*simp add: τmove2-iff*)
with *IH1[OF exec' - - ⟨P, h ⊢ stk [::≤] ST⟩ ⟨conf-xcp' (compP2 P) h xcp⟩ len bsok]* **obtain** *e'' xs''*
where *bisim': P, a, h' ⊢ (e'', xs'') ↔ (stk', loc', pc', xcp')*
and *red: ?red a' xs e'' xs'' a stk pc pc' xcp xcp'* **by** *auto*
from *bisim'* **have** *P, a·compareAndSwap(D·F, i, e), h' ⊢ (e''·compareAndSwap(D·F, i, e), xs'') ↔*
(stk', loc', pc', xcp')
by(*rule bisim1-bisims1.bisim1CAS1*)
moreover **from** *True* **have** *no-call2 (a·compareAndSwap(D·F, i, e)) pc = no-call2 a pc* **by**(*simp*
add: no-call2-def)
ultimately **show** *?thesis using red τ* **by**(*fastforce intro: CAS1Red1 elim!*: *CAS-τred1r-xt1 CAS-τred1t-xt1*)
next
case *False*
with *pc* **have** *pc: pc = length (compE2 a)* **by** *auto*
with *bisim1* **obtain** *v* **where** *a': is-val a' ⟶ a' = Val v*
and *stk: stk = [v]* **and** *xcp: xcp = None* **and** *call: call1 a' = None*
by(*auto dest: bisim1-pc-length-compE2D*)
with *bisim1 pc len bsok* **have** *rede1': τred1r P t h (a', xs) (Val v, loc)*

by(*auto intro: bisim1-Val- τ red1r simp add: bsok-def*)
hence τ red1r P t h ($a \cdot \text{compareAndSwap}(D \cdot F, i, e), xs$) ($\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, i, e), loc$)
by(*rule CAS- τ red1r-xt1*)
moreover from pc $exec$ stk xcp
have $exec'$: $exec\text{-meth-d}$ ($compP2$ P) ($compE2$ a @ $compE2$ i @ $compE2$ e @ [CAS F D]) ($compxE2$ a 0 0 @ $shift$ ($length$ ($compE2$ a)) ($stack\text{-xlift}$ ($length$ [v]) ($compxE2$ i 0 0) @ $shift$ ($length$ ($compE2$ i)) ($compxE2$ e 0 (Suc (Suc 0)))))) t h ($[]$ @ [v], loc , $length$ ($compE2$ a) + 0 , $None$) ta h' (stk' , loc' , pc' , xcp')
by(*simp add: compxE2-size-convs compxE2-stack-xlift-convs exec-move-def*)
hence $exec\text{-meth-d}$ ($compP2$ P) ($compE2$ i @ $compE2$ e @ [CAS F D]) ($stack\text{-xlift}$ ($length$ [v]) ($compxE2$ i 0 0) @ $shift$ ($length$ ($compE2$ i)) ($compxE2$ e 0 (Suc (Suc 0)))))) t h ($[]$ @ [v], loc , 0 , $None$) ta h' (stk' , loc' , $pc' - length$ ($compE2$ a), xcp')
by(*rule exec-meth-drop-xt*) *auto*
hence $exec\text{-meth-d}$ ($compP2$ P) ($compE2$ i) ($stack\text{-xlift}$ ($length$ [v]) ($compxE2$ i 0 0)) t h ($[]$ @ [v], loc , 0 , $None$) ta h' (stk' , loc' , $pc' - length$ ($compE2$ a), xcp')
by(*rule exec-meth-take-xt*) *simp*
with $bisim2$ **obtain** stk'' **where** stk' : $stk' = stk''$ @ [v]
and $exec''$: $exec\text{-move-d}$ P t i h ($[]$, loc , 0 , $None$) ta h' (stk'' , loc' , $pc' - length$ ($compE2$ a), xcp')
unfolding $exec\text{-move-def}$ **by**(*blast dest: exec-meth-stk-split*)
with pc xcp **have** τ : τmove2 ($compP2$ P) h [v] ($a \cdot \text{compareAndSwap}(D \cdot F, i, e)$) ($length$ ($compE2$ a)) $None = \tau\text{move2}$ ($compP2$ P) h $[]$ i 0 $None$)
using $\tau\text{instr-stk-drop-exec-move}$ **where** $stk = []$ **and** $vs = [v]$
by(*auto simp add: $\tau\text{move2-iff}$*)
from $bisim1$ **have** $length$ $xs = length$ loc **by**(*rule bisim1-length-xs*)
with $IH2$ [OF $exec'$] len $bsok$ **obtain** e'' xs''
where $bisim'$: $P, i, h' \vdash (e'', xs'') \leftrightarrow (stk'', loc', pc' - length$ ($compE2$ a), $xcp')$
and red : $?red$ i loc e'' xs'' i $[]$ 0 ($pc' - length$ ($compE2$ a)) $None$ xcp' **by** *fastforce*
from $bisim'$
have $P, a \cdot \text{compareAndSwap}(D \cdot F, i, e), h' \vdash (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, e'', e), xs'') \leftrightarrow (stk''$ @ [v], loc' , $length$ ($compE2$ a) + ($pc' - length$ ($compE2$ a)), $xcp')$
by(*rule bisim1-bisims1.bisim1CAS2*)
moreover from red τ **have** $?red$ ($\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, i, e)$) loc ($\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, e'', e)$) xs'' ($a \cdot \text{compareAndSwap}(D \cdot F, i, e)$) [v] ($length$ ($compE2$ a)) pc' $None$ xcp'
by(*fastforce intro: CAS1Red2 elim!: CAS- τ red1r-xt2 CAS- τ red1t-xt2 split: if-split-asm simp add: no-call2-def*)
moreover from $exec'$ **have** $pc' \geq length$ ($compE2$ a)
by(*rule exec-meth-drop-xt-pc*) *auto*
moreover have $no\text{-call2}$ ($a \cdot \text{compareAndSwap}(D \cdot F, i, e)$) pc **using** pc **by**(*simp add: no-call2-def*)
ultimately show $?thesis$ **using** τ stk' pc xcp stk **by**(*fastforce elim!: rtranclp-trans*)
qed
next
case ($bisim1CAS2$ i n i' xs stk loc pc xcp a e D F v)
note $IH2 = bisim1CAS2.IH(2)$
note $IH3 = bisim1CAS2.IH(6)$
note $exec = \langle ?exec$ ($a \cdot \text{compareAndSwap}(D \cdot F, i, e)$) (stk @ [v]) loc ($length$ ($compE2$ a) + pc) xcp stk' loc' pc' xcp' \rangle
note $bisim2 = \langle P, i, h \vdash (i', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
note $bisim3 = \langle P, e, h \vdash (e, loc) \leftrightarrow ([] , loc, 0, None) \rangle$
note $len = \langle n + max\text{-vars}$ ($\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, i', e)$) $\leq length$ $xs \rangle$
note $bsok = \langle bsok$ ($a \cdot \text{compareAndSwap}(D \cdot F, i, e)$) $n \rangle$
from $bisim2$ **have** pc : $pc \leq length$ ($compE2$ i) **by**(*rule bisim1-pc-length-compE2*)
show $?case$
proof(*cases* $pc < length$ ($compE2$ i))
case *True*

from *exec* **have** *exec'*: *exec-meth-d* (*compP2 P*) (*compE2 a @ compE2 i @ compE2 e @ [CAS F D]*) (*compxE2 a 0 0 @ shift* (*length* (*compE2 a*)) (*stack-xlift* (*length* [*v*] (*compxE2 i 0 0*) @ *shift* (*length* (*compE2 i*)) (*compxE2 e 0 (Suc (Suc 0))*))) *t h* (*stk @ [v]*, *loc*, *length* (*compE2 a*) + *pc*, *xcp*) *ta h'* (*stk'*, *loc'*, *pc'*, *xcp'*)

by(*simp add: shift-compxE2 stack-xlift-compxE2 ac-simps exec-move-def*)

hence *exec-meth-d* (*compP2 P*) (*compE2 i @ compE2 e @ [CAS F D]*) (*stack-xlift* (*length* [*v*] (*compxE2 i 0 0*) @ *shift* (*length* (*compE2 i*)) (*compxE2 e 0 (Suc (Suc 0))*))) *t h* (*stk @ [v]*, *loc*, *pc*, *xcp*) *ta h'* (*stk'*, *loc'*, *pc' - length* (*compE2 a*), *xcp'*)

by(*rule exec-meth-drop-xt*) *auto*

hence *exec-meth-d* (*compP2 P*) (*compE2 i*) (*stack-xlift* (*length* [*v*] (*compxE2 i 0 0*))) *t h* (*stk @ [v]*, *loc*, *pc*, *xcp*) *ta h'* (*stk'*, *loc'*, *pc' - length* (*compE2 a*), *xcp'*)

using *True* **by**(*rule exec-meth-take-xt*)

with *bisim2* **obtain** *stk''* **where** *stk'*: *stk' = stk'' @ [v]*

and *exec''*: *exec-move-d P t i h* (*stk*, *loc*, *pc*, *xcp*) *ta h'* (*stk''*, *loc'*, *pc' - length* (*compE2 a*), *xcp'*)

unfolding *exec-move-def* **by**(*blast dest: exec-meth-stk-split*)

with *True* **have** τ : τmove2 (*compP2 P*) *h* (*stk @ [v]*) (*a.compareAndSwap(D.F, i, e)*) (*length* (*compE2 a*) + *pc*) *xcp = \tau\text{move2}* (*compP2 P*) *h* *stk i pc xcp*

by(*auto simp add: \tau\text{move2-iff} \tau\text{instr-stk-drop-exec-move}*)

moreover from $\langle P, h \vdash \text{stk} @ [v] [:\leq] ST \rangle$ **obtain** *ST2* **where** $P, h \vdash \text{stk} [:\leq] ST2$ **by**(*auto simp add: list-all2-append1*)

from *IH2[OF exec'' - - this \langle conf-xcp' (compP2 P) h xcp \rangle len bsok]* **obtain** *e'' xs''*

where *bisim'*: $P, i, h' \vdash (e'', xs'') \leftrightarrow (\text{stk}'', \text{loc}', \text{pc}' - \text{length}(\text{compE2 } a), \text{xcp}')$

and *red*: $?red\ i' xs\ e'' xs''\ i\ \text{stk}\ \text{pc}\ (\text{pc}' - \text{length}(\text{compE2 } a))\ \text{xcp}\ \text{xcp}'$ **by** *fastforce*

from *bisim'*

have $P, a.\text{compareAndSwap}(D.F, i, e), h' \vdash (\text{Val } v.\text{compareAndSwap}(D.F, e'', e), \text{xs}'') \leftrightarrow (\text{stk}'' @ [v], \text{loc}', \text{length}(\text{compE2 } a) + (\text{pc}' - \text{length}(\text{compE2 } a)), \text{xcp}')$

by(*rule bisim1-bisims1.bisim1CAS2*)

moreover from *exec'* **have** $\text{pc}' \geq \text{length}(\text{compE2 } a)$

by(*rule exec-meth-drop-xt-pc*) *auto*

moreover have *no-call2 i pc* \implies *no-call2* (*a.compareAndSwap(D.F, i, e)*) (*length* (*compE2 a*) + *pc*) **by**(*simp add: no-call2-def*)

ultimately show *?thesis* **using** *red \tau stk' True*

by(*fastforce intro: CAS1Red2 elim!: CAS-\tau\text{red1r-xt2} CAS-\tau\text{red1t-xt2} split: if-split-asm*)

next

case *False*

with *pc* **have** [*simp*]: $\text{pc} = \text{length}(\text{compE2 } i)$ **by** *simp*

with *bisim2* **obtain** *v2* **where** *i'*: *is-val i' \longrightarrow i' = Val v2*

and *stk*: *stk = [v2]* **and** *xcp*: *xcp = None* **and** *call*: *call1 i' = None*

by(*auto dest: bisim1-pc-length-compE2D*)

with *bisim2* *pc len bsok* **have** *red*: $\tau\text{red1r } P\ t\ h\ (i', \text{xs})\ (\text{Val } v2, \text{loc})$

by(*auto intro: bisim1-Val-\tau\text{red1r} simp add: bsok-def*)

hence $\tau\text{red1r } P\ t\ h\ (\text{Val } v.\text{compareAndSwap}(D.F, i', e), \text{xs})\ (\text{Val } v.\text{compareAndSwap}(D.F, \text{Val } v2, e), \text{loc})$ **by**(*rule CAS-\tau\text{red1r-xt2}*)

moreover from *pc exec stk xcp*

have *exec'*: *exec-meth-d* (*compP2 P*) ((*compE2 a @ compE2 i*) @ *compE2 e @ [CAS F D]*) ((*compxE2 a 0 0 @ compxE2 i* (*length* (*compE2 a*)) (*Suc 0*)) @ *shift* (*length* (*compE2 a @ compE2 i*)) (*stack-xlift* (*length* [*v2*, *v*] (*compxE2 e 0 0*))) *t h* ($[\] @ [v2, v]$, *loc*, *length* (*compE2 a @ compE2 i*) + *0*, *None*) *ta h'* (*stk'*, *loc'*, *pc'*, *xcp'*)

by(*simp add: compxE2-size-conv s compxE2-stack-xlift-conv s exec-move-def*)

hence *exec-meth-d* (*compP2 P*) (*compE2 e @ [CAS F D]*) (*stack-xlift* (*length* [*v2*, *v*] (*compxE2 e 0 0*))) *t h* ($[\] @ [v2, v]$, *loc*, *0*, *None*) *ta h'* (*stk'*, *loc'*, *pc' - length* (*compE2 a @ compE2 i*), *xcp'*)

by(*rule exec-meth-drop-xt*) *auto*

hence *exec-meth-d* (*compP2 P*) (*compE2 e*) (*stack-xlift* (*length* [*v2*, *v*] (*compxE2 e 0 0*))) *t h* ($[\] @ [v2, v]$, *loc*, *0*, *None*) *ta h'* (*stk'*, *loc'*, *pc' - length* (*compE2 a @ compE2 i*), *xcp'*)

by(rule *exec-meth-take*) *simp*
with *bisim3* **obtain** *stk''* **where** *stk'*: *stk' = stk'' @ [v2, v]*
and *exec''*: *exec-move-d P t e h ([], loc, 0, None) ta h' (stk'', loc', pc' - length (compE2 a @ compE2 i), xcp')*
unfolding *exec-move-def* **by**(blast dest: *exec-meth-stk-split*)
with *pc xcp* **have** τ : $\tau \text{move2 (compP2 P) h [v2, v] (a \cdot \text{compareAndSwap}(D \cdot F, i, e)) (\text{length (compE2 a)} + \text{length (compE2 i)}) \text{None} = \tau \text{move2 (compP2 P) h [] e 0 None}$
using $\tau \text{instr-stk-drop-exec-move}$ **where** *stk* = [] **and** *vs* = [v2, v] **by**(*simp add: \tau move2-iff*)
from *bisim2* **have** *length xs = length loc* **by**(rule *bisim1-length-xs*)
with *IH3[OF exec'', of []] len bsok* **obtain** *e'' xs''*
where *bisim'*: $P, e, h' \vdash (e'', xs'') \leftrightarrow (stk'', loc', pc' - \text{length (compE2 a)} - \text{length (compE2 i)}, xcp')$
and *red*: $?red e loc e'' xs'' e [] 0 (pc' - \text{length (compE2 a)} - \text{length (compE2 i)}) \text{None } xcp'$
by *auto (fastforce simp only: length-append diff-diff-left)*
from *bisim'*
have $P, a \cdot \text{compareAndSwap}(D \cdot F, i, e), h' \vdash (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v2, e''), xs'') \leftrightarrow (stk'' @ [v2, v], loc', \text{length (compE2 a)} + \text{length (compE2 i)} + (pc' - \text{length (compE2 a)} - \text{length (compE2 i)}), xcp')$
by(rule *bisim1-bisims1.bisim1CAS3*)
moreover from *red \tau*
have $?red (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v2, e)) loc (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v2, e'')) xs'' (a \cdot \text{compareAndSwap}(D \cdot F, i, e) [v2, v] (\text{length (compE2 a)} + \text{length (compE2 i)}) pc' \text{None } xcp'$
by(*fastforce intro: CAS1Red3 elim!: CAS-\tau red1r-xt3 CAS-\tau red1t-xt3 split: if-split-asm simp add: no-call2-def*)
moreover from *exec'* **have** $pc' \geq \text{length (compE2 a @ compE2 i)}$
by(rule *exec-meth-drop-xt-pc*) *auto*
moreover have *no-call2 (a \cdot compareAndSwap(D \cdot F, i, e)) (\text{length (compE2 a)} + pc)* **by**(*simp add: no-call2-def*)
ultimately show *?thesis* **using** τ *stk' pc xcp stk* **by**(*fastforce elim!: rtranclp-trans*)
qed
next
case (*bisim1CAS3 e n e' xs stk loc pc xcp a i D F v v'*)
note *IH3 = bisim1CAS3.IH(2)*
note *exec = \langle ?exec (a \cdot compareAndSwap(D \cdot F, i, e)) (stk @ [v', v]) loc (\text{length (compE2 a)} + \text{length (compE2 i)} + pc) xcp stk' loc' pc' xcp \rangle*
note *bisim3 = \langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle*
note *len = \langle n + max-vars - \leq \text{length } xs \rangle*
note *bsok = \langle bsok (a \cdot compareAndSwap(D \cdot F, i, e)) n \rangle*
from $\langle P, h \vdash stk @ [v', v] [:\leq] ST \rangle$ **obtain** $T T' ST'$
where [*simp*]: $ST = ST' @ [T', T]$
and *wtv*: $P, h \vdash v : \leq T$ **and** *wtv'*: $P, h \vdash v' : \leq T'$ **and** *ST'*: $P, h \vdash stk [:\leq] ST'$
by(*auto simp add: list-all2-Cons1 list-all2-append1*)
from *bisim3* **have** $pc: pc \leq \text{length (compE2 e)}$ **by**(rule *bisim1-pc-length-compE2*)
show *?case*
proof(*cases pc < length (compE2 e)*)
case *True*
from *exec* **have** *exec'*: *exec-meth-d (compP2 P) ((compE2 a @ compE2 i) @ compE2 e @ [CAS F D]) ((compE2 a 0 0 @ compE2 i (\text{length (compE2 a)}) (Suc 0)) @ shift (\text{length (compE2 a @ compE2 i)}) (stack-xlift (\text{length [v', v]} (compE2 e 0 0))) t h (stk @ [v', v], loc, \text{length (compE2 a @ compE2 i)} + pc, xcp) ta h' (stk', loc', pc', xcp')*
by(*simp add: shift-compE2 stack-xlift-compE2 exec-move-def*)
hence *exec-meth-d (compP2 P) (compE2 e @ [CAS F D]) (stack-xlift (\text{length [v', v]} (compE2 e 0 0)) t h (stk @ [v', v], loc, pc, xcp) ta h' (stk', loc', pc' - \text{length (compE2 a @ compE2 i)}, xcp')*
by(rule *exec-meth-drop-xt*) *auto*

hence $exec\text{-}meth\text{-}d$ ($compP2$ P) ($compE2$ e) ($stack\text{-}xlift$ ($length$ [v' , v]) ($compE2$ e 0 0)) t h (stk @ [v' , v], loc , pc , xcp) ta h' (stk' , loc' , $pc' - length$ ($compE2$ a @ $compE2$ i), xcp')
using $True$ **by**($rule$ $exec\text{-}meth\text{-}take$)
with $bisim3$ **obtain** stk'' **where** $stk': stk' = stk''$ @ [v' , v]
and $exec''$: $exec\text{-}move\text{-}d$ P t e h (stk , loc , pc , xcp) ta h' (stk'' , loc' , $pc' - length$ ($compE2$ a @ $compE2$ i), xcp')
unfolding $exec\text{-}move\text{-}def$ **by**($blast$ $dest$: $exec\text{-}meth\text{-}stk\text{-}split$)
with $True$ **have** τ : $\tau move2$ ($compP2$ P) h (stk @ [v' , v]) ($a \cdot compareAndSwap(D \cdot F, i, e)$) ($length$ ($compE2$ a) + $length$ ($compE2$ i) + pc) $xcp = \tau move2$ ($compP2$ P) h stk e pc xcp
by($auto$ $simp$ add : $\tau move2\text{-}iff$ $\tau instr\text{-}stk\text{-}drop\text{-}exec\text{-}move$)
moreover from $IH3[OF$ $exec'' - - ST' \langle conf\text{-}xcp' (compP2 P) h xcp \rangle$ len $bsok$ **obtain** e'' xs''
where $bisim'$: $P, e, h' \vdash (e'', xs'') \leftrightarrow (stk'', loc', pc' - length$ ($compE2$ a) - $length$ ($compE2$ i), $xcp')$
and red : $?red$ $e' xs e'' xs'' e$ stk pc ($pc' - length$ ($compE2$ a) - $length$ ($compE2$ i)) xcp xcp'
by $auto$ ($fastforce$ $simp$ $only$: $length\text{-}append$ $diff\text{-}diff\text{-}left$)
from $bisim'$
have $P, a \cdot compareAndSwap(D \cdot F, i, e), h' \vdash (Val$ $v \cdot compareAndSwap(D \cdot F, Val$ $v', e''), xs'') \leftrightarrow (stk''$ @ [v' , v], loc' , $length$ ($compE2$ a) + $length$ ($compE2$ i) + ($pc' - length$ ($compE2$ a) - $length$ ($compE2$ i)), $xcp')$
by($rule$ $bisim1\text{-}bisims1.bisim1CAS3$)
moreover from $exec'$ **have** $pc' \geq length$ ($compE2$ a @ $compE2$ i)
by($rule$ $exec\text{-}meth\text{-}drop\text{-}xt\text{-}pc$) $auto$
moreover have $no\text{-}call2$ e $pc \implies no\text{-}call2$ ($a \cdot compareAndSwap(D \cdot F, i, e)$) ($length$ ($compE2$ a) + $length$ ($compE2$ i) + pc)
by($simp$ add : $no\text{-}call2\text{-}def$)
ultimately show $?thesis$ **using** red τ stk' $True$
by($fastforce$ $intro$: $CAS1Red3$ $elim!$: $CAS\text{-}\tau red1r\text{-}xt3$ $CAS\text{-}\tau red1t\text{-}xt3$ $split$: $if\text{-}split\text{-}asm$)
next
case $False$
with pc **have** [$simp$]: $pc = length$ ($compE2$ e) **by** $simp$
with $bisim3$ **obtain** $v2$ **where** stk : $stk = [v2]$ **and** xcp : $xcp = None$
by($auto$ $dest$: $bisim1\text{-}pc\text{-}length\text{-}compE2D$)
with $bisim3$ pc len $bsok$ **have** red : $\tau red1r$ P t h (e' , xs) (Val $v2$, loc)
by($auto$ $intro$: $bisim1\text{-}Val\text{-}\tau red1r$ $simp$ add : $bsok\text{-}def$)
hence $\tau red1r$ P t h (Val $v \cdot compareAndSwap(D \cdot F, Val$ $v', e')$, xs) (Val $v \cdot compareAndSwap(D \cdot F, Val$ v', Val $v2$), loc) **by**($rule$ $CAS\text{-}\tau red1r\text{-}xt3$)
moreover have τ : $\neg \tau move2$ ($compP2$ P) h [$v2$, v', v] ($a \cdot compareAndSwap(D \cdot F, i, e)$) ($length$ ($compE2$ a) + $length$ ($compE2$ i) + $length$ ($compE2$ e)) $None$
by($simp$ add : $\tau move2\text{-}iff$)
moreover
have $\exists ta' e''$. $P, a \cdot compareAndSwap(D \cdot F, i, e), h' \vdash (e'', loc) \leftrightarrow (stk', loc', pc', xcp') \wedge True, P, t \vdash 1 \langle Val$ $v \cdot compareAndSwap(D \cdot F, Val$ v', Val $v2), (h, loc) \rangle - ta' \rightarrow \langle e'', (h', loc) \rangle \wedge ta\text{-}bisim1$ ($extTA2J1$ P ta') ta
proof($cases$ $v = Null$)
case $True$ **with** $exec$ stk xcp **show** $?thesis$
by($fastforce$ $elim!$: $exec\text{-}meth.cases$ $simp$ add : $exec\text{-}move\text{-}def$ $intro$: $bisim1CASFail$ $CAS1Null$)
next
case $False$
have $P, a \cdot compareAndSwap(D \cdot F, i, e), h' \vdash (Val$ ($Bool$ b), loc) $\leftrightarrow ([Bool$ $b], loc, length$ ($compE2$ ($a \cdot compareAndSwap(D \cdot F, i, e)$)), $None)$ **for** b
by($rule$ $bisim1Val2$) $simp$
with $False$ $exec$ stk xcp **show** $?thesis$
by ($auto$ $elim!$: $exec\text{-}meth.cases$ $simp$ add : $exec\text{-}move\text{-}def$ $is\text{-}Ref\text{-}def$ $intro$: $Red1CASSucceed$ $Red1CASFail$)

```

      (fastforce intro!: Red1CASSucceed Red1CASFail simp add: ta-bisim-def ac-simps)+
    qed
    ultimately show ?thesis using exec xcp stk by(fastforce simp add: no-call2-def)
  qed
next
case (bisim1CASThrow1 A n a xs stk loc pc i e D F)
note exec = ⟨?exec (A.compareAndSwap(D·F, i, e)) stk loc pc [a] stk' loc' pc' xcp'⟩
note bisim1 = ⟨P,A,h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
from bisim1 have pc: pc < length (compE2 A) by(auto dest: bisim1-ThrowD)
from bisim1 have match-ex-table (compP2 P) (cname-of h a) (0 + pc) (compxE2 A 0 0) = None
  unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
with exec pc have False
  by(auto elim!: exec-meth.cases simp add: match-ex-table-not-pcs-None exec-move-def)
thus ?case ..
next
case (bisim1CASThrow2 i n a xs stk loc pc A e D F v)
note exec = ⟨?exec (A.compareAndSwap(D·F, i, e)) (stk @ [v]) loc (length (compE2 A) + pc) [a]
stk' loc' pc' xcp'⟩
note bisim2 = ⟨P,i,h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
from bisim2 have pc: pc < length (compE2 i) by(auto dest: bisim1-ThrowD)
from bisim2 have match-ex-table (compP2 P) (cname-of h a) (length (compE2 A) + pc) (compxE2
i (length (compE2 A)) 0) = None
  unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
with exec pc have False
  apply(auto elim!: exec-meth.cases simp add: compxE2-stack-xlift-convs compxE2-size-convs exec-move-def)
  apply(auto simp add: match-ex-table-append-not-pcs)
done
thus ?case ..
next
case (bisim1CASThrow3 e n a xs stk loc pc A i D F v' v)
note exec = ⟨?exec (A.compareAndSwap(D·F, i, e)) (stk @ [v', v]) loc (length (compE2 A) + length
(compE2 i) + pc) [a] stk' loc' pc' xcp'⟩
note bisim2 = ⟨P,e,h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
from bisim2 have match-ex-table (compP2 P) (cname-of h a) (length (compE2 A) + length (compE2
i) + pc) (compxE2 e (length (compE2 A) + length (compE2 i)) 0) = None
  unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
with exec have False
  apply(auto elim!: exec-meth.cases simp add: compxE2-stack-xlift-convs compxE2-size-convs exec-move-def)
  apply(auto dest!: match-ex-table-stack-xliftD match-ex-table-shift-pcD dest: match-ex-table-pcsD
simp add: match-ex-table-append match-ex-table-shift-pc-None)
done
thus ?case ..
next
case (bisim1CASFail a n i e D F ad xs v' v v'')
note exec = ⟨?exec (a.compareAndSwap(D·F, i, e)) [v', v, v''] xs (length (compE2 a) + length
(compE2 i) + length (compE2 e)) [ad] stk' loc' pc' xcp'⟩
hence False
  by(auto elim!: exec-meth.cases simp add: match-ex-table-append exec-move-def
dest!: match-ex-table-shift-pcD match-ex-table-pc-length-compE2)
thus ?case ..
next
case (bisim1Call1 obj n obj' xs stk loc pc xcp ps M')
note IH1 = bisim1Call1.IH(2)
note IH2 = bisim1Call1.IH(4)

```

```

note exec = ⟨?exec (obj·M'(ps)) stk loc pc xcp stk' loc' pc' xcp'⟩
note bisim1 = ⟨P,obj,h ⊢ (obj', xs) ↔ (stk, loc, pc, xcp)⟩
note bisim2 = ⟨P,ps,h ⊢ (ps, loc) [↔] ([], loc, 0, None)⟩
note len = ⟨n + max-vars (obj'·M'(ps)) ≤ length xs⟩
note bsok = ⟨bsok (obj·M'(ps)) n⟩
from bisim1 have pc: pc ≤ length (compE2 obj) by(rule bisim1-pc-length-compE2)
from bisim1 have len.xs: length xs = length loc by(rule bisim1-length-xs)
show ?case
proof(cases pc < length (compE2 obj))
  case True
    with exec have exec': ?exec obj stk loc pc xcp stk' loc' pc' xcp' by(simp add: exec-move-Call1)
    from True have τmove2 (compP2 P) h stk (obj·M'(ps)) pc xcp = τmove2 (compP2 P) h stk obj
    pc xcp by(simp add: τmove2-iff)
    moreover from True have no-call2 (obj·M'(ps)) pc = no-call2 obj pc by(simp add: no-call2-def)
    ultimately show ?thesis
      using IH1[OF exec' - - ⟨P,h ⊢ stk [≤] ST⟩ ⟨conf-xcp' (compP2 P) h xcp⟩ bisim2 len bsok
      by(fastforce intro: bisim1-bisims1.bisim1Call1 Call1Obj elim!: Call-τred1r-obj Call-τred1t-obj)
  next
    case False
      with pc have pc: pc = length (compE2 obj) by auto
      with bisim1 obtain v where stk: stk = [v] and xcp: xcp = None and call: call1 obj' = None
      and v: is-val obj' ⇒ obj' = Val v ∧ xs = loc
      by(auto dest: bisim1-pc-length-compE2D)
      with bisim1 pc len bsok have τred1r P t h (obj', xs) (Val v, loc)
      by(auto intro: bisim1-Val-τred1r simp add: bsok-def)
      hence red: τred1r P t h (obj'·M'(ps), xs) (Val v·M'(ps), loc) by(rule Call-τred1r-obj)
      show ?thesis
      proof(cases ps)
        case (Cons p ps')
          from pc exec stk xcp
          have exec-meth-d (compP2 P) (compE2 (obj·M'(ps))) (compxE2 (obj·M'(ps)) 0 0) t h ([ @ [v],
          loc, length (compE2 obj) + 0, None) ta h' (stk', loc', pc', xcp')
          by(simp add: compxE2-size-convx compxE2-stack-xlift-convx exec-move-def)
          hence exec': exec-meth-d (compP2 P) (compEs2 ps) (stack-xlift (length [v]) (compxEs2 ps 0 0))
          t h ([ @ [v], loc, 0, None) ta h' (stk', loc', pc' - length (compE2 obj), xcp')
          and pc': pc' ≥ length (compE2 obj) using Cons
          by(safe dest!: Call-execParamD) simp-all
          from exec' bisim2 obtain stk'' where stk': stk' = stk'' @ [v]
          and exec'': exec-moves-d P t ps h ([, loc, 0, None) ta h' (stk'', loc', pc' - length (compE2 obj),
          xcp')
          unfolding exec-moves-def by -(drule (1) exec-meth-stk-splits, auto)
          with pc xcp have τ: τmove2 (compP2 P) h [v] (obj·M'(ps)) (length (compE2 obj)) None =
          τmoves2 (compP2 P) h [] ps 0 None
          using τinstr-stk-drop-exec-moves[where stk=[] and vs=[v]]
          by(auto simp add: τmove2-iff τmoves2-iff)
          from IH2[OF exec'', of [] len len.xs bsok] obtain es'' xs''
          where bisim': P,ps,h' ⊢ (es'', xs'') [↔] (stk'', loc', pc' - length (compE2 obj), xcp')
          and red': ?reds ps loc es'' xs'' ps [] 0 (pc' - length (compE2 obj)) None xcp' by auto
          from bisim' have P,obj·M'(ps),h' ⊢ (Val v·M'(es''), xs'') ↔ (stk'' @ [v], loc', length (compE2
          obj) + (pc' - length (compE2 obj)), xcp')
          by(rule bisim1CallParams)
          moreover from pc Cons have no-call2 (obj·M'(ps)) pc by(simp add: no-call2-def)
          ultimately show ?thesis using red red' τ stk' pc xcp pc' stk call
          by(fastforce elim!: rtranclp-trans Call-τred1r-param Call-τred1t-param intro: Call1Params)

```

rtranclp-tranclp-tranclp split: if-split-asm)

next

case [*simp*]: *Nil*

from *exec pc stk xcp*

have $v = \text{Null} \vee (\text{is-Addr } v \wedge (\exists T C' Ts' Tr' D'. \text{typeof}_h v = \lfloor T \rfloor \wedge \text{class-type-of}' T = \lfloor C' \rfloor$
 $\wedge P \vdash C' \text{ sees } M': Ts' \rightarrow Tr' = \text{Native in } D')$ (**is** - \vee ?*rest*)

by(*fastforce elim!*: *exec-meth.cases simp add: is-Ref-def exec-move-def compP2-def has-method-def split: if-split-asm*)

thus ?*thesis*

proof

assume [*simp*]: $v = \text{Null}$

have $P, \text{obj} \cdot M'(\square), h \vdash (\text{THROW NullPointer}, \text{loc}) \leftrightarrow (\square @ [\text{Null}], \text{loc}, \text{length}(\text{compE2 obj}) +$
 $\text{length}(\text{compEs2}(\square :: \text{'addr expr1 list}), \lfloor \text{addr-of-sys-xcpt NullPointer} \rfloor)$

by(*safe intro!*: *bisim1CallThrow*) *simp-all*

moreover have $\text{True}, P, t \vdash 1 \langle \text{null} \cdot M'(\text{map Val } \square), (h, \text{loc}) \rangle -\varepsilon \rightarrow \langle \text{THROW NullPointer}, (h,$
 $\text{loc}) \rangle$

by(*rule Red1CallNull*)

moreover have $\tau \text{move1 } P h (\text{Val } v \cdot M'(\square)) \tau \text{move2} (\text{compP2 } P) h [\text{Null}] (\text{obj} \cdot M'(ps)) (\text{length}$
 $(\text{compE2 obj})) \text{None}$

by(*simp-all add: $\tau \text{move2-iff}$*)

ultimately show ?*thesis* **using** *exec pc stk xcp red*

by(*fastforce elim!*: *exec-meth.cases intro: rtranclp-trans simp add: exec-move-def*)

next

assume ?*rest*

then obtain $a \text{ Ta } Ts' Tr' D'$ **where** [*simp*]: $v = \text{Addr } a$

and $\text{Ta}: \text{typeof-addr } h a = \lfloor \text{Ta} \rfloor$

and *iec*: $P \vdash \text{class-type-of } \text{Ta} \text{ sees } M': Ts' \rightarrow Tr' = \text{Native in } D'$ **by** *auto*

with *exec pc stk xcp*

obtain $ta' va h'' U$ **where** *redex*: $(ta', va, h'') \in \text{red-external-aggr}(\text{compP2 } P) t a M' \square h$

and *ret*: $(xcp', h', [(stk', loc', \text{undefined}, \text{undefined}, pc')]) = \text{extRet2JVM } 0 h'' [\text{Addr } a] \text{loc}$
 $\text{undefined undefined} (\text{length}(\text{compE2 obj})) \square va$

and *wtext'*: $P, h \vdash a \cdot M'(\square) : U$

and $ta': ta = \text{extTA2JVM}(\text{compP2 } P) ta'$

by(*fastforce simp add: is-Ref-def exec-move-def compP2-def external-WT'-iff exec-meth-instr*)

from $\text{Ta } \text{iec}$ **have** $\tau: \tau \text{move1 } P h (\text{Val } v \cdot M'(\square)) \longleftrightarrow \tau \text{move2}(\text{compP2 } P) h [\text{Addr } a] (\text{obj} \cdot M'(ps))$
 $(\text{length}(\text{compE2 obj})) \text{None}$

by(*auto simp add: $\tau \text{move2-iff compP2-def}$*)

from *ret* **have** [*simp*]: $h'' = h'$ **by** *simp*

from *wtext'* **have** *wtext''*: $\text{compP2 } P, h \vdash a \cdot M'(\square) : U$ **by**(*simp add: external-WT'-iff compP2-def*)

from *wf* **have** *wf-jvm-prog* ($\text{compP2 } P$) **by**(*rule wt-compP2*)

then obtain *wf-md* **where** $wf': wf\text{-prog } wf\text{-md}(\text{compP2 } P)$

unfolding *wf-jvm-prog-def* **by**(*blast dest: wt-jvm-progD*)

from *tconf* **have** $tconf': \text{compP2 } P, h \vdash t \sqrt{t}$ **by**(*simp add: compP2-def tconf-def*)

from *redex* **have** $redex'': \text{compP2 } P, t \vdash \langle a \cdot M'(\square), h \rangle -ta' \rightarrow \text{ext} \langle va, h' \rangle$

by(*simp add: WT-red-external-list-conv[OF wf' wtext'' tconf', symmetric]*)

hence $redex': P, t \vdash \langle a \cdot M'(\square), h \rangle -ta' \rightarrow \text{ext} \langle va, h' \rangle$ **by**(*simp add: compP2-def*)

with $\text{Ta } \text{iec}$ **have** $\text{True}, P, t \vdash 1 \langle \text{addr } a \cdot M'(\text{map Val } \square), (h, \text{loc}) \rangle -ta' \rightarrow \langle \text{extRet2J}(\text{addr}$
 $a \cdot M'(\text{map Val } \square)) va, (h', \text{loc}) \rangle$

by $-(\text{rule Red1CallExternal}, \text{auto})$

moreover have $P, \text{obj} \cdot M'(ps), h' \vdash (\text{extRet2J}(\text{addr } a \cdot M'(\text{map Val } \square)) va, \text{loc}) \leftrightarrow (stk', loc',$
 $pc', xcp')$

proof(*cases va*)

case (*RetVal v*)

have $P, \text{obj} \cdot M'(\square), h' \vdash (\text{Val } v, \text{loc}) \leftrightarrow ([v], \text{loc}, \text{length}(\text{compE2}(\text{obj} \cdot M'(\square))), \text{None})$

```

    by(rule bisim1Val2) simp
  with ret RetVal show ?thesis by simp
next
  case (RetExc ad)
    have  $P, obj \cdot M'(\square), h' \vdash (\text{Throw } ad, \text{loc}) \leftrightarrow (\square @ [v], \text{loc}, \text{length } (\text{compE2 } (obj)) + \text{length } (\text{compEs2 } (\square :: 'addr \text{expr1 list})), [ad])$ 
    by(rule bisim1CallThrow) simp
  with ret RetExc show ?thesis by simp
next
  case RetStaySame
  have  $P, obj \cdot M'(\square), h' \vdash (\text{addr } a \cdot M'(\square), \text{loc}) \leftrightarrow ([\text{Addr } a], \text{loc}, \text{length } (\text{compE2 } obj), \text{None})$ 
  by(rule bisim1-bisims1.bisim1Call1)(rule bisim1Val2, simp)
  thus ?thesis using ret RetStaySame by simp
qed
  moreover from  $\langle \text{preallocated } h \rangle \text{ red-external-hext}[OF \text{redex}]$  have preallocated  $h'$  by(rule preallocated-hext)
  from  $\text{redex'' wtext'' } \langle hconf \ h \rangle$  have  $hconf \ h'$  by(rule external-call-hconf)
  with  $ta' \text{redex' } \langle \text{preallocated } h' \rangle$ 
  have  $ta\text{-bisim } wbisim1 \ (\text{extTA2J1 } P \ ta') \ ta$  by(auto intro: red-external-ta-bisim21[OF wf])
  moreover have  $\tau \text{move1 } P \ h \ (\text{Val } v \cdot M'(\square)) \implies ta' = \varepsilon \wedge h' = h$  using  $\text{redex' } Ta \text{ iec}$ 
  by(fastforce dest:  $\tau \text{external' -red-external-heap-unchanged } \tau \text{external' -red-external-TA-empty sees-method-fun simp add: } \tau \text{external' -def } \tau \text{external-def}$ )
  moreover from  $v \text{ call}$ 
  have  $\text{call1 } (obj' \cdot M'(ps)) \neq \text{None} \implies \text{Val } v \cdot M'(ps) = obj' \cdot M'(ps) \wedge \text{loc} = xs$ 
  by(auto split: if-split-asm)
  ultimately show ?thesis using  $\text{red } \tau \text{ pc xcp stk Ta call iec}$ 
  apply(auto simp del:  $\text{call1.simps calls1.simps}$ )
  apply(rule  $\text{exI conjI |assumption|erule rtranclp.rtrancl-into-rtrancl rtranclp-into-tranclp1 |drule (1) sees-method-fun |clarsimp}$ )+
  done
  qed
  qed
  qed
next
  case (bisim1CallParams  $ps \ n \ ps' \ xs \ stk \ \text{loc} \ \text{pc} \ \text{xcp} \ obj \ M' \ v$ )
  note  $\text{bisim2} = \langle P, ps, h \vdash (ps', xs) [\leftrightarrow] (stk, \text{loc}, \text{pc}, \text{xcp}) \rangle$ 
  note  $\text{bisim1} = \langle P, obj, h \vdash (obj, xs) \leftrightarrow (\square, xs, \theta, \text{None}) \rangle$ 
  note  $\text{IH2} = \text{bisim1CallParams.IH}(2)$ 
  note  $\text{exec} = \langle \text{exec-move-d } P \ t \ (obj \cdot M'(ps)) \ h \ (stk @ [v], \text{loc}, \text{length } (\text{compE2 } obj) + \text{pc}, \text{xcp}) \ ta \ h' \ (stk', \text{loc}', \text{pc}', \text{xcp}') \rangle$ 
  note  $\text{len} = \langle n + \text{max-vars } (\text{Val } v \cdot M'(ps')) \leq \text{length } xs \rangle$ 
  note  $\text{bsok} = \langle \text{bsok } (obj \cdot M'(ps)) \ n \rangle$ 
  from  $\langle P, h \vdash \text{stk} @ [v] [:\leq] ST \rangle$  obtain  $T \ ST'$  where  $ST': P, h \vdash \text{stk} [:\leq] ST'$  and  $T: \text{typeof}_h \ v = [T]$ 
  by(auto simp add:  $\text{list-all2-Cons1 list-all2-append1 conf-def}$ )
  from  $\text{bisim2}$  have  $\text{pc}: \text{pc} \leq \text{length } (\text{compEs2 } ps)$  by(rule bisims1-pc-length-compEs2)
  show ?case
  proof(cases  $\text{pc} < \text{length } (\text{compEs2 } ps)$ )
    case True
    from  $\text{exec}$  have  $\text{exec-meth-d } (\text{compP2 } P) \ (\text{compE2 } (obj \cdot M'(ps))) \ (\text{compxE2 } (obj \cdot M'(ps)) \ 0 \ 0) \ t \ h \ (stk @ [v], \text{loc}, \text{length } (\text{compE2 } obj) + \text{pc}, \text{xcp}) \ ta \ h' \ (stk', \text{loc}', \text{pc}', \text{xcp}')$ 
    by(simp add:  $\text{exec-move-def}$ )
    with True have  $\text{exec}': \text{exec-meth-d } (\text{compP2 } P) \ (\text{compEs2 } ps) \ (\text{stack-xlift } (\text{length } [v]) \ (\text{compxE2 } ps \ 0 \ 0)) \ t \ h \ (stk @ [v], \text{loc}, \text{pc}, \text{xcp}) \ ta \ h' \ (stk', \text{loc}', \text{pc}' - \text{length } (\text{compE2 } obj), \text{xcp}')$ 

```


and $pc': pc' \geq \text{length}(\text{compE2 } \text{obj})$ **by**(*safe dest!:* *Call-execParamD*)
from *exec' bisim2* **obtain** stk'' **where** $stk': stk' = stk'' @ [v]$
and $exec'': \text{exec-moves-d } P \ t \ ps \ h \ (stk, \text{loc}, pc, xcp) \ \text{ta } h' \ (stk'', \text{loc}', pc' - \text{length}(\text{compE2 } \text{obj}), xcp')$
by(*unfold exec-moves-def*)(*drule (1) exec-meth-stk-splits, auto*)
with *True* **have** $\tau: \tau\text{move2}(\text{compP2 } P) \ h \ (stk @ [v]) \ (\text{obj} \cdot M'(ps)) \ (\text{length}(\text{compE2 } \text{obj}) + pc)$
 $xcp = \tau\text{moves2}(\text{compP2 } P) \ h \ stk \ ps \ pc \ xcp$
by(*auto simp add: \tau\text{move2-iff } \tau\text{moves2-iff } \tau\text{instr-stk-drop-exec-moves}*)
from *IH2[OF exec'' - - ST' \conf-xcp' (compP2 P) h xcp] len bsok* **obtain** $es'' \ xs''$
where $\text{bisim}': P, ps, h' \vdash (es'', xs'') \ [\leftrightarrow] \ (stk'', \text{loc}', pc' - \text{length}(\text{compE2 } \text{obj}), xcp')$
and $\text{red}': ?\text{reds } ps' \ xs \ es'' \ xs'' \ ps \ stk \ pc \ (pc' - \text{length}(\text{compE2 } \text{obj})) \ xcp \ xcp' \ \text{by } \text{auto}$
from *bisim'* **have** $P, \text{obj} \cdot M'(ps), h' \vdash (\text{Val } v \cdot M'(es''), xs'') \leftrightarrow (stk'' @ [v], \text{loc}', \text{length}(\text{compE2 } \text{obj}) + (pc' - \text{length}(\text{compE2 } \text{obj})), xcp')$
by(*rule bisim1-bisims1.bisim1CallParams*)
moreover from *True* **have** $\text{no-call2}(\text{obj} \cdot M'(ps)) \ (\text{length}(\text{compE2 } \text{obj}) + pc) = \text{no-calls2 } ps \ pc$
by(*simp add: no-calls2-def no-call2-def*)
moreover have $\text{calls1 } ps' = \text{None} \implies \text{call1}(\text{Val } v \cdot M'(ps')) = \text{None} \vee \text{is-vals } ps' \ \text{by } \text{simp}$
ultimately show *?thesis* **using** $\text{red}' \ \tau \ \text{stk}' \ \text{pc}'$
by(*fastforce intro: Call1Params elim!: Call-\tau\text{red1r-param } Call-\tau\text{red1t-param split: if-split-asm simp add: is-vals-conv}*)
next
case *False*
with *pc* **have** $[simp]: pc = \text{length}(\text{compEs2 } ps)$ **by** *simp*
with *bisim2* **obtain** *vs* **where** $[simp]: stk = \text{rev } vs \ xcp = \text{None}$
and $\text{lensvs}: \text{length } vs = \text{length } ps$ **and** $vs: \text{is-vals } ps' \implies ps' = \text{map } \text{Val } vs \wedge xs = \text{loc}$
and $\text{call}: \text{calls1 } ps' = \text{None}$
by(*auto dest: bisims1-pc-length-compEs2D*)
with *bisim2 len bsok* **have** $\text{reds}: \tau\text{reds1r } P \ t \ h \ (ps', xs) \ (\text{map } \text{Val } vs, \text{loc})$
by(*auto intro: bisims1-Val-\tau\text{Reds1r simp add: bsok-def}*)
from *exec T lensvs*
have $v = \text{Null} \vee (\text{is-Addr } v \wedge (\exists T \ C' \ Ts' \ Tr' \ D'. \ \text{typeof}_h \ v = [T] \wedge \text{class-type-of}' \ T = [C'] \wedge P \vdash C' \ \text{sees } M': Ts' \rightarrow Tr' = \text{Native in } D'))$ (**is** - \vee *?rest*)
by(*fastforce elim!: exec-meth.cases simp add: is-Ref-def exec-move-def compP2-def has-method-def split: if-split-asm*)
thus *?thesis*
proof
assume $[simp]: v = \text{Null}$
hence $\tau: \tau\text{move1 } P \ h \ (\text{Val } v \cdot M'(\text{map } \text{Val } vs))$
 $\tau\text{move2}(\text{compP2 } P) \ h \ (stk @ [v]) \ (\text{obj} \cdot M'(ps)) \ (\text{length}(\text{compE2 } \text{obj}) + \text{length}(\text{compEs2 } ps))$
None
using lensvs **by**(*auto simp add: \tau\text{move2-iff}*)
from lensvs
have $P, \text{obj} \cdot M'(ps), h \vdash (\text{THROW } \text{NullPointer}, \text{loc}) \leftrightarrow (\text{rev } vs @ [\text{Null}], \text{loc}, \text{length}(\text{compE2 } \text{obj}) + \text{length}(\text{compEs2 } ps), [\text{addr-of-sys-xcpt } \text{NullPointer}])$
by(*safe intro!: bisim1CallThrow*) *simp*
moreover have $\text{True}, P, t \vdash 1 \ \langle \text{null} \cdot M'(\text{map } \text{Val } vs), (h, \text{loc}) \rangle \ -\varepsilon \rightarrow \langle \text{THROW } \text{NullPointer}, (h, \text{loc}) \rangle$
by(*rule Red1CallNull*)
ultimately show *?thesis* **using** *exec pc \tau lensvs reds*
apply(*auto elim!: exec-meth.cases simp add: exec-move-def*)
apply(*rule exI conjI | assumption*) +
apply(*rule rtranclp.rtrancl-into-rtrancl*)
apply(*erule Call-\tau\text{red1r-param}*)
by *auto*

```

next
  assume ?rest
  then obtain a Ta C' Ts' Tr' D' where [simp]: v = Addr a
    and Ta: typeof-addr h a = [Ta]
    and iec: P ⊢ class-type-of Ta sees M': Ts' → Tr' = Native in D' by auto
  with exec pc lenvs
  obtain ta' va h'' U Ts Ts' where redex: (ta', va, h'') ∈ red-external-aggr (compP2 P) t a M' vs
  h
    and ret: (xcp', h', [(stk', loc', undefined, undefined, pc')]) = extRet2JVM (length vs) h'' (rev vs
  @ [Addr a]) loc undefined undefined (length (compE2 obj) + length (compEs2 ps)) [] va
    and wtext': P, h ⊢ a·M'(vs) : U
    and Ts: map typeofh vs = map Some Ts
    and ta': ta = extTA2JVM (compP2 P) ta'
    by(fastforce simp add: is-Ref-def exec-move-def compP2-def external-WT'-iff exec-meth-instr
  confs-conv-map)
    have τ: τmove1 P h (Val v·M'(map Val vs)) ↔ τmove2 (compP2 P) h (stk @ [v]) (obj·M'(ps))
  (length (compE2 obj) + length (compEs2 ps)) None
    using Ta iec lenvs
    by(auto simp add: τmove2-iff map-eq-append-conv compP2-def)
  from ret have [simp]: h'' = h' by simp
  from wtext' have wtext'': compP2 P, h ⊢ a·M'(vs) : U by(simp add: compP2-def external-WT'-iff)
  from wf have wf-jvm-prog (compP2 P) by(rule wt-compP2)
  then obtain wf-md where wf': wf-prog wf-md (compP2 P)
    unfolding wf-jvm-prog-def by(blast dest: wt-jvm-progD)
  from tconf have tconf': compP2 P, h ⊢ t √t by(simp add: compP2-def tconf-def)
  from redex have redex'': compP2 P, t ⊢ ⟨a·M'(vs), h⟩ -ta'→ext ⟨va, h^⟩
    by(simp add: WT-red-external-list-conv[OF wf' wtext'' tconf', symmetric])
  hence redex': P, t ⊢ ⟨a·M'(vs), h⟩ -ta'→ext ⟨va, h^⟩ by(simp add: compP2-def)
  with Ta iec have True, P, t ⊢ 1 ⟨addr a·M'(map Val vs), (h, loc)⟩ -ta'→ ⟨extRet2J (addr a·M'(map
  Val vs)) va, (h', loc)⟩
    by -(rule Red1CallExternal, auto)
  moreover have P, obj·M'(ps), h' ⊢ (extRet2J (addr a·M'(map Val vs)) va, loc) ↔ (stk', loc', pc',
  xcp')
  proof(cases va)
    case (RetVal v)
    have P, obj·M'(ps), h' ⊢ (Val v, loc) ↔ ([v], loc, length (compE2 (obj·M'(ps))), None)
      by(rule bisim1Val2)(simp)
    with ret RetVal show ?thesis by simp
  next
    case (RetExc ad)
    from lenvs have length ps = length (rev vs) by simp
    hence P, obj·M'(ps), h' ⊢ (Throw ad, loc) ↔ (rev vs @ [v], loc, length (compE2 (obj)) + length
  (compEs2 ps), [ad])
      by(rule bisim1CallThrow)
    with ret RetExc show ?thesis by simp
  next
    case RetStaySame
    from lenvs have length ps = length vs by simp
    from bisims1-map-Val-append[OF bisims1Nil this, of P h' loc]
    have P, ps, h' ⊢ (map Val vs, loc) [↔] (rev vs, loc, length (compEs2 ps), None) by simp
    hence P, obj·M'(ps), h' ⊢ (addr a·M'(map Val vs), loc) ↔ (rev vs @ [Addr a], loc, length (compE2
  obj) + length (compEs2 ps), None)
      by(rule bisim1-bisims1.bisim1CallParams)
    thus ?thesis using ret RetStaySame by simp

```

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qed
moreover from ⟨preallocated h⟩ red-external-heap[OF redex'] have preallocated h' by(rule preal-
located-heap)
from redex'' wtext'' ⟨hconf h⟩ have hconf h' by(rule external-call-hconf)
with ta' redex' ⟨preallocated h'⟩
have ta-bisim wbisim1 (extTA2J1 P ta') ta by(auto intro: red-external-ta-bisim21[OF wf])
moreover have τmove1 P h (Val v·M'(map Val vs)) ⇒ ta' = ε ∧ h' = h
using Ta iec redex'
by(fastforce dest: τexternal'-red-external-heap-unchanged τexternal'-red-external-TA-empty
sees-method-fun simp add: τexternal'-def τexternal-def map-eq-append-conv)
moreover from vs call have call1 (Val v·M'(ps')) ≠ None ⇒ ps' = map Val vs ∧ loc = xs
by(auto split: if-split-asm simp add: is-vals-conv)
ultimately show ?thesis using reds τ pc Ta call
apply(auto simp del: split-paired-Ex call1.simps calls1.simps split: if-split-asm simp add:
map-eq-append-conv)
apply(auto 4 4 simp del: split-paired-Ex call1.simps calls1.simps intro: rtranclp.rtrancl-into-rtrancl[OF
Call-τred1r-param] rtranclp-into-tranclp1[OF Call-τred1r-param])[3]
apply((assumption|rule exI conjI|erule Call-τred1r-param|simp add: map-eq-append-conv)+)
done
qed
qed
next
case (bisim1CallThrowObj obj n a xs stk loc pc ps M')
note exec = ⟨?exec (obj·M'(ps)) stk loc pc [a] stk' loc' pc' xcp'⟩
note bisim1 = ⟨P,obj,h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
from bisim1 have pc: pc < length (compE2 obj) by(auto dest: bisim1-ThrowD)
from bisim1 have match-ex-table (compP2 P) (cname-of h a) (0 + pc) (compxE2 obj 0 0) = None
unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
with exec pc have False
by(auto elim!: exec-meth.cases simp add: exec-move-def match-ex-table-not-pcs-None)
thus ?case ..
next
case (bisim1CallThrowParams ps n vs a ps' xs stk loc pc obj M' v)
note exec = ⟨?exec (obj·M'(ps)) (stk @ [v]) loc (length (compE2 obj) + pc) [a] stk' loc' pc' xcp'⟩
note bisim2 = ⟨P,ps,h ⊢ (map Val vs @ Throw a # ps', xs) [↔] (stk, loc, pc, [a])⟩
from bisim2 have pc: pc < length (compEs2 ps) by(auto dest: bisims1-ThrowD)
from bisim2 have match-ex-table (compP2 P) (cname-of h a) (0 + pc) (compEs2 ps 0 0) = None
unfolding compP2-def by(rule bisims1-xcp-Some-not-caught)
with exec pc have False
apply(auto elim!: exec-meth.cases simp add: compEs2-size-conv compEs2-stack-xlift-conv exec-move-def)
apply(auto simp add: match-ex-table-append dest!: match-ex-table-shift-pcD match-ex-table-stack-xliftD
match-ex-table-pc-length-compE2)
done
thus ?case ..
next
case (bisim1CallThrow ps vs obj n M' a xs v)
note lenvs = ⟨length ps = length vs⟩
note exec = ⟨?exec (obj·M'(ps)) (vs @ [v]) xs (length (compE2 obj) + length (compEs2 ps)) [a] stk'
loc' pc' xcp'⟩
with lenvs have False
by(auto elim!: exec-meth.cases simp add: match-ex-table-append-not-pcs exec-move-def dest!: match-ex-table-pc-length-compE2)
thus ?case ..
next
case (bisim1BlockSome1 e n V Ty v xs)

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have  $\tau\text{move2}$  (compP2 P) h [] {V:Ty=[v]; e} 0 None by(simp add:  $\tau\text{move2-iff}$ )
with  $\langle ?\text{exec} \{V:Ty=[v]; e\} [] xs 0 \text{None} \text{stk}' \text{loc}' \text{pc}' \text{pcp}' \rangle \langle P, e, h \vdash (e, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$ 
show  $?case$  by(fastforce elim!: exec-meth.cases intro: bisim1BlockSome2 simp add: exec-move-def)
next
case (bisim1BlockSome2 e n V Ty v xs)
have  $\tau\text{move2}$  (compP2 P) h [v] {V:Ty=[v]; e} (Suc 0) None by(simp add:  $\tau\text{move2-iff}$ )
with  $\langle ?\text{exec} \{V:Ty=[v]; e\} [v] xs (\text{Suc } 0) \text{None} \text{stk}' \text{loc}' \text{pc}' \text{pcp}' \rangle \langle P, e, h \vdash (e, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$ 
show  $?case$  by(fastforce intro: r-into-rtranclp Block1Some bisim1BlockSome4 [OF bisim1-refl] simp
add: exec-move-def
elim!: exec-meth.cases)
next
case (bisim1BlockSome4 e n e' xs stk loc pc xcp V Ty v)
note IH = bisim1BlockSome4.IH(2)
note exec =  $\langle ?\text{exec} \{V:Ty=[v]; e\} \text{stk} \text{loc} (\text{Suc} (\text{Suc} \text{pc})) \text{xcp} \text{stk}' \text{loc}' \text{pc}' \text{pcp}' \rangle$ 
note bisim =  $\langle P, e, h \vdash (e', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \rangle$ 
note bsok =  $\langle \text{bsok} \{V:Ty=[v]; e\} n \rangle$ 
with  $\langle n + \text{max-vars} \{V:Ty=\text{None}; e'\} \leq \text{length } xs \rangle$ 
have V: V < length xs and len: Suc n + max-vars e'  $\leq$  length xs and [simp]: n = V by simp-all
let ?pre = [Push v, Store V]
from exec have exec': exec-meth-d (compP2 P) (?pre @ compE2 e) (shift (length ?pre) (compxE2 e
0 0)) t h (stk, loc, length ?pre + pc, xcp) ta h' (stk', loc', pc', xcp')
by(simp add: compxE2-size-convs exec-move-def)
hence pc': pc'  $\geq$  length ?pre
by(rule exec-meth-drop-pc) auto
hence pc'': Suc (Suc (pc' - Suc (Suc 0))) = pc' by simp
moreover from exec' have exec-move-d P t e h (stk, loc, pc, xcp) ta h' (stk', loc', pc' - length ?pre,
xcp')
unfolding exec-move-def by(rule exec-meth-drop) auto
from IH[OF this len -  $\langle P, h \vdash \text{stk} [:\leq] ST \rangle \langle \text{conf-xcp}' (\text{compP2 } P) h \text{xcp}' \rangle \text{bsok}$  obtain e'' xs''
where bisim':  $P, e, h' \vdash (e'', xs'') \leftrightarrow (\text{stk}', \text{loc}', \text{pc}' - \text{length } ?pre, \text{xcp}')$ 
and red':  $?red e' xs e'' xs'' e \text{stk} \text{pc} (\text{pc}' - \text{length } ?pre) \text{xcp} \text{xcp}'$  by auto
from bisim' have P, {V:Ty=[v]; e}, h'  $\vdash (\{V:Ty=\text{None}; e''\}, xs'') \leftrightarrow (\text{stk}', \text{loc}', \text{Suc} (\text{Suc} (\text{pc}' -
\text{length } ?pre)), \text{xcp}')$ 
by(rule bisim1-bisims1.bisim1BlockSome4)
moreover from pc' pc'' have  $\tau\text{move2}$  (compP2 P) h stk {V:Ty=[v]; e} (Suc (Suc pc)) xcp =
 $\tau\text{move2}$  (compP2 P) h stk e pc xcp by(simp add:  $\tau\text{move2-iff}$ )
moreover from red' have length xs'' = length xs
by(auto split: if-split-asm dest!:  $\tau\text{red1r-preserves-len } \tau\text{red1t-preserves-len } \text{red1-preserves-len}$ )
ultimately show  $?case$  using red' pc'' V
by(fastforce elim!: Block-None- $\tau\text{red1r-xt}$  Block-None- $\tau\text{red1t-xt}$  intro: Block1Red split: if-split-asm
simp add: no-call2-def)
next
case (bisim1BlockThrowSome e n a xs stk loc pc V Ty v)
note exec =  $\langle ?\text{exec} \{V:Ty=[v]; e\} \text{stk} \text{loc} (\text{Suc} (\text{Suc} \text{pc})) [a] \text{stk}' \text{loc}' \text{pc}' \text{pcp}' \rangle$ 
note bisim =  $\langle P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a]) \rangle$ 
from bisim have pc: pc < length (compE2 e) by(auto dest: bisim1-ThrowD)
from bisim have match-ex-table (compP2 P) (cname-of h a) (0 + pc) (compxE2 e 0 0) = None
unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
with exec pc have False
apply(auto elim!: exec-meth.cases simp add: match-ex-table-not-pcs-None exec-move-def)
apply(auto simp only: compxE2-size-convs dest!: match-ex-table-shift-pcD)
apply simp
done

```

thus $?case \dots$
next
case ($bisim1BlockNone\ e\ n\ e'\ xs\ stk\ loc\ pc\ xcp\ V\ Ty$)
note $IH = bisim1BlockNone.IH(2)$
note $exec = \langle ?exec\ \{V:Ty=None; e\}\ stk\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$
note $bisim = \langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
note $bsok = \langle bsok\ \{V:Ty=None; e\}\ n \rangle$
with $\langle n + max-vars\ \{V:Ty=None; e'\} \leq length\ xs \rangle$
have $V: V < length\ xs$ **and** $len: Suc\ n + max-vars\ e' \leq length\ xs$ **by** $simp-all$
have $\tau move2\ (compP2\ P)\ h\ stk\ \{V:Ty=None; e\}\ pc\ xcp = \tau move2\ (compP2\ P)\ h\ stk\ e\ pc\ xcp$
by ($simp\ add: \tau move2-iff$)
moreover
from $exec$ **have** $?exec\ e\ stk\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp'$ **by** ($simp\ add: exec-move-BlockNone$)
from $IH[OF\ this\ len - \langle P, h \vdash stk\ [:\leq]\ ST \rangle \langle conf-xcp'\ (compP2\ P)\ h\ xcp \rangle\ bsok]$ **obtain** $e''\ xs''$
where $bisim': P, e, h' \vdash (e'', xs'') \leftrightarrow (stk', loc', pc', xcp')$
and $red': ?red\ e'\ xs\ e''\ xs''\ e\ stk\ pc\ pc'\ xcp\ xcp'$ **by** $auto$
from $bisim'$ **have** $P, \{V:Ty=None; e'\}, h' \vdash (\{V:Ty=None; e''\}, xs'') \leftrightarrow (stk', loc', pc', xcp')$
by ($rule\ bisim1-bisims1.bisim1BlockNone$)
ultimately show $?case\ using\ V\ red'$
by ($fastforce\ elim!: Block1Red\ Block-None-\tau red1r-xt\ Block-None-\tau red1t-xt\ simp\ add: no-call2-def$)
next
case ($bisim1BlockThrowNone\ e\ n\ a\ xs\ stk\ loc\ pc\ V\ Ty$)
note $exec = \langle ?exec\ \{V:Ty=None; e\}\ stk\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$
note $bisim = \langle P, e, h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$
from $bisim$ **have** $pc: pc < length\ (compE2\ e)$ **by** ($auto\ dest: bisim1-ThrowD$)
from $bisim$ **have** $match-ex-table\ (compP2\ P)\ (cname-of\ h\ a)\ (0 + pc)\ (compxE2\ e\ 0\ 0) = None$
unfolding $compP2-def$ **by** ($rule\ bisim1-xcp-Some-not-caught$)
with $exec\ pc$ **have** $False$ **by** ($auto\ elim!: exec-meth.cases\ simp\ add: exec-move-def$)
thus $?case \dots$
next
case ($bisim1Sync1\ e1\ n\ e'\ xs\ stk\ loc\ pc\ xcp\ e2\ V$)
note $IH = bisim1Sync1.IH(2)$
note $exec = \langle ?exec\ (sync_V\ (e1)\ e2)\ stk\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$
note $bisim = \langle P, e1, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
note $bisim2 = \langle P, e2, h \vdash (e2, xs) \leftrightarrow ([], xs, 0, None) \rangle$
note $len = \langle n + max-vars\ (sync_V\ (e1)\ e2) \leq length\ xs \rangle$
note $bsok = \langle bsok\ (sync_V\ (e1)\ e2)\ n \rangle$
from $bisim$ **have** $pc: pc \leq length\ (compE2\ e1)$ **by** ($rule\ bisim1-pc-length-compE2$)
show $?case$
proof ($cases\ pc < length\ (compE2\ e1)$)
case $True$
with $exec$ **have** $exec': ?exec\ e1\ stk\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp'$ **by** ($simp\ add: exec-move-Sync1$)
from $True$ **have** $\tau move2\ (compP2\ P)\ h\ stk\ (sync_V\ (e1)\ e2)\ pc\ xcp = \tau move2\ (compP2\ P)\ h\ stk\ e1\ pc\ xcp$ **by** ($simp\ add: \tau move2-iff$)
with $IH[OF\ exec' - - \langle P, h \vdash stk\ [:\leq]\ ST \rangle \langle conf-xcp'\ (compP2\ P)\ h\ xcp \rangle\ len\ bisim2\ bsok]$ **show**
 $?thesis$
by ($fastforce\ intro: bisim1-bisims1.bisim1Sync1\ Synchronized1Red1\ elim!: Sync-\tau red1r-xt\ Sync-\tau red1t-xt\ simp\ add: no-call2-def$)
next
case $False$
with pc **have** $[simp]: pc = length\ (compE2\ e1)$ **by** $simp$
with $bisim$ **obtain** v **where** $stk: stk = [v]$ **and** $xcp: xcp = None$
by ($auto\ dest: bisim1-pc-length-compE2D$)
with $bisim\ pc\ len\ bsok$ **have** $red: \tau red1r\ P\ t\ h\ (e', xs)\ (Val\ v, loc)$

by(*auto intro: bisim1-Val- τ -red1r simp add: bsok-def*)
hence τ red1r P t h (sync_V (e') $e2$, xs) (sync_V ($\text{Val } v$) $e2$, loc) **by**(*rule Sync- τ -red1r-xt*)
moreover have τ : τ move2 ($\text{compP2 } P$) h [v] (sync_V ($e1$) $e2$) pc None **by**(*simp add: τ move2-iff*)
moreover
have $P, (\text{sync}_V$ ($e1$) $e2), h \vdash ((\text{sync}_V$ ($\text{Val } v$) $e2), loc) \leftrightarrow ([v, v], loc, \text{Suc} (\text{length} (\text{compE2 } e1)), \text{None})$
by(*rule bisim1Sync2*)
ultimately show *?thesis using exec stk xcp*
by(*fastforce elim!: exec-meth.cases simp add: exec-move-def*)
qed
next
case (*bisim1Sync2 e1 n e2 V v xs*)
note $\text{exec} = \langle ?\text{exec} (\text{sync}_V$ ($e1$) $e2$) [v , v] xs ($\text{Suc} (\text{length} (\text{compE2 } e1)))$) None stk' loc' pc' $\text{xcp}' \rangle$
note $\text{bisim} = \langle P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$
note $\text{bisim2} = \langle P, e2, h \vdash (e2, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$
have τ move2 ($\text{compP2 } P$) h [v , v] (sync_V ($e1$) $e2$) ($\text{Suc} (\text{length} (\text{compE2 } e1)))$) None **by**(*rule τ move2Sync3*)
thus *?case using exec*
by(*fastforce elim!: exec-meth.cases intro: bisim1Sync3 simp add: exec-move-def*)
next
case (*bisim1Sync3 e1 n e2 V v xs*)
note $\text{exec} = \langle ?\text{exec} (\text{sync}_V$ ($e1$) $e2$) [v] ($xs[V := v]$) ($\text{Suc} (\text{Suc} (\text{length} (\text{compE2 } e1))))$) None stk' loc' pc' $\text{xcp}' \rangle$
note $\text{bisim} = \langle P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$
note $\text{bisim2} = \langle \bigwedge xs. P, e2, h \vdash (e2, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$
note $\text{len} = \langle n + \text{max-vars} (\text{sync}_V$ ($\text{Val } v$) $e2$) $\leq \text{length } xs \rangle$
note $\text{bsok} = \langle \text{bsok} (\text{sync}_V$ ($e1$) $e2$) $n \rangle$
with len have $V: V < \text{length } xs$ **by** *simp*
have τ : $\neg \tau$ move2 ($\text{compP2 } P$) h [v] (sync_V ($e1$) $e2$) ($\text{Suc} (\text{Suc} (\text{length} (\text{compE2 } e1))))$) None **by**(*simp add: τ move2-iff*)
from exec **have** $(\exists a. v = \text{Addr } a \wedge \text{stk}' = [] \wedge \text{loc}' = xs[V := v] \wedge ta = \{\text{Lock} \rightarrow a, \text{SyncLock } a\} \wedge \text{xcp}' = \text{None} \wedge \text{pc}' = \text{Suc} (\text{Suc} (\text{Suc} (\text{length} (\text{compE2 } e1)))) \vee (v = \text{Null} \wedge \text{stk}' = [v] \wedge \text{loc}' = xs[V := v] \wedge ta = \varepsilon \wedge \text{xcp}' = [\text{addr-of-sys-xcpt NullPointer}] \wedge \text{pc}' = \text{Suc} (\text{Suc} (\text{length} (\text{compE2 } e1))))$) (**is** *?c1* \vee *?c2*)
by(*fastforce elim!: exec-meth.cases simp add: is-Ref-def expand-finfun-eq fun-eq-iff finfun-upd-apply exec-move-def*)
thus *?case*
proof
assume *?c1*
then obtain a **where** [*simp*]: $v = \text{Addr } a$ $\text{stk}' = []$ $\text{loc}' = xs[V := v]$ $ta = \{\text{Lock} \rightarrow a, \text{SyncLock } a\}$ $\text{xcp}' = \text{None}$ $\text{pc}' = \text{Suc} (\text{Suc} (\text{Suc} (\text{length} (\text{compE2 } e1))))$) **by** *blast*
have $\text{True}, P, t \vdash 1 \langle \text{sync}_V$ ($\text{addr } a$) $e2$, (h , xs) $\rightarrow \{\text{Lock} \rightarrow a, \text{SyncLock } a\} \rightarrow \langle \text{insync}_V$ (a) $e2$, (h , $xs[V := \text{Addr } a]$) \rangle
using V **by**(*rule Lock1Synchronized*)
moreover from bisim2 **have** P, sync_V ($e1$) $e2, h \vdash (\text{insync}_V$ (a) $e2$, $xs[V := \text{Addr } a]) \leftrightarrow ([], xs[V := \text{Addr } a], \text{Suc} (\text{Suc} (\text{Suc} (\text{length} (\text{compE2 } e1))))$), None)
by(*auto intro: bisim1Sync4 [where pc = 0, simplified]*)
ultimately show *?case using exec τ*
by(*fastforce elim!: exec-meth.cases split: if-split-asm simp add: is-Ref-def exec-move-def ta-bisim-def ta-upd-simps*)
next
assume *?c2*
hence [*simp*]: $v = \text{Null}$ $\text{stk}' = [v]$ $\text{loc}' = xs[V := v]$ $ta = \varepsilon$ $\text{xcp}' = [\text{addr-of-sys-xcpt NullPointer}]$ $\text{pc}' = \text{Suc} (\text{Suc} (\text{length} (\text{compE2 } e1)))$) **by** *simp-all*

from V **have** $True, P, t \vdash 1 \langle sync_V (null) e2, (h, xs) \rangle -\varepsilon \rightarrow \langle THROW NullPointer, (h, xs[V := Null]) \rangle$
by(rule Synchronized1Null)
moreover
have $P, sync_V (e1) e2, h \vdash (THROW NullPointer, xs[V := Null]) \leftrightarrow ([Null], xs[V := Null], Suc (Suc (length (compE2 e1))), [addr-of-sys-xcpt NullPointer])$
by(rule bisim1Sync11)
ultimately show $?case$ **using** $exec \tau$
by(fastforce elim!: exec-meth.cases split: if-split-asm simp add: is-Ref-def exec-move-def)
qed
next
case (bisim1Sync4 e2 n e' xs stk loc pc xcp e1 V a)
note $IH = bisim1Sync4.IH(2)$
note $exec = \langle ?exec (sync_V (e1) e2) stk loc (Suc (Suc (Suc (length (compE2 e1) + pc)))) xcp stk' loc' pc' xcp' \rangle$
note $bisim1 = \langle P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, None) \rangle$
note $bisim2 = \langle P, e2, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
note $len = \langle n + max-vars (insync_V (a) e') \leq length xs \rangle$
note $bsok = \langle bsok (sync_V (e1) e2) n \rangle$
with len **have** $V: V < length xs$ **and** $len': Suc n + max-vars e' \leq length xs$ **by** simp-all
from bisim2 **have** $pc: pc \leq length (compE2 e2)$ **by**(rule bisim1-pc-length-compE2)
let $?pre = compE2 e1 @ [Dup, Store V, MEnter]$
let $?post = [Load V, MExit, Goto 4, Load V, MExit, ThrowExc]$
from $exec$ **have** $exec': exec-meth-d (compP2 P) (?pre @ compE2 e2 @ ?post)$
 $(compxE2 e1 0 0 @ shift (length ?pre) (compxE2 e2 0 0 @ [(0, length (compE2 e2), None, 3 + length (compE2 e2), 0)])) t$
 $h (stk, loc, length ?pre + pc, xcp) ta h' (stk', loc', pc', xcp')$
by(simp add: ac-simps eval-nat-numeral shift-compxE2 exec-move-def)
hence $pc': pc' \geq length ?pre$
by(rule exec-meth-drop-xt-pc[where n'=1]) auto
from $exec'$ **have** $exec'': exec-meth-d (compP2 P) (compE2 e2 @ ?post)$
 $(compxE2 e2 0 0 @ [(0, length (compE2 e2), None, 3 + length (compE2 e2), 0)])$ t
 $h (stk, loc, pc, xcp) ta h' (stk', loc', pc' - length ?pre, xcp')$
by(rule exec-meth-drop-xt[where n=1]) auto
show $?case$
proof(cases $pc < length (compE2 e2)$)
case True
note $pc = True$
hence $\tau: \tau move2 (compP2 P) h stk (sync_V (e1) e2) (Suc (Suc (Suc (length (compE2 e1) + pc))))$
 $xcp = \tau move2 (compP2 P) h stk e2 pc xcp$
by(simp add: $\tau move2$ -iff)
show $?thesis$
proof(cases $xcp = None \vee (\exists a'. xcp = [a'] \wedge match-ex-table (compP2 P) (cname-of h a') pc (compxE2 e2 0 0) \neq None)$)
case False
then obtain a' **where** $Some: xcp = [a']$
and $True: match-ex-table (compP2 P) (cname-of h a') pc (compxE2 e2 0 0) = None$ **by**(auto simp del: not-None-eq)
from $Some \langle conf-xcp' (compP2 P) h xcp \rangle$ **obtain** D
where $ha': typeof-addr h a' = [Class-type D]$ **and** $subcls: P \vdash D \preceq^* Throwable$ **by**(auto simp add: compP2-def)
from ha' $subcls$ bisim2 True bsok **have** $\tau red1r P t h (e', xs) (Throw a', loc)$
using len' **unfolding** $Some$ compP2-def **by**(auto dest!: bisim1-xcp- τ Red simp add: bsok-def)
moreover from pc **have** $\tau move2 (compP2 P) h stk (sync_V (e1) e2) (Suc (Suc (Suc (pc +$

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length (compE2 e1)))) [a']
  by(auto intro:  $\tau$ move2xcp)
  moreover
  have P, syncV (e1) e2, h  $\vdash$  (insyncV (a) Throw a', loc)  $\leftrightarrow$  ([Addr a'], loc, 6 + length (compE2
e1) + length (compE2 e2), None)
  by(rule bisim1Sync7)
  ultimately show ?thesis using exec True pc Some ha' subcls
  apply(auto elim!: exec-meth.cases simp add: ac-simps eval-nat-numeral match-ex-table-append
matches-ex-entry-def compP2-def exec-move-def)

  apply(simp-all only: compxE2-size-conv, auto dest: match-ex-table-shift-pcD match-ex-table-pc-length-compE2)
  apply(fastforce elim!: InSync- $\tau$ red1r-xt)
  done
next
case True
  with exec'' pc have exec-meth-d (compP2 P) (compE2 e2 @ ?post) (compxE2 e2 0 0) t
  h (stk, loc, pc, xcp) ta h' (stk', loc', pc' - length ?pre, xcp')
  by(auto elim!: exec-meth.cases intro: exec-meth.intros simp add: match-ex-table-append exec-move-def)
  hence exec-move-d P t e2 h (stk, loc, pc, xcp) ta h' (stk', loc', pc' - length ?pre, xcp')
  using pc unfolding exec-move-def by(rule exec-meth-take)
  from IH[OF this len' -  $\langle P, h \vdash \text{stk} [\leq] ST \rangle \langle \text{conf-xcp}' (\text{compP2 } P) h \text{ xcp}' \rangle$ ] bsok obtain e'' xs''
  where bisim': P, e2, h'  $\vdash$  (e'', xs'')  $\leftrightarrow$  (stk', loc', pc' - length ?pre, xcp')
  and red': ?red e' xs e'' xs'' e2 stk pc (pc' - length ?pre) xcp xcp' by auto
  from bisim'
  have P, syncV (e1) e2, h'  $\vdash$  (insyncV (a) e'', xs'')  $\leftrightarrow$  (stk', loc', Suc (Suc (Suc (length (compE2
e1) + (pc' - length ?pre))))), xcp')
  by(rule bisim1-bisims1.bisim1Sync4)
  moreover from pc' have Suc (Suc (Suc (length (compE2 e1) + (pc' - Suc (Suc (Suc (length
(compE2 e1))))))) = pc'
  Suc (Suc (Suc (pc' - Suc (Suc (Suc 0)))))) = pc'
  by simp-all
  ultimately show ?thesis using red'  $\tau$ 
  by(fastforce intro: Synchronized1Red2 simp add: eval-nat-numeral no-call2-def elim!: In-
Sync- $\tau$ red1r-xt InSync- $\tau$ red1t-xt split: if-split-asm)
  qed
next
case False
  with pc have [simp]: pc = length (compE2 e2) by simp
  with bisim2 obtain v where [simp]: stk = [v] xcp = None by(auto dest: bisim1-pc-length-compE2D)
  have  $\tau$ move2 (compP2 P) h [v] (syncV (e1) e2) (Suc (Suc (Suc (length (compE2 e1) + length
(compE2 e2)))))) None by(simp add:  $\tau$ move2-iff)
  moreover from bisim2 pc len bsok have red:  $\tau$ red1r P t h (e', xs) (Val v, loc)
  by(auto intro!: bisim1-Val- $\tau$ red1r simp add: bsok-def)
  hence  $\tau$ red1r P t h (insyncV (a) e', xs) (insyncV (a) (Val v), loc) by(rule InSync- $\tau$ red1r-xt)
  moreover
  have P, syncV (e1) e2, h  $\vdash$  (insyncV (a) (Val v), loc)  $\leftrightarrow$  ([loc ! V, v], loc, 4 + length (compE2 e1)
+ length (compE2 e2), None)
  by(rule bisim1Sync5)
  ultimately show ?thesis using exec
  by(auto elim!: exec-meth.cases simp add: eval-nat-numeral exec-move-def) blast
  qed
next
case (bisim1Sync5 e1 n e2 V a v xs)
  note bisim1 =  $\langle P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$ 

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note $bisim2 = \langle P, e2, h \vdash (e2, xs) \leftrightarrow ([], xs, 0, None) \rangle$
note $exec = \langle ?exec (sync_V (e1) e2) [xs ! V, v] xs (4 + length (compE2 e1) + length (compE2 e2))$
None stk' loc' pc' xcp' \rangle
from $\langle n + max-vars (insync_V (a) Val v) \leq length xs \rangle \langle bsok (sync_V (e1) e2) n \rangle$ **have** $V: V < length$
xs by simp
have $\tau: \neg \tau move2 (compP2 P) h [xs ! V, v] (sync_V (e1) e2) (4 + length (compE2 e1) + length$
(compE2 e2)) None
by(*simp add: \tau move2-iff*)
have $\tau': \neg \tau move1 P h (insync_V (a) Val v)$ **by** *auto*
from $exec$ **have** $(\exists a'. xs ! V = Addr a') \vee xs ! V = Null$ (**is** $?c1 \vee ?c2$)
by(*auto elim!: exec-meth.cases simp add: split-beta is-Ref-def exec-move-def split: if-split-asm*)
thus $?case$
proof
assume $?c1$
then obtain a' **where** $xsV [simp]: xs ! V = Addr a' ..$
have $P, sync_V (e1) e2, h \vdash (Val v, xs) \leftrightarrow ([v], xs, 5 + length (compE2 e1) + length (compE2 e2),$
None)
 $P, sync_V (e1) e2, h \vdash (THROW IllegalMonitorState, xs) \leftrightarrow ([Addr a', v], xs, 4 + length (compE2$
 $e1) + length (compE2 e2), [addr-of-sys-xcpt IllegalMonitorState])$
by(*rule bisim1Sync6, rule bisim1Sync12*)
moreover from $xsV V$ **have** $True, P, t \vdash 1 \langle insync_V (a) Val v, (h, xs) \rangle -\{\! \{ Unlock \rightarrow a', SyncUnlock$
 $a' \} \! \} \rightarrow \langle Val v, (h, xs) \rangle$
by(*rule Unlock1Synchronized*)
moreover from $xsV V$ **have** $True, P, t \vdash 1 \langle insync_V (a) Val v, (h, xs) \rangle -\{\! \{ UnlockFail \rightarrow a' \} \! \} \rightarrow$
 $\langle THROW IllegalMonitorState, (h, xs) \rangle$
by(*rule Unlock1SynchronizedFail[OF TrueI]*)
ultimately show $?case$ **using** $\tau \tau' exec$
by (*fastforce elim!: exec-meth.cases simp add: is-Ref-def ta-bisim-def exec-move-def ac-simps*
ta-upd-simps
simp del: conj.left-commute)
next
assume $xsV: xs ! V = Null$
have $P, sync_V (e1) e2, h \vdash (THROW NullPointer, xs) \leftrightarrow ([Null, v], xs, 4 + length (compE2 e1) +$
 $length (compE2 e2), [addr-of-sys-xcpt NullPointer])$
by(*rule bisim1Sync12*)
thus $?case$ **using** $\tau \tau' exec xsV V$
by (*fastforce elim!: exec-meth.cases intro: Unlock1SynchronizedNull simp add: is-Ref-def ta-bisim-def*
ac-simps exec-move-def
simp del: conj.left-commute)
qed
next
case (*bisim1Sync6 e1 n e2 V v x*)
note $bisim1 = \langle P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, None) \rangle$
note $bisim2 = \langle P, e2, h \vdash (e2, xs) \leftrightarrow ([], xs, 0, None) \rangle$
note $exec = \langle ?exec (sync_V (e1) e2) [v] x (5 + length (compE2 e1) + length (compE2 e2)) None$
stk' loc' pc' xcp' \rangle
have $\tau move2 (compP2 P) h [v] (sync_V (e1) e2) (5 + length (compE2 e1) + length (compE2 e2))$
None by (simp add: \tau move2-iff)
moreover
have $P, sync_V (e1) e2, h \vdash (Val v, x) \leftrightarrow ([v], x, length (compE2 (sync_V (e1) e2)), None)$
by(*rule bisim1Val2*) *simp*
moreover have $nat (9 + (int (length (compE2 e1)) + int (length (compE2 e2)))) = 9 + length$
 $(compE2 e1) + length (compE2 e2)$ **by** *arith*
ultimately show $?case$ **using** $exec$

by(*fastforce elim!*: *exec-meth.cases simp add: eval-nat-numeral exec-move-def*)
next
case (*bisim1Sync7 e1 n e2 V a a' xs*)
note $\langle ?exec (sync_V (e1) e2) [Addr a'] xs (6 + length (compE2 e1) + length (compE2 e2)) None$
stk' loc' pc' xcp'
moreover
have $P, sync_V (e1) e2, h' \vdash (insync_V (a) Throw a', xs) \leftrightarrow$
 $([xs ! V, Addr a'], xs, 7 + length (compE2 e1) + length (compE2 e2), None)$
by(*rule bisim1Sync8*)
moreover have $\tau move2 (compP2 P) h [Addr a'] (sync_V (e1) e2) (6 + length (compE2 e1) +$
 $length (compE2 e2)) None$
by(*simp add: $\tau move2$ -iff*)
ultimately show $?case$ **by**(*fastforce elim!*: *exec-meth.cases simp add: eval-nat-numeral exec-move-def*)
next
case (*bisim1Sync8 e1 n e2 V a a' xs*)
from $\langle n + max\text{-vars} (insync_V (a) Throw a') \leq length xs \rangle \langle bsok (sync_V (e1) e2) n \rangle$ **have** $V: V <$
 $length xs$ **by** *simp*
note $\langle ?exec (sync_V (e1) e2) [xs ! V, Addr a'] xs (7 + length (compE2 e1) + length (compE2 e2))$
 $None stk' loc' pc' xcp' \rangle$
moreover have $\neg \tau move2 (compP2 P) h [xs ! V, Addr a'] (sync_V (e1) e2) (7 + length (compE2$
 $e1) + length (compE2 e2)) None$
by(*simp add: $\tau move2$ -iff*)
moreover
have $P, sync_V (e1) e2, h \vdash (Throw a', xs) \leftrightarrow ([Addr a'], xs, 8 + length (compE2 e1) + length$
 $(compE2 e2), None)$
 $P, sync_V (e1) e2, h \vdash (THROW IllegalMonitorState, xs) \leftrightarrow ([xs ! V, Addr a'], xs, 7 + length (compE2$
 $e1) + length (compE2 e2), |addr\text{-of}\text{-sys}\text{-xcp}\ IllegalMonitorState|)$
 $P, sync_V (e1) e2, h \vdash (THROW NullPointerException, xs) \leftrightarrow ([Null, Addr a'], xs, 7 + length (compE2 e1) +$
 $length (compE2 e2), |addr\text{-of}\text{-sys}\text{-xcp}\ NullPointerException|)$
by(*rule bisim1Sync9 bisim1Sync14*)+
moreover {
fix A
assume $xs ! V = Addr A$
hence $True, P, t \vdash 1 \langle insync_V (a) Throw a', (h, xs) \rangle -\{\!| Unlock \rightarrow A, SyncUnlock A \}\! \rightarrow \langle Throw a',$
 $(h, xs) \rangle$
 $True, P, t \vdash 1 \langle insync_V (a) Throw a', (h, xs) \rangle -\{\!| UnlockFail \rightarrow A \}\! \rightarrow \langle THROW IllegalMonitorState,$
 $(h, xs) \rangle$
using V **by**(*rule Synchronized1Throw2 Synchronized1Throw2Fail[OF TrueI]*)+ }
moreover {
assume $xs ! V = Null$
hence $True, P, t \vdash 1 \langle insync_V (a) Throw a', (h, xs) \rangle -\varepsilon \rightarrow \langle THROW NullPointerException, (h, xs) \rangle$
using V **by**(*rule Synchronized1Throw2Null*) }
moreover have $\neg \tau move1 P h (insync_V (a) Throw a')$ **by** *fastforce*
ultimately show $?case$
by(*fastforce elim!*: *exec-meth.cases simp add: eval-nat-numeral is-Ref-def ta-bisim-def ta-upd-simps*
exec-move-def split: if-split-asm)
next
case (*bisim1Sync9 e1 n e2 V a xs*)
note $\langle ?exec (sync_V (e1) e2) [Addr a] xs (8 + length (compE2 e1) + length (compE2 e2)) None$
 $stk' loc' pc' xcp' \rangle$
moreover
have $P, sync_V (e1) e2, h \vdash (Throw a, xs) \leftrightarrow ([Addr a], xs, 8 + length (compE2 e1) + length (compE2$
 $e2), [a])$
by(*rule bisim1Sync10*)

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moreover have  $\tau_{move2}$  (compP2 P) h [Addr a] (syncV (e1) e2) (8 + length (compE2 e1) + length
(compE2 e2)) None
  by(rule  $\tau_{move2Sync8}$ )
ultimately show ?case
  by(fastforce elim!: exec-meth.cases simp add: eval-nat-numeral exec-move-def split: if-split-asm)
next
  case (bisim1Sync10 e1 n e2 V a xs)
  from  $\langle ?exec$  (syncV (e1) e2) [Addr a] xs (8 + length (compE2 e1) + length (compE2 e2)) [a] stk'
loc' pc' xcp'  $\rangle$ 
  have False by(auto elim!: exec-meth.cases simp add: matches-ex-entry-def match-ex-table-append-not-pcs
exec-move-def)
  thus ?case ..
next
  case (bisim1Sync11 e1 n e2 V xs)
  from  $\langle ?exec$  (syncV (e1) e2) [Null] xs (Suc (Suc (length (compE2 e1)))) [addr-of-sys-xcpt Null-
Pointer] stk' loc' pc' xcp'  $\rangle$ 
  have False by(auto elim!: exec-meth.cases simp add: matches-ex-entry-def match-ex-table-append-not-pcs
exec-move-def)
  thus ?case ..
next
  case (bisim1Sync12 e1 n e2 V a xs v v')
  from  $\langle ?exec$  (syncV (e1) e2) [v, v'] xs (4 + length (compE2 e1) + length (compE2 e2)) [a] stk'
loc' pc' xcp'  $\rangle$ 
  have False by(auto elim!: exec-meth.cases simp add: matches-ex-entry-def match-ex-table-append-not-pcs
exec-move-def)
  thus ?case ..
next
  case (bisim1Sync14 e1 n e2 V a xs v a')
  from  $\langle ?exec$  (syncV (e1) e2) [v, Addr a] xs (7 + length (compE2 e1) + length (compE2 e2)) [a]
stk' loc' pc' xcp'  $\rangle$ 
  have False by(auto elim!: exec-meth.cases simp add: matches-ex-entry-def match-ex-table-append-not-pcs
exec-move-def)
  thus ?case ..
next
  case bisim1InSync thus ?case by simp
next
  case (bisim1SyncThrow e1 n a xs stk loc pc e2 V)
  note exec =  $\langle ?exec$  (syncV (e1) e2) stk loc pc [a] stk' loc' pc' xcp'  $\rangle$ 
  note bisim1 =  $\langle P, e1, h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$ 
  from bisim1 have pc: pc < length (compE2 e1) by(auto dest: bisim1-ThrowD)
  from bisim1 have match-ex-table (compP2 P) (cname-of h a) (0 + pc) (compE2 e1 0 0) = None
  unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
  with exec pc have False
  apply(auto elim!: exec-meth.cases simp add: match-ex-table-append-not-pcs exec-move-def)
  apply(auto dest!: match-ex-table-shift-pcD simp add: matches-ex-entry-def)
  done
  thus ?case ..
next
  case (bisim1Seq1 e1 n e' xs stk loc pc xcp e2)
  note IH = bisim1Seq1.IH(2)
  note exec =  $\langle ?exec$  (e1;; e2) stk loc pc xcp stk' loc' pc' xcp'  $\rangle$ 
  note bisim =  $\langle P, e1, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
  note len =  $\langle n + max\text{-vars}(e'; e2) \leq length\ xs \rangle$ 
  note bsok =  $\langle bsok(e1;; e2)\ n \rangle$ 

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from bisim have pc:  $pc \leq \text{length}(\text{compE2 } e1)$  by(rule bisim1-pc-length-compE2)
show ?case
proof(cases  $pc < \text{length}(\text{compE2 } e1)$ )
  case True
    let ?post = Pop # compE2 e2
      from exec have exec': ?exec e1 stk loc pc xcp stk' loc' pc' xcp' using True by(simp add:
exec-move-Seq1)
      from True have  $\tau\text{move2}(\text{compP2 } P) h \text{ stk } (e1;;e2) pc xcp = \tau\text{move2}(\text{compP2 } P) h \text{ stk } e1 pc$ 
xcp by(simp add:  $\tau\text{move2-iff}$ )
      with IH[OF exec' - -  $\langle P, h \vdash \text{stk } [:\leq] ST \rangle$   $\langle \text{conf-xcp}'(\text{compP2 } P) h \text{ xcp} \rangle$ ] len bsok show ?thesis
      by(fastforce intro: bisim1-bisims1.bisim1Seq1 Seq1Red elim!: Seq- $\tau$ red1r-xt Seq- $\tau$ red1t-xt simp add:
no-call2-def)
    next
      case False
        with pc have [simp]:  $pc = \text{length}(\text{compE2 } e1)$  by simp
        with bisim obtain v where stk:  $stk = [v]$  and xcp:  $xcp = \text{None}$ 
          by(auto dest: bisim1-pc-length-compE2D)
        with bisim pc len bsok have red:  $\tau\text{red1r } P t h (e', xs) (Val v, loc)$ 
          by(auto intro: bisim1-Val- $\tau$ red1r simp add: bsok-def)
        hence  $\tau\text{red1r } P t h (e';; e2, xs) (Val v;;e2, loc)$  by(rule Seq- $\tau$ red1r-xt)
        also have  $\tau\text{move1 } P h (Val v;;e2)$  by(rule  $\tau\text{move1SeqRed}$ )
        hence  $\tau\text{red1r } P t h (Val v;;e2, loc) (e2, loc)$  by(auto intro: Red1Seq r-into-rtranclp)
        also have  $\tau$ :  $\tau\text{move2}(\text{compP2 } P) h [v] (e1;;e2) pc \text{ None}$  by(simp add:  $\tau\text{move2-iff}$ )
        moreover from  $\langle P, e2, h \vdash (e2, loc) \leftrightarrow ([], loc, 0, \text{None}) \rangle$ 
          have  $P, e1;;e2, h \vdash (e2, loc) \leftrightarrow ([], loc, \text{Suc}(\text{length}(\text{compE2 } e1) + 0), \text{None})$ 
          by(rule bisim1Seq2)
        ultimately show ?thesis using exec stk xcp
          by(fastforce elim!: exec-meth.cases simp add: exec-move-def)
      qed
    next
      case (bisim1SeqThrow1 e1 n a xs stk loc pc e2)
        note exec =  $\langle ?exec (e1;;e2) \text{ stk loc pc } [a] \text{ stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$ 
        note bisim1 =  $\langle P, e1, h \vdash (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a]) \rangle$ 
        from bisim1 have pc:  $pc < \text{length}(\text{compE2 } e1)$  by(auto dest: bisim1-ThrowD)
        from bisim1 have match-ex-table (compP2 P) (cname-of h a) ( $0 + pc$ ) (compxE2 e1 0 0) = None
          unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
        with exec pc have False
          by(auto elim!: exec-meth.cases simp add: match-ex-table-not-pcs-None exec-move-def)
        thus ?case ..
      next
        case (bisim1Seq2 e2 n e' xs stk loc pc xcp e1)
          note IH = bisim1Seq2.IH(2)
          note bisim1 =  $\langle \bigwedge xs. P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$ 
          note bisim2 =  $\langle P, e2, h \vdash (e', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \rangle$ 
          note len =  $\langle n + \text{max-vars } e' \leq \text{length } xs \rangle$ 
          note exec =  $\langle ?exec (e1;; e2) \text{ stk loc } (\text{Suc}(\text{length}(\text{compE2 } e1) + \text{pc})) \text{ xcp } \text{stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$ 
          note bsok =  $\langle \text{bsok } (e1;; e2) n \rangle$ 
          let ?pre = compE2 e1 @ [Pop]
            from exec have exec': exec-meth-d (compP2 P) (?pre @ compE2 e2) (compxE2 e1 0 0 @ shift (length
?pre) (compxE2 e2 0 0)) t h (stk, loc, length ?pre + pc, xcp) ta h' (stk', loc', pc', xcp')
              by(simp add: shift-compxE2 exec-move-def)
            hence ?exec e2 stk loc pc xcp stk' loc' (pc' - length ?pre) xcp'
              unfolding exec-move-def by(rule exec-meth-drop-xt, auto)
            from IH[OF this len -  $\langle P, h \vdash \text{stk } [:\leq] ST \rangle$   $\langle \text{conf-xcp}'(\text{compP2 } P) h \text{ xcp} \rangle$ ] bsok obtain e'' xs''

```

where $\text{bisim}' : P, e2, h' \vdash (e'', xs'') \leftrightarrow (\text{stk}', \text{loc}', \text{pc}' - \text{length } ?pre, \text{xcp}')$
and $\text{red} : ?red \ e' \ xs \ e'' \ xs'' \ e2 \ \text{stk} \ \text{pc} \ (\text{pc}' - \text{length } ?pre) \ \text{xcp} \ \text{xcp}'$ **by** *auto*
from bisim'
have $P, e1;;e2, h' \vdash (e'', xs'') \leftrightarrow (\text{stk}', \text{loc}', \text{Suc} (\text{length} (\text{compE2 } e1) + (\text{pc}' - \text{length } ?pre)), \text{xcp}')$
by (*rule bisim1-bisims1.bisim1Seq2*)
moreover have $\tau : \tau\text{move2} (\text{compP2 } P) \ h \ \text{stk} \ (e1;;e2) \ (\text{Suc} (\text{length} (\text{compE2 } e1) + \text{pc})) \ \text{xcp} =$
 $\tau\text{move2} (\text{compP2 } P) \ h \ \text{stk} \ e2 \ \text{pc} \ \text{xcp}$
by (*simp add: $\tau\text{move2-iff}$*)
moreover from exec' **have** $\text{pc}' \geq \text{length } ?pre$
by (*rule exec-meth-drop-xt-pc*) *auto*
ultimately show $?case$ **using** red **by** (*fastforce split: if-split-asm simp add: no-call2-def*)
next
case (*bisim1Cond1 e n e' xs stk loc pc xcp e1 e2*)
note $IH = \text{bisim1Cond1.IH}(2)$
note $\text{bisim} = \langle P, e, h \vdash (e', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \rangle$
note $\text{bisim1} = \langle \bigwedge xs. P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$
note $\text{bisim2} = \langle \bigwedge xs. P, e2, h \vdash (e2, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$
from $\langle n + \text{max-vars} \ (\text{if } (e') \ e1 \ \text{else } e2) \leq \text{length } xs \rangle$
have $\text{len} : n + \text{max-vars } e' \leq \text{length } xs$ **by** *simp*
note $\text{exec} = \langle ?exec \ (\text{if } (e) \ e1 \ \text{else } e2) \ \text{stk} \ \text{loc} \ \text{pc} \ \text{xcp} \ \text{stk}' \ \text{loc}' \ \text{pc}' \ \text{xcp}' \rangle$
note $\text{bsok} = \langle \text{bsok} \ (\text{if } (e) \ e1 \ \text{else } e2) \ n \rangle$
from bisim **have** $\text{pc} : \text{pc} \leq \text{length} (\text{compE2 } e)$ **by** (*rule bisim1-pc-length-compE2*)
show $?case$
proof (*cases pc < length (compE2 e)*)
case *True*
let $?post = \text{IfFalse} \ (2 + \text{int} (\text{length} (\text{compE2 } e1))) \ \# \ \text{compE2 } e1 \ @ \ \text{Goto} \ (1 + \text{int} (\text{length} (\text{compE2 } e2))) \ \# \ \text{compE2 } e2$
from exec **have** $\text{exec}' : ?exec \ e \ \text{stk} \ \text{loc} \ \text{pc} \ \text{xcp} \ \text{stk}' \ \text{loc}' \ \text{pc}' \ \text{xcp}'$ **using** *True*
by (*simp add: exec-move-Cond1*)
from *True* **have** $\tau\text{move2} (\text{compP2 } P) \ h \ \text{stk} \ (\text{if } (e) \ e1 \ \text{else } e2) \ \text{pc} \ \text{xcp} = \tau\text{move2} (\text{compP2 } P) \ h$
 $\text{stk} \ e \ \text{pc} \ \text{xcp}$
by (*simp add: $\tau\text{move2-iff}$*)
with $IH[OF \ \text{exec}' \ - \ \langle P, h \vdash \text{stk} \ [:\leq] \ ST \rangle \ \langle \text{conf-} \text{xcp}' \ (\text{compP2 } P) \ h \ \text{xcp}' \rangle] \ \text{len} \ \text{bsok}$ **show** $?thesis$
by (*fastforce intro: bisim1-bisims1.bisim1Cond1 Cond1Red elim!: Cond- $\tau\text{red1r-xt}$ Cond- $\tau\text{red1t-xt}$ simp add: no-call2-def*)
next
case *False*
with pc **have** $[\text{simp}] : \text{pc} = \text{length} (\text{compE2 } e)$ **by** *simp*
with bisim **obtain** v **where** $\text{stk} : \text{stk} = [v]$ **and** $\text{xcp} : \text{xcp} = \text{None}$
by (*auto dest: bisim1-pc-length-compE2D*)
with $\text{bisim} \ \text{pc} \ \text{len} \ \text{bsok}$ **have** $\text{red} : \tau\text{red1r } P \ t \ h \ (e', xs) \ (\text{Val } v, \text{loc})$
by (*auto intro: bisim1-Val- τred1r simp add: bsok-def*)
hence $\tau\text{red1r } P \ t \ h \ (\text{if } (e') \ e1 \ \text{else } e2, xs) \ (\text{if } (\text{Val } v) \ e1 \ \text{else } e2, \text{loc})$ **by** (*rule Cond- $\tau\text{red1r-xt}$*)
moreover have $\tau\text{move1 } P \ h \ (\text{if } (\text{Val } v) \ e1 \ \text{else } e2)$ **by** (*rule $\tau\text{move1CondRed}$*)
moreover have $\tau : \tau\text{move2} (\text{compP2 } P) \ h \ [v] \ (\text{if } (e) \ e1 \ \text{else } e2) \ \text{pc} \ \text{None}$ **by** (*simp add: $\tau\text{move2-iff}$*)
moreover from $\text{bisim1}[\text{of } \text{loc}]$
have $P, \text{if } (e) \ e1 \ \text{else } e2, h \vdash (e1, \text{loc}) \leftrightarrow ([], \text{loc}, \text{Suc} (\text{length} (\text{compE2 } e) + 0), \text{None})$
by (*rule bisim1CondThen*)
moreover from $\text{bisim2}[\text{of } \text{loc}]$
have $P, \text{if } (e) \ e1 \ \text{else } e2, h \vdash (e2, \text{loc}) \leftrightarrow ([], \text{loc}, \text{Suc} (\text{Suc} (\text{length} (\text{compE2 } e) + \text{length} (\text{compE2 } e1) + 0)), \text{None})$
by (*rule bisim1CondElse*)
moreover have $\text{nat} \ (\text{int} (\text{length} (\text{compE2 } e)) + (2 + \text{int} (\text{length} (\text{compE2 } e1)))) = \text{Suc} \ (\text{Suc} \ (\text{length} (\text{compE2 } e) + \text{length} (\text{compE2 } e1) + 0))$ **by** *simp*

moreover from $\text{exec } xcp \text{ stk have } \text{typeof}_h v = [\text{Boolean}]$ **by**($\text{auto simp add: exec-move-def exec-meth-instr}$)

ultimately show $?thesis$ **using** $\text{exec } stk \text{ xcp}$

by($\text{fastforce elim!: exec-meth.cases intro: Red1CondT Red1CondF elim!: rtranclp.rtrancl-into-rtrancl simp add: eval-nat-numeral exec-move-def}$)

qed

next

case ($\text{bisim1CondThen } e1 \ n \ e' \ xs \ stk \ loc \ pc \ xcp \ e \ e2$)

note $IH = \text{bisim1CondThen.IH}(2)$

note $\text{bisim1} = \langle P, e1, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$

note $\text{bisim} = \langle \bigwedge xs. P, e, h \vdash (e, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$

note $\text{bisim2} = \langle \bigwedge xs. P, e2, h \vdash (e2, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$

note $\text{len} = \langle n + \text{max-vars } e' \leq \text{length } xs \rangle$

note $\text{exec} = \langle ?\text{exec} \text{ (if } (e) \ e1 \ \text{else } e2) \text{ stk } loc \text{ (Suc (length (compE2 } e) + pc)) \text{ xcp } stk' \ loc' \ pc' \ xcp' \rangle$

note $\text{bsok} = \langle \text{bsok} \text{ (if } (e) \ e1 \ \text{else } e2) \ n \rangle$

from bisim1 **have** $pc: pc \leq \text{length (compE2 } e1)$ **by**($\text{rule bisim1-pc-length-compE2}$)

show $?case$

proof($\text{cases } pc < \text{length (compE2 } e1)$)

case True

let $?pre = \text{compE2 } e \ @ \ [\text{IfFalse } (2 + \text{int (length (compE2 } e1)))]$

let $?post = \text{Goto } (1 + \text{int (length (compE2 } e2))) \ # \ \text{compE2 } e2$

from exec **have** $\text{exec}': \text{exec-meth-d (compP2 } P) (?pre \ @ \ \text{compE2 } e1 \ @ \ ?post)$

$(\text{compxE2 } e \ 0 \ 0 \ @ \ \text{shift (length ?pre) (compxE2 } e1 \ 0 \ 0 \ @ \ \text{shift (length (compE2 } e1)) (compxE2 } e2 \ (\text{Suc } 0) \ 0)) \ t$

$h \text{ (stk, loc, length ?pre + pc, xcp) ta } h' \text{ (stk', loc', pc', xcp')}$

by($\text{simp add: shift-compxE2 ac-simps exec-move-def}$)

hence $\text{exec-meth-d (compP2 } P) (\text{compE2 } e1 \ @ \ ?post) (\text{compxE2 } e1 \ 0 \ 0 \ @ \ \text{shift (length (compE2 } e1)) (compxE2 } e2 \ (\text{Suc } 0) \ 0)) \ t$

$h \text{ (stk, loc, pc, xcp) ta } h' \text{ (stk', loc', pc' - length ?pre, xcp')}$

by($\text{rule exec-meth-drop-xt}$) auto

hence $\text{exec-move-d } P \ t \ e1 \ h \text{ (stk, loc, pc, xcp) ta } h' \text{ (stk', loc', pc' - length ?pre, xcp')}$

using True **unfolding** exec-move-def **by**($\text{rule exec-meth-take-xt}$)

from $IH[\text{OF this len - } \langle P, h \vdash \text{stk } [:\leq] \text{ST} \rangle \langle \text{conf-xcp' (compP2 } P) \ h \ \text{xcp} \rangle]$ bsok **obtain** $e'' \ xs''$

where $\text{bisim}': P, e1, h' \vdash (e'', xs'') \leftrightarrow (stk', loc', pc' - \text{length ?pre}, xcp')$

and $\text{red}: ?\text{red } e' \ xs \ e'' \ xs'' \ e1 \ stk \ pc \ (pc' - \text{length ?pre}) \ xcp \ xcp'$ **by** auto

from bisim'

have $P, \text{if } (e) \ e1 \ \text{else } e2, h' \vdash (e'', xs'') \leftrightarrow (stk', loc', \text{Suc (length (compE2 } e) + (pc' - \text{length ?pre})), xcp')$

by($\text{rule bisim1-bisims1.bisim1CondThen}$)

moreover from True **have** $\tau: \tau\text{move2 (compP2 } P) \ h \ \text{stk} \text{ (if } (e) \ e1 \ \text{else } e2) \ (\text{Suc (length (compE2 } e) + pc)) \ xcp = \tau\text{move2 (compP2 } P) \ h \ \text{stk} \ e1 \ pc \ xcp$

by($\text{simp add: } \tau\text{move2-iff}$)

moreover from exec' **have** $pc' \geq \text{length ?pre}$

by($\text{rule exec-meth-drop-xt-pc}$) auto

ultimately show $?thesis$ **using** red **by**($\text{fastforce split: if-split-asm simp add: no-call2-def}$)

next

case False

with pc **have** $[\text{simp}]: pc = \text{length (compE2 } e1)$ **by** simp

with bisim1 **obtain** v **where** $\text{stk}: \text{stk} = [v]$ **and** $\text{xcp}: \text{xcp} = \text{None}$

by($\text{auto dest: bisim1-pc-length-compE2D}$)

with bisim1 pc len bsok **have** $\text{red}: \tau\text{red1r } P \ t \ h \ (e', xs) \ (\text{Val } v, loc)$

by($\text{auto intro: bisim1-Val-}\tau\text{red1r simp add: bsok-def}$)

moreover have $\tau: \tau\text{move2 (compP2 } P) \ h \ [v] \text{ (if } (e) \ e1 \ \text{else } e2) \ (\text{Suc (length (compE2 } e) + \text{length (compE2 } e1))) \ \text{None}$

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  by(simp add:  $\tau$ move2-iff)
moreover
  have  $P, \text{if } (e) e1 \text{ else } e2, h \vdash (\text{Val } v, \text{loc}) \leftrightarrow ([v], \text{loc}, \text{length } (\text{compE2 } (\text{if } (e) e1 \text{ else } e2)), \text{None})$ 
    by(rule bisim1Val2) simp
  moreover have  $\text{nat } (2 + (\text{int } (\text{length } (\text{compE2 } e)) + (\text{int } (\text{length } (\text{compE2 } e1)) + \text{int } (\text{length } (\text{compE2 } e2)))) = \text{length } (\text{compE2 } (\text{if } (e) e1 \text{ else } e2))$  by simp
  ultimately show ?thesis using exec xcp stk by(fastforce elim!: exec-meth.cases simp add: exec-move-def)
qed
next
  case (bisim1CondElse e2 n e' xs stk loc pc xcp e e1)
  note  $IH = \text{bisim1CondElse.IH}(2)$ 
  note  $\text{bisim2} = \langle P, e2, h \vdash (e', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \rangle$ 
  note  $\text{bisim} = \langle \bigwedge xs. P, e, h \vdash (e, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$ 
  note  $\text{bisim1} = \langle \bigwedge xs. P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$ 
  note  $\text{len} = \langle n + \text{max-vars } e' \leq \text{length } xs \rangle$ 
  note  $\text{exec} = \langle ?\text{exec } (\text{if } (e) e1 \text{ else } e2) \text{ stk loc } (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e) + \text{length } (\text{compE2 } e1) + \text{pc}))) \text{ xcp stk' loc' pc' xcp}' \rangle$ 
  note  $\text{bsok} = \langle \text{bsok } (\text{if } (e) e1 \text{ else } e2) n \rangle$ 
  let  $?pre = \text{compE2 } e @ \text{IfFalse } (2 + \text{int } (\text{length } (\text{compE2 } e1))) \# \text{compE2 } e1 @ [\text{Goto } (1 + \text{int } (\text{length } (\text{compE2 } e2)))]$ 
  from  $\text{exec}$  have  $\text{exec}' : \text{exec-meth-d } (\text{compP2 } P) (?pre @ \text{compE2 } e2) ((\text{compxE2 } e 0 0 @ \text{compxE2 } e1 (\text{Suc } (\text{length } (\text{compE2 } e))) 0) @ \text{shift } (\text{length } ?pre) (\text{compxE2 } e2 0 0)) t$ 
   $h (\text{stk}, \text{loc}, \text{length } ?pre + \text{pc}, \text{xcp}) \text{ ta } h' (\text{stk}', \text{loc}', \text{pc}', \text{xcp}')$ 
  by(simp add: shift-compxE2 ac-simps exec-move-def)
  hence  $?exec e2 \text{ stk loc pc xcp stk' loc' } (\text{pc}' - \text{length } ?pre) \text{ xcp}'$ 
  unfolding  $\text{exec-move-def}$  by(rule exec-meth-drop-xt) auto
  from  $IH[OF \text{ this len} - \langle P, h \vdash \text{stk } [:\leq] ST \rangle \langle \text{conf-xcp}' (\text{compP2 } P) h \text{ xcp}' \rangle \text{bsok}]$  obtain  $e'' xs''$ 
  where  $\text{bisim}' : P, e2, h' \vdash (e'', xs'') \leftrightarrow (\text{stk}', \text{loc}', \text{pc}' - \text{length } ?pre, \text{xcp}'^{\wedge})$ 
  and  $\text{red} : ?\text{red } e' xs e'' xs'' e2 \text{ stk pc } (\text{pc}' - \text{length } ?pre) \text{ xcp xcp}'$  by  $\text{auto}$ 
  from  $\text{bisim}'$ 
  have  $P, \text{if } (e) e1 \text{ else } e2, h' \vdash (e'', xs'') \leftrightarrow (\text{stk}', \text{loc}', \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e) + \text{length } (\text{compE2 } e1) + (\text{pc}' - \text{length } ?pre))), \text{xcp}'^{\wedge})$ 
  by(rule bisim1-bisims1.bisim1CondElse)
  moreover have  $\tau : \tau\text{move2 } (\text{compP2 } P) h \text{ stk } (\text{if } (e) e1 \text{ else } e2) (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e) + \text{length } (\text{compE2 } e1) + \text{pc}))) \text{ xcp} = \tau\text{move2 } (\text{compP2 } P) h \text{ stk } e2 \text{ pc xcp}$ 
  by(simp add:  $\tau$ move2-iff)
  moreover from  $\text{exec}'$  have  $\text{pc}' \geq \text{length } ?pre$ 
  by(rule exec-meth-drop-xt-pc) auto
  moreover hence  $\text{Suc } (\text{Suc } (\text{pc}' - \text{Suc } (\text{Suc } 0))) = \text{pc}'$  by  $\text{simp}$ 
  ultimately show ?case using red by(fastforce simp add: eval-nat-numeral no-call2-def split: if-split-asm)
next
  case (bisim1CondThrow e n a xs stk loc pc e1 e2)
  note  $\text{exec} = \langle ?\text{exec } (\text{if } (e) e1 \text{ else } e2) \text{ stk loc pc } [a] \text{ stk' loc' pc' xcp}' \rangle$ 
  note  $\text{bisim} = \langle P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a]) \rangle$ 
  from  $\text{bisim}$  have  $\text{pc} : \text{pc} < \text{length } (\text{compE2 } e)$  by(auto dest: bisim1-ThrowD)
  from  $\text{bisim}$  have  $\text{match-ex-table } (\text{compP2 } P) (\text{cname-of } h a) (0 + \text{pc}) (\text{compxE2 } e 0 0) = \text{None}$ 
  unfolding  $\text{compP2-def}$  by(rule bisim1-xcp-Some-not-caught)
  with  $\text{exec pc}$  have  $\text{False}$ 
  by(auto elim!: exec-meth.cases simp add: match-ex-table-not-pcs-None exec-move-def)
  thus  $?case \dots$ 
next
  case (bisim1While1 c n e xs)
  note  $IH = \text{bisim1While1.IH}(2)$ 

```

note $bisim = \langle \bigwedge xs. P, c, h \vdash (c, xs) \leftrightarrow ([], xs, 0, None) \rangle$
note $bisim1 = \langle \bigwedge xs. P, e, h \vdash (e, xs) \leftrightarrow ([], xs, 0, None) \rangle$
from $\langle n + max\text{-vars } (while\ (c)\ e) \leq length\ xs \rangle$
have $len: n + max\text{-vars } c \leq length\ xs$ **by** *simp*
note $exec = \langle ?exec\ (while\ (c)\ e) \ []\ xs\ 0\ None\ stk'\ loc'\ pc'\ xcp' \rangle$
note $bsok = \langle bsok\ (while\ (c)\ e)\ n \rangle$
let $?post = IfFalse\ (int\ (length\ (compE2\ e)) + 3) \# compE2\ e\ @\ [Pop,\ Goto\ (-2 + (- int\ (length\ (compE2\ e)) - int\ (length\ (compE2\ c))))],\ Push\ Unit]$
from $exec$ **have** $?exec\ c \ []\ xs\ 0\ None\ stk'\ loc'\ pc'\ xcp'$ **by** (*simp add: exec-move-While1*)
from $IH[OF\ this\ len]\ bsok$ **obtain** $e''\ xs''$
where $bisim': P, c, h' \vdash (e'', xs'') \leftrightarrow (stk', loc', pc', xcp')$
and $red: ?red\ c\ xs\ e''\ xs''\ c \ []\ 0\ pc'\ None\ xcp'$ **by** *fastforce*
from $bisim'$
have $P, while\ (c)\ e, h' \vdash (if\ (e'')\ (e;;while(c)\ e)\ else\ unit,\ xs'') \leftrightarrow (stk', loc', pc', xcp')$
by (*rule bisim1While3*)
moreover **have** $True, P, t \vdash 1 \langle while\ (c)\ e,\ (h,\ xs) \rangle -\varepsilon \rightarrow \langle if\ (c)\ (e;;while\ (c)\ e)\ else\ unit,\ (h,\ xs) \rangle$
by (*rule Red1While*)
hence $\tau red1r\ P\ t\ h\ (while\ (c)\ e,\ xs)\ (if\ (c)\ (e;;while\ (c)\ e)\ else\ unit,\ xs)$
by (*auto intro: r-into-rtranclp \tau move1WhileRed*)
moreover **have** $\tau move2\ (compP2\ P)\ h \ []\ (while\ (c)\ e)\ 0\ None = \tau move2\ (compP2\ P)\ h \ []\ c\ 0\ None$
by (*simp add: \tau move2-iff*)
ultimately show $?case$ **using** *red*
by (*fastforce elim!: rtranclp-trans rtranclp-tranclp-tranclp intro: Cond1Red elim!: Cond-\tau red1r-xt Cond-\tau red1t-xt simp add: no-call2-def*)
next
case (*bisim1While3 c n e' xs stk loc pc xcp e*)
note $IH = bisim1While3.IH(2)$
note $bisim = \langle P, c, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
note $bisim1 = \langle \bigwedge xs. P, e, h \vdash (e, xs) \leftrightarrow ([], xs, 0, None) \rangle$
from $\langle n + max\text{-vars } (if\ (e')\ (e;;while\ (c)\ e)\ else\ unit) \leq length\ xs \rangle$
have $len: n + max\text{-vars } e' \leq length\ xs$ **by** *simp*
note $bsok = \langle bsok\ (while\ (c)\ e)\ n \rangle$
note $exec = \langle ?exec\ (while\ (c)\ e)\ stk\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$
from $bisim$ **have** $pc: pc \leq length\ (compE2\ c)$ **by** (*rule bisim1-pc-length-compE2*)
show $?case$
proof (*cases pc < length (compE2 c)*)
case *True*
let $?post = IfFalse\ (int\ (length\ (compE2\ e)) + 3) \# compE2\ e\ @\ [Pop,\ Goto\ (-2 + (- int\ (length\ (compE2\ e)) - int\ (length\ (compE2\ c))))],\ Push\ Unit]$
from $exec$ **have** $exec\ meth\ d\ (compP2\ P)\ (compE2\ c\ @\ ?post)\ (compxE2\ c\ 0\ 0\ @\ shift\ (length\ (compE2\ c))\ (compxE2\ e\ (Suc\ 0)\ 0))\ t\ h\ (stk,\ loc,\ pc,\ xcp)\ ta\ h'\ (stk', loc', pc', xcp')$
by (*simp add: shift-compxE2 exec-move-def*)
hence $?exec\ c\ stk\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp'$
using *True unfolding exec-move-def* **by** (*rule exec-meth-take-xt*)
from $IH[OF\ this\ len - \langle P, h \vdash stk\ [:\leq]\ ST \rangle \langle conf\ xcp'\ (compP2\ P)\ h\ xcp' \rangle]$ $bsok$ **obtain** $e''\ xs''$
where $bisim': P, c, h' \vdash (e'', xs'') \leftrightarrow (stk', loc', pc', xcp')$
and $red: ?red\ e'\ xs\ e''\ xs''\ c\ stk\ pc\ pc'\ xcp\ xcp'$ **by** *auto*
from $bisim'$
have $P, while\ (c)\ e, h' \vdash (if\ (e'')\ (e;;while(c)\ e)\ else\ unit,\ xs'') \leftrightarrow (stk', loc', pc', xcp')$
by (*rule bisim1-bisims1.bisim1While3*)
moreover **have** $\tau move2\ (compP2\ P)\ h\ stk\ (while\ (c)\ e)\ pc\ xcp = \tau move2\ (compP2\ P)\ h\ stk\ c\ pc$
xcp **using** *True*
by (*simp add: \tau move2-iff*)
ultimately show $?thesis$ **using** *red*


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  by(fastforce elim!: rtranclp-trans intro: Cond1Red elim!: Cond- $\tau$ red1r-xt Cond- $\tau$ red1t-xt simp add:
no-call2-def)
next
  case False
  with pc have [simp]: pc = length (compE2 c) by simp
  with bisim obtain v where stk: stk = [v] and xcp: xcp = None
  by(auto dest: bisim1-pc-length-compE2D)
  with bisim pc len bsok have red:  $\tau$ red1r P t h (e', xs) (Val v, loc)
  by(auto intro: bisim1-Val- $\tau$ red1r simp add: bsok-def)
  hence  $\tau$ red1r P t h (if (e') (e;; while (c) e) else unit, xs) (if (Val v) (e;; while (c) e) else unit, loc)
  by(rule Cond- $\tau$ red1r-xt)
  moreover have  $\tau$ :  $\tau$ move2 (compP2 P) h [v] (while (c) e) (length (compE2 c)) None by(simp
add:  $\tau$ move2-iff)
  moreover have  $\tau$ move1 P h (if (Val v) (e;;while (c) e) else unit) by(rule  $\tau$ move1CondRed)
  moreover from bisim1[of loc]
  have P,while (c) e,h  $\vdash$  (e;;while(c) e, loc)  $\leftrightarrow$  ([], loc, Suc (length (compE2 c) + 0), None)
  by(rule bisim1While4)
  moreover
  have P,while (c) e,h  $\vdash$  (unit, loc)  $\leftrightarrow$  ([], loc, Suc (Suc (Suc (length (compE2 c) + length (compE2
e))))), None)
  by(rule bisim1While7)
  moreover from exec stk xcp have typeofh v = [Boolean]
  by(auto simp add: exec-meth-instr exec-move-def)
  moreover have nat (int (length (compE2 c)) + (3 + int (length (compE2 e)))) = Suc (Suc (Suc
(length (compE2 c) + length (compE2 e)))) by simp
  ultimately show ?thesis using exec stk xcp
  by(fastforce elim!: exec-meth.cases rtranclp-trans intro: Red1CondT Red1CondF simp add:
eval-nat-numeral exec-move-def)
qed
next
  case (bisim1While4 e n e' xs stk loc pc xcp c)
  note IH = bisim1While4.IH(2)
  note bisim =  $\langle \wedge xs. P, c, h \vdash (c, xs) \leftrightarrow ([], xs, 0, None) \rangle$ 
  note bisim1 =  $\langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
  from  $\langle n + \text{max-vars } (e'; \text{ while } (c) e) \leq \text{length } xs \rangle$ 
  have len:  $n + \text{max-vars } e' \leq \text{length } xs$  by simp
  note exec =  $\langle ?exec (while (c) e) \text{ stk loc } (Suc (\text{length } (\text{compE2 } c) + pc)) \text{ xcp } \text{stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$ 
  note bsok =  $\langle \text{bsok } (while (c) e) \text{ n} \rangle$ 
  from bisim1 have pc:  $pc \leq \text{length } (\text{compE2 } e)$  by(rule bisim1-pc-length-compE2)
  show ?case
  proof(cases pc < length (compE2 e))
  case True
  let ?pre = compE2 c @ [IfFalse (int (length (compE2 e)) + 3)]
  let ?post = [Pop, Goto (-2 + (- int (length (compE2 e)) - int (length (compE2 c))))], Push Unit]
  from exec have exec-meth-d (compP2 P) ((?pre @ compE2 e) @ ?post) (compE2 c 0 0 @ shift
(length ?pre) (compE2 e 0 0)) t h (stk, loc, length ?pre + pc, xcp) ta h' (stk', loc', pc', xcp')
  by(simp add: shift-compE2 exec-move-def)
  hence exec': exec-meth-d (compP2 P) (?pre @ compE2 e) (compE2 c 0 0 @ shift (length ?pre)
(compE2 e 0 0)) t h (stk, loc, length ?pre + pc, xcp) ta h' (stk', loc', pc', xcp')
  by(rule exec-meth-take)(simp add: True)
  hence ?exec e stk loc pc xcp stk' loc' (pc' - length ?pre) xcp'
  unfolding exec-move-def by(rule exec-meth-drop-xt) auto
  from IH[OF this len -  $\langle P, h \vdash \text{stk } [:\leq] \text{ ST} \rangle$   $\langle \text{conf-xcp}' (\text{compP2 } P) h \text{ xcp}' \rangle$ ] bsok obtain e'' xs''
  where bisim':  $P, e, h' \vdash (e'', xs'') \leftrightarrow (stk', loc', pc' - \text{length } ?pre, xcp')$ 

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    and red: ?red e' xs e'' xs'' e stk pc (pc' - length ?pre) xcp xcp' by auto
  from red have ?red (e';while (c) e) xs (e'';while (c) e) xs'' e stk pc (pc' - length ?pre) xcp xcp'
    by(fastforce intro: Seq1Red elim!: Seq-τred1r-xt Seq-τred1t-xt split: if-split-asm)
  moreover from bisim'
  have P,while (c) e,h' ⊢ (e'';while(c) e, xs'') ↔ (stk', loc', Suc (length (compE2 c) + (pc' - length
    ?pre)), xcp')
    by(rule bisim1-bisims1.bisim1While4)
  moreover have τmove2 (compP2 P) h stk (while (c) e) (Suc (length (compE2 c) + pc)) xcp =
    τmove2 (compP2 P) h stk e pc xcp
    using True by(simp add: τmove2-iff)
  moreover from exec' have pc' ≥ length ?pre
    by(rule exec-meth-drop-xt-pc) auto
  moreover have no-call2 e pc ⇒ no-call2 (while (c) e) (Suc (length (compE2 c) + pc))
    by(simp add: no-call2-def)
  ultimately show ?thesis
    apply(auto split: if-split-asm)
    apply(fastforce+)[6]
    apply(rule exI conjI|assumption|rule rtranclp.rtrancl-refl|simp)+
    done
next
case False
with pc have [simp]: pc = length (compE2 e) by simp
with bisim1 obtain v where stk: stk = [v] and xcp: xcp = None
  by(auto dest: bisim1-pc-length-compE2D)
with bisim1 pc len bsok have red: τred1r P t h (e', xs) (Val v, loc)
  by(auto intro: bisim1-Val-τred1r simp add: bsok-def)
hence τred1r P t h (e'; while (c) e, xs) (Val v;; while (c) e, loc) by(rule Seq-τred1r-xt)
  moreover have τ: τmove2 (compP2 P) h [v] (while (c) e) (Suc (length (compE2 c) + length
    (compE2 e))) None
    by(simp add: τmove2-iff)
  moreover have τmove1 P h (Val v;;while (c) e) by(rule τmove1SeqRed)
  moreover
  have P,while (c) e,h ⊢ (while(c) e, loc) ↔ ([], loc, Suc (Suc (length (compE2 c) + length (compE2
    e))), None)
    by(rule bisim1While6)
  ultimately show ?thesis using exec stk xcp
    by(fastforce elim!: exec-meth.cases rtranclp-trans intro: Red1Seq simp add: eval-nat-numeral
    exec-move-def)
qed
next
case (bisim1While6 c n e xs)
note exec = ⟨?exec (while (c) e) [] xs (Suc (Suc (length (compE2 c) + length (compE2 e)))) None
  stk' loc' pc' xcp'⟩
  moreover have τmove2 (compP2 P) h [] (while (c) e) (Suc (Suc (length (compE2 c) + length
    (compE2 e)))) None
    by(simp add: τmove2-iff)
  moreover
  have P,while (c) e,h' ⊢ (if (c) (e;; while (c) e) else unit, xs) ↔ ([], xs, 0, None)
    by(rule bisim1While3[OF bisim1-refl])
  moreover have τred1t P t h (while (c) e, xs) (if (c) (e;; while (c) e) else unit, xs)
    by(rule tranclp.r-into-trancl)(auto intro: Red1While)
  ultimately show ?case
    by(fastforce elim!: exec-meth.cases simp add: exec-move-def)
next

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case (bisim1While7 c n e xs)
  note ⟨?exec (while (c) e) [] xs (Suc (Suc (Suc (length (compE2 c) + length (compE2 e)))))) None
  stk' loc' pc' xcp'⟩
  moreover have τmove2 (compP2 P) h [] (while (c) e) (Suc (Suc (Suc (length (compE2 c) + length
  (compE2 e)))))) None
    by(simp add: τmove2-iff)
  moreover have P,while (c) e,h' ⊢ (unit, xs) ↔ ([Unit], xs, length (compE2 (while (c) e)), None)
    by(rule bisim1Val2) simp
  ultimately show ?case by(fastforce elim!: exec-meth.cases simp add: exec-move-def)
next
case (bisim1WhileThrow1 c n a xs stk loc pc e)
  note exec = ⟨?exec (while (c) e) stk loc pc [a] stk' loc' pc' xcp'⟩
  note bisim1 = ⟨P,c,h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
  from bisim1 have pc: pc < length (compE2 c) by(auto dest: bisim1-ThrowD)
  from bisim1 have match-ex-table (compP2 P) (cname-of h a) (0 + pc) (compxE2 c 0 0) = None
    unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
  with exec pc have False
    by(auto elim!: exec-meth.cases simp add: match-ex-table-not-pcs-None exec-move-def)
  thus ?case ..
next
case (bisim1WhileThrow2 e n a xs stk loc pc c)
  note exec = ⟨?exec (while (c) e) stk loc (Suc (length (compE2 c) + pc)) [a] stk' loc' pc' xcp'⟩
  note bisim = ⟨P,e,h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
  from bisim have pc: pc < length (compE2 e) by(auto dest: bisim1-ThrowD)
  from bisim have match-ex-table (compP2 P) (cname-of h a) (0 + pc) (compxE2 e 0 0) = None
    unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
  with exec pc have False
    apply(auto elim!: exec-meth.cases simp add: match-ex-table-not-pcs-None exec-move-def)
    apply(auto dest!: match-ex-table-shift-pcD simp only: compxE2-size-convs)
    apply simp
    done
  thus ?case ..
next
case (bisim1Throw1 e n e' xs stk loc pc xcp)
  note IH = bisim1Throw1.IH(2)
  note exec = ⟨?exec (throw e) stk loc pc xcp stk' loc' pc' xcp'⟩
  note bisim = ⟨P,e,h ⊢ (e', xs) ↔ (stk, loc, pc, xcp)⟩
  note len = ⟨n + max-vars (throw e') ≤ length xs⟩
  note bsok = ⟨bsok (throw e) n⟩
  from bisim have pc: pc ≤ length (compE2 e) by(rule bisim1-pc-length-compE2)
  show ?case
  proof(cases pc < length (compE2 e))
    case True
      with exec have exec': ?exec e stk loc pc xcp stk' loc' pc' xcp' by(simp add: exec-move-Throw)
      from True have τmove2 (compP2 P) h stk (throw e) pc xcp = τmove2 (compP2 P) h stk e pc xcp
by(simp add: τmove2-iff)
      with IH[OF exec' - - ⟨P,h ⊢ stk [:≤] ST⟩ ⟨conf-xcp' (compP2 P) h xcp⟩] len bsok show ?thesis
      by(fastforce intro: bisim1-bisims1.bisim1Throw1 Throw1Red elim!: Throw-τred1r-xt Throw-τred1t-xt
      simp add: no-call2-def)
    next
    case False
      with pc have [simp]: pc = length (compE2 e) by simp
      with bisim obtain v where stk: stk = [v] and xcp: xcp = None
        by(auto dest: bisim1-pc-length-compE2D)

```

with *bisim pc len bsok* **have** *red*: $\tau red1r P t h (e', xs) (Val v, loc)$
by(*auto intro: bisim1-Val- $\tau red1r$ simp add: bsok-def*)
hence $\tau red1r P t h (throw e', xs) (throw (Val v), loc)$ **by**(*rule Throw- $\tau red1r$ -xt*)
moreover **have** τ : $\tau move2 (compP2 P) h [v] (throw e) pc None$ **by**(*simp add: $\tau move2$ -iff*)
moreover
have $\bigwedge a. P, throw e, h \vdash (Throw a, loc) \leftrightarrow ([Addr a], loc, length (compE2 e), [a])$
by(*rule bisim1Throw2*)
moreover
have $P, throw e, h \vdash (THROW NullPointer, loc) \leftrightarrow ([Null], loc, length (compE2 e), [addr-of-sys-xcpt NullPointer])$
by(*rule bisim1ThrowNull*)
moreover from *exec stk xcp* $\langle P, h \vdash stk [:\leq] ST \rangle$ **obtain** *T'* **where** *T'*: $typeof_h v = [T'] P \vdash T' \leq Class Throwable$
by(*auto simp add: exec-move-def exec-meth-instr list-all2-Cons1 conf-def compP2-def*)
moreover with *T'* **have** $v \neq Null \implies \exists C. T' = Class C$ **by**(*cases T'*)(*auto dest: Array-widen*)
moreover **have** $\tau red1r P t h (throw null, loc) (THROW NullPointer, loc)$
by(*auto intro: r-into-rtranclp Red1ThrowNull $\tau move1$ ThrowNull*)
ultimately show *?thesis* **using** *exec stk xcp T'* **unfolding** *exec-move-def*
by(*cases v*)(*fastforce elim!: exec-meth.cases intro: rtranclp-trans*)+
qed
next
case (*bisim1Throw2 e n a xs*)
note *exec* = $\langle ?exec (throw e) [Addr a] xs (length (compE2 e)) [a] stk' loc' pc' xcp' \rangle$
hence *False* **by**(*auto elim!: exec-meth.cases dest: match-ex-table-pc-length-compE2 simp add: exec-move-def*)
thus *?case ..*
next
case (*bisim1ThrowNull e n xs*)
note *exec* = $\langle ?exec (throw e) [Null] xs (length (compE2 e)) [addr-of-sys-xcpt NullPointer] stk' loc' pc' xcp' \rangle$
hence *False* **by**(*auto elim!: exec-meth.cases dest: match-ex-table-pc-length-compE2 simp add: exec-move-def*)
thus *?case ..*
next
case (*bisim1ThrowThrow e n a xs stk loc pc*)
note *exec* = $\langle ?exec (throw e) stk loc pc [a] stk' loc' pc' xcp' \rangle$
note *bisim1* = $\langle P, e, h \vdash (Throw a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$
from *bisim1* **have** *pc*: $pc < length (compE2 e)$ **by**(*auto dest: bisim1-ThrowD*)
from *bisim1* **have** *match-ex-table (compP2 P) (cname-of h a) (0 + pc) (compE2 e 0 0) = None*
unfolding *compP2-def* **by**(*rule bisim1-xcp-Some-not-caught*)
with *exec pc* **have** *False* **by**(*auto elim!: exec-meth.cases simp add: exec-move-def*)
thus *?case ..*
next
case (*bisim1Try e n e' xs stk loc pc xcp e2 C' V*)
note *IH* = *bisim1Try.IH(2)*
note *bisim* = $\langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$
note *bisim1* = $\langle \bigwedge xs. P, e2, h \vdash (e2, xs) \leftrightarrow ([], xs, 0, None) \rangle$
note *exec* = $\langle ?exec (try e catch(C' V) e2) stk loc pc xcp stk' loc' pc' xcp' \rangle$
note *bsok* = $\langle bsok (try e catch(C' V) e2) n \rangle$
with $\langle n + max-vars (try e' catch(C' V) e2) \leq length xs \rangle$
have *len*: $n + max-vars e' \leq length xs$ **and** *V*: $V < length xs$ **by** *simp-all*
from *bisim* **have** *pc*: $pc \leq length (compE2 e)$ **by**(*rule bisim1-pc-length-compE2*)
show *?case*
proof(*cases pc < length (compE2 e)*)
case *True*
note *pc* = *True*

show *?thesis*
proof(*cases* $xcp = \text{None} \vee (\exists a'. xcp = \lfloor a' \rfloor \wedge \text{match-ex-table } (\text{compP2 } P) (\text{cname-of } h \ a') \text{ pc } (\text{compxE2 } e \ 0 \ 0) \neq \text{None})$)
case *False*
then obtain a' **where** *Some*: $xcp = \lfloor a' \rfloor$
and *True*: $\text{match-ex-table } (\text{compP2 } P) (\text{cname-of } h \ a') \text{ pc } (\text{compxE2 } e \ 0 \ 0) = \text{None}$ **by**(*auto simp del: not-None-eq*)
from *Some* $\text{bisim} \langle \text{conf-xcp}' (\text{compP2 } P) \ h \ xcp \rangle$ **have** $\exists C. \text{typeof}_h (\text{Addr } a') = \lfloor \text{Class } C \rfloor \wedge P \vdash C \preceq^* \text{Throwable}$
by(*auto simp add: compP2-def*)
then obtain C'' **where** $ha': \text{typeof-addr } h \ a' = \lfloor \text{Class-type } C'' \rfloor$
and $\text{subclsThrow}: P \vdash C'' \preceq^* \text{Throwable}$ **by**(*auto*)
with *exec True Some pc* **have** $\text{subcls}: P \vdash C'' \preceq^* C'$
apply(*auto elim!: exec-meth.cases simp add: match-ex-table-append compP2-def matches-ex-entry-def exec-move-def cname-of-def split: if-split-asm*)
apply(*simp only: compxE2-size-convs, simp*)
done
moreover from $ha' \text{subclsThrow}$ **bsok** **have** $\text{red}: \tau \text{red1r } P \ t \ h \ (e', \ xs) (\text{Throw } a', \ \text{loc})$
and $\text{bisim}' : P, e, h \vdash (\text{Throw } a', \ \text{loc}) \leftrightarrow (\text{stk}, \ \text{loc}, \ \text{pc}, \ \lfloor a' \rfloor)$ **using** *bisim True len*
unfolding *Some compP2-def* **by**(*auto dest!: bisim1-xcp- τ Red simp add: bsok-def*)
from red **have** $\text{lenloc}: \text{length } \text{loc} = \text{length } \text{xs}$ **by**(*rule τred1r -preserves-len*)
from red **have** $\tau \text{red1r } P \ t \ h \ (\text{try } e' \ \text{catch}(C' \ V) \ e2, \ \text{xs}) (\text{try } (\text{Throw } a') \ \text{catch}(C' \ V) \ e2, \ \text{loc})$
by(*rule Try- τred1r -xt*)
hence $\tau \text{red1r } P \ t \ h \ (\text{try } e' \ \text{catch}(C' \ V) \ e2, \ \text{xs}) (\{V: \text{Class } C' = \text{None}; \ e2\}, \ \text{loc}[V := \text{Addr } a'])$
using $ha' \text{subcls } V$ **unfolding** *lenloc[symmetric]*
by(*auto intro: rtrancl.rtrancl-into-rtrancl Red1TryCatch $\tau \text{move1TryThrow}$*)
moreover from pc **have** $\tau \text{move2} (\text{compP2 } P) \ h \ \text{stk} (\text{try } e \ \text{catch}(C' \ V) \ e2) \ \text{pc} \ \lfloor a' \rfloor$ **by**(*simp add: τmove2 -iff*)
moreover from $\text{bisim}' \ ha' \ \text{subcls}$
have $P, \text{try } e \ \text{catch}(C' \ V) \ e2, h \vdash (\{V: \text{Class } C' = \text{None}; \ e2\}, \ \text{loc}[V := \text{Addr } a']) \leftrightarrow ([\text{Addr } a'], \ \text{loc}, \ \text{Suc} (\text{length } (\text{compE2 } e)), \ \text{None})$
by(*rule bisim1TryCatch1*)
ultimately show *?thesis* **using** *exec True pc Some ha' subclsThrow*
apply(*auto elim!: exec-meth.cases simp add: ac-simps eval-nat-numeral match-ex-table-append matches-ex-entry-def compP2-def exec-move-def cname-of-def*)
apply *fastforce*
apply(*simp-all only: compxE2-size-convs, auto dest: match-ex-table-shift-pcD*)
done
next
case *True*
let $?post = \text{Goto} (\text{int } (\text{length } (\text{compE2 } e2)) + 2) \ \# \ \text{Store } V \ \# \ \text{compE2 } e2$
from *exec True* **have** $\text{exec-meth-d} (\text{compP2 } P) (\text{compE2 } e \ @ \ ?post) (\text{compxE2 } e \ 0 \ 0 \ @ \ \text{shift} (\text{length } (\text{compE2 } e)) (\text{compxE2 } e2 \ (\text{Suc} (\text{Suc } 0)) \ 0)) \ t \ h \ (\text{stk}, \ \text{loc}, \ \text{pc}, \ xcp) \ ta \ h' \ (\text{stk}', \ \text{loc}', \ \text{pc}', \ xcp')$
by(*auto elim!: exec-meth.cases intro: exec-meth.intros simp add: match-ex-table-append shift-compxE2 exec-move-def*)
hence $?exec \ e \ \text{stk} \ \text{loc} \ \text{pc} \ xcp \ \text{stk}' \ \text{loc}' \ \text{pc}' \ xcp'$
using pc **unfolding** *exec-move-def* **by**(*rule exec-meth-take-xt*)
from $\text{IH}[\text{OF } \text{this } \text{len} - \langle P, h \vdash \text{stk} \ [:\leq] \ ST \rangle \langle \text{conf-xcp}' (\text{compP2 } P) \ h \ xcp \rangle]$ **bsok** **obtain** $e'' \ \text{xs}''$
where $\text{bisim}' : P, e, h' \vdash (e'', \ \text{xs}'') \leftrightarrow (\text{stk}', \ \text{loc}', \ \text{pc}', \ xcp')$
and $\text{red}' : ?red \ e' \ \text{xs} \ e'' \ \text{xs}'' \ e \ \text{stk} \ \text{pc} \ \text{pc}' \ xcp \ xcp'$ **by** *auto*
from bisim'
have $P, \text{try } e \ \text{catch}(C' \ V) \ e2, h' \vdash (\text{try } e'' \ \text{catch}(C' \ V) \ e2, \ \text{xs}'') \leftrightarrow (\text{stk}', \ \text{loc}', \ \text{pc}', \ xcp')$
by(*rule bisim1-bisims1.bisim1Try*)
moreover from pc **have** $\tau \text{move2} (\text{compP2 } P) \ h \ \text{stk} (\text{try } e \ \text{catch}(C' \ V) \ e2) \ \text{pc} \ xcp = \tau \text{move2}$

```

(compP2 P) h stk e pc xcp
  by(simp add:  $\tau$ move2-iff)
  ultimately show ?thesis using red' by(fastforce intro: Try1Red elim!: Try- $\tau$ red1r-xt Try- $\tau$ red1t-xt
simp add: no-call2-def)
  qed
  next
  case False
  with pc have [simp]: pc = length (compE2 e) by simp
  with bisim obtain v where stk: stk = [v] and xcp: xcp = None
    by(auto dest: bisim1-pc-length-compE2D)
  with bisim pc len bsok have red:  $\tau$ red1r P t h (e', xs) (Val v, loc)
    by(auto intro: bisim1-Val- $\tau$ red1r simp add: bsok-def)
  hence  $\tau$ red1r P t h (try e' catch(C' V) e2, xs) (try (Val v) catch(C' V) e2, loc) by(rule
Try- $\tau$ red1r-xt)
  hence  $\tau$ red1r P t h (try e' catch(C' V) e2, xs) (Val v, loc)
    by(auto intro: rtranclp.rtrancl-into-rtrancl Red1Try  $\tau$ move1TryRed)
  moreover have  $\tau$ :  $\tau$ move2 (compP2 P) h [v] (try e catch(C' V) e2) pc None by(simp add:
 $\tau$ move2-iff)
  moreover
  have P, try e catch(C' V) e2, h  $\vdash$  (Val v, loc)  $\leftrightarrow$  ([v], loc, length (compE2 (try e catch(C' V) e2)),
None)
    by(rule bisim1Val2) simp
  moreover have nat (int (length (compE2 e)) + (int (length (compE2 e2)) + 2)) = length (compE2
(try e catch(C' V) e2)) by simp
  ultimately show ?thesis using exec stk xcp
    by(fastforce elim!: exec-meth.cases simp add: exec-move-def)
  qed
  next
  case (bisim1TryCatch1 e n a xs stk loc pc C'' C' e2 V)
  note exec =  $\langle ?exec (try e catch(C' V) e2) [Addr a] loc (Suc (length (compE2 e))) None stk' loc' pc'
xcp' \rangle$ 
  note bisim2 =  $\langle P, e2, h \vdash (e2, loc[V := Addr a]) \leftrightarrow ([], loc[V := Addr a], 0, None) \rangle$ 
  note bisim1 =  $\langle P, e, h \vdash (Throw a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$ 
  hence [simp]: xs = loc by(auto dest: bisim1-ThrowD)
  from bisim2
  have P, try e catch(C' V) e2, h  $\vdash$  ( $\{V:Class C'=None; e2\}$ , loc[V := Addr a])  $\leftrightarrow$  ([], loc[V :=
Addr a], Suc (Suc (length (compE2 e) + 0)), None)
    by(rule bisim1TryCatch2)
  moreover have  $\tau$ move2 (compP2 P) h [Addr a] (try e catch(C' V) e2) (Suc (length (compE2 e)))
None by(simp add:  $\tau$ move2-iff)
  ultimately show ?case using exec by(fastforce elim!: exec-meth.cases simp add: exec-move-def)
  next
  case (bisim1TryCatch2 e2 n e' xs stk loc pc xcp e C' V)
  note IH = bisim1TryCatch2.IH(2)
  note bisim2 =  $\langle P, e2, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
  note bisim =  $\langle \bigwedge xs. P, e, h \vdash (e, xs) \leftrightarrow ([], xs, 0, None) \rangle$ 
  note exec =  $\langle ?exec (try e catch(C' V) e2) stk loc (Suc (Suc (length (compE2 e) + pc))) xcp stk'
loc' pc' xcp' \rangle$ 
  note bsok =  $\langle bsok (try e catch(C' V) e2) n \rangle$ 
  with  $\langle n + max-vars \{V:Class C'=None; e'\} \leq length xs \rangle$ 
  have len: Suc n + max-vars e'  $\leq$  length xs and V: V < length xs by simp-all
  let ?pre = compE2 e @ [Goto (int (length (compE2 e2)) + 2), Store V]
  from exec have exec-meth-d (compP2 P) (?pre @ compE2 e2)
    (compE2 e 0 0 @ shift (length ?pre) (compE2 e2 0 0) @ [(0, length (compE2 e), [C'], Suc (length

```

$(\text{compE2 } e), 0)) t$
 $h (stk, loc, length \text{ ?pre} + pc, xcp) ta h' (stk', loc', pc', xcp')$
 $\text{by}(\text{simp add: shift-compxE2 exec-move-def})$
 $\text{hence } exec': \text{exec-meth-d } (\text{compP2 } P) (\text{?pre} @ \text{compE2 } e2) (\text{compxE2 } e \ 0 \ 0 @ \text{shift } (\text{length } \text{?pre}))$
 $(\text{compxE2 } e2 \ 0 \ 0) t$
 $h (stk, loc, length \text{ ?pre} + pc, xcp) ta h' (stk', loc', pc', xcp')$
 $\text{by}(\text{auto elim!: exec-meth.cases intro: exec-meth.intros simp add: match-ex-table-append matches-ex-entry-def})$
 $\text{hence } \text{?exec } e2 \ stk \ loc \ pc \ xcp \ stk' \ loc' (pc' - \text{length } \text{?pre}) \ xcp'$
 $\text{unfolding exec-move-def by}(\text{rule exec-meth-drop-xt}) \text{ auto}$
 $\text{from IH}[OF \text{ this len} - \langle P, h \vdash \text{stk} [\leq] ST \rangle \langle \text{conf-xcp}' (\text{compP2 } P) h \ xcp \rangle] \text{ bsok } \text{obtain } e'' \ xs''$
 $\text{where } \text{bisim!}: P, e2, h' \vdash (e'', xs'') \leftrightarrow (stk', loc', pc' - \text{length } \text{?pre}, xcp')$
 $\text{and } \text{red}: \text{?red } e' \ xs \ e'' \ xs'' \ e2 \ stk \ pc (pc' - \text{length } \text{?pre}) \ xcp \ xcp' \text{ by auto}$
 $\text{from red have } \text{length } xs'' = \text{length } xs$
 $\text{by}(\text{auto dest!: } \tau \text{red1r-preserves-len } \tau \text{red1t-preserves-len } \text{red1-preserves-len } \text{split: if-split-asm})$
 $\text{with red } V \text{ have } \text{?red } \{V:\text{Class } C'=\text{None}; e'\} \ xs \ \{V:\text{Class } C'=\text{None}; e''\} \ xs'' \ e2 \ stk \ pc (pc' - \text{length } \text{?pre}) \ xcp \ xcp'$
 $\text{by}(\text{fastforce elim!: Block-None-}\tau\text{red1r-xt Block-None-}\tau\text{red1t-xt intro: Block1Red split: if-split-asm})$
 moreover
 $\text{from } \text{bisim}'$
 $\text{have } P, \text{try } e \ \text{catch}(C' \ V) \ e2, h' \vdash (\{V:\text{Class } C'=\text{None}; e''\}, xs'') \leftrightarrow (stk', loc', \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e) + (pc' - \text{length } \text{?pre}))), xcp')$
 $\text{by}(\text{rule bisim1-bisims1.bisim1TryCatch2})$
 $\text{moreover have } \tau \text{move2 } (\text{compP2 } P) h \ stk (\text{try } e \ \text{catch}(C' \ V) \ e2) (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e) + pc))) \ xcp = \tau \text{move2 } (\text{compP2 } P) h \ stk \ e2 \ pc \ xcp$
 $\text{by}(\text{simp add: } \tau \text{move2-iff})$
 $\text{moreover from } exec' \text{ have } pc' \geq \text{length } \text{?pre}$
 $\text{by}(\text{rule exec-meth-drop-xt-pc}) \text{ auto}$
 $\text{moreover hence } \text{Suc } (\text{Suc } (pc' - \text{Suc } (\text{Suc } 0))) = pc' \text{ by simp}$
 $\text{moreover have } \text{no-call2 } e2 \ pc \implies \text{no-call2 } (\text{try } e \ \text{catch}(C' \ V) \ e2) (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e) + pc)))$
 $\text{by}(\text{simp add: no-call2-def})$
 $\text{ultimately show } \text{?case using red } V \text{ by}(\text{fastforce simp add: eval-nat-numeral split: if-split-asm})$
 next
 $\text{case } (\text{bisim1TryFail } e \ n \ a \ xs \ stk \ loc \ pc \ C'' \ C' \ e2 \ V)$
 $\text{note } \text{bisim} = \langle P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$
 $\text{from } \text{bisim} \text{ have } pc: pc < \text{length } (\text{compE2 } e) \text{ by}(\text{auto dest: bisim1-ThrowD})$
 $\text{from } \text{bisim} \text{ have } \text{match-ex-table } (\text{compP2 } P) (\text{cname-of } h \ a) (0 + pc) (\text{compxE2 } e \ 0 \ 0) = \text{None}$
 $\text{unfolding compP2-def by}(\text{rule bisim1-xcp-Some-not-caught})$
 $\text{with } \langle \text{?exec } (\text{try } e \ \text{catch}(C' \ V) \ e2) \ stk \ loc \ pc \ [a] \ stk' \ loc' \ pc' \ xcp' \rangle \text{ pc } \langle \text{typeof-addr } h \ a = [\text{Class-type } C''] \rangle \langle \neg P \vdash C'' \preceq^* C' \rangle$
 $\text{have False by}(\text{auto elim!: exec-meth.cases simp add: matches-ex-entry-def compP2-def match-ex-table-append-not-pcs exec-move-def cname-of-def})$
 $\text{thus } \text{?case ..}$
 next
 $\text{case } (\text{bisim1TryCatchThrow } e2 \ n \ a \ xs \ stk \ loc \ pc \ e \ C' \ V)$
 $\text{note } \text{bisim} = \langle P, e2, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$
 $\text{from } \text{bisim} \text{ have } pc: pc < \text{length } (\text{compE2 } e2) \text{ by}(\text{auto dest: bisim1-ThrowD})$
 $\text{from } \text{bisim} \text{ have } \text{match-ex-table } (\text{compP2 } P) (\text{cname-of } h \ a) (0 + pc) (\text{compxE2 } e2 \ 0 \ 0) = \text{None}$
 $\text{unfolding compP2-def by}(\text{rule bisim1-xcp-Some-not-caught})$
 $\text{with } \langle \text{?exec } (\text{try } e \ \text{catch}(C' \ V) \ e2) \ stk \ loc \ (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e) + pc))) \ [a] \ stk' \ loc' \ pc' \ xcp' \rangle \text{ pc}$
 $\text{have False apply}(\text{auto elim!: exec-meth.cases simp add: compxE2-size-convs match-ex-table-append-not-pcs exec-move-def})$
 $\text{apply}(\text{auto dest!: match-ex-table-shift-pcD simp add: match-ex-table-append matches-ex-entry-def})$

```

compP2-def)
  done
  thus ?case ..
next
  case bisims1Nil
  hence False by(auto elim!: exec-meth.cases simp add: exec-moves-def)
  thus ?case ..
next
  case (bisims1List1 e n e' xs stk loc pc xcp es)
  note IH1 = bisims1List1.IH(2)
  note IH2 = bisims1List1.IH(4)
  note exec = ⟨?execs (e # es) stk loc pc xcp stk' loc' pc' xcp'⟩
  note bisim1 = ⟨P,e,h ⊢ (e', xs) ↔ (stk, loc, pc, xcp)⟩
  note bisim2 = ⟨P,es,h ⊢ (es, loc) [↔] ([], loc, 0, None)⟩
  note len = ⟨n + max-varss (e' # es) ≤ length xs⟩
  note bsok = ⟨bsoks (e # es) n⟩
  from bisim1 have pc: pc ≤ length (compE2 e) by(rule bisim1-pc-length-compE2)
  from bisim1 have lenxs: length xs = length loc by(rule bisim1-length-xs)
  show ?case
  proof(cases pc < length (compE2 e))
    case True
    with exec have exec': ?exec e stk loc pc xcp stk' loc' pc' xcp'
    by(auto simp add: compxEs2-size-convs exec-moves-def exec-move-def intro: exec-meth-take-xt)
    from True have τmoves2 (compP2 P) h stk (e # es) pc xcp = τmove2 (compP2 P) h stk e pc xcp
    by(simp add: τmove2-iff τmoves2-iff)
    moreover from True have no-calls2 (e # es) pc = no-call2 e pc
    by(simp add: no-call2-def no-calls2-def)
    ultimately show ?thesis
    using IH1[OF exec' - - ⟨P,h ⊢ stk [::≤] ST⟩ ⟨conf-xcp' (compP2 P) h xcp⟩] bsok len
    by(fastforce intro: bisim1-bisims1.bisims1List1 elim!: τred1r-inj-τreds1r τred1t-inj-τreds1t List1Red1)
  next
  case False
  with pc have pc [simp]: pc = length (compE2 e) by simp
  with bisim1 obtain v where stk: stk = [v] and xcp: xcp = None
  and v: is-val e' ⇒ e' = Val v ∧ xs = loc and call: call1 e' = None
  by(auto dest: bisim1-pc-length-compE2D)
  with bisim1 pc len bsok have red: τred1r P t h (e', xs) (Val v, loc)
  by(auto intro: bisim1-Val-τred1r simp add: bsok-def)
  hence τreds1r P t h (e' # es, xs) (Val v # es, loc) by(rule τred1r-inj-τreds1r)
  moreover from exec stk xcp
  have exec': exec-meth-d (compP2 P) (compE2 e @ compEs2 es) (compxE2 e 0 0 @ shift (length
(compE2 e) (stack-xlift (length [v]) (compxEs2 es 0 0))) t h ([] @ [v], loc, length (compE2 e) + 0,
None) ta h' (stk', loc', pc', xcp'))
  by(simp add: shift-compxEs2 stack-xlift-compxEs2 exec-moves-def)
  hence exec-meth-d (compP2 P) (compEs2 es) (stack-xlift (length [v]) (compxEs2 es 0 0)) t h ([] @
[v], loc, 0, None) ta h' (stk', loc', pc' - length (compE2 e), xcp')
  by(rule exec-meth-drop-xt) auto
  with bisim2 obtain stk'' where stk': stk' = stk'' @ [v]
  and exec'': exec-moves-d P t es h ([], loc, 0, None) ta h' (stk'', loc', pc' - length (compE2 e),
xcp')
  by(unfold exec-moves-def)(drule (1) exec-meth-stk-splits, auto)
  from IH2[OF exec''] len lenxs bsok obtain es'' xs''
  where bisim': P,es,h ⊢ (es'', xs'') [↔] (stk'', loc', pc' - length (compE2 e), xcp')
  and red': ?reds es loc es'' xs'' es [] 0 (pc' - length (compE2 e)) None xcp' by fastforce

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from bisim' have  $P, e \# es, h' \vdash (\text{Val } v \# es'', xs'') [\leftrightarrow] (stk'' @ [v], loc', \text{length} (\text{compE2 } e) + (pc' - \text{length} (\text{compE2 } e)), xcp')$ 
  by(rule bisims1List2)
moreover from exec''
have  $\tau \text{moves2} (\text{compP2 } P) h [v] (e \# es) (\text{length} (\text{compE2 } e)) \text{None} = \tau \text{moves2} (\text{compP2 } P) h []$ 
es 0 None
  using  $\tau \text{instr-stk-drop-exec-moves}$  [where stk=[] and vs=[v]] by(simp add:  $\tau \text{moves2-iff}$ )
moreover have  $\tau: \bigwedge es'. \tau \text{moves1 } P h (\text{Val } v \# es') \implies \tau \text{moves1 } P h es'$  by simp
from exec' have  $pc' \geq \text{length} (\text{compE2 } e)$ 
  by(rule exec-meth-drop-xt-pc) auto
moreover have  $\text{no-calls2 } es 0 \implies \text{no-calls2} (e \# es) (\text{length} (\text{compE2 } e))$ 
  by(simp add: no-calls2-def)
ultimately show ?thesis using red' xcp stk stk' call v
  apply(auto simp add: split-paired-Ex)
  apply(blast 25 intro: rtranclp-trans rtranclp-tranclp-tranclp  $\tau \text{reds1r-cons-}\tau \text{reds1r List1Red2}$ 
 $\tau \text{reds1t-cons-}\tau \text{reds1t dest: } \tau$ )+
  done
qed
next
case (bisims1List2 es n es' xs stk loc pc xcp e v)
note IH = bisims1List2.IH(2)
note exec =  $\langle ?execs (e \# es) (stk @ [v]) loc (\text{length} (\text{compE2 } e) + pc) xcp stk' loc' pc' xcp' \rangle$ 
note bisim1 =  $\langle P, e, h \vdash (e, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$ 
note bisim2 =  $\langle P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, xcp) \rangle$ 
note len =  $\langle n + \text{max-varss} (\text{Val } v \# es') \leq \text{length } xs \rangle$ 
note bsok =  $\langle bsoks (e \# es) n \rangle$ 
from  $\langle P, h \vdash stk @ [v] [:\leq] ST \rangle$  obtain ST' where ST':  $P, h \vdash stk [:\leq] ST'$ 
  by(auto simp add: list-all2-append1)
from exec have exec': exec-meth-d (compP2 P) (compE2 e @ compEs2 es) (compxE2 e 0 0 @ shift
(length (compE2 e)) (stack-xlift (length [v]) (compxEs2 es 0 0))) t h (stk @ [v], loc, length (compE2
e) + pc, xcp) ta h' (stk', loc', pc', xcp')
  by(simp add: shift-compxEs2 stack-xlift-compxEs2 exec-moves-def)
hence exec-meth-d (compP2 P) (compEs2 es) (stack-xlift (length [v]) (compxEs2 es 0 0)) t h (stk @
[v], loc, pc, xcp) ta h' (stk', loc', pc' - length (compE2 e), xcp')
  by(rule exec-meth-drop-xt) auto
with bisim2 obtain stk'' where stk':  $stk' = stk'' @ [v]$ 
  and exec'': exec-moves-d P t es h (stk, loc, pc, xcp) ta h' (stk'', loc', pc' - length (compE2 e), xcp')
  by(unfold exec-moves-def)(drule (1) exec-meth-stk-splits, auto)
from IH[OF exec'' - - ST'  $\langle \text{conf-xcp}' (\text{compP2 } P) h xcp \rangle$  len bsok obtain es'' xs''
  where bisim':  $P, es, h' \vdash (es'', xs'') [\leftrightarrow] (stk'', loc', pc' - \text{length} (\text{compE2 } e), xcp')$ 
  and red':  $?reds es' xs es'' xs'' es stk pc (pc' - \text{length} (\text{compE2 } e)) xcp xcp'$  by auto
from bisim' have  $P, e \# es, h' \vdash (\text{Val } v \# es'', xs'') [\leftrightarrow] (stk'' @ [v], loc', \text{length} (\text{compE2 } e) + (pc' - \text{length} (\text{compE2 } e)), xcp')$ 
  by(rule bisim1-bisims1.bisims1List2)
moreover from exec'' have  $\tau \text{moves2} (\text{compP2 } P) h (stk @ [v]) (e \# es) (\text{length} (\text{compE2 } e) + pc)$ 
xcp =  $\tau \text{moves2} (\text{compP2 } P) h stk es pc xcp$ 
  by(auto simp add:  $\tau \text{moves2-iff} \tau \text{instr-stk-drop-exec-moves}$ )
moreover have  $\tau: \bigwedge es'. \tau \text{moves1 } P h (\text{Val } v \# es') \implies \tau \text{moves1 } P h es'$  by simp
from exec' have  $pc' \geq \text{length} (\text{compE2 } e)$ 
  by(rule exec-meth-drop-xt-pc) auto
moreover have  $\text{no-calls2 } es pc \implies \text{no-calls2} (e \# es) (\text{length} (\text{compE2 } e) + pc)$ 
  by(simp add: no-calls2-def)
ultimately show ?case using red' stk'
  by(auto split: if-split-asm simp add: split-paired-Ex)(blast intro: rtranclp-trans rtranclp-tranclp-tranclp

```

$\tau\text{reds1r-cons-}\tau\text{reds1r List1Red2 } \tau\text{reds1t-cons-}\tau\text{reds1t dest: } \tau)+$
qed

end

inductive *sim21-size-aux* :: *nat* \Rightarrow (*pc* \times '*addr option*) \Rightarrow (*pc* \times '*addr option*) \Rightarrow *bool*

for *len* :: *nat*

where

$\llbracket pc1 \leq len; pc2 \leq len; xcp1 \neq None \wedge xcp2 = None \wedge pc1 = pc2 \vee xcp1 = None \wedge pc1 > pc2 \rrbracket$
 $\implies \text{sim21-size-aux } len (pc1, xcp1) (pc2, xcp2)$

definition *sim21-size* :: '*addr jvm-prog* \Rightarrow '*addr jvm-thread-state* \Rightarrow '*addr jvm-thread-state* \Rightarrow *bool*

where

sim21-size *P xcpfrs xcpfrs'* \longleftrightarrow
 (*xcpfrs, xcpfrs'*) \in
inv-image (*less-than* $\langle *lex* \rangle$ *same-fst* ($\lambda n. True$) ($\lambda n. \{(pcxcp, pcxcp'). \text{sim21-size-aux } n \text{ pcxcp } pcxcp'\}$))
 ($\lambda(xcp, frs). (\text{length } frs, \text{case } frs \text{ of } [] \Rightarrow \text{undefined}$
 $\mid (stk, loc, C, M, pc) \# frs \Rightarrow (\text{length } (fst (snd (snd (the (snd (snd (snd (method$
P C M))))))))), pc, xcp))

lemma *wfP-sim21-size-aux*: *wfP* (*sim21-size-aux* *n*)

proof –

let *?f* = $\lambda(pc, xcp). \text{case } xcp \text{ of } None \Rightarrow Suc (2 * (n - pc)) \mid Some - \Rightarrow 2 * (n - pc)$
have *wf* $\{(m, m'). (m :: nat) < m'\}$ **by**(*rule wf-less*)
hence *wf* (*inv-image* $\{(m, m'). m < m'\}$ *?f*) **by**(*rule wf-inv-image*)
moreover have $\{(pcxcp1, pcxcp2). \text{sim21-size-aux } n \text{ pcxcp1 } pcxcp2\} \subseteq \text{inv-image } \{(m, m'). m < m'\}$ *?f*
by(*auto elim!: sim21-size-aux.cases*)
ultimately show *?thesis unfolding wfp-def* **by**(*rule wf-subset*)
qed

lemma *Collect-split-mem*: $\{(x, y). (x, y) \in Q\} = Q$ **by** *simp*

lemma *wfP-sim21-size*: *wfP* (*sim21-size* *P*)

unfolding *wfp-def Collect-split-mem sim21-size-def [abs-def]*

apply(*rule wf-inv-image*)

apply(*rule wf-lex-prod*)

apply(*rule wf-less-than*)

apply(*rule wf-same-fst*)

apply(*rule wfp-sim21-size-aux[unfolded wfp-def]*)

done

declare *split-beta[simp]*

context *J1-JVM-heap-base* **begin**

lemma *bisim1-Invoke-Red*:

$\llbracket P, E, h \vdash (e, xs) \leftrightarrow (\text{rev } vs @ Addr a \# stk', loc, pc, None); pc < \text{length } (compE2 E);$
 $\text{compE2 } E ! pc = Invoke M (\text{length } vs); n + \text{max-vars } e \leq \text{length } xs; \text{bsok } E n \rrbracket$
 $\implies \exists e' xs'. \tau\text{red1r } P t h (e, xs) (e', xs') \wedge P, E, h \vdash (e', xs') \leftrightarrow (\text{rev } vs @ Addr a \# stk', loc, pc,$
 $None) \wedge \text{call1 } e' = \llbracket (a, M, vs) \rrbracket$
 (**is** $\llbracket -; -; -; - \rrbracket \implies ?\text{concl } e \text{ xs } E n \text{ pc } stk' \text{ loc}$)

and *bisims1-Invoke-τReds*:
 $\llbracket P, Es, h \vdash (es, xs) [\leftrightarrow] (rev\ vs\ @\ Addr\ a\ \# \text{stk}', loc, pc, None); pc < length\ (compEs2\ Es);$
 $compEs2\ Es\ !\ pc = Invoke\ M\ (length\ vs); n + max\ vars\ es \leq length\ xs; bsoks\ Es\ n \rrbracket$
 $\implies \exists es'\ xs'. \tau reds1r\ P\ t\ h\ (es, xs)\ (es', xs') \wedge P, Es, h \vdash (es', xs') [\leftrightarrow] (rev\ vs\ @\ Addr\ a\ \# \text{stk}', loc,$
 $pc, None) \wedge calls1\ es' = \llbracket (a, M, vs) \rrbracket$
 $(is\ \llbracket -; -; -; - \rrbracket \implies ?concls\ es\ xs\ Es\ n\ pc\ \text{stk}'\ loc)$

proof(*induct E n e xs stk ≡ rev vs @ Addr a # stk' loc pc xcp ≡ None::'addr option*
and *Es n es xs stk ≡ rev vs @ Addr a # stk' loc pc xcp ≡ None::'addr option*
arbitrary: stk' and stk' rule: bisim1-bisims1-inducts-split)
case *bisim1Val2* **thus** *?case by simp*
next
case *bisim1New* **thus** *?case by simp*
next
case *bisim1NewArray* **thus** *?case*
by(*fastforce split: if-split-asm intro: bisim1-bisims1.bisim1NewArray dest: bisim1-pc-length-compE2*
elim!: NewArray-τred1r-xt)
next
case *bisim1Cast* **thus** *?case*
by(*fastforce split: if-split-asm intro: bisim1-bisims1.bisim1Cast dest: bisim1-pc-length-compE2 elim!:*
Cast-τred1r-xt)
next
case *bisim1InstanceOf* **thus** *?case*
by(*fastforce split: if-split-asm intro: bisim1-bisims1.bisim1InstanceOf dest: bisim1-pc-length-compE2*
elim!: InstanceOf-τred1r-xt)
next
case *bisim1Val* **thus** *?case by simp*
next
case *bisim1Var* **thus** *?case by simp*
next
case *bisim1BinOp1* **thus** *?case*
apply(*auto split: if-split-asm intro: bisim1-bisims1.bisim1BinOp1 dest: bisim1-pc-length-compE2*
elim: BinOp-τred1r-xt1)
apply(*fastforce elim!: BinOp-τred1r-xt1 intro: bisim1-bisims1.bisim1BinOp1*)
done
next
case (*bisim1BinOp2 e2 n e' xs stk loc pc e1 bop v1*)
note *IH = ⟨∧stk'. $\llbracket stk = rev\ vs\ @\ Addr\ a\ \# \text{stk}'; pc < length\ (compE2\ e2); compE2\ e2\ !\ pc =$*
Invoke\ M\ (length\ vs); n + max\ vars\ e' ≤ length\ xs; bsok\ e2\ n
 $\rrbracket \implies ?concl\ e'\ xs\ e2\ n\ pc\ \text{stk}'\ loc$
note *inv = ⟨compE2 (e1 «bop» e2) ! (length (compE2 e1) + pc) = Invoke M (length vs)⟩*
with *⟨length (compE2 e1) + pc < length (compE2 (e1 «bop» e2))⟩* **have** *pc: pc < length (compE2*
e2)⟩
by(*auto split: bop.splits if-split-asm*)
moreover with *inv* **have** *compE2 e2 ! pc = Invoke M (length vs)* **by** *simp*
moreover with *⟨P, e2, h ⊢ (e', xs) ↔ (stk, loc, pc, None)⟩* *pc*
obtain *vs'' v'' stk''* **where** *stk = vs'' @ v'' # stk'' and length vs'' = length vs*
by(*auto dest!: bisim1-Invoke-stkD*)
with *⟨stk @ [v1] = rev vs @ Addr a # stk'⟩* **obtain** *stk'''*
where *stk''': stk = rev vs @ Addr a # stk''' and stk: stk' = stk''' @ [v1]*
by(*cases stk' rule: rev-cases*) *auto*
from *⟨n + max-vars (Val v1 «bop» e') ≤ length xs⟩* **have** *n + max-vars e' ≤ length xs* **by** *simp*
moreover from *⟨bsok (e1 «bop» e2) n⟩* **have** *bsok e2 n* **by** *simp*
ultimately have *?concl e' xs e2 n pc stk''' loc using stk''' by-(rule IH)*
then obtain *e'' xs'* **where** *IH': τred1r P t h (e', xs) (e'', xs') call1 e'' = $\llbracket (a, M, vs) \rrbracket$*

and $\text{bisim}: P, e2, h \vdash (e'', xs') \leftrightarrow (\text{rev } vs \text{ @ Addr } a \# \text{stk}''', \text{loc}, pc, \text{None})$ **by** *blast*
from bisim **have** $P, e1 \llbracket \text{bop} \rrbracket e2, h \vdash (\text{Val } v1 \llbracket \text{bop} \rrbracket e'', xs') \leftrightarrow ((\text{rev } vs \text{ @ Addr } a \# \text{stk}''') \text{ @ } [v1], \text{loc}, \text{length } (\text{compE2 } e1) + pc, \text{None})$
by $(\text{rule } \text{bisim1-bisims1.bisim1BinOp2})$
with $\text{IH}' \text{ stk}$ **show** $?case$ **by** $(\text{fastforce } \text{elim!}: \text{BinOp-}\tau\text{red1r-xt2})$
next
case bisim1LAss1 **thus** $?case$
by $(\text{fastforce } \text{split}: \text{if-split-asm } \text{intro}: \text{bisim1-bisims1.bisim1LAss1 } \text{dest}: \text{bisim1-pc-length-compE2 } \text{elim!}: \text{LAss-}\tau\text{red1r})$
next
case bisim1LAss2 **thus** $?case$ **by** *simp*
next
case bisim1AAcc1 **thus** $?case$
apply $(\text{auto } \text{split}: \text{if-split-asm } \text{intro}: \text{bisim1-bisims1.bisim1AAcc1 } \text{dest}: \text{bisim1-pc-length-compE2 } \text{elim!}: \text{AAcc-}\tau\text{red1r-xt1})$
apply $(\text{fastforce } \text{elim!}: \text{AAcc-}\tau\text{red1r-xt1 } \text{intro}: \text{bisim1-bisims1.bisim1AAcc1})$
done
next
case $(\text{bisim1AAcc2 } e2 \ n \ e' \ xs \ \text{stk} \ \text{loc} \ pc \ e1 \ v1)$
note $\text{IH} = \langle \bigwedge \text{stk}'. \llbracket \text{stk} = \text{rev } vs \text{ @ Addr } a \# \text{stk}'; \text{pc} < \text{length } (\text{compE2 } e2); \text{compE2 } e2 \ ! \ \text{pc} = \text{Invoke } M \ (\text{length } vs); \ n + \text{max-vars } e' \leq \text{length } xs; \text{bsok } e2 \ n \rrbracket \implies ?\text{concl } e' \ xs \ e2 \ n \ \text{pc} \ \text{stk}' \ \text{loc} \rangle$
note $\text{inv} = \langle \text{compE2 } (e1 \llbracket e2 \rrbracket) \ ! \ (\text{length } (\text{compE2 } e1) + \text{pc}) = \text{Invoke } M \ (\text{length } vs) \rangle$
with $\langle \text{length } (\text{compE2 } e1) + \text{pc} < \text{length } (\text{compE2 } (e1 \llbracket e2 \rrbracket)) \rangle$ **have** $\text{pc}: \text{pc} < \text{length } (\text{compE2 } e2)$
by $(\text{auto } \text{split}: \text{if-split-asm})$
moreover with inv **have** $\text{compE2 } e2 \ ! \ \text{pc} = \text{Invoke } M \ (\text{length } vs)$ **by** *simp*
moreover with $\langle P, e2, h \vdash (e', xs) \leftrightarrow (\text{stk}, \text{loc}, pc, \text{None}) \rangle$ pc
obtain $vs'' \ v'' \ \text{stk}''$ **where** $\text{stk} = vs'' \text{ @ } v'' \# \text{stk}''$ **and** $\text{length } vs'' = \text{length } vs$
by $(\text{auto } \text{dest!}: \text{bisim1-Invoke-stkD})$
with $\langle \text{stk} \text{ @ } [v1] = \text{rev } vs \text{ @ Addr } a \# \text{stk}' \rangle$ **obtain** stk'''
where $\text{stk}''': \text{stk} = \text{rev } vs \text{ @ Addr } a \# \text{stk}'''$ **and** $\text{stk}: \text{stk}' = \text{stk}''' \text{ @ } [v1]$
by $(\text{cases } \text{stk}' \ \text{rule}: \text{rev-cases}) \ \text{auto}$
from $\langle n + \text{max-vars } (\text{Val } v1 \llbracket e' \rrbracket) \leq \text{length } xs \rangle$ **have** $n + \text{max-vars } e' \leq \text{length } xs$ **by** *simp*
moreover from $\langle \text{bsok } (e1 \llbracket e2 \rrbracket) \ n \rangle$ **have** $\text{bsok } e2 \ n$ **by** *simp*
ultimately have $?concl \ e' \ xs \ e2 \ n \ \text{pc} \ \text{stk}''' \ \text{loc}$ **using** stk''' **by** $-(\text{rule } \text{IH})$
then obtain $e'' \ xs'$ **where** $\text{IH}': \tau\text{red1r } P \ t \ h \ (e', xs) \ (e'', xs') \ \text{call1 } e'' = \llbracket (a, M, vs) \rrbracket$
and $\text{bisim}: P, e2, h \vdash (e'', xs') \leftrightarrow (\text{rev } vs \text{ @ Addr } a \# \text{stk}''', \text{loc}, pc, \text{None})$ **by** *blast*
from bisim **have** $P, e1 \llbracket e2 \rrbracket, h \vdash (\text{Val } v1 \llbracket e' \rrbracket, xs') \leftrightarrow ((\text{rev } vs \text{ @ Addr } a \# \text{stk}''') \text{ @ } [v1], \text{loc}, \text{length } (\text{compE2 } e1) + pc, \text{None})$
by $(\text{rule } \text{bisim1-bisims1.bisim1AAcc2})$
with $\text{IH}' \ \text{stk}$ **show** $?case$ **by** $(\text{fastforce } \text{elim!}: \text{AAcc-}\tau\text{red1r-xt2})$
next
case $(\text{bisim1AAss1 } e \ n \ e' \ xs \ \text{loc} \ pc \ e2 \ e3)$
note $\text{IH} = \langle \llbracket \text{pc} < \text{length } (\text{compE2 } e); \text{compE2 } e \ ! \ \text{pc} = \text{Invoke } M \ (\text{length } vs); \ n + \text{max-vars } e' \leq \text{length } xs; \text{bsok } e \ n \rrbracket \implies ?\text{concl } e' \ xs \ e \ n \ \text{pc} \ \text{stk}' \ \text{loc} \rangle$
note $\text{bisim} = \langle P, e, h \vdash (e', xs) \leftrightarrow (\text{rev } vs \text{ @ Addr } a \# \text{stk}', \text{loc}, pc, \text{None}) \rangle$
note $\text{len} = \langle n + \text{max-vars } (e' \llbracket e2 \rrbracket) := e3 \leq \text{length } xs \rangle$
hence len' : $n + \text{max-vars } e' \leq \text{length } xs$ **by** *simp*
note $\text{inv} = \langle \text{compE2 } (e \llbracket e2 \rrbracket) := e3 \ ! \ \text{pc} = \text{Invoke } M \ (\text{length } vs) \rangle$
with $\langle \text{pc} < \text{length } (\text{compE2 } (e \llbracket e2 \rrbracket) := e3) \rangle$ bisim **have** $\text{pc}: \text{pc} < \text{length } (\text{compE2 } e)$
by $(\text{auto } \text{split}: \text{if-split-asm } \text{dest}: \text{bisim1-pc-length-compE2})$
moreover with inv **have** $\text{compE2 } e \ ! \ \text{pc} = \text{Invoke } M \ (\text{length } vs)$ **by** *simp*
moreover from $\langle \text{bsok } (e \llbracket e2 \rrbracket) := e3 \ n \rangle$ **have** $\text{bsok } e \ n$ **by** *simp*

ultimately have $?concl\ e'\ xs\ e\ n\ pc\ stk'\ loc$ using len' by—(rule IH)
thus $?case$
by(fastforce intro: $bisim1-bisims1.bisim1AAss1\ elim!$: $AAss-\tau red1r-xt1$)
next
case ($bisim1AAss2\ e2\ n\ e'\ xs\ stk\ loc\ pc\ e1\ e3\ v1$)
note $IH = \langle \bigwedge stk'. \llbracket stk = rev\ vs\ @\ Addr\ a\ \# \ stk';\ pc < length\ (compE2\ e2);\ compE2\ e2\ !\ pc = Invoke\ M\ (length\ vs); n + max-vars\ e' \leq length\ xs; bsok\ e2\ n \rrbracket \implies ?concl\ e'\ xs\ e2\ n\ pc\ stk'\ loc \rangle$
note $inv = \langle compE2\ (e1[e2] := e3)\ !\ (length\ (compE2\ e1) + pc) = Invoke\ M\ (length\ vs) \rangle$
note $bisim = \langle P, e2, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, None) \rangle$
with $inv\ \langle length\ (compE2\ e1) + pc < length\ (compE2\ (e1[e2] := e3)) \rangle$ have $pc: pc < length\ (compE2\ e2)$
by(auto split: if-split-asm dest: $bisim1-pc-length-compE2$)
moreover with inv have $compE2\ e2\ !\ pc = Invoke\ M\ (length\ vs)$ by $simp$
moreover with $bisim\ pc$
obtain $vs''\ v''\ stk''$ where $stk = vs''\ @\ v''\ \# \ stk''$ and $length\ vs'' = length\ vs$
by(auto dest!: $bisim1-Invoke-stkD$)
with $\langle stk\ @\ [v1] = rev\ vs\ @\ Addr\ a\ \# \ stk' \rangle$ obtain stk'''
where $stk''': stk = rev\ vs\ @\ Addr\ a\ \# \ stk'''$ and $stk: stk' = stk'''\ @\ [v1]$
by(cases stk' rule: $rev-cases$) auto
from $\langle n + max-vars\ (Val\ v1[e'] := e3) \leq length\ xs \rangle$ have $n + max-vars\ e' \leq length\ xs$ by $simp$
moreover from $\langle bsok\ (e1[e2] := e3)\ n \rangle$ have $bsok\ e2\ n$ by $simp$
ultimately have $?concl\ e'\ xs\ e2\ n\ pc\ stk''' \ loc$ using stk''' by—(rule IH)
then obtain $e''\ xs'$ where $IH': \tau red1r\ P\ t\ h\ (e', xs)\ (e'', xs')\ call1\ e'' = [(a, M, vs)]$
and $bisim: P, e2, h \vdash (e'', xs') \leftrightarrow (rev\ vs\ @\ Addr\ a\ \# \ stk''', loc, pc, None)$ by $blast$
from $bisim$
have $P, e1[e2] := e3, h \vdash (Val\ v1[e'] := e3, xs') \leftrightarrow ((rev\ vs\ @\ Addr\ a\ \# \ stk''')\ @\ [v1], loc, length\ (compE2\ e1) + pc, None)$
by(rule $bisim1-bisims1.bisim1AAss2$)
with $IH'\ stk$ show $?case$ by(fastforce $elim!$: $AAss-\tau red1r-xt2$)
next
case ($bisim1AAss3\ e3\ n\ e'\ xs\ stk\ loc\ pc\ e1\ e2\ v1\ v2$)
note $IH = \langle \bigwedge stk'. \llbracket stk = rev\ vs\ @\ Addr\ a\ \# \ stk';\ pc < length\ (compE2\ e3);\ compE2\ e3\ !\ pc = Invoke\ M\ (length\ vs); n + max-vars\ e' \leq length\ xs; bsok\ e3\ n \rrbracket \implies ?concl\ e'\ xs\ e3\ n\ pc\ stk'\ loc \rangle$
note $inv = \langle compE2\ (e1[e2] := e3)\ !\ (length\ (compE2\ e1) + length\ (compE2\ e2) + pc) = Invoke\ M\ (length\ vs) \rangle$
with $\langle length\ (compE2\ e1) + length\ (compE2\ e2) + pc < length\ (compE2\ (e1[e2] := e3)) \rangle$
have $pc: pc < length\ (compE2\ e3)$ by($simp\ add: nth-Cons\ split: nat.split-asm\ if-split-asm$)
moreover with inv have $compE2\ e3\ !\ pc = Invoke\ M\ (length\ vs)$ by $simp$
moreover with $\langle P, e3, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, None) \rangle$ pc
obtain $vs''\ v''\ stk''$ where $stk = vs''\ @\ v''\ \# \ stk''$ and $length\ vs'' = length\ vs$
by(auto dest!: $bisim1-Invoke-stkD$)
with $\langle stk\ @\ [v2, v1] = rev\ vs\ @\ Addr\ a\ \# \ stk' \rangle$ obtain stk'''
where $stk''': stk = rev\ vs\ @\ Addr\ a\ \# \ stk'''$ and $stk: stk' = stk'''\ @\ [v2, v1]$
by(cases stk' rule: $rev-cases$) auto
from $\langle n + max-vars\ (Val\ v1[Val\ v2] := e') \leq length\ xs \rangle$ have $n + max-vars\ e' \leq length\ xs$ by $simp$
moreover from $\langle bsok\ (e1[e2] := e3)\ n \rangle$ have $bsok\ e3\ n$ by $simp$
ultimately have $?concl\ e'\ xs\ e3\ n\ pc\ stk''' \ loc$ using stk''' by—(rule IH)
then obtain $e''\ xs'$ where $IH': \tau red1r\ P\ t\ h\ (e', xs)\ (e'', xs')\ call1\ e'' = [(a, M, vs)]$
and $bisim: P, e3, h \vdash (e'', xs') \leftrightarrow (rev\ vs\ @\ Addr\ a\ \# \ stk''', loc, pc, None)$ by $blast$
from $bisim$
have $P, e1[e2] := e3, h \vdash (Val\ v1[Val\ v2] := e'', xs') \leftrightarrow ((rev\ vs\ @\ Addr\ a\ \# \ stk''')\ @\ [v2, v1], loc, length\ (compE2\ e1) + length\ (compE2\ e2) + pc, None)$

```

  by  $\text{--}(rule\ bisim1\text{-}bisims1.bisim1AAss3, auto)$ 
  with  $IH' stk$  show  $?case$  by  $(fastforce\ elim!: AAss\text{-}\tau red1r\text{-}xt3)$ 
next
  case  $bisim1AAss4$  thus  $?case$  by  $simp$ 
next
  case  $bisim1ALength$  thus  $?case$ 
  by  $(fastforce\ split: if\text{-}split\text{-}asm\ intro: bisim1\text{-}bisims1.bisim1ALength\ dest: bisim1\text{-}pc\text{-}length\text{-}compE2$ 
 $elim!: ALength\text{-}\tau red1r\text{-}xt)$ 
next
  case  $bisim1FAcc$  thus  $?case$ 
  by  $(fastforce\ split: if\text{-}split\text{-}asm\ intro: bisim1\text{-}bisims1.bisim1FAcc\ dest: bisim1\text{-}pc\text{-}length\text{-}compE2\ elim!:$ 
 $FAcc\text{-}\tau red1r\text{-}xt)$ 
next
  case  $bisim1FAss1$  thus  $?case$ 
  apply  $(auto\ split: if\text{-}split\text{-}asm\ intro: bisim1\text{-}bisims1.bisim1FAss1\ dest: bisim1\text{-}pc\text{-}length\text{-}compE2$ 
 $elim!: FAss\text{-}\tau red1r\text{-}xt1)$ 
  by  $(fastforce\ intro: bisim1\text{-}bisims1.bisim1FAss1\ elim!: FAss\text{-}\tau red1r\text{-}xt1)$ 
next
  case  $(bisim1FAss2\ e2\ n\ e'\ xs\ stk\ loc\ pc\ e1\ F\ D\ v1)$ 
  note  $IH = \langle \bigwedge stk'. \llbracket stk = rev\ vs\ @\ Addr\ a\ \# stk'; pc < length\ (compE2\ e2); compE2\ e2\ !\ pc =$ 
 $Invoke\ M\ (length\ vs); n + max\text{-}vars\ e' \leq length\ xs; bsok\ e2\ n$ 
 $\rrbracket \implies ?concl\ e'\ xs\ e2\ n\ pc\ stk'\ loc \rangle$ 
  note  $inv = \langle compE2\ (e1 \cdot F\{D\}) := e2 \rangle ! (length\ (compE2\ e1) + pc) = Invoke\ M\ (length\ vs)$ 
  with  $\langle length\ (compE2\ e1) + pc < length\ (compE2\ (e1 \cdot F\{D\}) := e2) \rangle$  have  $pc: pc < length\ (compE2$ 
 $e2)$ 
  by  $(simp\ split: if\text{-}split\text{-}asm\ nat.\text{split}\text{-}asm\ add: nth\text{-}Cons)$ 
  moreover with  $inv$  have  $compE2\ e2\ !\ pc = Invoke\ M\ (length\ vs)$  by  $simp$ 
  moreover with  $\langle P, e2, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, None) \rangle pc$ 
  obtain  $vs''\ v''\ stk''$  where  $stk = vs'' @ v'' \# stk''$  and  $length\ vs'' = length\ vs$ 
  by  $(auto\ dest!: bisim1\text{-}Invoke\text{-}stkD)$ 
  with  $\langle stk @ [v1] = rev\ vs @ Addr\ a \# stk' \rangle$  obtain  $stk'''$ 
  where  $stk''': stk = rev\ vs @ Addr\ a \# stk'''$  and  $stk: stk' = stk''' @ [v1]$ 
  by  $(cases\ stk'\ rule: rev\text{-}cases)\ auto$ 
  from  $\langle n + max\text{-}vars\ (Val\ v1 \cdot F\{D\}) := e' \rangle \leq length\ xs$  have  $n + max\text{-}vars\ e' \leq length\ xs$  by  $simp$ 
  moreover from  $\langle bsok\ (e1 \cdot F\{D\}) := e2 \rangle n$  have  $bsok\ e2\ n$  by  $simp$ 
  ultimately have  $?concl\ e'\ xs\ e2\ n\ pc\ stk''' loc$  using  $stk'''$  by  $\text{--}(rule\ IH)$ 
  then obtain  $e''\ xs'$  where  $IH': \tau red1r\ P\ t\ h\ (e', xs) (e'', xs')\ call1\ e'' = \lfloor (a, M, vs) \rfloor$ 
  and  $bisim: P, e2, h \vdash (e'', xs') \leftrightarrow (rev\ vs @ Addr\ a \# stk''', loc, pc, None)$  by  $blast$ 
  from  $bisim$  have  $P, e1 \cdot F\{D\} := e2, h \vdash (Val\ v1 \cdot F\{D\}) := e'', xs' \leftrightarrow ((rev\ vs @ Addr\ a \# stk''') @$ 
 $[v1], loc, length\ (compE2\ e1) + pc, None)$ 
  by  $(rule\ bisim1\text{-}bisims1.bisim1FAss2)$ 
  with  $IH' stk$  show  $?case$  by  $(fastforce\ elim!: FAss\text{-}\tau red1r\text{-}xt2)$ 
next
  case  $bisim1FAss3$  thus  $?case$  by  $simp$ 
next
  case  $(bisim1CAS1\ e\ n\ e'\ xs\ loc\ pc\ e2\ e3\ D\ F)$ 
  note  $IH = \langle \llbracket pc < length\ (compE2\ e); compE2\ e\ !\ pc = Invoke\ M\ (length\ vs); n + max\text{-}vars\ e' \leq$ 
 $length\ xs; bsok\ e\ n$ 
 $\rrbracket \implies ?concl\ e'\ xs\ e\ n\ pc\ stk'\ loc \rangle$ 
  note  $bisim = \langle P, e, h \vdash (e', xs) \leftrightarrow (rev\ vs @ Addr\ a \# stk', loc, pc, None) \rangle$ 
  note  $len = \langle n + max\text{-}vars - \leq length\ xs \rangle$ 
  hence  $len': n + max\text{-}vars\ e' \leq length\ xs$  by  $simp$ 
  note  $inv = \langle compE2 - !\ pc = Invoke\ M\ (length\ vs) \rangle$ 
  with  $\langle pc < length - \rangle bisim$  have  $pc: pc < length\ (compE2\ e)$ 

```

by(*auto split: if-split-asm dest: bisim1-pc-length-compE2*)
moreover with *inv* **have** *compE2 e ! pc = Invoke M (length vs)* **by** *simp*
moreover from $\langle \text{bsok } - \ n \rangle$ **have** *bsok e n* **by** *simp*
ultimately have $?concl\ e'\ xs\ e\ n\ pc\ stk'\ loc$ **using** *len'* **by**-(*rule IH*)
thus $?case$
by(*fastforce intro: bisim1-bisims1.bisim1CAS1 elim!: CAS- τ red1r-xt1*)
next
case (*bisim1CAS2 e2 n e' xs stk loc pc e1 e3 D F v1*)
note $IH = \langle \bigwedge stk'. \llbracket stk = rev\ vs\ @\ Addr\ a\ \# \ stk';\ pc < length\ (compE2\ e2);\ compE2\ e2\ !\ pc =$
Invoke M (length vs); n + max-vars e' \leq length xs; bsok e2 n
 $\rrbracket \implies ?concl\ e'\ xs\ e2\ n\ pc\ stk'\ loc \rangle$
note *inv = $\langle compE2\ -!\ (length\ (compE2\ e1) + pc) = Invoke\ M\ (length\ vs) \rangle$*
note *bisim = $\langle P, e2, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, None) \rangle$*
with *inv* $\langle length\ (compE2\ e1) + pc < length\ (compE2\ -) \rangle$ **have** *pc: pc < length (compE2 e2)*
by(*auto split: if-split-asm dest: bisim1-pc-length-compE2*)
moreover with *inv* **have** *compE2 e2 ! pc = Invoke M (length vs)* **by** *simp*
moreover with *bisim pc*
obtain $vs''\ v''\ stk''$ **where** $stk = vs''\ @\ v''\ \# \ stk''$ **and** $length\ vs'' = length\ vs$
by(*auto dest!: bisim1-Invoke-stkD*)
with $\langle stk\ @\ [v1] = rev\ vs\ @\ Addr\ a\ \# \ stk' \rangle$ **obtain** stk'''
where $stk''': stk = rev\ vs\ @\ Addr\ a\ \# \ stk'''$ **and** $stk: stk' = stk'''\ @\ [v1]$
by(*cases stk' rule: rev-cases*) *auto*
from $\langle n + max-vars - \leq length\ xs \rangle$ **have** $n + max-vars\ e' \leq length\ xs$ **by** *simp*
moreover from $\langle \text{bsok } - \ n \rangle$ **have** *bsok e2 n* **by** *simp*
ultimately have $?concl\ e'\ xs\ e2\ n\ pc\ stk''' \ loc$ **using** stk''' **by**-(*rule IH*)
then obtain $e''\ xs'$ **where** $IH': \tau red1r\ P\ t\ h\ (e',\ xs)\ (e'',\ xs')\ call1\ e'' = \llbracket (a, M, vs) \rrbracket$
and *bisim: $P, e2, h \vdash (e'', xs') \leftrightarrow (rev\ vs\ @\ Addr\ a\ \# \ stk''', loc, pc, None)$* **by** *blast*
from *bisim*
have $P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h \vdash (Val\ v1 \cdot compareAndSwap(D \cdot F, e'', e3), xs') \leftrightarrow ((rev\ vs\ @\ Addr\ a\ \# \ stk''')\ @\ [v1], loc, length\ (compE2\ e1) + pc, None)$
by(*rule bisim1-bisims1.bisim1CAS2*)
with $IH'\ stk$ **show** $?case$ **by**(*fastforce elim!: CAS- τ red1r-xt2*)
next
case (*bisim1CAS3 e3 n e' xs stk loc pc e1 e2 D F v1 v2*)
note $IH = \langle \bigwedge stk'. \llbracket stk = rev\ vs\ @\ Addr\ a\ \# \ stk';\ pc < length\ (compE2\ e3);\ compE2\ e3\ !\ pc =$
Invoke M (length vs); n + max-vars e' \leq length xs; bsok e3 n
 $\rrbracket \implies ?concl\ e'\ xs\ e3\ n\ pc\ stk'\ loc \rangle$
note *inv = $\langle compE2\ -!\ (length\ (compE2\ e1) + length\ (compE2\ e2) + pc) = Invoke\ M\ (length\ vs) \rangle$*
with $\langle length\ (compE2\ e1) + length\ (compE2\ e2) + pc < length\ (compE2\ -) \rangle$
have *pc: pc < length (compE2 e3)* **by**(*simp add: nth-Cons split: nat.split-asm if-split-asm*)
moreover with *inv* **have** *compE2 e3 ! pc = Invoke M (length vs)* **by** *simp*
moreover with $\langle P, e3, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, None) \rangle$ *pc*
obtain $vs''\ v''\ stk''$ **where** $stk = vs''\ @\ v''\ \# \ stk''$ **and** $length\ vs'' = length\ vs$
by(*auto dest!: bisim1-Invoke-stkD*)
with $\langle stk\ @\ [v2, v1] = rev\ vs\ @\ Addr\ a\ \# \ stk' \rangle$ **obtain** stk'''
where $stk''': stk = rev\ vs\ @\ Addr\ a\ \# \ stk'''$ **and** $stk: stk' = stk'''\ @\ [v2, v1]$
by(*cases stk' rule: rev-cases*) *auto*
from $\langle n + max-vars - \leq length\ xs \rangle$ **have** $n + max-vars\ e' \leq length\ xs$ **by** *simp*
moreover from $\langle \text{bsok } - \ n \rangle$ **have** *bsok e3 n* **by** *simp*
ultimately have $?concl\ e'\ xs\ e3\ n\ pc\ stk''' \ loc$ **using** stk''' **by**-(*rule IH*)
then obtain $e''\ xs'$ **where** $IH': \tau red1r\ P\ t\ h\ (e',\ xs)\ (e'',\ xs')\ call1\ e'' = \llbracket (a, M, vs) \rrbracket$
and *bisim: $P, e3, h \vdash (e'', xs') \leftrightarrow (rev\ vs\ @\ Addr\ a\ \# \ stk''', loc, pc, None)$* **by** *blast*
from *bisim*
have $P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h \vdash (Val\ v1 \cdot compareAndSwap(D \cdot F, Val\ v2, e''), xs') \leftrightarrow$

$((rev\ vs\ @\ Addr\ a\ \# \ stk''')\ @\ [v2,\ v1],\ loc,\ length\ (compE2\ e1)\ +\ length\ (compE2\ e2)\ +\ pc,\ None)$
by $-(rule\ bisim1-bisims1.bisim1CAS3,\ auto)$
with $IH'\ stk\ show\ ?case\ by(fastforce\ elim!:\ CAS-\tau red1r-xt3)$
next
case $(bisim1Call1\ obj\ n\ obj'\ xs\ loc\ pc\ ps\ M')$
note $IH = \langle [pc < length\ (compE2\ obj); compE2\ obj ! pc = Invoke\ M\ (length\ vs); n + max-vars\ obj' \leq length\ xs; bsok\ obj\ n$
 $\quad] \implies ?concl\ obj'\ xs\ obj\ n\ pc\ stk'\ loc \rangle$
note $bisim = \langle P, obj, h \vdash (obj', xs) \leftrightarrow (rev\ vs\ @\ Addr\ a\ \# \ stk',\ loc,\ pc,\ None) \rangle$
note $len = \langle n + max-vars\ (obj'.M'(ps)) \leq length\ xs \rangle$
hence $len': n + max-vars\ obj' \leq length\ xs$ **by** $simp$
from $\langle bsok\ (obj'.M'(ps))\ n \rangle$ **have** $bsok: bsok\ obj\ n$ **by** $simp$
note $inv = \langle compE2\ (obj'.M'(ps)) ! pc = Invoke\ M\ (length\ vs) \rangle$
with $\langle pc < length\ (compE2\ (obj'.M'(ps))) \rangle$ $bisim$
have $pc: pc < length\ (compE2\ obj) \vee ps = [] \wedge pc = length\ (compE2\ obj)$
by $(cases\ ps)(auto\ split:\ if-split-asm\ dest:\ bisim1-pc-length-compE2)$
thus $?case$
proof
assume $pc < length\ (compE2\ obj)$
moreover with inv **have** $compE2\ obj ! pc = Invoke\ M\ (length\ vs)$ **by** $simp$
ultimately have $?concl\ obj'\ xs\ obj\ n\ pc\ stk'\ loc$ **using** $len'\ bsok$ **by** $(rule\ IH)$
thus $?thesis$ **by** $(fastforce\ intro:\ bisim1-bisims1.bisim1Call1\ elim!:\ Call-\tau red1r-obj)$
next
assume $[simp]: ps = [] \wedge pc = length\ (compE2\ obj)$
with inv **have** $[simp]: vs = []\ M' = M$ **by** $simp-all$
with $bisim$ **have** $[simp]: vs = []\ stk' = []$ **by** $(auto\ dest:\ bisim1-pc-length-compE2D)$
with $bisim\ len'\ bsok$ **have** $\tau red1r\ P\ t\ h\ (obj',\ xs)\ (addr\ a,\ loc)$
by $(auto\ intro:\ bisim1-Val-\tau red1r\ simp\ add:\ bsok-def)$
moreover
have $P, obj.M([], h) \vdash (addr\ a.M([], loc) \leftrightarrow ([Addr\ a],\ loc,\ length\ (compE2\ obj),\ None)$
by $(rule\ bisim1-bisims1.bisim1Call1[OF\ bisim1Val2])\ simp-all$
ultimately show $?thesis$ **by** $auto(fastforce\ elim!:\ Call-\tau red1r-obj)$
qed
next
case $(bisim1CallParams\ ps\ n\ ps'\ xs\ stk\ loc\ pc\ obj\ M'\ v)$
note $IH = \langle \bigwedge stk'. [stk = rev\ vs\ @\ Addr\ a\ \# \ stk'; pc < length\ (compEs2\ ps); compEs2\ ps ! pc = Invoke\ M\ (length\ vs); n + max-varss\ ps' \leq length\ xs; bsoks\ ps\ n$
 $\quad] \implies ?concls\ ps'\ xs\ ps\ n\ pc\ stk'\ loc \rangle$
note $bisim = \langle P, ps, h \vdash (ps', xs) [\leftrightarrow] (stk,\ loc,\ pc,\ None) \rangle$
note $len = \langle n + max-varss\ (Val\ v.M'(ps')) \leq length\ xs \rangle$
hence $len': n + max-varss\ ps' \leq length\ xs$ **by** $simp$
note $stk = \langle stk\ @\ [v] = rev\ vs\ @\ Addr\ a\ \# \ stk' \rangle$
note $inv = \langle compE2\ (obj.M'(ps)) ! (length\ (compE2\ obj) + pc) = Invoke\ M\ (length\ vs) \rangle$
from $\langle bsok\ (obj.M'(ps))\ n \rangle$ **have** $bsok: bsoks\ ps\ n$ **by** $simp$
from $\langle length\ (compE2\ obj) + pc < length\ (compE2\ (obj.M'(ps))) \rangle$
have $pc < length\ (compEs2\ ps) \vee pc = length\ (compEs2\ ps)$ **by** $(auto)$
thus $?case$
proof
assume $pc: pc < length\ (compEs2\ ps)$
moreover with inv **have** $compEs2\ ps ! pc = Invoke\ M\ (length\ vs)$ **by** $simp$
moreover with $bisim\ pc$
obtain $vs''\ v''\ stk''$ **where** $stk = vs''\ @\ v''\ \# \ stk''$ **and** $length\ vs'' = length\ vs$
by $(auto\ dest!:\ bisims1-Invoke-stkD)$
with $\langle stk\ @\ [v] = rev\ vs\ @\ Addr\ a\ \# \ stk' \rangle$ **obtain** stk'''

where stk''' : $stk = rev\ vs\ @\ Addr\ a\ \# \ stk'''$ **and** stk : $stk' = stk''' @ [v]$
by(cases stk' rule: rev-cases) auto
note $len'\ stk'''$
ultimately have $?concl\ ps'\ xs\ ps\ n\ pc\ stk'''\ loc$ **using** $bsok$ **by**-(rule IH)
then obtain $es''\ xs'$ **where** IH' : $\tau reds1r\ P\ t\ h\ (ps',\ xs)\ (es'',\ xs')\ calls1\ es'' = [(a,\ M,\ vs)]$
and $bisim$: $P, ps, h \vdash (es'', xs') [\leftrightarrow] (rev\ vs\ @\ Addr\ a\ \# \ stk''',\ loc,\ pc,\ None)$ **by** blast
from $bisim$ **have** $P, obj \cdot M'(ps), h \vdash (Val\ v \cdot M'(es''), xs') \leftrightarrow ((rev\ vs\ @\ Addr\ a\ \# \ stk''') @ [v], loc,$
 $length\ (compE2\ obj) + pc,\ None)$
by(rule $bisim1$ - $bisims1$. $bisim1CallParams$)
with IH' stk **show** $?case$
by(fastforce elim!: $Call$ - $\tau red1r$ -param simp add: is-vals-conv)
next
assume $[simp]$: $pc = length\ (compEs2\ ps)$
from $bisim$ **obtain** vs' **where** $[simp]$: $stk = rev\ vs'$
and $psvs'$: $length\ ps = length\ vs'$ **by**(auto dest: $bisims1$ - pc - $length$ - $compEs2D$)
from inv **have** $[simp]$: $M' = M$ **and** $vsps$: $length\ vs = length\ ps$ **by** simp-all
with $stk\ pvs'$ **have** $[simp]$: $v = Addr\ a\ stk' = []\ vs' = vs$ **by** simp-all
from $bisim\ len'\ bsok$ **have** $\tau reds1r\ P\ t\ h\ (ps',\ xs)\ (map\ Val\ vs,\ loc)$
by(auto intro: $bisims1$ - Val - $\tau Red1r$ simp add: $bsoks$ -def)
moreover from $bisims1$ - map - Val - $append$ [$OF\ bisims1Nil\ vsps$ [$symmetric$], $simplified$, of $P\ h\ loc$]
have $P, obj \cdot M(ps), h \vdash (addr\ a \cdot M(map\ Val\ vs), loc) \leftrightarrow (rev\ vs\ @\ [Addr\ a], loc,\ length\ (compE2\ obj)$
 $+ length\ (compEs2\ ps), None)$
by(rule $bisim1$ - $bisims1$. $bisim1CallParams$)
ultimately show $?thesis$ **by**(fastforce elim!: $Call$ - $\tau red1r$ -param)
qed
next
case $bisim1BlockSome1$ **thus** $?case$ **by** simp
next
case $bisim1BlockSome2$ **thus** $?case$ **by** simp
next
case ($bisim1BlockSome4\ e\ n\ e'\ xs\ loc\ pc\ V\ T\ v$)
note $IH = \langle [pc < length\ (compE2\ e); compE2\ e ! pc = Invoke\ M\ (length\ vs); Suc\ n + max-vars\ e'$
 $\leq length\ xs; bsok\ e\ (Suc\ n)$
 $\rangle \implies ?concl\ e'\ xs\ e\ (Suc\ V)\ pc\ stk'\ loc$
from $\langle Suc\ (Suc\ pc) < length\ (compE2\ \{V:T=[v]; e\}) \rangle$ **have** $pc < length\ (compE2\ e)$ **by** simp
moreover from $\langle compE2\ \{V:T=[v]; e\} ! Suc\ (Suc\ pc) = Invoke\ M\ (length\ vs) \rangle$
have $compE2\ e ! pc = Invoke\ M\ (length\ vs)$ **by** simp
moreover note $len = \langle n + max-vars\ \{V:T=None; e'\} \leq length\ xs \rangle$
hence $Suc\ n + max-vars\ e' \leq length\ xs$ **by** simp
moreover from $\langle bsok\ \{V:T=[v]; e\}\ n \rangle$ **have** $bsok\ e\ (Suc\ n)$ **by** simp
ultimately have $?concl\ e'\ xs\ e\ (Suc\ V)\ pc\ stk'\ loc$ **by**(rule IH)
then obtain $e''\ xs'$ **where** red : $\tau red1r\ P\ t\ h\ (e',\ xs)\ (e'',\ xs')$
and $bisim'$: $P, e, h \vdash (e'', xs') \leftrightarrow (rev\ vs\ @\ Addr\ a\ \# \ stk',\ loc,\ pc,\ None)$
and $call$: $call1\ e'' = [(a,\ M,\ vs)]$ **by** blast
from red **have** $\tau red1r\ P\ t\ h\ (\{V:T=None; e'\}, xs)\ (\{V:T=None; e''\}, xs')$ **by**(rule $Block$ - $None$ - $\tau red1r$ -xt)
with $bisim'$ $call$ **show** $?case$ **by**(fastforce intro: $bisim1$ - $bisims1$. $bisim1BlockSome4$)
next
case ($bisim1BlockNone\ e\ n\ e'\ xs\ loc\ pc\ V\ T$)
note $IH = \langle [pc < length\ (compE2\ e); compE2\ e ! pc = Invoke\ M\ (length\ vs); Suc\ n + max-vars\ e'$
 $\leq length\ xs; bsok\ e\ (Suc\ n)$
 $\rangle \implies ?concl\ e'\ xs\ e\ (Suc\ V)\ pc\ stk'\ loc$
from $\langle pc < length\ (compE2\ \{V:T=None; e\}) \rangle$ **have** $pc < length\ (compE2\ e)$ **by** simp
moreover from $\langle compE2\ \{V:T=None; e\} ! pc = Invoke\ M\ (length\ vs) \rangle$
have $compE2\ e ! pc = Invoke\ M\ (length\ vs)$ **by** simp

moreover note $len = \langle n + \text{max-vars } \{V:T=None; e'\} \leq \text{length } xs \rangle$
hence $Suc\ n + \text{max-vars } e' \leq \text{length } xs$ **by** *simp*
moreover from $\langle \text{bsok } \{V:T=None; e\} \ n \rangle$ **have** $\text{bsok } e\ (Suc\ n)$ **by** *simp*
ultimately have $?concl\ e'\ xs\ e\ (Suc\ V)\ pc\ stk'\ loc$ **by** (rule *IH*)
then obtain $e''\ xs'$ **where** $red: \tau red1r\ P\ t\ h\ (e',\ xs)\ (e'',\ xs')$
and $bisim': P, e, h \vdash (e'', xs') \leftrightarrow (rev\ vs\ @\ Addr\ a\ \# \ stk',\ loc,\ pc,\ None)$
and $call: call1\ e'' = \lfloor (a, M, vs) \rfloor$ **by** *blast*
from red **have** $\tau red1r\ P\ t\ h\ (\{V:T=None; e'\}, xs)\ (\{V:T=None; e''\}, xs')$ **by** (rule *Block-None- $\tau red1r$ -xt*)
with $bisim'\ call$ **show** $?case$ **by** (*fastforce* *intro: bisim1-bisims1.bisim1BlockNone*)
next
case *bisim1Sync1* **thus** $?case$
apply (*auto split: if-split-asm* *intro: bisim1-bisims1.bisim1Sync1* *dest: bisim1-pc-length-compE2*
elim!: Sync- $\tau red1r$ -xt)
by (*fastforce split: if-split-asm* *intro: bisim1-bisims1.bisim1Sync1* *elim!: Sync- $\tau red1r$ -xt*)
next
case *bisim1Sync2* **thus** $?case$ **by** *simp*
next
case *bisim1Sync3* **thus** $?case$ **by** *simp*
next
case *bisim1Sync4* **thus** $?case$
apply (*auto split: if-split-asm* *intro: bisim1-bisims1.bisim1Sync4* *dest: bisim1-pc-length-compE2*
elim!: InSync- $\tau red1r$ -xt)
by (*fastforce split: if-split-asm* *intro: bisim1-bisims1.bisim1Sync4* *elim!: InSync- $\tau red1r$ -xt*)
next
case *bisim1Sync5* **thus** $?case$ **by** *simp*
next
case *bisim1Sync6* **thus** $?case$ **by** *simp*
next
case *bisim1Sync7* **thus** $?case$ **by** *simp*
next
case *bisim1Sync8* **thus** $?case$ **by** *simp*
next
case *bisim1Sync9* **thus** $?case$ **by** *simp*
next
case *bisim1InSync* **thus** $?case$ **by** *simp*
next
case *bisim1Seq1* **thus** $?case$
apply (*auto split: if-split-asm* *intro: bisim1-bisims1.bisim1Seq1* *dest: bisim1-pc-length-compE2* *elim!:*
Seq- $\tau red1r$ -xt)
by (*fastforce split: if-split-asm* *intro: bisim1-bisims1.bisim1Seq1* *elim!: Seq- $\tau red1r$ -xt*)
next
case *bisim1Seq2* **thus** $?case$
by (*auto split: if-split-asm* *intro: bisim1-bisims1.bisim1Seq2* *dest: bisim1-pc-length-compE2*)
next
case *bisim1Cond1* **thus** $?case$
apply (*clarsimp split: if-split-asm*)
apply (*fastforce* *intro!: exI* *intro: bisim1-bisims1.bisim1Cond1* *elim!: Cond- $\tau red1r$ -xt*)
by (*fastforce* *dest: bisim1-pc-length-compE2*)
next
case *bisim1CondThen* **thus** $?case$
apply (*clarsimp split: if-split-asm*)
apply (*fastforce* *intro!: exI* *intro: bisim1-bisims1.bisim1CondThen*)
by (*fastforce* *dest: bisim1-pc-length-compE2*)
next

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case bisim1CondElse thus ?case
  by(clarsimp split: if-split-asm)(fastforce intro!: exI intro: bisim1-bisims1.bisim1CondElse)
next
case bisim1While1 thus ?case by simp
next
case bisim1While3 thus ?case
  apply(auto split: if-split-asm intro: bisim1-bisims1.bisim1While3 dest: bisim1-pc-length-compE2
elim!: Cond-τred1r-xt)
  by(fastforce split: if-split-asm intro: bisim1-bisims1.bisim1While3 elim!: Cond-τred1r-xt)
next
case bisim1While4 thus ?case
  apply(auto split: if-split-asm intro: bisim1-bisims1.bisim1While4 dest: bisim1-pc-length-compE2
elim!: Seq-τred1r-xt)
  by(fastforce split: if-split-asm intro: bisim1-bisims1.bisim1While4 elim!: Seq-τred1r-xt)
next
case bisim1While6 thus ?case by simp
next
case bisim1While7 thus ?case by simp
next
case bisim1Throw1 thus ?case
  by(fastforce split: if-split-asm intro: bisim1-bisims1.bisim1Throw1 dest: bisim1-pc-length-compE2
elim!: Throw-τred1r-xt)
next
case bisim1Try thus ?case
  apply(auto split: if-split-asm intro: bisim1-bisims1.bisim1Try dest: bisim1-pc-length-compE2 elim!:
Try-τred1r-xt)
  by(fastforce split: if-split-asm intro: bisim1-bisims1.bisim1Try elim!: Try-τred1r-xt)
next
case bisim1TryCatch1 thus ?case by simp
next
case (bisim1TryCatch2 e n e' xs loc pc e1 C V)
  note IH = ⟨[pc < length (compE2 e); compE2 e ! pc = Invoke M (length vs); Suc n + max-vars e' ≤ length xs; bsok e (Suc n)
    ⟩ ⇒ ?concl e' xs e (Suc V) pc stk' loc
  from ⟨Suc (Suc (length (compE2 e1) + pc)) < length (compE2 (try e1 catch(C V) e))⟩
  have pc < length (compE2 e) by simp
  moreover from ⟨compE2 (try e1 catch(C V) e) ! Suc (Suc (length (compE2 e1) + pc)) = Invoke
M (length vs)⟩
  have compE2 e ! pc = Invoke M (length vs) by simp
  moreover note len = ⟨n + max-vars {V:Class C=None; e'} ≤ length xs
  hence Suc n + max-vars e' ≤ length xs by simp
  moreover from ⟨bsok (try e1 catch(C V) e) n⟩ have bsok e (Suc n) by simp
  ultimately have ?concl e' xs e (Suc V) pc stk' loc by(rule IH)
  then obtain e'' xs' where red: τred1r P t h (e', xs) (e'', xs')
    and bisim': P, e, h ⊢ (e'', xs') ↔ (rev vs @ Addr a # stk', loc, pc, None)
    and call: call1 e'' = [(a, M, vs)] by blast
  from red have τred1r P t h ({V:Class C=None; e'}, xs) ({V:Class C=None; e''}, xs')
    by(rule Block-None-τred1r-xt)
  with bisim' call show ?case by(fastforce intro: bisim1-bisims1.bisim1TryCatch2)
next
case bisims1Nil thus ?case by simp
next
case (bisims1List1 e n e' xs loc pc es)
  note IH = ⟨[pc < length (compE2 e); compE2 e ! pc = Invoke M (length vs); n + max-vars e' ≤

```

$length\ xs; bsok\ e\ n$
 $\quad \mathbb{J} \implies ?concl\ e'\ xs\ e\ n\ pc\ stk'\ loc$
note $bisim = \langle P, e, h \vdash (e', xs) \leftrightarrow (rev\ vs\ @\ Addr\ a\ \# \ stk',\ loc,\ pc,\ None) \rangle$
note $len = \langle n + max\text{-}varss\ (e' \# es) \leq length\ xs \rangle$
hence $len': n + max\text{-}vars\ e' \leq length\ xs$ **by** *simp*
from $\langle bsoks\ (e \# es)\ n \rangle$ **have** $bsok: bsok\ e\ n$ **by** *simp*
note $inv = \langle compEs2\ (e \# es)\ !\ pc = Invoke\ M\ (length\ vs) \rangle$
with $\langle pc < length\ (compEs2\ (e \# es)) \rangle$ **bisim** **have** $pc: pc < length\ (compE2\ e)$
by $(cases\ es)(auto\ split: if\text{-}split\text{-}asm\ dest: bisim1\text{-}pc\text{-}length\text{-}compE2)$
moreover with inv **have** $compE2\ e\ !\ pc = Invoke\ M\ (length\ vs)$ **by** *simp*
ultimately have $?concl\ e'\ xs\ e\ n\ pc\ stk'\ loc$ **using** $len'\ bsok$ **by** $(rule\ IH)$
thus $?case$ **by** $(fastforce\ intro: bisim1\text{-}bisims1.bisims1List1\ elim!: \tau red1r\text{-}inj\text{-}\tau reds1r)$
next
case $(bisims1List2\ es\ n\ es'\ xs\ stk\ loc\ pc\ e\ v)$
note $IH = \langle \bigwedge stk'. \mathbb{J}stk = rev\ vs\ @\ Addr\ a\ \# \ stk'; pc < length\ (compEs2\ es); compEs2\ es\ !\ pc = Invoke\ M\ (length\ vs); n + max\text{-}varss\ es' \leq length\ xs; bsoks\ es\ n$
 $\quad \mathbb{J} \implies ?concls\ es'\ xs\ es\ n\ pc\ stk'\ loc \rangle$
note $bisim = \langle P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, None) \rangle$
note $len = \langle n + max\text{-}varss\ (Val\ v \# es') \leq length\ xs \rangle$
hence $len': n + max\text{-}varss\ es' \leq length\ xs$ **by** *simp*
from $\langle bsoks\ (e \# es)\ n \rangle$ **have** $bsok: bsoks\ es\ n$ **by** *simp*
note $stk = \langle stk\ @\ [v] = rev\ vs\ @\ Addr\ a\ \# \ stk' \rangle$
note $inv = \langle compEs2\ (e \# es)\ !\ (length\ (compE2\ e) + pc) = Invoke\ M\ (length\ vs) \rangle$
from $\langle length\ (compE2\ e) + pc < length\ (compEs2\ (e \# es)) \rangle$ **have** $pc: pc < length\ (compEs2\ es)$
by *auto*
moreover with inv **have** $compEs2\ es\ !\ pc = Invoke\ M\ (length\ vs)$ **by** *simp*
moreover with $bisim\ pc$
obtain $vs''\ v''\ stk''$ **where** $stk = vs''\ @\ v''\ \# \ stk''$ **and** $length\ vs'' = length\ vs$
by $(auto\ dest!: bisims1\text{-}Invoke\text{-}stkD)$
with $\langle stk\ @\ [v] = rev\ vs\ @\ Addr\ a\ \# \ stk' \rangle$ **obtain** stk'''
where $stk''': stk = rev\ vs\ @\ Addr\ a\ \# \ stk'''$ **and** $stk: stk' = stk''' \ @\ [v]$
by $(cases\ stk'\ rule: rev\text{-}cases)\ auto$
note $len'\ stk'''$
ultimately have $?concls\ es'\ xs\ es\ n\ pc\ stk''' \ loc$ **using** $bsok$ **by** $-(rule\ IH)$
then obtain $es''\ xs'$ **where** $red: \tau reds1r\ P\ t\ h\ (es', xs)\ (es'', xs')$
and $call: calls1\ es'' = [(a, M, vs)]$
and $bisim: P, es, h \vdash (es'', xs') [\leftrightarrow] (rev\ vs\ @\ Addr\ a\ \# \ stk''',\ loc,\ pc,\ None)$ **by** *blast*
from $bisim$ **have** $P, e \# es, h \vdash (Val\ v \# es'', xs') [\leftrightarrow]$
 $((rev\ vs\ @\ Addr\ a\ \# \ stk''') \ @\ [v], loc, length\ (compE2\ e) + pc, None)$
by $(rule\ bisim1\text{-}bisims1.bisims1List2)$
moreover from red **have** $\tau reds1r\ P\ t\ h\ (Val\ v \# es', xs)\ (Val\ v \# es'', xs')$ **by** $(rule\ \tau reds1r\text{-}cons\text{-}\tau reds1r)$
ultimately show $?case$ **using** $stk\ call$ **by** *fastforce*
qed
end
declare $split\text{-}beta$ [*simp del*]
context $J1\text{-}JVM\text{-}conf\text{-}read$ **begin**
lemma $\tau Red1\text{-}simulates\text{-}exec\text{-}1\text{-}\tau$:
assumes $wf: wf\text{-}J1\text{-}prog\ P$
and $exec: exec\text{-}1\text{-}d\ (compP2\ P)\ t\ (Normal\ (xcp, h, frs))\ ta\ (Normal\ (xcp', h', frs'))$
and $bisim: bisim1\text{-}list1\ t\ h\ (e, xs)\ exs\ xcp\ frs$

and $\tau: \tau\text{Move2 } (\text{compP2 } P) (xcp, h, frs)$
shows $h = h' \wedge (\exists e' xs' exs'. (\text{if sim21-size } (\text{compP2 } P) (xcp', frs') (xcp, frs) \text{ then } \tau\text{Red1r else } \tau\text{Red1t}) P t h ((e, xs), exs) ((e', xs'), exs') \wedge \text{bisim1-list1 } t h (e', xs') exs' xcp' frs')$
using *bisim*
proof(*cases*)
case (*bl1-Normal stk loc C M pc FRS Ts T body D*)
hence [*simp*]: $frs = (stk, loc, C, M, pc) \# FRS$
and *conf*: $\text{compTP } P \vdash t: (xcp, h, (stk, loc, C, M, pc) \# FRS) \checkmark$
and *sees*: $P \vdash C \text{ sees } M: Ts \rightarrow T = [body] \text{ in } D$
and *bisim*: $P, \text{blocks1 } 0 (\text{Class } D \# Ts) body, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp)$
and *lenxs*: $\text{max-vars } e \leq \text{length } xs$
and *bisims*: $\text{list-all2 } (\text{bisim1-fr } P h) exs FRS \text{ by auto}$
from *sees-method-compP*[*OF sees, where f = $\lambda C M Ts T. \text{compMb2}$*]
have *sees'*: $\text{compP2 } P \vdash C \text{ sees } M: Ts \rightarrow T = [(max\text{-stack } body, max\text{-vars } body, \text{compE2 } body @ [Return], \text{compxE2 } body 0 0)] \text{ in } D$
by(*simp add: compP2-def compMb2-def*)
from *bisim* **have** *pc*: $pc \leq \text{length } (\text{compE2 } body)$ **by**(*auto dest: bisim1-pc-length-compE2*)
from *conf* **have** *hconf*: $hconf h \text{ preallocated } h$ **by**(*simp-all add: correct-state-def*)

from *sees wf* **have** *bsok*: $bsok (\text{blocks1 } 0 (\text{Class } D \# Ts) body) 0$
by(*auto dest!: sees-wf-mdecl simp add: bsok-def wf-mdecl-def WT1-expr-locks*)

from *exec obtain* *check*: $check (\text{compP2 } P) (xcp, h, frs)$
and *exec*: $\text{compP2 } P, t \vdash (xcp, h, frs) \text{ --ta-jvm--} \rightarrow (xcp', h', frs')$
by(*rule jvmd-NormalE*)(*auto simp add: exec-1-iff*)

from *wt-compTP-compP2*[*OF wf*] *exec conf*
have *conf'*: $\text{compTP } P \vdash t: (xcp', h', frs') \checkmark$ **by**(*auto intro: BV-correct-1*)

from *conf* **have** *tconf*: $P, h \vdash t \checkmark$ **unfolding** *correct-state-def*
by(*simp add: compP2-def tconf-def*)

show *?thesis*
proof(*cases xcp*)
case [*simp*]: *None*
from *exec* **have** *execi*: $(ta, xcp', h', frs') \in \text{exec-instr } (\text{instrs-of } (\text{compP2 } P) C M ! pc) (\text{compP2 } P) t h stk loc C M pc FRS$
by(*simp add: exec-1-iff*)
show *?thesis*
proof(*cases pc < length (compE2 body)*)
case *True*
with *execi* *sees'* **have** *execi*: $(ta, xcp', h', frs') \in \text{exec-instr } (\text{compE2 } body ! pc) (\text{compP2 } P) t h stk loc C M pc FRS$
by(*simp*)
from $\tau \text{ sees' } True$ **have** $\tau i: \tau\text{move2 } (\text{compP2 } P) h stk body pc None$ **by**(*simp add: $\tau\text{move2-iff}$*)

show *?thesis*
proof(*cases length frs' = Suc (length FRS)*)
case *False*
with *execi* *sees* *True* *compE2-not-Return*[*of body*]
have $(\exists M n. \text{compE2 } body ! pc = \text{Invoke } M n)$
apply(*cases compE2 body ! pc*)
apply(*auto split: if-split-asm sum.split-asm simp add: split-beta compP2-def compMb2-def*)
apply(*metis in-set-conv-nth*)+

```

done
then obtain MM n where ins: compE2 body ! pc = Invoke MM n by blast
with bisim1-Invoke-stkD[OF bisim[unfolded None], of MM n] True obtain vs' v' stk'
  where [simp]: stk = vs' @ v' # stk' n = length vs' by auto
from check sees True ins have is-Ref v'
  by(auto split: if-split-asm simp add: split-beta compP2-def compMb2-def check-def)
moreover from execi sees True ins False sees' have v' ≠ Null by auto
ultimately obtain a' where [simp]: v' = Addr a' by(auto simp add: is-Ref-def)
from bisim have Bisim': P,blocks1 0 (Class D#Ts) body,h ⊢ (e, xs) ↔ (rev (rev vs') @ Addr
a' # stk', loc, pc, None)
  by simp
from bisim1-Invoke-τRed[OF this - - bsok, of MM t] True ins lenxs
obtain e' xs' where red: τred1r P t h (e, xs) (e', xs')
  and bisim': P,blocks1 0 (Class D#Ts) body,h ⊢ (e', xs') ↔ (rev (rev vs') @ Addr a' # stk',
loc, pc, None)
  and call': call1 e' = [(a', MM, rev vs')] by auto
from red have Red: τRed1r P t h ((e, xs), exs) ((e', xs'), exs)
  by(rule τred1r-into-τRed1r)

from False execi True check ins sees' obtain U' Ts' T' meth D'
  where ha': typeof-addr h a' = [U']
  and Sees': compP2 P ⊢ class-type-of U' sees MM:Ts' → T' = [meth] in D'
by(auto simp add: check-def has-method-def split: if-split-asm)(auto split: extCallRet.split-asm)
from sees-method-compPD[OF Sees'[unfolded compP2-def]] obtain body'
  where Sees: P ⊢ class-type-of U' sees MM:Ts' → T'=[body'] in D'
  and [simp]: meth = (max-stack body', max-vars body', compE2 body' @ [Return], compxE2
body' 0 0)
  by(auto simp add: compMb2-def)

let ?e = blocks1 0 (Class D'#Ts') body'
let ?xs = Addr a' # rev vs' @ replicate (max-vars body') undefined-value
let ?e'xs' = (e', xs')
let ?f = (stk, loc, C, M, pc)
let ?f' = ([],Addr a' # rev vs' @ replicate (max-vars body') undefined-value, D', MM, 0)

from execi pc ins False ha' Sees' sees'
have [simp]: xcp' = None ta = ε frs' = ?f' # ?f # FRS h' = h
  by(auto split: if-split-asm simp add: split-beta)

from bisim' have bisim'': P,blocks1 0 (Class D#Ts) body,h ⊢ (e', xs') ↔ (rev (rev vs') @ Addr
a' # stk', loc, pc, None)
  by simp
have n = length vs' by simp
from conf' Sees' ins sees' True have n = length Ts'
  apply(auto simp add: correct-state-def)
  apply(drule (1) sees-method-fun)+
  apply(auto dest: sees-method-idemp sees-method-fun)
done
with ⟨n = length vs'⟩ have vs'Ts': length (rev vs') = length Ts' by simp

with call' ha' Sees
have True,P,t ⊢ 1 ⟨(e', xs')/exs,h⟩ -ε→ ⟨(e, ?xs)/ (e', xs') # exs, h⟩ by(rule red1Call)
hence True,P,t ⊢ 1 ⟨(e', xs')/exs,h⟩ -ε→ ⟨(e, ?xs)/ ?e'xs' # exs, h⟩ by(simp)
moreover from call' Sees ha' have τMove1 P h ((e', xs'), exs)

```

by(*auto simp add: synthesized-call-def dest!: τ move1-not-call1*[**where** $P=P$ **and** $h=h$] *dest: sees-method-fun*)

ultimately have τ Red1t P t h $((e', xs'), exs)$ $((?e, ?xs), ?e'xs' \# exs)$ **by** *auto*

moreover have *bisim1-list1* t h $(?e, ?xs)$ $(?e'xs' \# exs)$ *None* $(?f' \# ?f \# FRS)$

proof

from *conf'* **show** *compTP* $P \vdash t:(None, h, ?f' \# ?f \# FRS) \checkmark$ **by** *simp*

from *Sees* **show** $P \vdash D'$ *sees* *MM: $Ts' \rightarrow T' = [body']$* *in* D' **by**(*rule sees-method-idemp*)

show $P, blocks1\ 0$ (*Class* $D' \# Ts'$) *body', h* $\vdash (blocks1\ 0$ (*Class* $D' \# Ts'$) *body', ?xs*) $\leftrightarrow ([], Addr$

$a' \# rev\ vs' @ replicate$ (*max-vars* *body'*) *undefined-value, 0, None*)

by(*rule bisim1-refl*)

show *max-vars* ($blocks1\ 0$ (*Class* $D' \# Ts'$) *body'*) $\leq length\ ?xs$ **using** $vs' Ts'$ **by**(*simp add: blocks1-max-vars*)

from *sees* **have** *bisim1-fr* P h $?e'xs' ?f$

proof

show $P, blocks1\ 0$ (*Class* $D \# Ts$) *body, h* $\vdash (e', xs') \leftrightarrow (stk, loc, pc, None)$

using *bisim''* **by** *simp*

from *call'* **show** *call1* $e' = [(a', MM, rev\ vs')]$.

from *red* **have** $xs'xs: length\ xs' = length\ xs$ **by**(*rule τ red1r-preserves-len*)

with *red lenxs* **show** *max-vars* $e' \leq length\ xs'$ **by**(*auto dest: τ red1r-max-vars*)

qed

with *bisims* **show** *list-all2* (*bisim1-fr* P h) $(?e'xs' \# exs)$ $(?f \# FRS)$ **by** *simp*

qed

ultimately show *?thesis* **using** *Red*

by *auto*(*blast intro: rtranclp-trans rtranclp-tranclp-tranclp tranclp-into-rtranclp*)
next

case *True*

note $pc = \langle pc < length\ (compE2\ body) \rangle$

with *execi* *True* **have** $\exists\ stk' loc' pc'. frs' = (stk', loc', C, M, pc') \# FRS$

by(*cases* (*compE2* *body* @ [*Return*]) ! *pc*)(*auto split: if-split-asm sum.split-asm simp: split-beta, auto split: extCallRet.splits*)

then obtain $stk' loc' pc'$ **where** [*simp*]: $frs' = (stk', loc', C, M, pc') \# FRS$ **by** *blast*

from *conf* **obtain** *ST* **where** *compP2* $P, h \vdash stk$ $[:\leq] ST$ **by**(*auto simp add: correct-state-def conf-f-def2*)

hence *ST: $P, h \vdash stk$ $[:\leq] ST$* **by**(*rule List.list-all2-mono*)(*simp add: compP2-def*)

from *execi sees pc check*

have *exec'*: *exec-move-d* P t ($blocks1\ 0$ (*Class* $D \# Ts$) *body*) h (stk, loc, pc, xcp) *ta* $h' (stk', loc', pc', xcp')$

apply(*auto simp add: compP2-def compMb2-def exec-move-def check-def exec-meth-instr split: if-split-asm sum.split-asm*)

apply(*cases* *compE2* *body* ! *pc*)

apply(*auto simp add: neq-Nil-conv split-beta split: if-split-asm sum.split-asm*)

apply(*force split: extCallRet.split-asm*)

apply(*cases* *compE2* *body* ! *pc, auto simp add: split-beta neq-Nil-conv split: if-split-asm sum.split-asm*)

done

from *red1-simulates-exec-instr*[*OF wf hconf tconf bisim this - bsok ST*] *lenxs τi* **obtain** $e'' xs''$

where *bisim'*: $P, blocks1\ 0$ (*Class* $D \# Ts$) *body, h'* $\vdash (e'', xs'') \leftrightarrow (stk', loc', pc', xcp')$

and *red*: (*if* $xcp' = None \longrightarrow pc < pc'$ *then* $\tau red1r$ *else* $\tau red1t$) P t h (e, xs) (e'', xs'') **and** [*simp*]: $h' = h$

by(*auto simp del: blocks1.simps*)

have *Red*: (*if* *sim21-size* (*compP2* P) (xcp', frs') (xcp, frs) *then* $\tau Red1r$ *else* $\tau Red1t$) P t h $((e, xs), exs)$ $((e'', xs''), exs)$

proof(*cases* $xcp' = None \longrightarrow pc < pc'$)

case *True*

```

from bisim bisim' have  $pc \leq \text{Suc} (\text{length} (\text{compE2 } \text{body}))$   $pc' \leq \text{Suc} (\text{length} (\text{compE2 } \text{body}))$ 
  by(auto dest: bisim1-pc-length-compE2)
moreover {
  fix a assume  $xcp' = [a]$ 
  with exec' have  $pc = pc'$  by(auto dest: exec-move-raise-xcp-pcD) }
ultimately have sim21-size (compP2 P) (xcp', frs') (xcp, frs) using sees True
  by(auto simp add: sim21-size-def)(auto simp add: compP2-def compMb2-def intro!:
sim21-size-aux.intros)
  with red True show ?thesis by simp(rule  $\tau\text{red1r-into-}\tau\text{Red1r}$ )
next
  case False
  thus ?thesis using red by(auto intro:  $\tau\text{red1t-into-}\tau\text{Red1t}$   $\tau\text{red1r-into-}\tau\text{Red1r}$ )
qed
moreover from red lenxs
have max-vars  $e'' \leq \text{length } xs''$ 
  apply(auto dest:  $\tau\text{red1r-max-vars}$   $\tau\text{red1r-preserves-len}$   $\tau\text{red1t-max-vars}$   $\tau\text{red1t-preserves-len}$ 
split: if-split-asm)
  apply(frule  $\tau\text{red1r-max-vars}$   $\tau\text{red1t-max-vars}$ , drule  $\tau\text{red1r-preserves-len}$   $\tau\text{red1t-preserves-len}$ ,
simp)+
  done
with conf' sees bisim'
have bisim1-list1 t h (e'', xs'') exs xcp' ((stk', loc', C, M, pc') # FRS)
  unfolding  $\langle frs' = (stk', loc', C, M, pc') \# FRS \rangle$   $\langle h' = h \rangle$ 
  using bisims by(rule bisim1-list1.bl1-Normal)
ultimately show ?thesis by(auto split del: if-split)
qed
next
case False
with pc have [simp]:  $pc = \text{length} (\text{compE2 } \text{body})$  by simp
with execi sees have [simp]:  $xcp' = \text{None}$ 
by(cases compE2 body ! pc)(auto split: if-split-asm simp add: compP2-def compMb2-def split-beta)
from bisim have Bisim: P,blocks1 0 (Class D#Ts) body,h  $\vdash (e, xs) \leftrightarrow (stk, loc, \text{length} (\text{compE2}$ 
(blocks1 0 (Class D#Ts) body)), None) by simp
then obtain v where [simp]:  $stk = [v]$  by(blast dest: bisim1-pc-length-compE2D)
with Bisim lenxs bsok have red:  $\tau\text{red1r } P t h (e, xs) (Val v, loc)$ 
  by clarify (erule bisim1-Val- $\tau\text{red1r}$ [where n=0], simp-all add: bsok-def)
hence Red:  $\tau\text{Red1r } P t h ((e, xs), exs) ((Val v, loc), exs)$  by(rule  $\tau\text{red1r-into-}\tau\text{Red1r}$ )
show ?thesis
proof(cases FRS)
  case [simp]: Nil
  with bisims have [simp]:  $exs = []$  by simp
  with exec sees' have [simp]:  $ta = \varepsilon$   $xcp' = \text{None}$   $h' = h$   $frs' = []$ 
    by(auto simp add: exec-1-iff)
  from hconf have bisim1-list1 t h (Val v, loc) [] None [] by(rule bl1-finalVal)
  then show ?thesis using Red
    by(auto intro: rtranclp.rtrancl-into-rtrancl rtranclp-into-tranclp1 simp del:  $\tau\text{Red1-conv}$  simp
add: sim21-size-def)
next
  case (Cons f' FRS')
  then obtain  $stk'' loc'' C'' M'' pc''$ 
    where [simp]:  $FRS = (stk'', loc'', C'', M'', pc'') \# FRS'$  by(cases f') fastforce
  from bisims obtain  $e'' xs'' EXS'$  where [simp]:  $exs = (e'', xs'') \# EXS'$ 
    by(auto simp add: list-all2-Cons2)
  with bisims have bisim1-fr P h (e'', xs'')  $(stk'', loc'', C'', M'', pc'')$  by simp

```



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then obtain  $E'' Ts'' T'' body'' D'' a'' M''' vs''$ 
  where  $[simp]: e'' = E''$ 
  and  $sees'': P \vdash C'' sees M'': Ts'' \rightarrow T'' = [body'']$  in  $D''$ 
  and  $bisim'': P, blocks1\ 0\ (Class\ D''\#Ts'')\ body'', h \vdash (E'', xs'') \leftrightarrow (stk'', loc'', pc'', None)$ 
  and  $call'': call1\ E'' = [(a'', M''', vs'')]$ 
  and  $lenxs'': max-vars\ E'' \leq length\ xs''$ 
  by  $(cases)\ fastforce$ 
let  $?ee' = inline-call\ (Val\ v)\ E''$ 
let  $?e' = ?ee'$ 
let  $?xs' = xs''$ 

from  $bisim''\ call''\ \mathbf{have}\ pc'': pc'' < length\ (compE2\ (blocks1\ 0\ (Class\ D''\#Ts'')\ body''))$ 
  by  $(rule\ bisim1-call-pcD)$ 
hence  $pc'': pc'' < length\ (compE2\ body'')$  by  $simp$ 
with  $sees-method-compP[OF\ sees'', \mathbf{where}\ f = \lambda C\ M\ Ts\ T.\ compMb2]$ 
   $sees-method-compP[OF\ sees, \mathbf{where}\ f = \lambda C\ M\ Ts\ T.\ compMb2]$  conf
obtain  $ST\ LT\ \mathbf{where}\ \Phi: compTP\ P\ C''\ M''!\ pc'' = [(ST, LT)]$ 
  and  $conf'': conf-f\ (compP\ (\lambda C\ M\ Ts\ T.\ compMb2)\ P)\ h\ (ST, LT)\ (compE2\ body''\ @\ [Return])$ 
 $(stk'', loc'', C'', M'', pc'')$ 
  and  $ins: (compE2\ body''\ @\ [Return])!\ pc'' = Invoke\ M\ (length\ Ts)$ 
unfolding  $correct-state-def$  by  $(fastforce\ simp\ add: compP2-def\ compMb2-def\ dest: sees-method-fun)$ 
from  $bisim1-callD[OF\ bisim''\ call'',\ of\ M\ length\ Ts]\ ins\ pc''$ 
have  $[simp]: M''' = M$  by  $simp$ 

from  $call''\ \mathbf{have}\ call1\ E'' = [(a'', M''', vs'')]$  by  $simp$ 
have  $True, P, t \vdash 1\ \langle (Val\ v, loc)/(E'', xs'')\ \# EXS', h \rangle -\varepsilon \rightarrow$ 
 $\langle (inline-call\ (Val\ v)\ E'', xs'')/EXS', h \rangle$ 
  by  $(rule\ red1Return)\ simp$ 
hence  $True, P, t \vdash 1\ \langle (Val\ v, loc)/(E'', xs'')\ \# EXS', h \rangle -\varepsilon \rightarrow \langle (?e', ?xs')/EXS', h \rangle$ 
  by  $simp$ 
moreover have  $\tau Move1\ P\ h\ ((Val\ v, loc), (E'', xs'')\ \# EXS')$  by  $auto$ 
ultimately have  $\tau Red1\ P\ t\ h\ ((Val\ v, loc), (E'', xs'')\ \# EXS')\ ((?e', ?xs'), EXS')$  by  $auto$ 
moreover from  $exec\ sees\ \mathbf{have}\ [simp]: ta = \varepsilon\ h' = h$ 
  and  $[simp]: frs' = (v\ \# drop\ (length\ Ts + 1)\ stk'', loc'', C'', M'', pc'' + 1)\ \# FRS'$ 
  by  $(auto\ simp\ add: compP2-def\ compMb2-def\ exec-1-iff)$ 

  have  $bisim1-list1\ t\ h\ (?e', ?xs')\ EXS'\ None\ ((v\ \# drop\ (length\ Ts + 1)\ stk'', loc'', C'', M'',$ 
 $pc'' + 1)\ \# FRS')$ 
  proof
    from  $conf'\ \mathbf{show}\ compTP\ P\ \vdash\ t: (None, h, (v\ \# drop\ (length\ Ts + 1)\ stk'', loc'', C'', M'',$ 
 $pc'' + 1)\ \# FRS')$   $\checkmark$  by  $simp$ 
    from  $sees''\ \mathbf{show}\ P\ \vdash\ C''\ sees\ M'': Ts'' \rightarrow T'' = [body'']$  in  $D''$  .
    from  $bisim1-inline-call-Val[OF\ bisim''\ call'',\ of\ length\ Ts\ v]\ ins\ pc''$ 
    show  $P, blocks1\ 0\ (Class\ D''\#Ts'')\ body'', h \vdash (inline-call\ (Val\ v)\ E'', xs'') \leftrightarrow (v\ \# drop$ 
 $(length\ Ts + 1)\ stk'', loc'', pc'' + 1, None)$  by  $simp$ 
    from  $lenxs''\ max-vars-inline-call[of\ Val\ v\ E'']$ 
    show  $max-vars\ (inline-call\ (Val\ v)\ E'') \leq length\ xs''$  by  $simp$ 
    from  $bisims\ \mathbf{show}\ list-all2\ (bisim1-fr\ P\ h)\ EXS'\ FRS'$  by  $simp$ 
  qed
  ultimately show  $?thesis$  using  $Red$ 
  by  $(auto\ simp\ del: \tau Red1-conv\ intro: rtranclp-into-tranclp1\ rtranclp.rtrancl-into-rtrancl)$ 
qed
qed
next

```

```

case [simp]: (Some a')
from exec have execs: (xcp', h', frs') = exception-step (compP2 P) a' h (stk, loc, C, M, pc) FRS
and [simp]: ta = ε by(auto simp add: exec-1-iff)
from conf have confxcp': conf-xcp' P h xcp
unfolding correct-state-def by(auto simp add: compP2-def)
then obtain D' where ha': typeof-addr h a' = [Class-type D'] and subclsD': P ⊢ D' ≼* Throwable
by auto
from bisim have pc: pc < length (compE2 body) by(auto dest: bisim1-xcp-pcD)
show ?thesis
proof(cases match-ex-table (compP2 P) (cname-of h a') pc (ex-table-of (compP2 P) C M))
case None
from bisim have pc: pc < length (compE2 body) by(auto dest: bisim1-xcp-pcD)
with sees' None have match: match-ex-table (compP2 P) (cname-of h a') pc (compxE2 body 0)
0) = None
by(auto)
with execs sees' have [simp]: ta = ε xcp' = [a'] h' = h frs' = FRS using match sees' by auto
from conf obtain CCC where ha: typeof-addr h a' = [Class-type CCC] and subcls: P ⊢ CCC
≼* Throwable
unfolding correct-state-def by(auto simp add: conf-f-def2 compP2-def)
from bisim1-xcp-τRed[OF ha subcls bisim[unfolded Some], of λC M Ts T. compMb2] match lenxs
bsok
have red: τred1r P t h (e, xs) (Throw a', loc)
and b': P, blocks1 0 (Class D#Ts) body, h ⊢ (Throw a', loc) ↔ (stk, loc, pc, [a'])
by(auto simp add: compP2-def bsok-def)
from red have Red: τRed1r P t h ((e, xs), exs) ((Throw a', loc), exs)
by(rule τred1r-into-τRed1r)
show ?thesis
proof(cases FRS)
case (Cons f' FRS')
then obtain stk'' loc'' C'' M'' pc''
where [simp]: FRS = (stk'', loc'', C'', M'', pc'') # FRS' by(cases f') fastforce
from bisims obtain e'' xs'' EXS' where [simp]: exs = (e'', xs'') # EXS'
by(auto simp add: list-all2-Cons2)
with bisims have bisim1-fr P h (e'', xs'') (stk'', loc'', C'', M'', pc'') by simp
then obtain E'' Ts'' T'' body'' D'' a'' M''' vs''
where [simp]: e'' = E''
and sees'': P ⊢ C'' sees M'': Ts'' → T'' = [body''] in D''
and bisim'': P, blocks1 0 (Class D''#Ts'') body'', h ⊢ (E'', xs'') ↔ (stk'', loc'', pc'', None)
and call'': call1 E'' = [(a'', M''', vs'')]
and lenxs'': max-vars E'' ≤ length xs''
by(cases) fastforce
let ?ee' = inline-call (Throw a') E''
let ?e' = ?ee'
let ?xs' = xs''

from bisim'' call'' have pc'': pc'' < length (compE2 (blocks1 0 (Class D''#Ts'') body''))
by(rule bisim1-call-pcD)
hence pc'': pc'' < length (compE2 body'') by simp
with sees-method-compP[OF sees'', where f=λC M Ts T. compMb2]
sees-method-compP[OF sees, where f=λC M Ts T. compMb2] conf
obtain ST LT where Φ: compTP P C'' M'' ! pc'' = [(ST, LT)]
and conf': conf-f (compP (λC M Ts T. compMb2) P) h (ST, LT) (compE2 body'' @ [Return])
(stk'', loc'', C'', M'', pc'')
and ins: (compE2 body'' @ [Return]) ! pc'' = Invoke M (length Ts)

```

```

unfolding correct-state-def
by(fastforce simp add: compP2-def compMb2-def dest: sees-method-fun)
from bisim1-callD[OF bisim'' call'', of M length Ts] ins pc''
have [simp]:  $M''' = M$  by simp

have  $\text{True}, P, t \vdash 1 \langle (\text{Throw } a', \text{loc}) / (E'', xs'') \# EXS', h \rangle -\varepsilon \rightarrow \langle (\text{inline-call } (\text{Throw } a') E'', xs'') / EXS', h \rangle$ 
by(rule red1Return) simp
moreover have  $\tau \text{Move1 } P h \langle (\text{Throw } a', \text{loc}), (E'', xs'') \# EXS' \rangle$  by fastforce
ultimately have  $\tau \text{Red1 } P t h \langle (\text{Throw } a', \text{loc}), (E'', xs'') \# EXS' \rangle ((?e', ?xs''), EXS')$  by simp
moreover
have bisim1-list1 t h (?e', ?xs'') EXS' [a'] ((stk'', loc'', C'', M'', pc'') \# FRS')
proof
from conf' show  $\text{compTP } P \vdash t: ([a'], h, (stk'', loc'', C'', M'', pc'') \# FRS') \checkmark$  by simp
from sees'' show  $P \vdash C'' \text{ sees } M'': Ts'' \rightarrow T'' = [body''] \text{ in } D''$  .
from bisim1-inline-call-Throw[OF bisim'' call'', of length Ts a'] ins pc''
show  $P, \text{blocks1 } 0 \text{ (Class } D'' \# Ts'') \text{ body'', } h \vdash (\text{inline-call } (\text{Throw } a') E'', xs'') \leftrightarrow (stk'', loc'', pc'', [a'])$ 
by simp
from lenxs'' max-vars-inline-call[of Throw a' E'']
show  $\text{max-vars } (\text{inline-call } (\text{Throw } a') E'') \leq \text{length } xs''$  by simp
from bisims show list-all2 (bisim1-fr P h) EXS' FRS' by simp
qed
ultimately show ?thesis using Red
by(auto simp del: \tau Red1-conv intro: rtranclp.rtrancl-into-rtrancl rtranclp-into-tranclp1)
next
case [simp]: Nil
with bisims have [simp]:  $exs = []$  by simp
from hconf have bisim1-list1 t h (Throw a', loc) [] [a'] [] by(rule bl1-finalThrow)
thus ?thesis using Red
by(auto simp del: \tau Red1-conv intro: rtranclp.rtrancl-into-rtrancl rtranclp-into-tranclp1 simp add: sim21-size-def)
qed
next
case (Some pcd)
then obtain pch d where match: match-ex-table (compP2 P) (cname-of h a') pc (ex-table-of (compP2 P) C M) = [(pch, d)]
by(cases pcd) auto
with  $\tau \text{sees}' pc$  have  $\tau': \tau \text{move2 } (\text{compP2 } P) h \text{ stk } \text{body } pc [a']$  by(simp add: compP2-def compMb2-def \tau move2-iff)
from match execs have [simp]:  $h' = h \text{ xcp}' = \text{None}$ 
 $\text{frs}' = (\text{Addr } a' \# \text{drop } (\text{length } \text{stk} - d) \text{ stk}, \text{loc}, C, M, \text{pch}) \# \text{FRS}$  by simp-all
from bisim match sees'
have  $d \leq \text{length } \text{stk}$  by(auto intro: bisim1-match-Some-stk-length simp add: compP2-def compMb2-def)
with match sees'
have execm: exec-move-d P t (blocks1 0 (Class D#Ts) body) h (stk, loc, pc, [a']) ta h' (Addr a' \# drop (length stk - d) stk, loc, pch, None)
by(auto simp add: exec-move-def exec-meth-xcpt)
from conf obtain ST where  $\text{compP2 } P, h \vdash \text{stk} [:\leq] ST$  by(auto simp add: correct-state-def conf-f-def2)
hence  $ST: P, h \vdash \text{stk} [:\leq] ST$  by(rule List.list-all2-mono)(simp add: compP2-def)
from red1-simulates-exec-instr[OF wf hconf tconf bisim[unfolded \langle xcp = [a'] \rangle] execm - bsok ST]
 $\text{lenxs } ha' \text{ subclsD}' \tau'$ 
obtain  $e'' xs''$ 

```

where b' : $P, \text{blocks1 } 0 \text{ (Class } D \# Ts) \text{ body}, h \vdash (e'', xs'') \leftrightarrow (\text{Addr } a' \# \text{drop } (\text{length } stk - d) \text{ stk}, loc, pch, \text{None})$
and red : $(\text{if } pc < pch \text{ then } \tau red1r \text{ else } \tau red1t) P t h (e, xs) (e'', xs'')$ **and** $[simp]$: $h' = h$
by $(\text{auto split: if-split-asm intro: } \tau \text{move2xcp simp add: compP2-def simp del: blocks1.simps})$
have Red : $(\text{if } sim21\text{-size } (\text{compP2 } P) (xcp', frs') (xcp, frs) \text{ then } \tau Red1r \text{ else } \tau Red1t) P t h ((e, xs), exs) ((e'', xs''), exs)$
proof $(\text{cases } pc < pch)$
case $True$
from $bisim b'$ **have** $pc \leq Suc (\text{length } (\text{compE2 } body))$ $pch \leq Suc (\text{length } (\text{compE2 } body))$
by $(\text{auto dest: bisim1-pc-length-compE2})$
with $sees True$ **have** $sim21\text{-size } (\text{compP2 } P) (xcp', frs') (xcp, frs)$
by $(\text{auto simp add: sim21-size-def})(\text{auto simp add: compP2-def compMb2-def intro: sim21-size-aux.intros})$
with $red True$ **show** $?thesis$ **by** $\text{simp}(\text{rule } \tau red1r\text{-into-}\tau Red1r)$
next
case $False$
thus $?thesis$ **using** red **by** $(\text{auto intro: } \tau red1t\text{-into-}\tau Red1t \tau red1r\text{-into-}\tau Red1r)$
qed
moreover from red $lenxs$
have $max\text{-vars } e'' \leq \text{length } xs''$
apply $(\text{auto dest: } \tau red1r\text{-max-vars } \tau red1r\text{-preserves-len } \tau red1t\text{-max-vars } \tau red1t\text{-preserves-len split: if-split-asm})$
apply $(\text{frule } \tau red1r\text{-max-vars } \tau red1t\text{-max-vars, drule } \tau red1r\text{-preserves-len } \tau red1t\text{-preserves-len, simp})+$
done
with $conf' sees b'$
have $bisim1\text{-list1 } t h (e'', xs'') exs \text{None} ((\text{Addr } a' \# \text{drop } (\text{length } stk - d) \text{ stk}, loc, C, M, pch) \# FRS)$
using $bisims$ **unfolding** $\langle h' = h \rangle \langle xcp' = \text{None} \rangle$
 $\langle frs' = (\text{Addr } a' \# \text{drop } (\text{length } stk - d) \text{ stk}, loc, C, M, pch) \# FRS \rangle$
by $rule$
ultimately show $?thesis$ **by** $(\text{auto split del: if-split})$
qed
qed
qed $(\text{insert exec, auto simp add: exec-1-iff elim!: jvmd-NormalE})$

lemma $\tau Red1\text{-simulates-exec-1-not-}\tau$:

assumes wf : $wf\text{-J1-prog } P$
and $exec$: $exec\text{-1-d } (\text{compP2 } P) t (\text{Normal } (xcp, h, frs)) ta (\text{Normal } (xcp', h', frs'))$
and $bisim$: $bisim1\text{-list1 } t h (e, xs) exs xcp frs$
and τ : $\neg \tau \text{Move2 } (\text{compP2 } P) (xcp, h, frs)$
shows $\exists e' xs' exs' ta' e'' xs'' exs'' . \tau Red1r P t h ((e, xs), exs) ((e', xs'), exs') \wedge$
 $True, P, t \vdash 1 \langle (e', xs') / exs', h \rangle - ta' \rightarrow \langle (e'', xs'') / exs'', h \rangle \wedge$
 $\neg \tau \text{Move1 } P h ((e', xs'), exs') \wedge ta\text{-bisim } wbisim1 ta' ta \wedge$
 $bisim1\text{-list1 } t h' (e'', xs'') exs'' xcp' frs' \wedge$
 $(\text{call1 } e = \text{None} \vee$
 $(\text{case } frs \text{ of } Nil \Rightarrow False \mid (\text{stk}, loc, C, M, pc) \# FRS \Rightarrow \forall M' n.$
 $\text{instrs-of } (\text{compP2 } P) C M ! pc \neq \text{Invoke } M' n) \vee$
 $e' = e \wedge xs' = xs \wedge exs' = exs)$

using $bisim$

proof $cases$

case $(bl1\text{-Normal } stk \text{ loc } C M pc FRS Ts T \text{ body } D)$

hence $[simp]$: $frs = (\text{stk}, loc, C, M, pc) \# FRS$

and $conf$: $\text{compTP } P \vdash t: (xcp, h, (\text{stk}, loc, C, M, pc) \# FRS) \checkmark$

and sees: $P \vdash C \text{ sees } M: Ts \rightarrow T = [body] \text{ in } D$
and bisim: $P, blocks1\ 0 \text{ (Class } D \# Ts) \text{ body}, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp)$
and lenxs: $max\text{-vars } e \leq \text{length } xs$
and bisims: $list\text{-all2} \text{ (bisim1-fr } P \ h) \text{ exs FRS by auto}$

from sees-method-compP $[OF \text{ sees, where } f = \lambda C \ M \ Ts \ T. \text{ compMb2}]$
have sees': $compP2\ P \vdash C \text{ sees } M: Ts \rightarrow T = [(max\text{-stack } body, max\text{-vars } body, compE2 \text{ body } @$
 $[Return], compxE2 \text{ body } 0 \ 0)] \text{ in } D$
by $(simp \text{ add: } compP2\text{-def } compMb2\text{-def})$
from bisim have pc: $pc \leq \text{length} \text{ (compE2 } body) \text{ by (auto dest: bisim1-pc-length-compE2)}$
from conf have hconf: $hconf \ h \text{ preallocated } h \text{ and } tconf: P, h \vdash t \checkmark$
unfolding correct-state-def by $(simp\text{-all add: } compP2\text{-def } tconf\text{-def})$

from sees wf have bsok: $bsok \text{ (blocks1 } 0 \text{ (Class } D \# Ts) \text{ body) } 0$
by $(auto \text{ dest!: sees-wf-mdecl } simp \text{ add: bsok-def wf-mdecl-def WT1-expr-locks})$

from exec obtain check: $check \text{ (compP2 } P) \text{ (xcp, h, frs)}$
and exec: $compP2\ P, t \vdash (xcp, h, frs) \text{ --ta-jvm--} \rightarrow (xcp', h', frs')$
by $(rule \text{ jvmd-NormalE})(auto \text{ simp add: exec-1-iff})$

from wt-compTP-compP2 $[OF \text{ wf}] \text{ exec conf}$
have conf': $compTP\ P \vdash t: (xcp', h', frs') \checkmark \text{ by (auto intro: BV-correct-1)}$

show ?thesis
proof $(cases \ xcp)$
case $[simp]: None$
from exec have execi: $(ta, xcp', h', frs') \in \text{exec-instr (instrs-of (compP2 } P) \ C \ M \ ! \ pc) \text{ (compP2}$
 $P) \ t \ h \ stk \ loc \ C \ M \ pc \ FRS$
by $(simp \text{ add: exec-1-iff})$
show ?thesis
proof $(cases \ \text{length } frs' = Suc \ (\text{length } FRS))$
case True
with pc execi sees' have pc: $pc < \text{length} \text{ (compE2 } body)$
by $(auto \text{ split: if-split-asm } simp \text{ add: split-beta})$
with execi sees' have execi: $(ta, xcp', h', frs') \in \text{exec-instr (compE2 } body \ ! \ pc) \text{ (compP2 } P) \ t \ h$
 $stk \ loc \ C \ M \ pc \ FRS$
by $(simp)$
from τ sees' True pc have $\tau i:$ $\neg \tau \text{ move2 (compP2 } P) \ h \ stk \ body \ pc \ None \text{ by (simp add: } \tau \text{ move2-iff)}$
from execi True sees' pc have $\exists stk' \ loc' \ pc'. frs' = (stk', loc', C, M, pc') \# FRS$
by $(cases \ \text{compE2 } body \ @ \ [Return]) \ ! \ pc)(auto \text{ split: if-split-asm } sum.\text{split-asm } simp \text{ add:}$
 $\text{split-beta, auto split: extCallRet.splits})$
then obtain $stk' \ loc' \ pc'$ where $[simp]: frs' = (stk', loc', C, M, pc') \# FRS \text{ by blast}$
from conf obtain ST where $compP2\ P, h \vdash stk \ [:\leq] \ ST \text{ by (auto simp add: correct-state-def}$
 $conf\text{-f-def2})$
hence ST: $P, h \vdash stk \ [:\leq] \ ST \text{ by (rule List.list-all2-mono)(simp add: compP2-def)}$
from execi sees True check pc
have exec': $\text{exec-move-d } P \ t \text{ (blocks1 } 0 \text{ (Class } D \# Ts) \text{ body) } h \text{ (stk, loc, pc, xcp) } ta \ h' \text{ (stk', loc',}$
 $pc', xcp')$
apply $(auto \text{ simp add: compP2-def } compMb2\text{-def } exec\text{-move-def } check\text{-def } exec\text{-meth-instr } split:$
 $\text{if-split-asm } sum.\text{split-asm})$
apply $(cases \ \text{compE2 } body \ ! \ pc)$
apply $(auto \text{ simp add: neq-Nil-conv } split\text{-beta } split: \text{if-split-asm } sum.\text{split-asm})$
apply $(force \text{ split: extCallRet.split-asm})$
apply $(cases \ \text{compE2 } body \ ! \ pc, auto \text{ simp add: split-beta } neq\text{-Nil-conv } split: \text{if-split-asm})$

```

sum.split-asm)
  done
  from red1-simulates-exec-instr[OF wf hconf tconf bisim this - bsok ST] lenxs  $\tau i$  obtain  $e'' xs''$ 
  ta' e' xs'
    where bisim': P,blocks1 0 (Class D#Ts) body,h'  $\vdash (e'', xs'') \leftrightarrow (stk', loc', pc', xcp')$ 
    and red1:  $\tau red1r P t h (e, xs) (e', xs')$  and red2: True,P,t  $\vdash 1 \langle e', (h, xs') \rangle - ta' \rightarrow \langle e'', (h', xs'') \rangle$ 
    and  $\tau 1$ :  $\neg \tau move1 P h e'$  and tabisim: ta-bisim wabisim1 (extTA2J1 P ta') ta
    and call: call1 e = None  $\vee$  no-call2 (blocks1 0 (Class D # Ts) body) pc  $\vee e' = e \wedge xs' = xs$ 
    by(fastforce simp del: blocks1.simps)
  from red1 have Red1:  $\tau Red1r P t h ((e, xs), exs) ((e', xs'), exs)$ 
    by(rule  $\tau red1r$ -into- $\tau Red1r$ )
  moreover from red2 have True,P,t  $\vdash 1 \langle (e', xs')/exs, h \rangle - extTA2J1 P ta' \rightarrow \langle (e'', xs'')/exs, h' \rangle$ 
    by(rule red1Red)
  moreover from  $\tau 1$  red2 have  $\neg \tau Move1 P h ((e', xs'), exs)$  by auto
  moreover from  $\tau red1r$ -max-vars[OF red1] lenxs  $\tau red1r$ -preserves-len[OF red1]
  have max-vars  $e' \leq$  length  $xs'$  by simp
  with red1-preserves-len[OF red2] red1-max-vars[OF red2]
  have max-vars  $e'' \leq$  length  $xs''$  by simp
  with conf' sees bisim'
  have bisim1-list1 t h' ( $e'', xs''$ ) exs xcp' ( $(stk', loc', C, M, pc') \# FRS$ )
    unfolding  $\langle frs' = (stk', loc', C, M, pc') \# FRS \rangle$ 
  proof
    from red2 have hext h h' by(auto dest: red1-hext-incr)
    from bisims show list-all2 (bisim1-fr P h') exs FRS
      by(rule List.list-all2-mono)(erule bisim1-fr-hext-mono[OF -  $\langle hext h h' \rangle$ ])
  qed
  moreover from call sees'
  have call1 e = None  $\vee (\forall M' n. instrs-of (compP2 P) C M ! pc \neq Invoke M' n) \vee e' = e \wedge xs' = xs$ 
    = xs
    by(auto simp add: no-call2-def)
  ultimately show ?thesis using tabisim
    by(fastforce simp del: split-paired-Ex)
  next
  case False
  with execi sees pc compE2-not-Return[of body]
  have (pc = length (compE2 body)  $\vee (\exists M n. compE2 body ! pc = Invoke M n) \wedge xcp' = None$ )
    apply(cases compE2 body ! pc)
    apply(auto split: if-split-asm sum.split-asm simp add: split-beta compP2-def compMb2-def)
    apply(auto split: extCallRet.splits)
    apply(metis in-set-conv-nth)+
  done
  hence [simp]:  $xcp' = None$ 
    and pc = length (compE2 body)  $\vee (\exists M n. compE2 body ! pc = Invoke M n)$  by simp-all
  moreover
  { assume [simp]: pc = length (compE2 body)
    with sees-method-compP[OF sees, where  $f = \lambda C M Ts T. compMb2$ ]  $\tau$  have False by(auto
  simp add: compMb2-def compP2-def)
    hence ?thesis .. }
  moreover {
    assume  $\exists M n. compE2 body ! pc = Invoke M n$ 
    and pc  $\neq$  length (compE2 body)
    with pc obtain MM n where ins: compE2 body ! pc = Invoke MM n
    and pc: pc < length (compE2 body) by auto
    with bisim1-Invoke-stkD[OF bisim[unfolded None], of MM n] obtain vs' v' stk'

```

```

    where [simp]: stk = vs' @ v' # stk' n = length vs' by auto
    with False  $\tau$  sees' execi pc ins have False by auto (auto split: extCallRet.split-asm) }
    ultimately show ?thesis by blast
qed
next
case [simp]: (Some ad)
from bisim have pc: pc < length (compE2 body) by (auto dest: bisim1-xcp-pcD)
with  $\tau$  sees' have False by auto
thus ?thesis ..
qed
qed(insert exec, auto simp add: exec-1-iff elim!: jvmd-NormalE)

end

end

```

7.19 Correctness of Stage 2: The multithreaded setting

```

theory Correctness2
imports
  J1JVM
  JVMJ1
  ../BV/BVProgressThreaded
begin

declare Listn.lesub-list-impl-same-size[simp del]

context J1-JVM-heap-conf-base begin

lemma bisim1-list1-has-methodD: bisim1-list1 t h ex exs xcp ((stk, loc, C, M, pc) # frs)  $\implies$  P  $\vdash$  C
has M
by(fastforce elim!: bisim1-list1.cases intro: has-methodI)

end

declare compP-has-method [simp]

sublocale J1-JVM-heap-conf-base < Red1-exec:
  delay-bisimulation-base mred1 P t mexec (compP2 P) t wbisim1 t ta-bisim wbisim1  $\tau$ MOVE1 P
 $\tau$ MOVE2 (compP2 P)
  for t
.

sublocale J1-JVM-heap-conf-base < Red1-execd: delay-bisimulation-base
  mred1 P t
  mexecd (compP2 P) t
  wbisim1 t
  ta-bisim wbisim1
   $\tau$ MOVE1 P
   $\tau$ MOVE2 (compP2 P)
  for t
.

```

context *JVM-heap-base* **begin**

lemma $\tau exec\text{-}1\text{-}d\text{-}silent\text{-}move$:

$\tau exec\text{-}1\text{-}d P t (xcp, h, frs) (xcp', h', frs')$

$\implies \tau trsys.silent\text{-}move (mexecd P t) (\tau MOVE2 P) ((xcp, frs), h) ((xcp', frs'), h')$

apply(*rule* $\tau trsys.silent\text{-}move.intros$)

apply *auto*

apply(*rule* $exec\text{-}1\text{-}d\text{-}NormalI$)

apply(*auto simp add: exec\text{-}1\text{-}iff exec\text{-}d\text{-}def*)

done

lemma $silent\text{-}move\text{-}\tau exec\text{-}1\text{-}d$:

$\tau trsys.silent\text{-}move (mexecd P t) (\tau MOVE2 P) ((xcp, frs), h) ((xcp', frs'), h')$

$\implies \tau exec\text{-}1\text{-}d P t (xcp, h, frs) (xcp', h', frs')$

apply(*erule* $\tau trsys.silent\text{-}move.cases$)

apply *clarsimp*

apply(*erule* $jvmd\text{-}NormalE$)

apply(*auto simp add: exec\text{-}1\text{-}iff*)

done

lemma $\tau Exec\text{-}1\text{-}dr\text{-}rtranclpD$:

$\tau Exec\text{-}1\text{-}dr P t (xcp, h, frs) (xcp', h', frs')$

$\implies \tau trsys.silent\text{-}moves (mexecd P t) (\tau MOVE2 P) ((xcp, frs), h) ((xcp', frs'), h')$

by(*induct rule: rtranclp-induct3*)(*blast intro: rtranclp.rtrancl-into-rtrancl* $\tau exec\text{-}1\text{-}d\text{-}silent\text{-}move$)**+**

lemma $\tau Exec\text{-}1\text{-}dt\text{-}tranclpD$:

$\tau Exec\text{-}1\text{-}dt P t (xcp, h, frs) (xcp', h', frs')$

$\implies \tau trsys.silent\text{-}movet (mexecd P t) (\tau MOVE2 P) ((xcp, frs), h) ((xcp', frs'), h')$

by(*induct rule: tranclp-induct3*)(*blast intro: tranclp.trancl-into-trancl* $\tau exec\text{-}1\text{-}d\text{-}silent\text{-}move$)**+**

lemma $rtranclp\text{-}\tau Exec\text{-}1\text{-}dr$:

$\tau trsys.silent\text{-}moves (mexecd P t) (\tau MOVE2 P) ((xcp, frs), h) ((xcp', frs'), h')$

$\implies \tau Exec\text{-}1\text{-}dr P t (xcp, h, frs) (xcp', h', frs')$

by(*induct rule: rtranclp-induct[of - ((ax, ay), az) ((bx, by), bz), split-rule, consumes 1]*)(*blast intro: rtranclp.rtrancl-into-rtrancl silent-move- $\tau exec\text{-}1\text{-}d$*)**+**

lemma $tranclp\text{-}\tau Exec\text{-}1\text{-}dt$:

$\tau trsys.silent\text{-}movet (mexecd P t) (\tau MOVE2 P) ((xcp, frs), h) ((xcp', frs'), h')$

$\implies \tau Exec\text{-}1\text{-}dt P t (xcp, h, frs) (xcp', h', frs')$

by(*induct rule: tranclp-induct[of - ((ax, ay), az) ((bx, by), bz), split-rule, consumes 1]*)(*blast intro: tranclp.trancl-into-trancl silent-move- $\tau exec\text{-}1\text{-}d$*)**+**

lemma $\tau Exec\text{-}1\text{-}dr\text{-}conv\text{-}rtranclp$:

$\tau Exec\text{-}1\text{-}dr P t (xcp, h, frs) (xcp', h', frs') =$

$\tau trsys.silent\text{-}moves (mexecd P t) (\tau MOVE2 P) ((xcp, frs), h) ((xcp', frs'), h')$

by(*blast intro: $\tau Exec\text{-}1\text{-}dr\text{-}rtranclpD rtranclp\text{-}\tau Exec\text{-}1\text{-}dr$*)

lemma $\tau Exec\text{-}1\text{-}dt\text{-}conv\text{-}tranclp$:

$\tau Exec\text{-}1\text{-}dt P t (xcp, h, frs) (xcp', h', frs') =$

$\tau trsys.silent\text{-}movet (mexecd P t) (\tau MOVE2 P) ((xcp, frs), h) ((xcp', frs'), h')$

by(*blast intro: $\tau Exec\text{-}1\text{-}dt\text{-}tranclpD tranclp\text{-}\tau Exec\text{-}1\text{-}dt$*)

end

context *J1-JVM-conf-read* begin

lemma *Red1-execd-weak-bisim*:

assumes *wf*: *wf-J1-prog P*

shows *delay-bisimulation-measure* (*mred1 P t*) (*mexecd (compP2 P) t*) (*wbisim1 t*) (*ta-bisim wbisim1*) ($\tau\text{MOVE1 } P$) ($\tau\text{MOVE2 (compP2 P)}$) ($\lambda(((e, xs), eks), h) (((e', xs'), eks'), h')$). *sim12-size e* < *sim12-size e'* ($\lambda(xcpfrs, h) (xcpfrs', h)$). *sim21-size (compP2 P) xcpfrs xcpfrs'*)

proof

fix *s1 s2 s1'*

assume *wbisim1 t s1 s2* and $\tau\text{trsys.silent-move (mred1 P t) (\tau\text{MOVE1 P}) s1 s1'}$

moreover obtain *e xs eks h* where *s1*: *s1* = $((e, xs), eks), h$ by(*cases s1*) *auto*

moreover obtain *e' xs' eks' h1'* where *s1'*: *s1'* = $((e', xs'), eks'), h1'$ by(*cases s1'*) *auto*

moreover obtain *xcp frs h2* where *s2*: *s2* = $((xcp, frs), h2)$ by(*cases s2*) *auto*

ultimately have [*simp*]: *h2* = *h* and *red*: *True*, $P, t \vdash 1 \langle (e, xs)/eks, h \rangle -\varepsilon \rightarrow \langle (e', xs')/eks', h1' \rangle$

and τ : $\tau\text{Move1 } P h ((e, xs), eks)$ and *bisim*: *bisim1-list1 t h (e, xs) eks xcp frs* by(*auto*)

from *red* τ *bisim* have *h1'* [*simp*]: *h1'* = *h* by(*auto dest: \tau move1-heap-unchanged elim!: Red1.cases bisim1-list1.cases*)

from *exec-1-simulates-Red1-\tau[OF wf red[unfolded h1'] bisim \tau]* obtain *xcp' frs'*

where *exec*: (*if sim12-size e' < sim12-size e then \tauExec-1-dr else \tauExec-1-dt*) (*compP2 P*) *t (xcp, h, frs) (xcp', h, frs')*

and *bisim'*: *bisim1-list1 t h (e', xs') eks' xcp' frs'* by *blast*

from *exec* have (*if* ($\lambda(((e, xs), eks), h) (((e', xs'), eks'), h')$). *sim12-size e* < *sim12-size e'*) ($((e', xs'), eks'), h$) ($((e, xs), eks), h$) then $\tau\text{trsys.silent-moves (mexecd (compP2 P) t) (\tau\text{MOVE2 (compP2 P)})$ else $\tau\text{trsys.silent-movet (mexecd (compP2 P) t) (\tau\text{MOVE2 (compP2 P)})$) ($((xcp, frs), h) ((xcp', frs'), h)$)

by(*auto simp add: \tauExec-1-dr-conv-rtranclp \tauExec-1-dt-conv-tranclp*)

thus *wbisim1 t s1' s2* \wedge ($\lambda(((e, xs), eks), h) (((e', xs'), eks'), h')$. *sim12-size e* < *sim12-size e'*)⁺⁺ *s1' s1* \vee

($\exists s2'$. ($\tau\text{trsys.silent-movet (mexecd (compP2 P) t) (\tau\text{MOVE2 (compP2 P)})$) *s2 s2'* \wedge *wbisim1 t s1' s2'*)

using *bisim' s1 s1' s2*

by (*rule delay-bisimulation-base.simulation-silentII'*, *auto split del: if-split*)

next

fix *s1 s2 s2'*

assume *wbisim1 t s1 s2* and $\tau\text{trsys.silent-move (mexecd (compP2 P) t) (\tau\text{MOVE2 (compP2 P)}) s2 s2'$

moreover obtain *e xs eks h1* where *s1*: *s1* = $((e, xs), eks), h1$ by(*cases s1*) *auto*

moreover obtain *xcp frs h* where *s2*: *s2* = $((xcp, frs), h)$ by(*cases s2*) *auto*

moreover obtain *xcp' frs' h2'* where *s2'*: *s2'* = $((xcp', frs'), h2')$ by(*cases s2'*) *auto*

ultimately have [*simp*]: *h1* = *h* and *exec*: *exec-1-d (compP2 P) t (Normal (xcp, h, frs)) \varepsilon (Normal (xcp', h2', frs'))*

and τ : $\tau\text{Move2 (compP2 P) (xcp, h, frs)}$ and *bisim*: *bisim1-list1 t h (e, xs) eks xcp frs* by(*auto*)

from $\tau\text{Red1-simulates-exec-1-\tau[OF wf exec bisim \tau]}$

obtain *e' xs' eks' h2'* where [*simp*]: *h2'* = *h*

and *red*: (*if sim21-size (compP2 P) (xcp', frs') (xcp, frs) then \tauRed1r else \tauRed1t*) *P t h ((e, xs), eks) ((e', xs'), eks')*

and *bisim'*: *bisim1-list1 t h (e', xs') eks' xcp' frs'* by *blast*

from *red* have (*if* ($(\lambda(xcpfrs, h) (xcpfrs', h)$. *sim21-size (compP2 P) xcpfrs xcpfrs')*) ($((xcp', frs'), h2')$) ($((xcp, frs), h)$) then $\tau\text{trsys.silent-moves (mred1 P t) (\tau\text{MOVE1 P})$ else $\tau\text{trsys.silent-movet (mred1 P t) (\tau\text{MOVE1 P})$) ($((e, xs), eks), h$) ($((e', xs'), eks'), h$)

by(*auto dest: \tauRed1r-rtranclpD \tauRed1t-tranclpD*)

thus *wbisim1 t s1 s2'* \wedge ($\lambda(xcpfrs, h) (xcpfrs', h)$. *sim21-size (compP2 P) xcpfrs xcpfrs')*⁺⁺ *s2' s2* \vee

($\exists s1'$. $\tau\text{trsys.silent-movet (mred1 P t) (\tau\text{MOVE1 P}) s1 s1' \wedge wbisim1 t s1' s2'$)

```

    using bisim' s1 s2 s2'
    by -(rule delay-bisimulation-base.simulation-silent2I', auto split del: if-split)
next
fix s1 s2 tl1 s1'
assume wbisim1 t s1 s2 and mred1 P t s1 tl1 s1' and  $\neg \tau$ MOVE1 P s1 tl1 s1'
moreover obtain e xs exs h where s1: s1 = (((e, xs), exs), h) by(cases s1) auto
moreover obtain e' xs' exs' h1' where s1': s1' = (((e', xs'), exs'), h1') by(cases s1') auto
moreover obtain xcp frs h2 where s2: s2 = ((xcp, frs), h2) by(cases s2) auto
ultimately have [simp]: h2 = h and red: True,P,t  $\vdash$ 1  $\langle\langle e, xs \rangle / exs, h \rangle - tl1 \rightarrow \langle\langle e', xs' \rangle / exs', h1' \rangle$ 
and  $\tau$ :  $\neg \tau$ Move1 P h ((e, xs), exs) and bisim: bisim1-list1 t h (e, xs) exs xcp frs
by(fastforce elim!: Red1.cases dest: red1- $\tau$ -taD)+
from exec-1-simulates-Red1-not- $\tau$ [OF wf red bisim  $\tau$ ] obtain ta' xcp' frs' xcp'' frs''
where exec1:  $\tau$ Exec-1-dr (compP2 P) t (xcp, h, frs) (xcp', h, frs')
and exec2: exec-1-d (compP2 P) t (Normal (xcp', h, frs')) ta' (Normal (xcp'', h1', frs''))
and  $\tau'$ :  $\neg \tau$ Move2 (compP2 P) (xcp', h, frs')
and bisim': bisim1-list1 t h1' (e', xs') exs' xcp'' frs''
and ta': ta-bisim wbisim1 tl1 ta' by blast
from exec1 have  $\tau$ trsys.silent-moves (mexecd (compP2 P) t) ( $\tau$ MOVE2 (compP2 P)) ((xcp, frs),
h) ((xcp', frs'), h)
by(rule  $\tau$ Exec-1-dr-rtranclpD)
thus  $\exists s2' s2'' tl2$ .  $\tau$ trsys.silent-moves (mexecd (compP2 P) t) ( $\tau$ MOVE2 (compP2 P)) s2 s2'  $\wedge$ 
mexecd (compP2 P) t s2' tl2 s2''  $\wedge$   $\neg \tau$ MOVE2 (compP2 P) s2' tl2 s2''  $\wedge$ 
wbisim1 t s1' s2''  $\wedge$  ta-bisim wbisim1 tl1 tl2
using bisim' exec2  $\tau'$  s1 s1' s2 ta' unfolding  $\langle h2 = h \rangle$ 
apply(subst (1 2) split-paired-Ex)
apply(subst (1 2) split-paired-Ex)
by clarify ((rule exI conjI|assumption)+, auto)
next
fix s1 s2 tl2 s2'
assume wbisim1 t s1 s2 and mexecd (compP2 P) t s2 tl2 s2' and  $\neg \tau$ MOVE2 (compP2 P) s2 tl2
s2'
moreover obtain e xs exs h1 where s1: s1 = (((e, xs), exs), h1) by(cases s1) auto
moreover obtain xcp frs h where s2: s2 = ((xcp, frs), h) by(cases s2) auto
moreover obtain xcp' frs' h2' where s2': s2' = ((xcp', frs'), h2') by(cases s2') auto
ultimately have [simp]: h1 = h and exec: exec-1-d (compP2 P) t (Normal (xcp, h, frs)) tl2
(Normal (xcp', h2', frs'))
and  $\tau$ :  $\neg \tau$ Move2 (compP2 P) (xcp, h, frs) and bisim: bisim1-list1 t h (e, xs) exs xcp frs
apply auto
apply(erule jvmd-NormalE)
apply(cases xcp)
apply auto
apply(rename-tac stk loc C M pc frs)
apply(case-tac instrs-of (compP2 P) C M ! pc)
apply(simp-all split: if-split-asm)
apply(auto dest!:  $\tau$ external-red-external-aggr-TA-empty simp add: check-def has-method-def  $\tau$ external-def
 $\tau$ external'-def)
done
from  $\tau$ Red1-simulates-exec-1-not- $\tau$ [OF wf exec bisim  $\tau$ ] obtain e' xs' exs' ta' e'' xs'' exs''
where red1:  $\tau$ Red1r P t h ((e, xs), exs) ((e', xs'), exs')
and red2: True,P,t  $\vdash$ 1  $\langle\langle e', xs' \rangle / exs', h \rangle - ta' \rightarrow \langle\langle e'', xs'' \rangle / exs'', h2' \rangle$ 
and  $\tau'$ :  $\neg \tau$ Move1 P h ((e', xs'), exs') and ta': ta-bisim wbisim1 ta' tl2
and bisim': bisim1-list1 t h2' (e'', xs'') exs'' xcp' frs' by blast
from red1 have  $\tau$ trsys.silent-moves (mred1 P t) ( $\tau$ MOVE1 P) (((e, xs), exs), h) (((e', xs'), exs'),
h)

```

by(rule τ Red1r-rtrancpD)
thus $\exists s1' s1'' tl1. \tau$ trsys.silent-moves (mred1 P t) (τ MOVE1 P) s1 s1' \wedge mred1 P t s1' tl1 s1'' \wedge
 $\neg \tau$ MOVE1 P s1' tl1 s1'' \wedge wbisim1 t s1'' s2' \wedge ta-bisim wbisim1 tl1 tl2
using bisim' red2 τ' s1 s2 s2' $\langle h1 = h \rangle$ ta'
apply –
apply(rule exI[**where** $x = ((e', xs'), exs')$, h])
apply(rule exI[**where** $x = ((e'', xs''), exs'')$, h2])
apply(rule exI[**where** $x = ta'$])
apply auto
done
next
have wf (inv-image $\{(x, y). x < y\}$ ($\lambda(((e, xs), exs), h). \text{sim12-size } e$))
by(rule wf-inv-image)(rule wf-less)
also have inv-image $\{(x, y). x < y\}$ ($\lambda(((e, xs), exs), h). \text{sim12-size } e$) =
 $\{(x, y). (\lambda(((e, xs), exs), h) (((e', xs'), exs'), h')). \text{sim12-size } e < \text{sim12-size } e') x y\}$ **by** auto
finally show wfP ($\lambda(((e, xs), exs), h) (((e', xs'), exs'), h')). \text{sim12-size } e < \text{sim12-size } e'$
unfolding wfp-def .
next
from wfp-sim21-size
have wf $\{(xcpfrs, xcpfrs'). \text{sim21-size } (\text{compP2 } P) xcpfrs xcpfrs'\}$ **by**(unfold wfp-def)
hence wf (inv-image $\{(xcpfrs, xcpfrs'). \text{sim21-size } (\text{compP2 } P) xcpfrs xcpfrs'\}$ fst) **by**(rule wf-inv-image)
also have inv-image $\{(xcpfrs, xcpfrs'). \text{sim21-size } (\text{compP2 } P) xcpfrs xcpfrs'\}$ fst =
 $\{((xcpfrs, h), (xcpfrs', h)). \text{sim21-size } (\text{compP2 } P) xcpfrs xcpfrs'\}$ **by** auto
also have ... = $\{(x, y). (\lambda(xcpfrs, h) (xcpfrs', h)). \text{sim21-size } (\text{compP2 } P) xcpfrs xcpfrs'\} x y$
by(auto)
finally show wfP ($\lambda(xcpfrs, h) (xcpfrs', h)). \text{sim21-size } (\text{compP2 } P) xcpfrs xcpfrs'$
unfolding wfp-def .
qed

lemma Red1-execd-delay-bisim:

assumes wf: wf-J1-prog P
shows delay-bisimulation-diverge (mred1 P t) (mexecd (compP2 P) t) (wbisim1 t) (ta-bisim wbisim1)
(τ MOVE1 P) (τ MOVE2 (compP2 P))

proof –

interpret delay-bisimulation-measure

mred1 P t mexecd (compP2 P) t wbisim1 t ta-bisim wbisim1 τ MOVE1 P τ MOVE2 (compP2 P)

$\lambda(((e, xs), exs), h) (((e', xs'), exs'), h')). \text{sim12-size } e < \text{sim12-size } e'$

$\lambda(xcpfrs, h) (xcpfrs', h). \text{sim21-size } (\text{compP2 } P) xcpfrs xcpfrs'$

using wf **by**(rule Red1-execd-weak-bisim)

show ?thesis **by**(unfold-locales)

qed

end

definition bisim-wait1JVM ::

'addr jvm-prog \Rightarrow ('addr expr1 \times 'addr locals1) \times ('addr expr1 \times 'addr locals1) list \Rightarrow 'addr
jvm-thread-state \Rightarrow bool

where

bisim-wait1JVM P \equiv

$\lambda((e1, xs1), exs1) (xcp, frs). \text{call1 } e1 \neq \text{None} \wedge$

(case frs of Nil \Rightarrow False | (stk, loc, C, M, pc) # frs' $\Rightarrow \exists M' n. \text{instrs-of } P C M ! pc = \text{Invoke } M' n$)

sublocale J1-JVM-heap-conf-base < Red1-execd:

```

FWbisimulation-base
  final-expr1
  mred1 P
  JVM-final
  mexecd (compP2 P)
  convert-RA
  wbisim1
  bisim-wait1JVM (compP2 P)
.

```

sublocale *JVM-heap-base* < *execd-mthr*:

```

 $\tau$ multithreaded
  JVM-final
  mexecd P
  convert-RA
   $\tau$ MOVE2 P
for P
by(unfold-locales)

```

sublocale *J1-JVM-heap-conf-base* < *Red1-execd*:

```

FWdelay-bisimulation-base
  final-expr1
  mred1 P
  JVM-final
  mexecd (compP2 P)
  convert-RA
  wbisim1
  bisim-wait1JVM (compP2 P)
   $\tau$ MOVE1 P
   $\tau$ MOVE2 (compP2 P)
by(unfold-locales)

```

context *J1-JVM-conf-read* **begin**

theorem *Red1-exec1-FWwbisim*:

assumes *wf*: *wf-J1-prog* P

shows *FWdelay-bisimulation-diverge* *final-expr1* (*mred1* P) *JVM-final* (*mexecd* (compP2 P)) *wbisim1* (*bisim-wait1JVM* (compP2 P)) (τ MOVE1 P) (τ MOVE2 (compP2 P))

proof –

let *?exec* = *mexecd* (compP2 P)

let *? τ exec* = $\lambda t.$ *τ trsys.silent-moves* (*mexecd* (compP2 P) t) (τ MOVE2 (compP2 P))

let *? τ red* = $\lambda t.$ *τ trsys.silent-moves* (*mred1* P t) (τ MOVE1 P)

interpret *delay-bisimulation-diverge*

mred1 P t *?exec* t *wbisim1* t *ta-bisim* *wbisim1* τ MOVE1 P τ MOVE2 (compP2 P)

for t

using *wf* **by**(*rule Red1-execd-delay-bisim*)

show *?thesis*

proof

fix t s1 s2

assume *wbisim1* t s1 s2 ($\lambda(x1, m).$ *final-expr1* x1) s1

moreover obtain e xs *exs* m1 **where** [*simp*]: s1 = ((e, xs), *exs*), m1) **by**(*cases* s1) *auto*

moreover obtain xcp frs m2 **where** [*simp*]: s2 = ((xcp, frs), m2) **by**(*cases* s2) *auto*

ultimately have [*simp*]: m2 = m1 *exs* = \square

and *bisim1-list1* t m1 (e, xs) \square xcp frs

and final e by auto
from $\langle \text{bisim1-list1 } t \ m1 \ (e, \ xs) \ [] \ xcp \ \text{frs} \rangle \langle \text{final } e \rangle$
show $\exists s2'. \ ?\tau \text{exec } t \ s2 \ s2' \wedge \text{wbisim1 } t \ s1 \ s2' \wedge (\lambda(x2, m). \text{JVM-final } x2) \ s2'$
proof cases
case $(\text{bl1-Normal } \text{stk} \ \text{loc} \ C \ M \ \text{pc} \ \text{frs}' \ Ts \ T \ \text{body} \ D)$
hence $[\text{simp}]: \text{frs} = [(\text{stk}, \ \text{loc}, \ C, \ M, \ \text{pc})]$
and conf: $\text{compTP } P \vdash t:(xcp, \ m1, \ \text{frs}) \ \checkmark$
and sees: $P \vdash C \ \text{sees } M: Ts \rightarrow T = [\text{body}] \ \text{in } D$
and bisim: $P, \text{blocks1 } 0 \ (\text{Class } D \ \# \ Ts) \ \text{body}, m1 \vdash (e, \ xs) \leftrightarrow (\text{stk}, \ \text{loc}, \ \text{pc}, \ xcp)$
and var: $\text{max-vars } e \leq \text{length } xs \ \text{by auto}$
from $\langle \text{final } e \rangle$ **show** $?\text{thesis}$
proof cases
fix v
assume $[\text{simp}]: e = \text{Val } v$
with bisim have $[\text{simp}]: xcp = \text{None } xs = \text{loc}$
and exec: $\tau \text{Exec-mover-a } P \ t \ (\text{blocks1 } 0 \ (\text{Class } D \ \# \ Ts) \ \text{body}) \ m1 \ (\text{stk}, \ \text{loc}, \ \text{pc}, \ xcp) \ ([v], \ \text{loc},$
 $\text{length } (\text{compE2 } \text{body}), \ \text{None})$
by $(\text{auto } \text{dest}!: \text{bisim1Val2D1})$
from exec have $\tau \text{Exec-mover-a } P \ t \ \text{body} \ m1 \ (\text{stk}, \ \text{loc}, \ \text{pc}, \ xcp) \ ([v], \ \text{loc}, \ \text{length } (\text{compE2 } \text{body}),$
 $\text{None})$
unfolding $\tau \text{Exec-mover-blocks1} \ .$
with sees have $\tau \text{Exec-1r } (\text{compP2 } P) \ t \ (xcp, \ m1, \ \text{frs}) \ (\text{None}, \ m1, \ [[v], \ \text{loc}, \ C, \ M, \ \text{length}$
 $(\text{compE2 } \text{body})])$
by $(\text{auto } \text{intro}: \tau \text{Exec-mover-}\tau \text{Exec-1r})$
with wt-compTP-compP2[OF wf]
have execd: $\tau \text{Exec-1-dr } (\text{compP2 } P) \ t \ (xcp, \ m1, \ \text{frs}) \ (\text{None}, \ m1, \ [[v], \ \text{loc}, \ C, \ M, \ \text{length } (\text{compE2}$
 $\text{body})])$
using conf by $(\text{rule } \tau \text{Exec-1r-}\tau \text{Exec-1-dr})$
also from sees-method-compP[OF sees, of $\lambda C \ M \ Ts \ T. \ \text{compMb2}$] **sees** $\text{max-stack1}[\text{of } \text{body}]$
have $\tau \text{exec-1-d } (\text{compP2 } P) \ t \ (\text{None}, \ m1, \ [[v], \ \text{loc}, \ C, \ M, \ \text{length } (\text{compE2 } \text{body})]) \ (\text{None}, \ m1,$
 $[])$
by $(\text{auto } \text{simp } \text{add}: \tau \text{exec-1-d-def } \text{compP2-def } \text{compMb2-def } \text{check-def } \text{has-methodI } \text{intro}: \text{exec-1I})$
finally have $?\tau \text{exec } t \ s2 \ ((\text{None}, \ []), \ m1)$
unfolding $\tau \text{Exec-1-dr-conv-rtranclp}$ **by** simp
moreover have $\text{JVM-final } (\text{None}, \ [])$ **by** simp
moreover from conf have $\text{hconf } m1 \ \text{preallocated } m1$ **unfolding** correct-state-def **by** (simp-all)
hence $\text{wbisim1 } t \ s1 \ ((\text{None}, \ []), \ m1)$ **by** $(\text{auto } \text{intro}: \text{bisim1-list1.intros})$
ultimately show $?\text{thesis}$ **by** blast
next
fix a
assume $[\text{simp}]: e = \text{throw } (\text{addr } a)$
hence $\exists \text{stk}' \ \text{loc}' \ \text{pc}'. \ \tau \text{Exec-mover-a } P \ t \ \text{body} \ m1 \ (\text{stk}, \ \text{loc}, \ \text{pc}, \ xcp) \ (\text{stk}', \ \text{loc}', \ \text{pc}', \ [a]) \wedge$
 $P, \text{blocks1 } 0 \ (\text{Class } D \ \# \ Ts) \ \text{body}, m1 \vdash (\text{Throw } a, \ xs) \leftrightarrow (\text{stk}', \ \text{loc}', \ \text{pc}', \ [a])$
proof $(\text{cases } xcp)$
case None
with bisim show $?\text{thesis}$
by $(\text{fastforce } \text{dest}!: \text{bisim1-Throw-}\tau \text{Exec-movet } \text{simp } \text{del}: \text{blocks1.simps } \text{intro}: \text{tranclp-into-rtranclp})$
next
case $(\text{Some } a')$
with bisim have $a = a'$ **by** $(\text{auto } \text{dest}: \text{bisim1-ThrowD})$
with Some bisim show $?\text{thesis}$ **by** (auto)
qed
then obtain $\text{stk}' \ \text{loc}' \ \text{pc}'$
where exec: $\tau \text{Exec-mover-a } P \ t \ \text{body} \ m1 \ (\text{stk}, \ \text{loc}, \ \text{pc}, \ xcp) \ (\text{stk}', \ \text{loc}', \ \text{pc}', \ [a])$

and $\text{bisim}' : P, \text{blocks1 } 0 \text{ (Class } D \# Ts) \text{ body}, m1 \vdash (\text{throw } (\text{addr } a), xs) \leftrightarrow (\text{stk}', \text{loc}', \text{pc}', [a])$ **by** *blast*
with sees **have** $\tau \text{Exec-1r } (\text{compP2 } P) t (xcp, m1, \text{frs}) ([a], m1, [(stk', \text{loc}', C, M, \text{pc}')])$
by (*auto intro: $\tau \text{Exec-mover-}\tau \text{Exec-1r}$*)
with *wt-compTP-compP2[OF wf]*
have $\text{execd} : \tau \text{Exec-1-dr } (\text{compP2 } P) t (xcp, m1, \text{frs}) ([a], m1, [(stk', \text{loc}', C, M, \text{pc}')])$
using *conf* **by** (*rule $\tau \text{Exec-1r-}\tau \text{Exec-1-dr}$*)
also {
from *bisim1-xcp-Some-not-caught[OF bisim', of $\lambda C M Ts T. \text{compMb2 } 0 0$]*
have $\text{match-ex-table } (\text{compP2 } P) (\text{cname-of } m1 a) \text{pc}' (\text{compxE2 body } 0 0) = \text{None}$ **by** (*simp add: compP2-def*)
moreover from *bisim'* **have** $\text{pc}' < \text{length } (\text{compE2 body})$ **by** (*auto dest: bisim1-ThrowD*)
ultimately have $\tau \text{exec-1 } (\text{compP2 } P) t ([a], m1, [(stk', \text{loc}', C, M, \text{pc}')]) ([a], m1, [])$
using *sees-method-compP[OF sees, of $\lambda C M Ts T. \text{compMb2}$]* *sees*
by (*auto simp add: $\tau \text{exec-1-def compP2-def compMb2-def has-methodI intro: exec-1I}$*)
moreover from *wt-compTP-compP2[OF wf]* *execd conf*
have $\text{compTP } P \vdash t : ([a], m1, [(stk', \text{loc}', C, M, \text{pc}')]) \surd$ **by** (*rule $\tau \text{Exec-1-dr-preserves-correct-state}$*)
ultimately have $\tau \text{exec-1-d } (\text{compP2 } P) t ([a], m1, [(stk', \text{loc}', C, M, \text{pc}')]) ([a], m1, [])$
using *wt-compTP-compP2[OF wf]*
by (*auto simp add: $\tau \text{exec-1-def } \tau \text{exec-1-d-def welltyped-commute[symmetric] elim: jvmd-NormalE}$*)
}
finally have $? \tau \text{exec } t s2 (([a], []), m1)$
unfolding *$\tau \text{Exec-1-dr-conv-rtranclp}$* **by** *simp*
moreover have *JVM-final* $([a], [])$ **by** *simp*
moreover from *conf* **have** *hconf m1 preallocated m1* **by** (*simp-all add: correct-state-def*)
hence *wbisim1 t s1* $(([a], []), m1)$ **by** (*auto intro: bisim1-list1.intros*)
ultimately show *?thesis* **by** *blast*
qed
qed(*auto intro!: exI bisim1-list1.intros*)
next
fix *t s1 s2*
assume *wbisim1 t s1 s2* $(\lambda(x2, m). \text{JVM-final } x2) s2$
moreover obtain *e xs exs m1* **where** [*simp*]: $s1 = ((e, xs), exs), m1$ **by** (*cases s1*) *auto*
moreover obtain *xcp frs m2* **where** [*simp*]: $s2 = ((xcp, frs), m2)$ **by** (*cases s2*) *auto*
ultimately have [*simp*]: $m2 = m1 \text{ exs} = [] \text{ frs} = []$
and *bisim: bisim1-list1 t m1* $(e, xs) [] xcp []$ **by** (*auto elim: bisim1-list1.cases*)
hence *final e* **by** (*auto elim: bisim1-list1.cases*)
thus $\exists s1'. ? \tau \text{red } t s1 s1' \wedge \text{wbisim1 } t s1' s2 \wedge (\lambda(x1, m). \text{final-expr1 } x1) s1'$ **using** *bisim* **by** *auto*
next
fix $t' x m1 xx m2 t x1 x2 x1' ta1 x1'' m1' x2' ta2 x2'' m2'$
assume *b: wbisim1 t'* $(x, m1) (xx, m2)$ **and** *b': wbisim1 t* $(x1, m1) (x2, m2)$
and $\tau \text{red} : ? \tau \text{red } t (x1, m1) (x1', m1)$
and $\text{mred1 } P t (x1', m1) ta1 (x1'', m1')$
and $\neg \tau \text{MOVE1 } P (x1', m1) ta1 (x1'', m1')$
and $\tau \text{exec} : ? \tau \text{exec } t (x2, m2) (x2', m2)$
and $\text{exec} : ? \text{exec } t (x2', m2) ta2 (x2'', m2')$
and $\neg \tau \text{MOVE2 } (\text{compP2 } P) (x2', m2) ta2 (x2'', m2')$
and *b2: wbisim1 t* $(x1'', m1') (x2'', m2')$
from *red* **have** *hext m1 m1'* **by** (*auto simp add: split-beta intro: Red1-hext-incr*)
moreover from *b2* **have** $m1' = m2'$ **by** (*cases x1'', cases x2''*) *simp*
moreover from *b2* **have** *hconf m2'*
by (*cases x1'', cases x2''*)(*auto elim!: bisim1-list1.cases simp add: correct-state-def*)
moreover from *b' exec* **have** *preallocated m2*
by (*cases x1, cases x2*)(*auto elim!: bisim1-list1.cases simp add: correct-state-def*)

moreover from $b' \tau red$ **red have** $tconf: compP2 P, m2 \vdash t \checkmark$
by(cases $x1$, cases $x2$)(auto elim!: bisim1-list1.cases Red1.cases simp add: correct-state-def
 $\tau mreds1-Val-Nil \tau mreds1-Throw-Nil$)
from $\tau exec$ **have** $\tau exec': \tau Exec-1-dr (compP2 P) t (fst x2, m2, snd x2) (fst x2', m2, snd x2')$
unfolding $\tau Exec-1-dr-conv-rtranclp$ **by** simp
with $b' tconf$ **have** $compTP P \vdash t: (fst x2', m2, snd x2') \checkmark$
using $\langle preallocated m2 \rangle$
apply(cases $x1$, cases $x2$)
apply(erule $\tau Exec-1-dr-preserves-correct-state[OF wt-compTP-compP2[OF wf]]$)
apply(auto elim!: bisim1-list1.cases simp add: correct-state-def)
done
ultimately show $wbisim1 t' (x, m1') (xx, m2')$ **using** $b exec$
apply(cases x , cases xx)
apply(auto elim!: bisim1-list1.cases intro!: bisim1-list1.intros simp add: split-beta intro: preallo-
cated-hext)
apply(erule (2) correct-state-heap-change[OF wt-compTP-compP2[OF wf]])
apply(erule (1) bisim1-hext-mono)
apply(erule List.list-all2-mono)
apply(erule (1) bisim1-fr-hext-mono)
done
next
fix $t x1 m1 x2 m2 x1' ta1 x1'' m1' x2' ta2 x2'' m2' w$
assume $b: wbisim1 t (x1, m1) (x2, m2)$
and $\tau red: ?\tau red t (x1, m1) (x1', m1)$
and $red: mred1 P t (x1', m1) ta1 (x1'', m1')$
and $\neg \tau MOVE1 P (x1', m1) ta1 (x1'', m1')$
and $\tau exec: ?\tau exec t (x2, m2) (x2', m2)$
and $exec: ?exec t (x2', m2) ta2 (x2'', m2')$
and $\neg \tau MOVE2 (compP2 P) (x2', m2) ta2 (x2'', m2')$
and $b': wbisim1 t (x1'', m1') (x2'', m2')$
and $ta-bisim wbisim1 ta1 ta2$
and $Suspend: Suspend w \in set \{ta1\}_w \text{ } Suspend w \in set \{ta2\}_w$
from $red Suspend$
have $call1 (fst (fst x1'')) \neq None$
by(cases $x1'$)(cases $x1''$, auto dest: Red1-Suspend-is-call)
moreover from $mexecd-Suspend-Invoke[OF exec Suspend(2)]$
obtain $xcp stk loc C M pc frs' M' n$ **where** $x2'' = (xcp, (stk, loc, C, M, pc) \# frs')$
 $instrs-of (compP2 P) C M ! pc = Invoke M' n$ **by** blast
ultimately show $bisim-wait1JVM (compP2 P) x1'' x2''$
by(simp add: bisim-wait1JVM-def split-beta)
next
fix $t x1 m1 x2 m2 ta1 x1' m1'$
assume $wbisim1 t (x1, m1) (x2, m2)$
and $bisim-wait1JVM (compP2 P) x1 x2$
and $mred1 P t (x1, m1) ta1 (x1', m1')$
and $wakeup: Notified \in set \{ta1\}_w \vee WokenUp \in set \{ta1\}_w$
moreover obtain $e1 xs1 exs1$ **where** [simp]: $x1 = ((e1, xs1), exs1)$ **by**(cases $x1$) auto
moreover obtain $xcp frs$ **where** [simp]: $x2 = (xcp, frs)$ **by**(cases $x2$)
moreover obtain $e1' xs1' exs1'$ **where** [simp]: $x1' = ((e1', xs1'), exs1')$ **by**(cases $x1'$) auto
ultimately have [simp]: $m1 = m2$
and $bisim: bisim1-list1 t m2 (e1, xs1) exs1 xcp frs$
and $red: True, P, t \vdash 1 \langle (e1, xs1) / exs1, m2 \rangle -ta1 \rightarrow \langle (e1', xs1') / exs1', m1' \rangle$
and $call: call1 e1 \neq None$
 $case frs of [] \Rightarrow False \mid (stk, loc, C, M, pc) \# frs' \Rightarrow \exists M' n. instrs-of (compP2 P) C M$

```

! pc = Invoke M' n
  by(auto simp add: bisim-wait1JVM-def split-def)
  from red wakeup have  $\neg \tau \text{Move1 } P \ m2 \ ((e1, xs1), exs1)$ 
    by(auto elim!: Red1.cases dest: red1- $\tau$ -taD simp add: split-beta ta-upd-simps)
  from exec-1-simulates-Red1-not- $\tau$ [OF wf red bisim this] call
  show  $\exists ta2 \ x2' \ m2'. \ mexecd \ (compP2 \ P) \ t \ (x2, m2) \ ta2 \ (x2', m2') \wedge \ wbisim1 \ t \ (x1', m1') \ (x2', m2') \wedge \ ta\text{-bisim} \ wbisim1 \ ta1 \ ta2$ 
    by(auto simp del: not-None-eq simp add: split-paired-Ex ta-bisim-def ta-upd-simps split: list.split-asm)
  next
  fix t x1 m1 x2 m2 ta2 x2' m2'
  assume wbisim1 t (x1, m1) (x2, m2)
    and bisim-wait1JVM (compP2 P) x1 x2
    and mexecd (compP2 P) t (x2, m2) ta2 (x2', m2')
    and wakeup: Notified  $\in$  set  $\{ta2\}_w \vee \text{WokenUp} \in$  set  $\{ta2\}_w$ 
  moreover obtain e1 xs1 exs1 where [simp]:  $x1 = ((e1, xs1), exs1)$  by(cases x1) auto
  moreover obtain xcp frs where [simp]:  $x2 = (xcp, frs)$  by(cases x2)
  moreover obtain xcp' frs' where [simp]:  $x2' = (xcp', frs')$  by(cases x2')
  ultimately have [simp]:  $m1 = m2$ 
    and bisim: bisim1-list1 t m2 (e1, xs1) exs1 xcp frs
    and exec:  $compP2 \ P, t \vdash \text{Normal} \ (xcp, m2, frs) \text{-}ta2\text{-jvmd} \rightarrow \text{Normal} \ (xcp', m2', frs')$ 
    and call: call1 e1  $\neq$  None
      case frs of []  $\Rightarrow$  False | (stk, loc, C, M, pc) # frs'  $\Rightarrow \exists M' n. \text{instrs-of} \ (compP2 \ P) \ C \ M$ 
! pc = Invoke M' n
  by(auto simp add: bisim-wait1JVM-def split-def)
  from exec wakeup have  $\neg \tau \text{Move2} \ (compP2 \ P) \ (xcp, m2, frs)$ 
    by(auto dest:  $\tau \text{exec-1-taD}$  simp add: split-beta ta-upd-simps)
  from  $\tau \text{Red1-simulates-exec-1-not-}\tau$ [OF wf exec bisim this] call
  show  $\exists ta1 \ x1' \ m1'. \ mred1 \ P \ t \ (x1, m1) \ ta1 \ (x1', m1') \wedge \ wbisim1 \ t \ (x1', m1') \ (x2', m2') \wedge \ ta\text{-bisim} \ wbisim1 \ ta1 \ ta2$ 
    by(auto simp del: not-None-eq simp add: split-paired-Ex ta-bisim-def ta-upd-simps split: list.split-asm)
  next
  show  $(\exists x. \text{final-expr1 } x) \longleftrightarrow (\exists x. \text{JVM-final } x)$ 
    by(auto simp add: split-paired-Ex final-iff)
  qed
qed
end

```

sublocale J1-JVM-heap-conf-base < Red1-mexecd:

```

FWbisimulation-base
final-expr1
mred1 P
JVM-final
mexecd (compP2 P)
convert-RA
wbisim1
bisim-wait1JVM (compP2 P)
.

```

context J1-JVM-heap-conf **begin**

```

lemma bisim-J1-JVM-start:
  assumes wf: wf-J1-prog P
  and wf-start: wf-start-state P C M vs

```



```

shows Red1-execd.mbisim (J1-start-state P C M vs) (JVM-start-state (compP2 P) C M vs)
proof –
from wf-start obtain Ts T body D where start: start-heap-ok
and sees: P ⊢ C sees M: Ts → T = [body] in D and conf: P, start-heap ⊢ vs [⋮] Ts by cases

let ?e = blocks1 0 (Class D # Ts) body
let ?xs = Null # vs @ replicate (max-vars body) undefined-value

from sees-wf-mdecl[OF wf sees] obtain T'
  where B: B body (Suc (length Ts))
  and wt: P, Class D # Ts ⊢ 1 body :: T'
  and da: D body [⋮..length Ts]
  and sv: syncvars body
  by(auto simp add: wf-mdecl-def)

have P, ?e, start-heap ⊢ (?e, ?xs) ↔ ([], ?xs, 0, None) by(rule bisim1-refl)
moreover
from wf have wf': wf-jvm-progcompTP P (compP2 P) by(rule wt-compTP-compP2)
from sees-method-compP[OF sees, of λC M Ts T. compMb2]
have sees': compP2 P ⊢ C sees M: Ts → T = [compMb2 body] in D by(simp add: compP2-def)
from conf have compP2 P, start-heap ⊢ vs [⋮] Ts by(simp add: compP2-def heap-base.compP-confs)
from BV-correct-initial[OF wf' start sees' this] sees'
have compTP P ⊢ start-tid:(None, start-heap, ([], ?xs, D, M, 0)) ✓
  by(simp add: JVM-start-state'-def compP2-def compMb2-def)
hence bisim1-list1 start-tid start-heap (?e, ?xs) [] None ([], ?xs, D, M, 0)
  using sees-method-idemp[OF sees]
proof
  show P, ?e, start-heap ⊢ (?e, ?xs) ↔ ([], ?xs, 0, None)
    by(rule bisim1-refl)
  show max-vars ?e ≤ length ?xs using conf
    by(auto simp add: blocks1-max-vars dest: list-all2-lengthD)
qed simp
thus ?thesis
  using sees sees' unfolding start-state-def
  by –(rule Red1-execd.mbisimI, auto split: if-split-asm intro: wset-thread-okI simp add: compP2-def
  compMb2-def)
qed

lemmas τred1-Val-simps = τred1r-Val τred1t-Val τreds1r-map-Val τreds1t-map-Val

end

end

```

7.20 Indexing variables in variable lists

theory ListIndex **imports** Main **begin**

In order to support local variables and arbitrarily nested blocks, the local variables are arranged as an indexed list. The outermost local variable (“this”) is the first element in the list, the most recently created local variable the last element. When descending into a block structure, a corresponding list Vs of variable names is maintained. To find the index of some variable V , we have to find the index of the *last* occurrence of V in Vs . This is what *index*

does:

primrec $index :: 'a\ list \Rightarrow 'a \Rightarrow nat$

where

$index []\ y = 0$
 $| index (x\#\!xs)\ y =$
 $(if\ x=y\ then\ if\ x \in set\ xs\ then\ index\ xs\ y + 1\ else\ 0\ else\ index\ xs\ y + 1)$

definition $hidden :: 'a\ list \Rightarrow nat \Rightarrow bool$

where $hidden\ xs\ i \equiv i < size\ xs \wedge xs!\!i \in set(drop\ (i+1)\ xs)$

7.20.1 $index$

lemma $[simp]: index\ (xs\ @\ [x])\ x = size\ xs$

lemma $[simp]: (index\ (xs\ @\ [x])\ y = size\ xs) = (x = y)$

lemma $[simp]: x \in set\ xs \Longrightarrow xs!\!index\ xs\ x = x$

lemma $[simp]: x \notin set\ xs \Longrightarrow index\ xs\ x = size\ xs$

lemma $index\text{-}size\text{-}conv[simp]: (index\ xs\ x = size\ xs) = (x \notin set\ xs)$

lemma $size\text{-}index\text{-}conv[simp]: (size\ xs = index\ xs\ x) = (x \notin set\ xs)$

lemma $(index\ xs\ x < size\ xs) = (x \in set\ xs)$

lemma $[simp]: [y \in set\ xs; x \neq y] \Longrightarrow index\ (xs\ @\ [x])\ y = index\ xs\ y$

lemma $index\text{-}less\text{-}size[simp]: x \in set\ xs \Longrightarrow index\ xs\ x < size\ xs$

lemma $index\text{-}less\text{-}aux: [x \in set\ xs; size\ xs \leq n] \Longrightarrow index\ xs\ x < n$

lemma $[simp]: x \in set\ xs \vee y \in set\ xs \Longrightarrow (index\ xs\ x = index\ xs\ y) = (x = y)$

lemma $inj\text{-}on\text{-}index: inj\text{-}on\ (index\ xs)\ (set\ xs)$

lemma $index\text{-}drop: \bigwedge x\ i. [x \in set\ xs; index\ xs\ x < i] \Longrightarrow x \notin set(drop\ i\ xs)$

7.20.2 $hidden$

lemma $hidden\text{-}index: x \in set\ xs \Longrightarrow hidden\ (xs\ @\ [x])\ (index\ xs\ x)$

lemma $hidden\text{-}inacc: hidden\ xs\ i \Longrightarrow index\ xs\ x \neq i$

lemma $[simp]: hidden\ xs\ i \Longrightarrow hidden\ (xs\ @\ [x])\ i$

lemma $fun\text{-}upds\text{-}apply: \bigwedge m\ ys.$

$(m(xs[\mapsto]ys))\ x =$
 $(let\ xs' = take\ (size\ ys)\ xs$
 $in\ if\ x \in set\ xs'\ then\ Some(ys!\!index\ xs'\ x)\ else\ m\ x)$

lemma $map\text{-}upds\text{-}apply\text{-}eq\text{-}Some:$

$((m(xs[\mapsto]ys))\ x = Some\ y) =$
 $(let\ xs' = take\ (size\ ys)\ xs$
 $in\ if\ x \in set\ xs'\ then\ ys!\!index\ xs'\ x = y\ else\ m\ x = Some\ y)$

lemma *map-upds-upd-conv-index*:

$\llbracket x \in \text{set } xs; \text{size } xs \leq \text{size } ys \rrbracket$
 $\implies m(xs[\mapsto]ys, x \mapsto y) = m(xs[\mapsto]ys[\text{index } xs \ x := y])$

lemma *image-index*:

$A \subseteq \text{set}(xs @ [x]) \implies \text{index } (xs @ [x]) \ ' A =$
(if $x \in A$ then insert (size xs) (index $xs \ ' (A - \{x\}))$ else index $xs \ ' A)$

lemma *index-le-lengthD*: $\text{index } xs \ x < \text{length } xs \implies x \in \text{set } xs$

by(*erule contrapos-pp*)(*simp*)

lemma *not-hidden-index-nth*: $\llbracket i < \text{length } Vs; \neg \text{hidden } Vs \ i \rrbracket \implies \text{index } Vs \ (Vs \ ! \ i) = i$

by(*induct Vs arbitrary: i*)(*auto split: if-split-asm nat.split-asm simp add: nth-Cons hidden-def*)

lemma *hidden-snoc-nth*:

assumes *len*: $i < \text{length } Vs$

shows *hidden* $(Vs @ [Vs \ ! \ i]) \ i$

proof(*cases hidden Vs i*)

case True thus ?thesis by simp

next

case False

with len have $\text{index } Vs \ (Vs \ ! \ i) = i$ **by**(*rule not-hidden-index-nth*)

moreover from len have *hidden* $(Vs @ [Vs \ ! \ i]) \ (\text{index } Vs \ (Vs \ ! \ i))$

by(*auto intro: hidden-index*)

ultimately show ?thesis by simp

qed

lemma *map-upds-Some-eq-nth-index*:

assumes $[Vs \ [\mapsto] \ vs] \ V = \text{Some } v \ \text{length } Vs \leq \text{length } vs$

shows $vs \ ! \ \text{index } Vs \ V = v$

proof –

from $\langle [Vs \ [\mapsto] \ vs] \ V = \text{Some } v \rangle$ **have** $V \in \text{set } Vs$

by –(*rule classical, auto*)

with $\langle [Vs \ [\mapsto] \ vs] \ V = \text{Some } v \rangle \ \langle \text{length } Vs \leq \text{length } vs \rangle$ **show** *?thesis*

proof(*induct Vs arbitrary: vs*)

case Nil thus ?case by simp

next

case $(\text{Cons } x \ xs \ ys)$

note $IH = \langle \bigwedge vs. \llbracket [xs \ [\mapsto] \ vs] \ V = \text{Some } v; \text{length } xs \leq \text{length } vs; V \in \text{set } xs \rrbracket \implies vs \ ! \ \text{index } xs$

$V = v \rangle$

from $\langle [x \ \# \ xs \ [\mapsto] \ ys] \ V = \text{Some } v \rangle$ **obtain** $y \ Ys$ **where** $ys = y \ \# \ Ys$ **by**(*cases ys, auto*)

with $\langle \text{length } (x \ \# \ xs) \leq \text{length } ys \rangle$ **have** $\text{length } xs \leq \text{length } Ys$ **by simp**

show *?case*

proof(*cases V ∈ set xs*)

case True

with $\langle [x \ \# \ xs \ [\mapsto] \ ys] \ V = \text{Some } v \rangle \ \langle \text{length } xs \leq \text{length } Ys \rangle \ \langle ys = y \ \# \ Ys \rangle$

have $[xs \ [\mapsto] \ Ys] \ V = \text{Some } v$

apply(*auto simp add: map-upds-def map-of-eq-None-iff set-zip image-Collect split: if-split-asm*)

apply(*clarsimp simp add: in-set-conv-decomp*)

apply(*hypsubst-thin*)

apply(*erule-tac x=length ys in allE*)

by(*simp*)

with $IH[OF \ \text{this} \ \langle \text{length } xs \leq \text{length } Ys \rangle \ \text{True}] \ \langle ys = y \ \# \ Ys \rangle \ \text{True}$

show *?thesis by simp*

next

```

    case False with ⟨V ∈ set (x # xs)⟩ have x = V by auto
    with False ⟨[x # xs [↦] ys] V = Some v⟩ ⟨ys = y # Ys⟩ have y = v
      by(auto)
    with False ⟨x = V⟩ ⟨ys = y # Ys⟩
    show ?thesis by(simp)
  qed
qed
qed
end

```

7.21 Compilation Stage 1

theory *Compiler1* **imports**

PCompiler

J1State

ListIndex

begin

definition *fresh-var* :: *vname list* ⇒ *vname*
where *fresh-var* *Vs* = *sum-list* (*STR* "V" # *Vs*)

lemma *fresh-var-fresh*: *fresh-var* *Vs* ∉ *set* *Vs*

proof –

have *V* ∈ *set* *Vs* ⇒ *length* (*String.explode* *V*) < *length* (*String.explode* (*fresh-var* *Vs*)) **for** *V*
by (*induction* *Vs*) (*auto simp add: fresh-var-def Literal.rep-eq*)
then show ?*thesis*

by *auto*

qed

Replacing variable names by indices.

function *compE1* :: *vname list* ⇒ '*addr expr* ⇒ '*addr expr1*
and *compEs1* :: *vname list* ⇒ '*addr expr list* ⇒ '*addr expr1 list*

where

```

  compE1 Vs (new C) = new C
| compE1 Vs (newA T[e]) = newA T[compE1 Vs e]
| compE1 Vs (Cast T e) = Cast T (compE1 Vs e)
| compE1 Vs (e instanceof T) = (compE1 Vs e) instanceof T
| compE1 Vs (Val v) = Val v
| compE1 Vs (Var V) = Var(index Vs V)
| compE1 Vs (e«bop»e') = (compE1 Vs e)«bop»(compE1 Vs e')
| compE1 Vs (V:=e) = (index Vs V):= (compE1 Vs e)
| compE1 Vs (a[i]) = (compE1 Vs a)[compE1 Vs i]
| compE1 Vs (a[i]:=e) = (compE1 Vs a)[compE1 Vs i]:=compE1 Vs e
| compE1 Vs (a·length) = compE1 Vs a·length
| compE1 Vs (e·F{D}) = compE1 Vs e·F{D}
| compE1 Vs (e·F{D}:=e') = compE1 Vs e·F{D}:=compE1 Vs e'
| compE1 Vs (e·compareAndSwap(D·F, e', e'')) = compE1 Vs e·compareAndSwap(D·F, compE1 Vs
e', compE1 Vs e'')
| compE1 Vs (e·M(es)) = (compE1 Vs e)·M(compEs1 Vs es)
| compE1 Vs {V:T=vo; e} = {(size Vs):T=vo; compE1 (Vs@[V]) e}
| compE1 Vs (sync U (o') e) = synclength Vs (compE1 Vs o') (compE1 (Vs@[fresh-var Vs]) e)
| compE1 Vs (insync U (a) e) = insynclength Vs (a) (compE1 (Vs@[fresh-var Vs]) e)

```

```

| compE1 Vs (e1;;e2) = (compE1 Vs e1);;(compE1 Vs e2)
| compE1 Vs (if (b) e1 else e2) = (if (compE1 Vs b) (compE1 Vs e1) else (compE1 Vs e2))
| compE1 Vs (while (b) e) = (while (compE1 Vs b) (compE1 Vs e))
| compE1 Vs (throw e) = throw (compE1 Vs e)
| compE1 Vs (try e1 catch(C V) e2) = try(compE1 Vs e1) catch(C (size Vs)) (compE1 (Vs@[V]) e2)

```

```

| compEs1 Vs [] = []
| compEs1 Vs (e#es) = compE1 Vs e # compEs1 Vs es

```

by pat-completeness auto

termination

apply(relation case-sum ($\lambda p.$ size (snd p)) ($\lambda p.$ size-list size (snd p)) <*mlex*> {})

apply(rule wf-mlex[OF wf-empty])

apply(rule mlex-less, simp)+

done

lemmas compE1-compEs1-induct =

compE1-compEs1.induct[case-names New NewArray Cast InstanceOf Val Var BinOp LAss AAcc
AAss ALen FAcc FAss CAS Call Block Synchronized InSynchronized Seq Cond While throw TryCatch
Nil Cons]

lemma compEs1-conv-map [simp]: compEs1 Vs es = map (compE1 Vs) es

by(induct es) simp-all

lemmas compEs1-map-Val = compEs1-conv-map

lemma compE1-eq-Val [simp]: compE1 Vs e = Val v \longleftrightarrow e = Val v

apply(cases e, auto)

done

lemma Val-eq-compE1 [simp]: Val v = compE1 Vs e \longleftrightarrow e = Val v

apply(cases e, auto)

done

lemma compEs1-eq-map-Val [simp]: compEs1 Vs es = map Val vs \longleftrightarrow es = map Val vs

apply(induct es arbitrary: vs)

apply(auto, blast)

done

lemma compE1-eq-Var [simp]: compE1 Vs e = Var V \longleftrightarrow ($\exists V'. e = Var V' \wedge V = index Vs V'$)

by(cases e, auto)

lemma compE1-eq-Call [simp]:

compE1 Vs e = obj.M(params) \longleftrightarrow ($\exists obj' params'. e = obj'.M(params') \wedge compE1 Vs obj' = obj$
 $\wedge compEs1 Vs params' = params$)

by(cases e, auto)

lemma length-compEs2 [simp]:

length (compEs1 Vs es) = length es

by(simp add: compEs1-conv-map)

lemma fixes e :: 'addr expr and es :: 'addr expr list

shows expr-locks-compE1 [simp]: expr-locks (compE1 Vs e) = expr-locks e

and expr-lockss-compEs1 [simp]: expr-lockss (compEs1 Vs es) = expr-lockss es

by(induct Vs e and Vs es rule: compE1-compEs1.induct)(auto intro: ext)

lemma fixes $e :: 'addr\ expr$ **and** $es :: 'addr\ expr\ list$
shows $contains-insync-compE1$ [simp]: $contains-insync (compE1\ Vs\ e) = contains-insync\ e$
and $contains-insyncs-compEs1$ [simp]: $contains-insyncs (compEs1\ Vs\ es) = contains-insyncs\ es$
by($induct\ Vs\ e$ **and** $Vs\ es$ $rule: compE1-compEs1.induct$)*simp-all*

lemma fixes $e :: 'addr\ expr$ **and** $es :: 'addr\ expr\ list$
shows $max-vars-compE1$: $max-vars (compE1\ Vs\ e) = max-vars\ e$
and $max-varss-compEs1$: $max-varss (compEs1\ Vs\ es) = max-varss\ es$
apply($induct\ Vs\ e$ **and** $Vs\ es$ $rule: compE1-compEs1.induct$)
apply(*auto*)
done

lemma fixes $e :: 'addr\ expr$ **and** $es :: 'addr\ expr\ list$
shows \mathcal{B} : $size\ Vs = n \implies \mathcal{B} (compE1\ Vs\ e)\ n$
and $\mathcal{B}s$: $size\ Vs = n \implies \mathcal{B}s (compEs1\ Vs\ es)\ n$
apply($induct\ Vs\ e$ **and** $Vs\ es$ $arbitrary: n$ **and** n $rule: compE1-compEs1.induct$)
apply *auto*
done

lemma fixes $e :: 'addr\ expr$ **and** $es :: 'addr\ expr\ list$
shows $fv-compE1$: $fv\ e \subseteq set\ Vs \implies fv (compE1\ Vs\ e) = (index\ Vs) \text{ ` } (fv\ e)$
and $fvs-compEs1$: $fvs\ es \subseteq set\ Vs \implies fvs (compEs1\ Vs\ es) = (index\ Vs) \text{ ` } (fvs\ es)$
proof($induct\ Vs\ e$ **and** $Vs\ es$ $rule: compE1-compEs1.induct$)
case ($Block\ Vs\ V\ ty\ vo\ exp$)
have IH : $fv\ exp \subseteq set (Vs @ [V]) \implies fv (compE1 (Vs @ [V])\ exp) = index (Vs @ [V]) \text{ ` } fv\ exp$ **by** *fact*
from $\langle fv \{V:ty=vo; exp\} \subseteq set\ Vs \rangle$ **have** fv' : $fv\ exp \subseteq set (Vs @ [V])$ **by** *auto*
from IH [*OF this*] **have** IH' : $fv (compE1 (Vs @ [V])\ exp) = index (Vs @ [V]) \text{ ` } fv\ exp$.
have $fv (compE1 (Vs @ [V])\ exp) - \{length\ Vs\} = index\ Vs \text{ ` } (fv\ exp - \{V\})$
proof($rule\ equalityI$ [*OF subsetI subsetI*])
fix x
assume x : $x \in fv (compE1 (Vs @ [V])\ exp) - \{length\ Vs\}$
hence $x \neq length\ Vs$ **by** *simp*
from $x\ IH'$ **have** $x \in index (Vs @ [V]) \text{ ` } fv\ exp$ **by** *simp*
thus $x \in index\ Vs \text{ ` } (fv\ exp - \{V\})$
proof($rule\ imageE$)
fix y
assume [simp]: $x = index (Vs @ [V])\ y$
and y : $y \in fv\ exp$
have $y \neq V$
proof
assume [simp]: $y = V$
hence $x = length\ Vs$ **by** *simp*
with $\langle x \neq length\ Vs \rangle$ **show** *False* **by** *contradiction*
qed
moreover **with** $fv'\ y$ **have** $y \in set\ Vs$ **by** *auto*
ultimately **have** $index (Vs @ [V])\ y = index\ Vs\ y$ **by**(*simp*)
thus $?thesis$ **using** $y \langle y \neq V \rangle$ **by** *auto*
qed
next
fix x
assume x : $x \in index\ Vs \text{ ` } (fv\ exp - \{V\})$
thus $x \in fv (compE1 (Vs @ [V])\ exp) - \{length\ Vs\}$

```

proof(rule imageE)
  fix y
  assume [simp]:  $x = \text{index } Vs \ y$ 
  and  $y: y \in \text{fv } \text{exp} - \{V\}$ 
  with  $\text{fv}'$  have  $y \in \text{set } Vs \ y \neq V$  by auto
  hence  $\text{index } Vs \ y = \text{index } (Vs @ [V]) \ y$  by simp
  with  $y$  have  $x \in \text{index } (Vs @ [V]) \ ' \text{fv } \text{exp}$  by auto
  thus ?thesis using  $IH' \langle y \in \text{set } Vs \rangle$  by simp
qed
qed
thus ?case by simp
next
case (Synchronized  $Vs \ V \ \text{exp1} \ \text{exp2}$ )
have  $IH1: \text{fv } \text{exp1} \subseteq \text{set } Vs \implies \text{fv } (\text{compE1 } Vs \ \text{exp1}) = \text{index } Vs \ ' \ \text{fv } \text{exp1}$ 
  and  $IH2: \text{fv } \text{exp2} \subseteq \text{set } (Vs @ [\text{fresh-var } Vs]) \implies \text{fv } (\text{compE1 } (Vs @ [\text{fresh-var } Vs]) \ \text{exp2}) = \text{index } (Vs @ [\text{fresh-var } Vs]) \ ' \ \text{fv } \text{exp2}$ 
  by fact+
from  $\langle \text{fv } (\text{sync}_V (\text{exp1}) \ \text{exp2}) \subseteq \text{set } Vs \rangle$  have  $\text{fv1}: \text{fv } \text{exp1} \subseteq \text{set } Vs$ 
  and  $\text{fv2}: \text{fv } \text{exp2} \subseteq \text{set } Vs$  by auto
from  $\text{fv2}$  have  $\text{fv2}': \text{fv } \text{exp2} \subseteq \text{set } (Vs @ [\text{fresh-var } Vs])$  by auto
have  $\text{index } (Vs @ [\text{fresh-var } Vs]) \ ' \ \text{fv } \text{exp2} = \text{index } Vs \ ' \ \text{fv } \text{exp2}$ 
proof(rule equalityI[OF subsetI subsetI])
  fix x
  assume  $x: x \in \text{index } (Vs @ [\text{fresh-var } Vs]) \ ' \ \text{fv } \text{exp2}$ 
  thus  $x \in \text{index } Vs \ ' \ \text{fv } \text{exp2}$ 
  proof(rule imageE)
    fix y
    assume [simp]:  $x = \text{index } (Vs @ [\text{fresh-var } Vs]) \ y$ 
    and  $y: y \in \text{fv } \text{exp2}$ 
    from  $y \ \text{fv2}$  have  $y \in \text{set } Vs$  by auto
    moreover hence  $y \neq (\text{fresh-var } Vs)$  by(auto simp add: fresh-var-fresh)
    ultimately show ?thesis using  $y$  by(auto)
  qed
next
fix x
assume  $x: x \in \text{index } Vs \ ' \ \text{fv } \text{exp2}$ 
thus  $x \in \text{index } (Vs @ [\text{fresh-var } Vs]) \ ' \ \text{fv } \text{exp2}$ 
proof(rule imageE)
  fix y
  assume [simp]:  $x = \text{index } Vs \ y$ 
  and  $y: y \in \text{fv } \text{exp2}$ 
  from  $y \ \text{fv2}$  have  $y \in \text{set } Vs$  by auto
  moreover hence  $y \neq (\text{fresh-var } Vs)$  by(auto simp add: fresh-var-fresh)
  ultimately have  $\text{index } Vs \ y = \text{index } (Vs @ [\text{fresh-var } Vs]) \ y$  by simp
  thus ?thesis using  $y$  by(auto)
qed
qed
with  $IH1$ [OF  $\text{fv1}$ ]  $IH2$ [OF  $\text{fv2}'$ ] show ?case by(auto)
next
case (InSynchronized  $Vs \ V \ a \ \text{exp}$ )
have  $IH: \text{fv } \text{exp} \subseteq \text{set } (Vs @ [\text{fresh-var } Vs]) \implies \text{fv } (\text{compE1 } (Vs @ [\text{fresh-var } Vs]) \ \text{exp}) = \text{index } (Vs @ [\text{fresh-var } Vs]) \ ' \ \text{fv } \text{exp}$ 
  by fact
from  $\langle \text{fv } (\text{insync}_V (a) \ \text{exp}) \subseteq \text{set } Vs \rangle$  have  $\text{fv}: \text{fv } \text{exp} \subseteq \text{set } Vs$  by simp

```

```

hence  $fv'$ :  $fv\ exp \subseteq set\ (Vs\ @\ [fresh-var\ Vs])$  by auto
have  $index\ (Vs\ @\ [fresh-var\ Vs])\ 'fv\ exp = index\ Vs\ 'fv\ exp$ 
proof(rule\ equalityI[OF\ subsetI\ subsetI])
  fix  $x$ 
  assume  $x \in index\ (Vs\ @\ [fresh-var\ Vs])\ 'fv\ exp$ 
  thus  $x \in index\ Vs\ 'fv\ exp$ 
  proof(rule\ imageE)
    fix  $y$ 
    assume [simp]:  $x = index\ (Vs\ @\ [fresh-var\ Vs])\ y$ 
    and  $y: y \in fv\ exp$ 
    from  $y\ fv$  have  $y \in set\ Vs$  by auto
    moreover hence  $y \neq (fresh-var\ Vs)$  by(auto\ simp\ add: fresh-var-fresh)
    ultimately have  $index\ (Vs\ @\ [fresh-var\ Vs])\ y = index\ Vs\ y$  by simp
    thus ?thesis using  $y$  by simp
  qed
next
  fix  $x$ 
  assume  $x \in index\ Vs\ 'fv\ exp$ 
  thus  $x \in index\ (Vs\ @\ [fresh-var\ Vs])\ 'fv\ exp$ 
  proof(rule\ imageE)
    fix  $y$ 
    assume [simp]:  $x = index\ Vs\ y$ 
    and  $y: y \in fv\ exp$ 
    from  $y\ fv$  have  $y \in set\ Vs$  by auto
    moreover hence  $y \neq (fresh-var\ Vs)$  by(auto\ simp\ add: fresh-var-fresh)
    ultimately have  $index\ Vs\ y = index\ (Vs\ @\ [fresh-var\ Vs])\ y$  by simp
    thus ?thesis using  $y$  by auto
  qed
qed
with IH[OF\ fv] show ?case by simp
next
  case (TryCatch\ Vs\ exp1\ C\ V\ exp2)
  have IH1:  $fv\ exp1 \subseteq set\ Vs \implies fv\ (compE1\ Vs\ exp1) = index\ Vs\ 'fv\ exp1$ 
  and IH2:  $fv\ exp2 \subseteq set\ (Vs\ @\ [V]) \implies fv\ (compE1\ (Vs\ @\ [V])\ exp2) = index\ (Vs\ @\ [V])\ 'fv\ exp2$ 
  by fact+
  from  $\langle fv\ (try\ exp1\ catch\ (C\ V)\ exp2) \subseteq set\ Vs \rangle$  have fv1:  $fv\ exp1 \subseteq set\ Vs$ 
  and fv2:  $fv\ exp2 \subseteq set\ (Vs\ @\ [V])$  by auto
  have  $index\ (Vs\ @\ [V])\ 'fv\ exp2 - \{length\ Vs\} = index\ Vs\ '(fv\ exp2 - \{V\})$ 
  proof(rule\ equalityI[OF\ subsetI\ subsetI])
    fix  $x$ 
    assume  $x: x \in index\ (Vs\ @\ [V])\ 'fv\ exp2 - \{length\ Vs\}$ 
    hence  $x \neq length\ Vs$  by simp
    from  $x$  have  $x \in index\ (Vs\ @\ [V])\ 'fv\ exp2$  by auto
    thus  $x \in index\ Vs\ '(fv\ exp2 - \{V\})$ 
    proof(rule\ imageE)
      fix  $y$ 
      assume [simp]:  $x = index\ (Vs\ @\ [V])\ y$ 
      and  $y: y \in fv\ exp2$ 
      have  $y \neq V$ 
      proof
        assume [simp]:  $y = V$ 
        hence  $x = length\ Vs$  by simp
        with  $\langle x \neq length\ Vs \rangle$  show False by contradiction
      qed
    qed
  qed

```



```

qed
moreover with fv2 y have y ∈ set Vs by auto
ultimately have index (Vs @ [V]) y = index Vs y by(simp)
thus ?thesis using y ⟨y ≠ V⟩ by auto
qed
next
fix x
assume x: x ∈ index Vs ‘(fv exp2 - {V})
thus x ∈ index (Vs @ [V]) ‘fv exp2 - {length Vs}
proof(rule imageE)
  fix y
  assume [simp]: x = index Vs y
  and y: y ∈ fv exp2 - {V}
  with fv2 have y ∈ set Vs y ≠ V by auto
  hence index Vs y = index (Vs @ [V]) y by simp
  with y have x ∈ index (Vs @ [V]) ‘fv exp2 by auto
  thus ?thesis using ⟨y ∈ set Vs⟩ by simp
qed
qed
with IH1[OF fv1] IH2[OF fv2] show ?case by auto
qed(auto)

lemma fixes e :: 'addr expr and es :: 'addr expr list
  shows syncvars-compE1: fv e ⊆ set Vs ⇒ syncvars (compE1 Vs e)
  and syncvarss-compEs1: fvs es ⊆ set Vs ⇒ syncvarss (compEs1 Vs es)
proof(induct Vs e and Vs es rule: compE1-compEs1-induct)
  case (Block Vs V ty vo exp)
  from ⟨fv {V:ty=vo; exp} ⊆ set Vs⟩ have fv exp ⊆ set (Vs @ [V]) by auto
  from ⟨fv exp ⊆ set (Vs @ [V]) ⇒ syncvars (compE1 (Vs @ [V]) exp)⟩[OF this] show ?case
by(simp)
next
  case (Synchronized Vs V exp1 exp2)
  note IH1 = ⟨fv exp1 ⊆ set Vs ⇒ syncvars (compE1 Vs exp1)⟩
  note IH2 = ⟨fv exp2 ⊆ set (Vs @ [fresh-var Vs]) ⇒ syncvars (compE1 (Vs @ [fresh-var Vs]) exp2)⟩
  from ⟨fv (sync_V (exp1) exp2) ⊆ set Vs⟩ have fv1: fv exp1 ⊆ set Vs
  and fv2: fv exp2 ⊆ set Vs and fv2': fv exp2 ⊆ set (Vs @ [fresh-var Vs]) by auto
  have length Vs ∉ index (Vs @ [fresh-var Vs]) ‘fv exp2
  proof
    assume length Vs ∈ index (Vs @ [fresh-var Vs]) ‘fv exp2
    thus False
  proof(rule imageE)
    fix x
    assume x: length Vs = index (Vs @ [fresh-var Vs]) x
    and x': x ∈ fv exp2
    from x' fv2 have x ∈ set Vs x ≠ (fresh-var Vs) by(auto simp add: fresh-var-fresh)
    with x show ?thesis by(simp)
  qed
  qed
  with IH1[OF fv1] IH2[OF fv2'] fv2' show ?case by(simp add: fv-compE1)
next
  case (InSynchronized Vs V a exp)
  note IH = ⟨fv exp ⊆ set (Vs @ [fresh-var Vs]) ⇒ syncvars (compE1 (Vs @ [fresh-var Vs]) exp)⟩
  from ⟨fv (insync_V (a) exp) ⊆ set Vs⟩ have fv: fv exp ⊆ set Vs
  and fv': fv exp ⊆ set (Vs @ [fresh-var Vs]) by auto

```

```

have length Vs  $\notin$  index (Vs @ [fresh-var Vs]) ‘fv exp
proof
  assume length Vs  $\in$  index (Vs @ [fresh-var Vs]) ‘fv exp
  thus False
proof(rule imageE)
  fix x
  assume x: length Vs = index (Vs @ [fresh-var Vs]) x
  and x': x  $\in$  fv exp
  from x' fv have x  $\in$  set Vs x  $\neq$  (fresh-var Vs) by(auto simp add: fresh-var-fresh)
  with x show ?thesis by(simp)
qed
qed
with IH[OF fv'] fv' show ?case by(simp add: fv-compE1)
next
case (TryCatch Vs exp1 C V exp2)
note IH1 =  $\langle$ fv exp1  $\subseteq$  set Vs  $\implies$  syncvars (compE1 Vs exp1) $\rangle$ 
note IH2 =  $\langle$ fv exp2  $\subseteq$  set (Vs @ [V])  $\implies$  syncvars (compE1 (Vs @ [V]) exp2) $\rangle$ 
from  $\langle$ fv (try exp1 catch(C V) exp2)  $\subseteq$  set Vs $\rangle$  have fv1: fv exp1  $\subseteq$  set Vs
  and fv2: fv exp2  $\subseteq$  set (Vs @ [V]) by auto
from IH1[OF fv1] IH2[OF fv2] show ?case by auto
qed auto

lemma (in heap-base) synthesized-call-compP [simp]:
  synthesized-call (compP f P) h aMvs = synthesized-call P h aMvs
by(simp add: synthesized-call-def)

```

```

primrec fin1 :: 'addr expr  $\Rightarrow$  'addr expr1
where
  fin1 (Val v) = Val v
| fin1 (throw e) = throw (fin1 e)

```

```

lemma comp-final: final e  $\implies$  compE1 Vs e = fin1 e
by(erule finalE, simp-all)

```

```

lemma fixes e :: 'addr expr and es :: 'addr expr list
  shows [simp]: max-vars (compE1 Vs e) = max-vars e
  and max-varss (compEs1 Vs es) = max-varss es
by (induct Vs e and Vs es rule: compE1-compEs1-induct)(simp-all)

```

Compiling programs:

```

definition compP1 :: 'addr J-prog  $\Rightarrow$  'addr J1-prog
where
  compP1  $\equiv$  compP ( $\lambda$ C M Ts T (pns,body). compE1 (this#pns) body)

```

```

declare compP1-def[simp]

```

```

end

```

7.22 The bisimulation relation between source and intermediate language

```

theory J0J1Bisim imports

```

```

J1
J1WellForm
Compiler1
../J/JWellForm
J0
begin

```

7.22.1 Correctness of program compilation

```

primrec unmod :: 'addr expr1  $\Rightarrow$  nat  $\Rightarrow$  bool
and unmods :: 'addr expr1 list  $\Rightarrow$  nat  $\Rightarrow$  bool
where
  unmod (new C) i = True
| unmod (newA T[e]) i = unmod e i
| unmod (Cast C e) i = unmod e i
| unmod (e instanceof T) i = unmod e i
| unmod (Val v) i = True
| unmod (e1 «bop» e2) i = (unmod e1 i  $\wedge$  unmod e2 i)
| unmod (Var i) j = True
| unmod (i:=e) j = (i  $\neq$  j  $\wedge$  unmod e j)
| unmod (a[i]) j = (unmod a j  $\wedge$  unmod i j)
| unmod (a[i]:=e) j = (unmod a j  $\wedge$  unmod i j  $\wedge$  unmod e j)
| unmod (a.length) j = unmod a j
| unmod (e.F{D}) i = unmod e i
| unmod (e1.F{D}:=e2) i = (unmod e1 i  $\wedge$  unmod e2 i)
| unmod (e1.compareAndSwap(D.F, e2, e3)) i = (unmod e1 i  $\wedge$  unmod e2 i  $\wedge$  unmod e3 i)
| unmod (e.M(es)) i = (unmod e i  $\wedge$  unmods es i)
| unmod {j:T=vo; e} i = ((i = j  $\longrightarrow$  vo = None)  $\wedge$  unmod e i)
| unmod (syncV (o') e) i = (unmod o' i  $\wedge$  unmod e i  $\wedge$  i  $\neq$  V)
| unmod (insyncV (a) e) i = unmod e i
| unmod (e1;;e2) i = (unmod e1 i  $\wedge$  unmod e2 i)
| unmod (if (e) e1 else e2) i = (unmod e i  $\wedge$  unmod e1 i  $\wedge$  unmod e2 i)
| unmod (while (e) c) i = (unmod e i  $\wedge$  unmod c i)
| unmod (throw e) i = unmod e i
| unmod (try e1 catch(C i) e2) j = (unmod e1 j  $\wedge$  (if i=j then False else unmod e2 j))

| unmods ([]) i = True
| unmods (e#es) i = (unmod e i  $\wedge$  unmods es i)

```

```

lemma unmods-map-Val [simp]: unmods (map Val vs) V
by(induct vs) simp-all

```

```

lemma fixes e :: 'addr expr and es :: 'addr expr list
  shows hidden-unmod: hidden Vs i  $\Longrightarrow$  unmod (compE1 Vs e) i
  and hidden-unmods: hidden Vs i  $\Longrightarrow$  unmods (compEs1 Vs es) i
apply(induct Vs e and Vs es rule: compE1-compEs1-induct)
apply (simp-all add:hidden-inacc)
apply(auto simp add:hidden-def)
done

```

```

lemma unmod-extRet2J [simp]: unmod e i  $\Longrightarrow$  unmod (extRet2J e va) i
by(cases va) simp-all

```

```

lemma max-dest: (n :: nat) + max a b  $\leq$  c  $\Longrightarrow$  n + a  $\leq$  c  $\wedge$  n + b  $\leq$  c

```

apply(*auto simp add: max-def split: if-split-asm*)
done

declare *max-dest* [*dest!*]

lemma **fixes** *e* :: 'addr expr **and** *es* :: 'addr expr list

shows *fv-unmod-compE1*: $\llbracket i < \text{length } Vs; Vs ! i \notin \text{fv } e \rrbracket \Longrightarrow \text{unmod } (\text{compE1 } Vs e) i$
and *fvs-unmods-compEs1*: $\llbracket i < \text{length } Vs; Vs ! i \notin \text{fvs } es \rrbracket \Longrightarrow \text{unmods } (\text{compEs1 } Vs es) i$

proof(*induct* *Vs e* **and** *Vs es* *rule: compE1-compEs1-induct*)

case (*Block* *Vs V ty vo exp*)

note *IH* = $\langle \llbracket i < \text{length } (Vs @ [V]); (Vs @ [V]) ! i \notin \text{fv } exp \rrbracket \Longrightarrow \text{unmod } (\text{compE1 } (Vs @ [V]) exp) \rangle$

i

note *len* = $\langle i < \text{length } Vs \rangle$

hence *i*: $i < \text{length } (Vs @ [V])$ **by** *simp*

show ?*case*

proof(*cases* *Vs ! i = V*)

case *True*

from *len* **have** *hidden* (*Vs @ [Vs ! i]*) *i* **by**(*rule hidden-snoc-nth*)

with *len* *True* **show** ?*thesis* **by**(*auto intro: hidden-unmod*)

next

case *False*

with $\langle Vs ! i \notin \text{fv } \{V:ty=vo; exp\} \rangle$ *len* **have** (*Vs @ [V]*) ! *i* $\notin \text{fv } exp$

by(*auto simp add: nth-append*)

from *IH*[*OF i this*] *len* **show** ?*thesis* **by**(*auto*)

qed

next

case (*TryCatch* *Vs e1 C V e2*)

note *IH1* = $\langle \llbracket i < \text{length } Vs; Vs ! i \notin \text{fv } e1 \rrbracket \Longrightarrow \text{unmod } (\text{compE1 } Vs e1) i \rangle$

note *IH2* = $\langle \llbracket i < \text{length } (Vs @ [V]); (Vs @ [V]) ! i \notin \text{fv } e2 \rrbracket \Longrightarrow \text{unmod } (\text{compE1 } (Vs @ [V]) e2) \rangle$

i

note *len* = $\langle i < \text{length } Vs \rangle$

hence *i*: $i < \text{length } (Vs @ [V])$ **by** *simp*

have $\text{unmod } (\text{compE1 } (Vs @ [V]) e2) i$

proof(*cases* *Vs ! i = V*)

case *True*

from *len* **have** *hidden* (*Vs @ [Vs ! i]*) *i* **by**(*rule hidden-snoc-nth*)

with *len* *True* **show** ?*thesis* **by**(*auto intro: hidden-unmod*)

next

case *False*

with $\langle Vs ! i \notin \text{fv } (\text{try } e1 \text{ catch } (C V) e2) \rangle$ *len* **have** (*Vs @ [V]*) ! *i* $\notin \text{fv } e2$

by(*auto simp add: nth-append*)

from *IH2*[*OF i this*] *len* **show** ?*thesis* **by**(*auto*)

qed

with *IH1*[*OF len*] $\langle Vs ! i \notin \text{fv } (\text{try } e1 \text{ catch } (C V) e2) \rangle$ *len* **show** ?*case* **by**(*auto*)

qed(*auto dest: index-le-lengthD simp add: nth-append*)

lemma *hidden-lengthD*: *hidden* *Vs i* $\Longrightarrow i < \text{length } Vs$

by(*simp add: hidden-def*)

lemma **fixes** *e* :: 'addr expr1 **and** *es* :: 'addr expr1 list

shows *fv-B-unmod*: $\llbracket V \notin \text{fv } e; \mathcal{B} e n; V < n \rrbracket \Longrightarrow \text{unmod } e V$

and *fvs-Bs-unmods*: $\llbracket V \notin \text{fvs } es; \mathcal{B}s es n; V < n \rrbracket \Longrightarrow \text{unmods } es V$

by(*induct* *e* **and** *es* *arbitrary: n* **and** *n* *rule: unmod.induct unmods.induct*) *auto*

lemma assumes *fin*: *final e'*
shows *unmod-inline-call*: $\text{unmod } (\text{inline-call } e' e) V \longleftrightarrow \text{unmod } e V$
and *unmods-inline-calls*: $\text{unmods } (\text{inline-calls } e' es) V \longleftrightarrow \text{unmods } es V$
apply(*induct e and es rule*: *unmod.induct unmods.induct*)
apply(*insert fin*)
apply(*auto simp add: is-vals-conv*)
done

7.22.2 The delay bisimulation relation

Delay bisimulation for expressions

inductive *bisim* :: *vname list* \Rightarrow *'addr expr* \Rightarrow *'addr expr1* \Rightarrow *'addr val list* \Rightarrow *bool*
and *bisims* :: *vname list* \Rightarrow *'addr expr list* \Rightarrow *'addr expr1 list* \Rightarrow *'addr val list* \Rightarrow *bool*
where
bisimNew: $\text{bisim } Vs (\text{new } C) (\text{new } C) xs$
bisimNewArray: $\text{bisim } Vs e e' xs \Longrightarrow \text{bisim } Vs (\text{newA } T [e]) (\text{newA } T [e']) xs$
bisimCast: $\text{bisim } Vs e e' xs \Longrightarrow \text{bisim } Vs (\text{Cast } T e) (\text{Cast } T e') xs$
bisimInstanceOf: $\text{bisim } Vs e e' xs \Longrightarrow \text{bisim } Vs (e \text{ instanceof } T) (e' \text{ instanceof } T) xs$
bisimVal: $\text{bisim } Vs (\text{Val } v) (\text{Val } v) xs$
bisimBinOp1:
 $\llbracket \text{bisim } Vs e e' xs; \neg \text{is-val } e; \neg \text{contains-insync } e'' \rrbracket \Longrightarrow \text{bisim } Vs (e \llbracket \text{bop} \rrbracket e'') (e' \llbracket \text{bop} \rrbracket \text{compE1 } Vs e'') xs$
bisimBinOp2: $\text{bisim } Vs e e' xs \Longrightarrow \text{bisim } Vs (\text{Val } v \llbracket \text{bop} \rrbracket e) (\text{Val } v \llbracket \text{bop} \rrbracket e') xs$
bisimVar: $\text{bisim } Vs (\text{Var } V) (\text{Var } (\text{index } Vs V)) xs$
bisimLAss: $\text{bisim } Vs e e' xs \Longrightarrow \text{bisim } Vs (V := e) (\text{index } Vs V := e') xs$
bisimAAss1: $\llbracket \text{bisim } Vs a a' xs; \neg \text{is-val } a; \neg \text{contains-insync } i \rrbracket \Longrightarrow \text{bisim } Vs (a [i]) (a' [i]) (\text{compE1 } Vs i) xs$
bisimAAss2: $\text{bisim } Vs i i' xs \Longrightarrow \text{bisim } Vs (\text{Val } v [i]) (\text{Val } v [i']) xs$
bisimAAss1:
 $\llbracket \text{bisim } Vs a a' xs; \neg \text{is-val } a; \neg \text{contains-insync } i; \neg \text{contains-insync } e \rrbracket$
 $\Longrightarrow \text{bisim } Vs (a [i] := e) (a' [i] := \text{compE1 } Vs e) xs$
bisimAAss2: $\llbracket \text{bisim } Vs i i' xs; \neg \text{is-val } i; \neg \text{contains-insync } e \rrbracket \Longrightarrow \text{bisim } Vs (\text{Val } v [i] := e) (\text{Val } v [i'] := \text{compE1 } Vs e) xs$
bisimAAss3: $\text{bisim } Vs e e' xs \Longrightarrow \text{bisim } Vs (\text{Val } v [\text{Val } i] := e) (\text{Val } v [\text{Val } i'] := e') xs$
bisimALength: $\text{bisim } Vs a a' xs \Longrightarrow \text{bisim } Vs (a \cdot \text{length}) (a' \cdot \text{length}) xs$
bisimFAcc: $\text{bisim } Vs e e' xs \Longrightarrow \text{bisim } Vs (e \cdot F \{D\}) (e' \cdot F \{D\}) xs$
bisimFAss1: $\llbracket \text{bisim } Vs e e' xs; \neg \text{is-val } e; \neg \text{contains-insync } e'' \rrbracket \Longrightarrow \text{bisim } Vs (e \cdot F \{D\} := e'') (e' \cdot F \{D\} := \text{compE1 } Vs e'') xs$
bisimFAss2: $\text{bisim } Vs e e' xs \Longrightarrow \text{bisim } Vs (\text{Val } v \cdot F \{D\} := e) (\text{Val } v \cdot F \{D\} := e') xs$
bisimCAS1: $\llbracket \text{bisim } Vs e e' xs; \neg \text{is-val } e; \neg \text{contains-insync } e2; \neg \text{contains-insync } e3 \rrbracket$
 $\Longrightarrow \text{bisim } Vs (e \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) (e' \cdot \text{compareAndSwap}(D \cdot F, \text{compE1 } Vs e2, \text{compE1 } Vs e3)) xs$
bisimCAS2: $\llbracket \text{bisim } Vs e e' xs; \neg \text{is-val } e; \neg \text{contains-insync } e3 \rrbracket$
 $\Longrightarrow \text{bisim } Vs (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, e, e3)) (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, e', \text{compE1 } Vs e3)) xs$
bisimCAS3: $\text{bisim } Vs e e' xs \Longrightarrow \text{bisim } Vs (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', e)) (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', e')) xs$
bisimCallObj: $\llbracket \text{bisim } Vs e e' xs; \neg \text{is-val } e; \neg \text{contains-insyncs } es \rrbracket \Longrightarrow \text{bisim } Vs (e \cdot M(es)) (e' \cdot M(\text{compEs1 } Vs es)) xs$
bisimCallParams: $\text{bisims } Vs es es' xs \Longrightarrow \text{bisim } Vs (\text{Val } v \cdot M(es)) (\text{Val } v \cdot M(es')) xs$
bisimBlockNone: $\text{bisim } (Vs@[V]) e e' xs \Longrightarrow \text{bisim } Vs \{V:T=None; e\} \{(length\ Vs):T=None; e'\} xs$
bisimBlockSome: $\llbracket \text{bisim } (Vs@[V]) e e' (xs[length\ Vs := v]) \rrbracket \Longrightarrow \text{bisim } Vs \{V:T=[v]; e\} \{(length\ Vs):T=[v]; e'\} xs$
bisimBlockSomeNone: $\llbracket \text{bisim } (Vs@[V]) e e' xs; xs ! (length\ Vs) = v \rrbracket \Longrightarrow \text{bisim } Vs \{V:T=[v]; e\}$

$\{(length\ Vs):T=None; e'\} xs$
 $| bisimSynchronized:$
 $\llbracket bisim\ Vs\ o'\ o''\ xs; \neg\ contains-insync\ e \rrbracket$
 $\implies bisim\ Vs\ (sync(o')\ e)\ (sync_{length\ Vs}(o'')\ (compE1\ (Vs@[fresh-var\ Vs])\ e))\ xs$
 $| bisimInSynchronized:$
 $\llbracket bisim\ (Vs@[fresh-var\ Vs])\ e\ e'\ xs; xs!\ length\ Vs = Addr\ a \rrbracket \implies bisim\ Vs\ (insync(a)\ e)\ (insync_{length\ Vs}(a)\ e')\ xs$
 $| bisimSeq: \llbracket bisim\ Vs\ e\ e'\ xs; \neg\ contains-insync\ e'' \rrbracket \implies bisim\ Vs\ (e;;e'')\ (e';;compE1\ Vs\ e'')\ xs$
 $| bisimCond:$
 $\llbracket bisim\ Vs\ e\ e'\ xs; \neg\ contains-insync\ e1; \neg\ contains-insync\ e2 \rrbracket$
 $\implies bisim\ Vs\ (if\ (e)\ e1\ else\ e2)\ (if\ (e')\ (compE1\ Vs\ e1)\ else\ (compE1\ Vs\ e2))\ xs$
 $| bisimWhile:$
 $\llbracket \neg\ contains-insync\ b; \neg\ contains-insync\ e \rrbracket \implies bisim\ Vs\ (while\ (b)\ e)\ (while\ (compE1\ Vs\ b)\ (compE1\ Vs\ e))\ xs$
 $| bisimThrow: bisim\ Vs\ e\ e'\ xs \implies bisim\ Vs\ (throw\ e)\ (throw\ e')\ xs$
 $| bisimTryCatch:$
 $\llbracket bisim\ Vs\ e\ e'\ xs; \neg\ contains-insync\ e'' \rrbracket$
 $\implies bisim\ Vs\ (try\ e\ catch(C\ V)\ e'')\ (try\ e'\ catch(C\ (length\ Vs))\ compE1\ (Vs@[V])\ e'')\ xs$

 $| bisimsNil: bisims\ Vs\ []\ []\ xs$
 $| bisimsCons1: \llbracket bisim\ Vs\ e\ e'\ xs; \neg\ is-val\ e; \neg\ contains-insyncs\ es \rrbracket \implies bisims\ Vs\ (e\ #\ es)\ (e'\ #\ compEs1\ Vs\ es)\ xs$
 $| bisimsCons2: bisims\ Vs\ es\ es'\ xs \implies bisims\ Vs\ (Val\ v\ #\ es)\ (Val\ v\ #\ es')\ xs$

declare *bisimNew* [*iff*]
declare *bisimVal* [*iff*]
declare *bisimVar* [*iff*]
declare *bisimWhile* [*iff*]
declare *bisimsNil* [*iff*]

declare *bisim-bisims.intros* [*intro!*]
declare *bisimsCons1* [*rule del, intro*] *bisimsCons2* [*rule del, intro*]
bisimBinOp1 [*rule del, intro*] *bisimAAcc1* [*rule del, intro*]
bisimAAss1 [*rule del, intro*] *bisimAAss2* [*rule del, intro*]
bisimFAss1 [*rule del, intro*]
bisimCAS1 [*rule del, intro*] *bisimCAS2* [*rule del, intro*]
bisimCallObj [*rule del, intro*]

inductive-cases *bisim-safe-cases* [*elim!*]:

bisim\ Vs\ (new\ C)\ e'\ xs
bisim\ Vs\ (newA\ T[e])\ e'\ xs
bisim\ Vs\ (Cast\ T\ e)\ e'\ xs
bisim\ Vs\ (e\ instanceof\ T)\ e'\ xs
bisim\ Vs\ (Val\ v)\ e'\ xs
bisim\ Vs\ (Var\ V)\ e'\ xs
bisim\ Vs\ (V:=e)\ e'\ xs
bisim\ Vs\ (Val\ v[i])\ e'\ xs
bisim\ Vs\ (Val\ v[Val\ v' := e])\ e'\ xs
bisim\ Vs\ (Val\ v.compareAndSwap(D.F, Val\ v', e))\ e'\ xs
bisim\ Vs\ (a.length)\ e'\ xs
bisim\ Vs\ (e.F{D})\ e'\ xs
bisim\ Vs\ (sync(o')\ e)\ e'\ xs
bisim\ Vs\ (insync(a)\ e)\ e'\ xs
bisim\ Vs\ (e;;e')\ e''\ xs

bisim Vs (*if* (b) $e1$ *else* $e2$) e' xs
bisim Vs (*while* (b) e) e' xs
bisim Vs (*throw* e) e' xs
bisim Vs (*try* e *catch*(C V) e') e'' xs
bisim Vs e' (*new* C) xs
bisim Vs e' (*newA* $T[e]$) xs
bisim Vs e' (*Cast* T e) xs
bisim Vs e' (*e instanceof* T) xs
bisim Vs e' (*Val* v) xs
bisim Vs e' (*Var* V) xs
bisim Vs e' (*V:=e*) xs
bisim Vs e' (*Val* $v[i]$) xs
bisim Vs e' (*Val* $v[Val\ v^\wedge := e]$) xs
bisim Vs e' (*Val* v *compareAndSwap*($D \cdot F$, *Val* v' , e)) xs
bisim Vs e' (*a.length*) xs
bisim Vs e' (*e.F{D}*) xs
bisim Vs e' (*sync* V (o') e) xs
bisim Vs e' (*insync* V (a) e) xs
bisim Vs e'' ($e;;e^\wedge$) xs
bisim Vs e' (*if* (b) $e1$ *else* $e2$) xs
bisim Vs e' (*while* (b) e) xs
bisim Vs e' (*throw* e) xs
bisim Vs e'' (*try* e *catch*(C V) e') xs

inductive-cases *bisim-cases* [*elim*]:

bisim Vs ($e1$ «*bop*» $e2$) e' xs
bisim Vs ($a[i]$) e' xs
bisim Vs ($a[i]:=e$) e' xs
bisim Vs ($e \cdot F\{D\}:=e'$) e'' xs
bisim Vs ($e \cdot compareAndSwap(D \cdot F, e', e'')$) e''' xs
bisim Vs ($e \cdot M(es)$) e' xs
bisim Vs { $V:T=vo$; e } e' xs
bisim Vs e' ($e1$ «*bop*» $e2$) xs
bisim Vs e' ($a[i]$) xs
bisim Vs e' ($a[i]:=e$) xs
bisim Vs e'' ($e \cdot F\{D\}:=e'$) xs
bisim Vs e''' ($e \cdot compareAndSwap(D \cdot F, e', e'')$) xs
bisim Vs e' ($e \cdot M(es)$) xs
bisim Vs e' { $V:T=vo$; e } xs

inductive-cases *bisims-safe-cases* [*elim*]:

bisims Vs [] es xs
bisims Vs es [] xs

inductive-cases *bisims-cases* [*elim*]:

bisims Vs ($e \# es$) es' xs
bisims Vs es' ($e \# es$) xs

Delay bisimulation for call stacks

inductive *bisim01* :: '*addr expr* \Rightarrow '*addr expr1* \times '*addr locals1* \Rightarrow *bool*

where

[[*bisim* [] e e' xs ; *fv* $e = \{\}$; \mathcal{D} $e \in \{\}$]; *max-vars* $e' \leq$ *length* xs ; *call* $e = [aMvs]$; *call1* $e' = [aMvs]$]
 \Longrightarrow *bisim01* e (e' , xs)

inductive *bisim-list* :: 'addr expr list \Rightarrow ('addr expr1 \times 'addr locals1) list \Rightarrow bool

where

bisim-listNil: *bisim-list* [] []
| *bisim-listCons*:
[[*bisim-list* es *exs'*; *bisim* [] e e' *xs*;
fv e = {}; \mathcal{D} e [{}];
max-vars e' \leq length *xs*;
call e = [aMvs]; call1 e' = [aMvs]]]
 \Rightarrow *bisim-list* (e # es) ((e', *xs*) # *exs'*)

inductive-cases *bisim-list-cases* [elim!]:

bisim-list [] *exs'*
bisim-list (ex # *exs*) *exs'*
bisim-list *exs* (ex' # *exs'*)

fun *bisim-list1* ::

'addr expr \times 'addr expr list \Rightarrow ('addr expr1 \times 'addr locals1) \times ('addr expr1 \times 'addr locals1) list \Rightarrow bool

where

bisim-list1 (e, es) ((e1, *xs1*), *exs1*) \longleftrightarrow
bisim-list es *exs1* \wedge *bisim* [] e e1 *xs1* \wedge fv e = {} \wedge \mathcal{D} e [{}]
 \wedge max-vars e1 \leq length *xs1*

definition *bisim-red0-Red1* ::

(('addr expr \times 'addr expr list) \times 'heap)
 \Rightarrow ((('addr expr1 \times 'addr locals1) \times ('addr expr1 \times 'addr locals1) list) \times 'heap) \Rightarrow bool

where *bisim-red0-Red1* \equiv (λ (es, h) (exs, h'). *bisim-list1* es *exs* \wedge h = h')

abbreviation *ta-bisim01* ::

('addr, 'thread-id, 'heap) J0-thread-action \Rightarrow ('addr, 'thread-id, 'heap) J1-thread-action \Rightarrow bool

where

ta-bisim01 \equiv *ta-bisim* (λ t. *bisim-red0-Red1*)

definition *bisim-wait01* ::

('addr expr \times 'addr expr list) \Rightarrow ('addr expr1 \times 'addr locals1) \times ('addr expr1 \times 'addr locals1) list \Rightarrow bool

where *bisim-wait01* \equiv λ (e0, es0) ((e1, *xs1*), *exs1*). call e0 \neq None \wedge call1 e1 \neq None

lemma *bisim-list1I*[intro?]:

[[*bisim-list* es *exs1*; *bisim* [] e e1 *xs1*; fv e = {};
 \mathcal{D} e [{}]; max-vars e1 \leq length *xs1*]]
 \Rightarrow *bisim-list1* (e, es) ((e1, *xs1*), *exs1*)

by *simp*

lemma *bisim-list1E*[elim?]:

assumes *bisim-list1* (e, es) ((e1, *xs1*), *exs1*)
obtains *bisim-list* es *exs1* *bisim* [] e e1 *xs1* fv e = {} \mathcal{D} e [{}]
max-vars e1 \leq length *xs1*

using *assms* **by** *auto*

lemma *bisim-list1-elim*:

assumes *bisim-list1* es' *exs*

obtains e es e1 *xs1* *exs1*

where es' = (e, es) *exs* = ((e1, *xs1*), *exs1*)

and *bisim-list* es *exs1* *bisim* [] e e1 *xs1* fv e = {} \mathcal{D} e [{}]
max-vars e1 \leq length *xs1*

using *assms* **by**(cases es')(cases *exs*, *fastforce*)

declare *bisim-list1.simps* [*simp del*]

lemma *bisims-map-Val-conv* [*simp*]: *bisims Vs (map Val vs) es xs = (es = map Val vs)*
apply(*induct vs arbitrary: es*)
apply(*fastforce*)
apply(*simp*)
apply(*rule iffI*)
apply(*erule bisims-cases, auto*)
done

declare *compEs1-conv-map* [*simp del*]

lemma *bisim-contains-insync*: *bisim Vs e e' xs \implies contains-insync e = contains-insync e'*
and *bisims-contains-insyncs*: *bisims Vs es es' xs \implies contains-insyncs es = contains-insyncs es'*
by(*induct rule: bisim-bisims.inducts*)(*auto*)

lemma *bisims-map-Val-Throw*:

bisims Vs (map Val vs @ Throw a # es) es' xs \longleftrightarrow es' = map Val vs @ Throw a # compEs1 Vs es
 $\wedge \neg$ *contains-insyncs es*
apply(*induct vs arbitrary: es'*)
apply(*simp*)
apply(*fastforce simp add: compEs1-conv-map*)
apply(*fastforce elim!: bisims-cases intro: bisimsCons2*)
done

lemma *compE1-bisim* [*intro*]: \llbracket *fv e \subseteq set Vs; \neg contains-insync e* $\rrbracket \implies$ *bisim Vs e (compE1 Vs e) xs*
and *compEs1-bisims* [*intro*]: \llbracket *fv es \subseteq set Vs; \neg contains-insyncs es* $\rrbracket \implies$ *bisims Vs es (compEs1 Vs es) xs*

proof(*induct Vs e and Vs es arbitrary: xs and xs rule: compE1-compEs1-induct*)

case (*BinOp Vs exp1 bop exp2 x*)
thus *?case by(cases is-val exp1)(auto)*
next
case (*AAcc Vs exp1 exp2 x*)
thus *?case by(cases is-val exp1)(auto)*
next
case (*AAss Vs exp1 exp2 exp3 x*)
thus *?case by(cases is-val exp1, cases is-val exp2, fastforce+)*
next
case (*FAss Vs exp1 F D exp2 x*)
thus *?case by(cases is-val exp1, auto)*
next
case (*CAS Vs e1 D F e2 e3 x*)
thus *?case by(cases is-val e1, cases is-val e2, fastforce+)*
next
case (*Call Vs obj M params x*)
thus *?case by(cases is-val obj)(auto)*
next
case (*Block Vs V T vo exp xs*)
from \langle *fv {V:T=vo; exp} \subseteq set Vs* \rangle **have** *fv exp \subseteq set (Vs@[V])* **by**(*auto*)
with *Block show ?case by(cases vo)(auto)*
next
case (*Cons Vs exp list x*)

thus ?case **by**(cases is-val exp)(auto intro!: bisimsCons2)
qed(auto)

lemma bisim-hidden-unmod: $\llbracket \text{bisim } Vs \ e \ e' \ xs; \text{hidden } Vs \ i \rrbracket \implies \text{unmod } e' \ i$
and bisims-hidden-unmods: $\llbracket \text{bisims } Vs \ es \ es' \ xs; \text{hidden } Vs \ i \rrbracket \implies \text{unmods } es' \ i$
by(induct rule: bisim-bisims.inducts)(auto intro: hidden-unmod hidden-unmods dest: hidden-inacc hidden-lengthD)

lemma bisim-fv-unmod: $\llbracket \text{bisim } Vs \ e \ e' \ xs; \ i < \text{length } Vs; \ Vs \ ! \ i \notin \text{fv } e \rrbracket \implies \text{unmod } e' \ i$
and bisims-fvs-unmods: $\llbracket \text{bisims } Vs \ es \ es' \ xs; \ i < \text{length } Vs; \ Vs \ ! \ i \notin \text{fvs } es \rrbracket \implies \text{unmods } es' \ i$

proof(induct rule: bisim-bisims.inducts)

case (bisimBlockNone Vs V e e' xs T)

note len = $\langle i < \text{length } Vs \rangle$

have unmod e' i

proof(cases Vs ! i = V)

case True

from len **have** hidden (Vs @ [Vs ! i]) i **by**(rule hidden-snoc-nth)

with len True $\langle \text{bisim } (Vs \ @ \ [V]) \ e \ e' \ xs \rangle$ **show** ?thesis **by**(auto intro: bisim-hidden-unmod)

next

case False

with bisimBlockNone **show** ?thesis **by**(auto simp add: nth-append)

qed

thus ?case **by** simp

next

case (bisimBlockSome Vs V e e' xs v T)

note len = $\langle i < \text{length } Vs \rangle$

show ?case

proof(cases Vs ! i = V)

case True

from len **have** hidden (Vs @ [Vs ! i]) i **by**(rule hidden-snoc-nth)

with len True $\langle \text{bisim } (Vs \ @ \ [V]) \ e \ e' \ (xs[\text{length } Vs := v]) \rangle$

show ?thesis **by**(auto intro: bisim-hidden-unmod)

next

case False

with bisimBlockSome **show** ?thesis **by**(auto simp add: nth-append)

qed

next

case (bisimBlockSomeNone Vs V e e' xs v T)

note len = $\langle i < \text{length } Vs \rangle$

show ?case

proof(cases Vs ! i = V)

case True

from len **have** hidden (Vs @ [Vs ! i]) i **by**(rule hidden-snoc-nth)

with len True $\langle \text{bisim } (Vs \ @ \ [V]) \ e \ e' \ xs \rangle$

show ?thesis **by**(auto intro: bisim-hidden-unmod)

next

case False

with bisimBlockSomeNone **show** ?thesis **by**(auto simp add: nth-append)

qed

qed(fastforce dest: fv-unmod-compE1 fvs-unmods-compEs1 index-le-lengthD simp add: nth-append)+

lemma bisim-extRet2J [intro!]: $\text{bisim } Vs \ e \ e' \ xs \implies \text{bisim } Vs \ (\text{extRet2J } e \ va) \ (\text{extRet2J1 } e' \ va) \ xs$
by(cases va) auto

lemma *bisims-map-Val-conv2* [*simp*]: *bisims Vs es (map Val vs) xs = (es = map Val vs)*
apply(*induct vs arbitrary: es*)
apply(*fastforce elim!: bisims-cases*)
done

lemma *bisims-map-Val-Throw2*:
bisims Vs es' (map Val vs @ Throw a # es) xs \longleftrightarrow
($\exists es''$. $es' = map Val vs @ Throw a # es'' \wedge es = compEs1 Vs es'' \wedge \neg contains-insyncs es''$)
apply(*induct vs arbitrary: es'*)
apply(*simp*)
apply(*fastforce simp add: compEs1-conv-map*)
apply(*fastforce elim!: bisims-cases*)
done

lemma *hidden-bisim-unmod*: $\llbracket bisim Vs e e' xs; hidden Vs i \rrbracket \implies unmod e' i$
and *hidden-bisims-unmods*: $\llbracket bisims Vs es es' xs; hidden Vs i \rrbracket \implies unmods es' i$
apply(*induct rule: bisim-bisims.inducts*)
apply(*auto simp add: hidden-inacc intro: hidden-unmod hidden-unmods*)
apply(*auto simp add: hidden-def*)
done

lemma *bisim-list-list-all2-conv*:
bisim-list es exs' \longleftrightarrow list-all2 bisim01 es exs'
proof
assume *bisim-list es exs'*
thus *list-all2 bisim01 es exs'*
by *induct(auto intro!: bisim01.intros)*
next
assume *list-all2 bisim01 es exs'*
thus *bisim-list es exs'*
by(*induct es arbitrary: exs'*)(*auto intro!: bisim-listCons bisim-listNil elim!: bisim01.cases simp add: list-all2-Cons1*)
qed

lemma *bisim-list-extTA2J0-extTA2J1*:
assumes *wf: wf-J-prog P*
and *sees: P \vdash C sees M:[] \rightarrow T = [meth] in D*
shows *bisim-list1 (extNTA2J0 P (C, M, a)) (extNTA2J1 (compP1 P) (C, M, a))*
proof –
obtain *pns body where meth = (pns, body) by(cases meth)*
with *sees have sees: P \vdash C sees M:[] \rightarrow T = [(pns, body)] in D by simp*
moreover **let** *?xs = Addr a # replicate (max-vars body) undefined-value*
let *?e' = {0:Class D=None; compE1 (this # pns) body}*
have *bisim-list1 ({this:Class D=[Addr a]; body}, []) ((?e', ?xs), [])*
proof
show *bisim-list [] [] ..*
from *sees-wf-mdecl[OF wf-prog-wwf-prog[OF wf] sees] have fv body \subseteq set [this] pns = []*
by(*auto simp add: wf-mdecl-def*)
thus *fv ({this:Class D=[Addr a]; body}) = {} by simp*
from *sees-wf-mdecl[OF wf sees] obtain T' where P,[this \mapsto Class D] \vdash body :: T' this \notin set pns*
and *D body [dom [this \mapsto Addr a]] by(auto simp add: wf-mdecl-def)*
hence $\neg contains-insync body$ **by**(*auto simp add: contains-insync-conv dest: WT-expr-locks*)
with $\langle fv body \subseteq set [this] \rangle$
have *bisim ([@ [this]] body (compE1 (this # pns) body) ?xs*

unfolding *append.simps* $\langle pns = [] \rangle$ **by**(*rule compE1-bisim*)
hence *bisim* $[]$ $\{this:Class D=[Addr a]; body\}$ $\{length ([] :: String.literal list):Class D=None;$
*compE1 (this # pns) body\} $?xs$
by(*rule bisimBlockSomeNone*)(*simp*)
thus *bisim* $[]$ $(\{this:Class D=[Addr a]; body\})$ $?e' ?xs$ **by** *simp*
from $\langle \mathcal{D} body [dom [this \mapsto Addr a]] \rangle$ **show** \mathcal{D} $(\{this:Class D=[Addr a]; body\})$ $[\{ \}]$ **by** *simp*
show *max-vars* $?e' \leq length ?xs$ **by** *simp*
qed
ultimately show *?thesis* **by**(*simp*)
qed*

lemma *bisim-max-vars*: *bisim* $Vs e e' xs \implies max\text{-vars } e = max\text{-vars } e'$
and *bisims-max-varss*: *bisims* $Vs es es' xs \implies max\text{-varss } es = max\text{-varss } es'$
apply(*induct rule: bisim-bisims.inducts*)
apply(*auto simp add: max-vars-compE1 max-varss-compEs1*)
done

lemma *bisim-call*: *bisim* $Vs e e' xs \implies call e = call e'$
and *bisims-calls*: *bisims* $Vs es es' xs \implies calls es = calls es'$
apply(*induct rule: bisim-bisims.inducts*)
apply(*auto simp add: is-vals-conv*)
done

lemma *bisim-call-None-call1*: $\llbracket bisim Vs e e' xs; call e = None \rrbracket \implies call1 e' = None$
and *bisims-calls-None-calls1*: $\llbracket bisims Vs es es' xs; calls es = None \rrbracket \implies calls1 es' = None$
by(*induct rule: bisim-bisims.inducts*)(*auto simp add: is-vals-conv split: if-split-asm*)

lemma *bisim-call1-Some-call*:
 $\llbracket bisim Vs e e' xs; call1 e' = [aMvs] \rrbracket \implies call e = [aMvs]$
and *bisims-calls1-Some-calls*:
 $\llbracket bisims Vs es es' xs; calls1 es' = [aMvs] \rrbracket \implies calls es = [aMvs]$
by(*induct rule: bisim-bisims.inducts*)(*auto simp add: is-vals-conv split: if-split-asm*)

lemma *blocks-bisim*:
assumes *bisim*: *bisim* $(Vs @ pns) e e' xs$
and *length*: *length* $vs = length pns$ *length* $Ts = length pns$
and *xs*: $\forall i. i < length vs \longrightarrow xs ! (i + length Vs) = vs ! i$
shows *bisim* $Vs (blocks pns Ts vs e) (blocks1 (length Vs) Ts e') xs$
using *bisim length xs*
proof(*induct pns Ts vs e arbitrary: e' Vs rule: blocks.induct*)
case $(1 V Vs T Ts v vs e e' VS)$
note $IH = \langle \bigwedge e' Vsa. \llbracket bisim (Vsa @ Vs) e e' xs; \mathit{length } vs = \mathit{length } Vs; \mathit{length } Ts = \mathit{length } Vs; \forall i < \mathit{length } vs. xs ! (i + \mathit{length } Vsa) = vs ! i \rrbracket$
 $\implies bisim Vsa (blocks Vs Ts vs e) (blocks1 (length Vsa) Ts e') xs \rangle$
note $xs = \langle \forall i < \mathit{length } (v \# vs). xs ! (i + \mathit{length } VS) = (v \# vs) ! i \rangle$
hence $xs': \forall i < \mathit{length } vs. xs ! (i + \mathit{length } (VS @ [V])) = vs ! i$ **and** $v: xs ! \mathit{length } VS = v$ **by**(*auto*)
from $\langle bisim (VS @ V \# Vs) e e' xs \rangle$ **have** *bisim* $((VS @ [V]) @ Vs) e e' xs$ **by** *simp*
from $IH[OF this - - xs']$ $\langle \mathit{length } (v \# vs) = \mathit{length } (V \# Vs) \rangle$ $\langle \mathit{length } (T \# Ts) = \mathit{length } (V \# Vs) \rangle$
have *bisim* $(VS @ [V]) (blocks Vs Ts vs e) (blocks1 (length (VS @ [V])) Ts e') xs$
by *auto*
hence *bisim* $VS (\{V:T=[v]; blocks Vs Ts vs e\}) \{length VS:T=None; blocks1 (length (VS @ [V]))$

```

Ts e'} xs
  using v by(rule bisimBlockSomeNone)
  thus ?case by simp
qed(auto)

lemma fixes e :: ('a,'b,'addr) exp and es :: ('a,'b,'addr) exp list
  shows inline-call-max-vars: call e = [aMvs]  $\implies$  max-vars (inline-call e' e)  $\leq$  max-vars e + max-vars e'
  and inline-calls-max-varss: calls es = [aMvs]  $\implies$  max-varss (inline-calls e' es)  $\leq$  max-varss es + max-vars e'
  by(induct e and es rule: call.induct calls.induct)(auto)

```

```

lemma assumes final E bisim VS E E' xs
  shows inline-call-compE1: call e = [aMvs]  $\implies$  inline-call E' (compE1 Vs e) = compE1 Vs (inline-call E e)
  and inline-calls-compEs1: calls es = [aMvs]  $\implies$  inline-calls E' (compEs1 Vs es) = compEs1 Vs (inline-calls E es)
proof(induct Vs e and Vs es rule: compE1-compEs1-induct)
  case (Call Vs obj M params)
  note IHobj =  $\langle$ call obj = [aMvs]  $\implies$  inline-call E' (compE1 Vs obj) = compE1 Vs (inline-call E obj) $\rangle$ 
  note IHparams =  $\langle$ calls params = [aMvs]  $\implies$  inline-calls E' (compEs1 Vs params) = compEs1 Vs (inline-calls E params) $\rangle$ 
  obtain a M' vs where [simp]: aMvs = (a, M', vs) by (cases aMvs, auto)
  with  $\langle$ call (obj.M(params)) = [aMvs] $\rangle$  have call (obj.M(params)) = [(a, M', vs)] by simp
  thus ?case
proof(induct rule: call-callE)
  case CallObj
  with IHobj have inline-call E' (compE1 Vs obj) = compE1 Vs (inline-call E obj) by auto
  with CallObj show ?case by auto
next
  case (CallParams v)
  with IHparams have inline-calls E' (compEs1 Vs params) = compEs1 Vs (inline-calls E params)
by auto
  with CallParams show ?case by(auto simp add: is-vals-conv)
next
  case Call
  with  $\langle$ final E $\rangle$   $\langle$ bisim VS E E' xs $\rangle$  show ?case by(auto simp add: is-vals-conv)
qed
qed(auto split: if-split-asm)

```

```

lemma assumes bisim: bisim VS E E' XS
  and final: final E
  shows bisim-inline-call:
  [[ bisim Vs e e' xs; call e = [aMvs]; fv e  $\subseteq$  set Vs ]
 $\implies$  bisim Vs (inline-call E e) (inline-call E' e') xs

  and bisims-inline-calls:
  [[ bisims Vs es es' xs; calls es = [aMvs]; fvs es  $\subseteq$  set Vs ]
 $\implies$  bisims Vs (inline-calls E es) (inline-calls E' es') xs
proof(induct rule: bisim-bisims.inducts)
  case (bisimBinOp1 Vs e e' xs bop e'')
  thus ?case by(cases is-val (inline-call E e))(fastforce)+
next

```

```

  case (bisimAAcc1 Vs a a' xs i)
  thus ?case by(cases is-val (inline-call E a))(fastforce)+
next
  case (bisimAAss1 Vs a a' xs i e)
  thus ?case by(cases is-val (inline-call E a), cases is-val i)(fastforce)+
next
  case (bisimAAss2 Vs i i' xs a e)
  thus ?case by(cases is-val (inline-call E i))(fastforce)+
next
  case (bisimFAss1 Vs e e' xs F D e'')
  thus ?case by(cases is-val (inline-call E e))(fastforce)+
next
  case (bisimCAS1 Vs e e' xs e2 e3 D F)
  thus ?case
    apply(cases is-val (inline-call E e))
    apply(cases is-val e2)
    apply(fastforce)
    apply clarsimp
    apply(safe; clarsimp?)
    apply auto
  done
next
  case (bisimCAS2 Vs e e' xs e3 v D F)
  thus ?case by(cases is-val (inline-call E e); safe?; clarsimp; fastforce)
next
  case (bisimCallObj Vs e e' xs es M)
  obtain a M' vs where aMvs = (a, M', vs) by(cases aMvs, auto)
  with ⟨call (e.M(es)) = [aMvs]⟩ have call (e.M(es)) = [(a, M', vs)] by simp
  thus ?case
  proof(induct rule: call-callE)
    case CallObj
    with ⟨fv (e.M(es)) ⊆ set Vs⟩ ⟨aMvs = (a, M', vs)⟩
      ⟨[call e = [aMvs]; fv e ⊆ set Vs] ⟹ bisim Vs (inline-call E e) (inline-call E' e') xs⟩
    have IH': bisim Vs (inline-call E e) (inline-call E' e') xs by(auto)
    with ⟨bisim Vs e e' xs⟩ ⟨fv (e.M(es)) ⊆ set Vs⟩ CallObj ⟨¬ contains-insyncs es⟩ show ?thesis
      by(cases is-val (inline-call E e))(fastforce)+
  next
    case (CallParams v)
    hence inline-calls E' (compEs1 Vs es) = compEs1 Vs (inline-calls E es)
      by -(rule inline-calls-compEs1[OF final bisim])
    moreover from ⟨fv (e.M(es)) ⊆ set Vs⟩ final fvs-inline-calls[of E es]
    have fvs (inline-calls E es) ⊆ set Vs by(auto elim!: final.cases)
    moreover note CallParams ⟨bisim Vs e e' xs⟩ ⟨fv (e.M(es)) ⊆ set Vs⟩ ⟨¬ contains-insyncs es⟩
  final
    ultimately show ?case by(auto simp add: is-vals-conv final-iff)
  next
    case Call
    with final bisim ⟨bisim Vs e e' xs⟩ show ?case by(auto simp add: is-vals-conv)
  qed
next
  case (bisimCallParams Vs es es' xs v M)
  obtain a M' vs where [simp]: aMvs = (a, M', vs) by(cases aMvs, auto)
  with ⟨call (Val v.M(es)) = [aMvs]⟩ have call (Val v.M(es)) = [(a, M', vs)] by simp
  thus ?case

```

```

proof(induct rule: call-callE)
  case CallObj thus ?case by simp
next
  case (CallParams v')
  with ⟨ [calls es = [aMvs]; fvs es ⊆ set Vs] ⇒ bisims Vs (inline-calls E es) (inline-calls E' es')
xs ⟨fv (Val v·M(es)) ⊆ set Vs⟩
  have bisims Vs (inline-calls E es) (inline-calls E' es') xs by(auto)
  with final bisim ⟨bisims Vs es es' xs⟩ show ?case by(auto simp add: is-vals-conv)
next
  case Call
  with final bisim ⟨bisims Vs es es' xs⟩ show ?case by(auto)
qed
next
  case (bisimsCons1 Vs e e' xs es)
  thus ?case by(cases is-val (inline-call E e))(fastforce)+
qed(fastforce)+

declare hyperUn-ac [simp del]

lemma sqInt-lem3: [ A ⊆ A'; B ⊆ B' ] ⇒ A ∩ B ⊆ A' ∩ B'
by(auto simp add: hyperset-defs)

lemma sqUn-lem3: [ A ⊆ A'; B ⊆ B' ] ⇒ A ∪ B ⊆ A' ∪ B'
by(auto simp add: hyperset-defs)

lemma A-inline-call: call e = [aMvs] ⇒ A e ⊆ A (inline-call e' e)
  and As-inline-calls: calls es = [aMvs] ⇒ As es ⊆ As (inline-calls e' es)
proof(induct e and es rule: call.induct calls.induct)
  case (Call obj M params)
  obtain a M' vs where [simp]: aMvs = (a, M', vs) by(cases aMvs, auto)
  with ⟨call (obj·M(params)) = [aMvs]⟩ have call (obj·M(params)) = [(a, M', vs)] by simp
  thus ?case
  proof(induct rule: call-callE)
    case CallObj
    with ⟨call obj = [aMvs] ⇒ A obj ⊆ A (inline-call e' obj)⟩
    show ?case by(auto intro: sqUn-lem)
  next
    case CallParams
    with ⟨calls params = [aMvs] ⇒ As params ⊆ As (inline-calls e' params)⟩
    show ?case by(auto intro: sqUn-lem)
  next
    case Call
    thus ?case by(auto simp add: hyperset-defs)
  qed
next
  case Block thus ?case by(fastforce intro: diff-lem)
next
  case throw thus ?case by(simp add: hyperset-defs)
next
  case TryCatch thus ?case by(auto intro: sqInt-lem)
qed(fastforce intro: sqUn-lem sqUn-lem2)+

lemma assumes final e'
  shows defass-inline-call: [ call e = [aMvs]; D e A ] ⇒ D (inline-call e' e) A

```

and defass-inline-calls: $\llbracket \text{calls } es = \lfloor aMvs \rfloor; \mathcal{D}s \text{ es } A \rrbracket \implies \mathcal{D}s (\text{inline-calls } e' \text{ es}) A$
proof(*induct e and es arbitrary: A and A rule: call.induct calls.induct*)
case (*Call obj M params A*)
obtain $a \ M' \ vs$ **where** [*simp*]: $aMvs = (a, M', vs)$ **by**(*cases aMvs, auto*)
with $\langle \text{call } (obj \cdot M(\text{params})) = \lfloor aMvs \rfloor \rangle$ **have** $\text{call } (obj \cdot M(\text{params})) = \lfloor (a, M', vs) \rfloor$ **by** *simp*
thus *?case*
proof(*cases rule: call-callE*)
case *CallObj*
with $\langle \mathcal{D} (obj \cdot M(\text{params})) A \rangle \langle \llbracket \text{call } obj = \lfloor aMvs \rfloor; \mathcal{D} \text{ obj } A \rrbracket \implies \mathcal{D} (\text{inline-call } e' \text{ obj}) A \rangle$
have $\mathcal{D} (\text{inline-call } e' \text{ obj}) A$ **by** *simp*
moreover from *A-inline-call[OF CallObj, of e']*
have $A \sqcup (\mathcal{A} \text{ obj}) \sqsubseteq A \sqcup (\mathcal{A} (\text{inline-call } e' \text{ obj}))$ **by**(*rule sqUn-lem2*)
with $\langle \mathcal{D} (obj \cdot M(\text{params})) A \rangle$ **have** $\mathcal{D}s \text{ params } (A \sqcup \mathcal{A} (\text{inline-call } e' \text{ obj}))$ **by**(*auto elim: Ds-mono'*)
ultimately show *?thesis using CallObj by auto*
next
case (*CallParams v*)
with $\langle \mathcal{D} (obj \cdot M(\text{params})) A \rangle \langle \llbracket \text{calls } \text{params} = \lfloor aMvs \rfloor; \mathcal{D}s \text{ params } A \rrbracket \implies \mathcal{D}s (\text{inline-calls } e' \text{ params}) A \rangle$
have $\mathcal{D}s (\text{inline-calls } e' \text{ params}) A$ **by**(*simp*)
with *CallParams* **show** *?thesis by(auto)*
next
case *Call*
with $\langle \text{final } e' \rangle$ **show** *?thesis by(auto elim!: D-mono' simp add: hyperset-defs)*
qed
next
case (*Cons-exp exp exps A*)
show *?case*
proof(*cases is-val exp*)
case *True*
with $\langle \mathcal{D}s (exp \# \text{exps}) A \rangle \langle \llbracket \text{calls } \text{exps} = \lfloor aMvs \rfloor; \mathcal{D}s \text{ exps } A \rrbracket \implies \mathcal{D}s (\text{inline-calls } e' \text{ exps}) A \rangle$
 $\langle \text{calls } (exp \# \text{exps}) = \lfloor aMvs \rfloor \rangle$
have $\mathcal{D}s (\text{inline-calls } e' \text{ exps}) A$ **by**(*auto*)
with *True* **show** *?thesis by(auto)*
next
case *False*
with $\langle \llbracket \text{call } exp = \lfloor aMvs \rfloor; \mathcal{D} \text{ exp } A \rrbracket \implies \mathcal{D} (\text{inline-call } e' \text{ exp}) A \rangle \langle \text{calls } (exp \# \text{exps}) = \lfloor aMvs \rfloor \rangle$
 $\langle \mathcal{D}s (exp \# \text{exps}) A \rangle$
have $\mathcal{D} (\text{inline-call } e' \text{ exp}) A$ **by** *auto*
moreover from *False* $\langle \text{calls } (exp \# \text{exps}) = \lfloor aMvs \rfloor \rangle$ **have** $\mathcal{A} \text{ exp} \sqsubseteq \mathcal{A} (\text{inline-call } e' \text{ exp})$
by(*auto intro: A-inline-call*)
hence $A \sqcup \mathcal{A} \text{ exp} \sqsubseteq A \sqcup \mathcal{A} (\text{inline-call } e' \text{ exp})$ **by**(*rule sqUn-lem2*)
with $\langle \mathcal{D}s (exp \# \text{exps}) A \rangle$ **have** $\mathcal{D}s \text{ exps } (A \sqcup \mathcal{A} (\text{inline-call } e' \text{ exp}))$
by(*auto intro: Ds-mono'*)
ultimately show *?thesis using False by(auto)*
qed
qed(*fastforce split: if-split-asm elim: D-mono' intro: sqUn-lem2 sqUn-lem A-inline-call*)

lemma *bisim-B:* $\text{bisim } Vs \ e \ E \ xs \implies \mathcal{B} \ E \ (\text{length } Vs)$
and *bisims-Bs:* $\text{bisims } Vs \ es \ Es \ xs \implies \mathcal{B}s \ Es \ (\text{length } Vs)$
apply(*induct rule: bisim-bisims.inducts*)
apply(*auto intro: B Bs*)
done

lemma *bisim-expr-locks-eq:* $\text{bisim } Vs \ e \ e' \ xs \implies \text{expr-locks } e = \text{expr-locks } e'$

and *bisims-expr-lockss-eq*: $\text{bisims } Vs \text{ es es' } xs \implies \text{expr-lockss es} = \text{expr-lockss es'}$
by(*induct rule*: *bisim-bisims.inducts*)(*auto intro!*: *ext*)

lemma *bisim-list-expr-lockss-eq*: $\text{bisim-list es es' } \implies \text{expr-lockss es} = \text{expr-lockss (map fst es')}$
apply(*induct rule*: *bisim-list.induct*)
apply(*auto dest*: *bisim-expr-lockss-eq*)
done

context *J1-heap-base* **begin**

lemma [*simp*]:
fixes $e :: ('a, 'b, 'addr) \text{ exp}$ **and** $es :: ('a, 'b, 'addr) \text{ exp list}$
shows $\tau \text{move1-compP}: \tau \text{move1 (compP f P) h e} = \tau \text{move1 P h e}$
and $\tau \text{moves1-compP}: \tau \text{moves1 (compP f P) h es} = \tau \text{moves1 P h es}$
by(*induct e and es rule*: $\tau \text{move1.induct } \tau \text{moves1.induct}$) *auto*

lemma $\tau \text{Move1-compP}$ [*simp*]: $\tau \text{Move1 (compP f P)} = \tau \text{Move1 P}$
by(*intro ext*) *auto*

lemma *red1-preserves-unmod*:
 $\llbracket \text{uf}, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{unmod } e \text{ } i \rrbracket \implies (\text{lcl } s') ! i = (\text{lcl } s) ! i$
and *reds1-preserves-unmod*:
 $\llbracket \text{uf}, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{unmods } es \text{ } i \rrbracket \implies (\text{lcl } s') ! i = (\text{lcl } s) ! i$
apply(*induct rule*: *red1-reds1.inducts*)
apply(*auto split*: *if-split-asm*)
done

lemma *red1-unmod-preserved*:
 $\llbracket \text{uf}, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{unmod } e \text{ } i \rrbracket \implies \text{unmod } e' \text{ } i$
and *reds1-unmods-preserved*:
 $\llbracket \text{uf}, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{unmods } es \text{ } i \rrbracket \implies \text{unmods } es' \text{ } i$
by(*induct rule*: *red1-reds1.inducts*)(*auto split*: *if-split-asm*)

lemma $\tau \text{red1t-unmod-preserved}$:
 $\llbracket \tau \text{red1gt uf P t h (e, xs) (e', xs') ; \text{unmod } e \text{ } i \rrbracket \implies \text{unmod } e' \text{ } i$
by(*induct rule*: *tranclp-induct2*)(*auto intro*: *red1-unmod-preserved*)

lemma $\tau \text{red1r-unmod-preserved}$:
 $\llbracket \tau \text{red1gr uf P t h (e, xs) (e', xs') ; \text{unmod } e \text{ } i \rrbracket \implies \text{unmod } e' \text{ } i$
by(*induct rule*: *rtranclp-induct2*)(*auto intro*: *red1-unmod-preserved*)

lemma $\tau \text{red1t-preserves-unmod}$:
 $\llbracket \tau \text{red1gt uf P t h (e, xs) (e', xs') ; \text{unmod } e \text{ } i ; i < \text{length } xs \rrbracket$
 $\implies xs' ! i = xs ! i$
apply(*induct rule*: *tranclp-induct2*)
apply(*auto dest*: *red1-preserves-unmod*)
apply(*drule* *red1-preserves-unmod*)
apply(*erule* (1) $\tau \text{red1t-unmod-preserved}$)
apply(*drule* $\tau \text{red1t-preserves-len}$)
apply *auto*
done

lemma $\tau \text{red1}'r\text{-preserves-unmod}$:

```

  [τred1gr uf P t h (e, xs) (e', xs'); unmod e i; i < length xs ]
  ⇒ xs' ! i = xs ! i
apply(induct rule: converse-rtranclp-induct2)
apply(auto dest: red1-preserved-unmod red1-unmod-preserved red1-preserved-len)
apply(frule (1) red1-unmod-preserved)
apply(frule red1-preserved-len)
apply(frule (1) red1-preserved-unmod)
apply auto
done

end

context J-heap-base begin

lemma [simp]:
  fixes e :: ('a, 'b, 'addr) exp and es :: ('a, 'b, 'addr) exp list
  shows τmove0-compP: τmove0 (compP f P) h e = τmove0 P h e
  and τmoves0-compP: τmoves0 (compP f P) h es = τmoves0 P h es
by(induct e and es rule: τmove0.induct τmoves0.induct) auto

lemma τMove0-compP [simp]: τMove0 (compP f P) = τMove0 P
by(intro ext) auto

end

end

```

7.23 Unlocking a sync block never fails

theory Correctness1Threaded imports

J0J1Bisim

../Framework/FWInitFinLift

begin

definition lock-oks1 ::

('addr, 'thread-id) locks

\Rightarrow *('addr, 'thread-id, (('a, 'b, 'addr) exp \times 'c) \times (('a, 'b, 'addr) exp \times 'c) list) thread-info \Rightarrow bool*

where

\bigwedge *ln. lock-oks1 ls ts \equiv $\forall t. (case (ts t) of None \Rightarrow ($\forall l. has-locks (ls \$ l) t = 0$))$*

$| [((ex, exs), ln)] \Rightarrow (\forall l. has-locks (ls \$ l) t + ln \$ l = expr-lockss (map fst (ex$

$\# exs)) l)$

primrec el-loc-ok :: 'addr expr1 \Rightarrow 'addr locals1 \Rightarrow bool

and els-loc-ok :: 'addr expr1 list \Rightarrow 'addr locals1 \Rightarrow bool

where

el-loc-ok (new C) xs \longleftrightarrow True

$|$ *el-loc-ok (newA T[e]) xs \longleftrightarrow el-loc-ok e xs*

$|$ *el-loc-ok (Cast T e) xs \longleftrightarrow el-loc-ok e xs*

$|$ *el-loc-ok (e instanceof T) xs \longleftrightarrow el-loc-ok e xs*

$|$ *el-loc-ok (e «bop» e') xs \longleftrightarrow el-loc-ok e xs \wedge el-loc-ok e' xs*

$|$ *el-loc-ok (Var V) xs \longleftrightarrow True*

$|$ *el-loc-ok (Val v) xs \longleftrightarrow True*

$|$ *el-loc-ok (V := e) xs \longleftrightarrow el-loc-ok e xs*

$| \text{el-loc-ok } (a[i]) \text{ } xs \longleftrightarrow \text{el-loc-ok } a \text{ } xs \wedge \text{el-loc-ok } i \text{ } xs$
 $| \text{el-loc-ok } (a[i] := e) \text{ } xs \longleftrightarrow \text{el-loc-ok } a \text{ } xs \wedge \text{el-loc-ok } i \text{ } xs \wedge \text{el-loc-ok } e \text{ } xs$
 $| \text{el-loc-ok } (a.\text{length}) \text{ } xs \longleftrightarrow \text{el-loc-ok } a \text{ } xs$
 $| \text{el-loc-ok } (e.F\{D\}) \text{ } xs \longleftrightarrow \text{el-loc-ok } e \text{ } xs$
 $| \text{el-loc-ok } (e.F\{D\} := e') \text{ } xs \longleftrightarrow \text{el-loc-ok } e \text{ } xs \wedge \text{el-loc-ok } e' \text{ } xs$
 $| \text{el-loc-ok } (e.\text{compareAndSwap}(D.F, e', e'')) \text{ } xs \longleftrightarrow \text{el-loc-ok } e \text{ } xs \wedge \text{el-loc-ok } e' \text{ } xs \wedge \text{el-loc-ok } e'' \text{ } xs$
 $| \text{el-loc-ok } (e.M(ps)) \text{ } xs \longleftrightarrow \text{el-loc-ok } e \text{ } xs \wedge \text{els-loc-ok } ps \text{ } xs$
 $| \text{el-loc-ok } \{V:T=vo; e\} \text{ } xs \longleftrightarrow (\text{case } vo \text{ of } \text{None} \Rightarrow \text{el-loc-ok } e \text{ } xs \mid [v] \Rightarrow \text{el-loc-ok } e \text{ } (xs[V := v]))$
 $| \text{el-loc-ok } (\text{sync}_V(e) \text{ } e') \text{ } xs \longleftrightarrow \text{el-loc-ok } e \text{ } xs \wedge \text{el-loc-ok } e' \text{ } xs \wedge \text{unmod } e' \text{ } V$
 $| \text{el-loc-ok } (\text{insync}_V(a) \text{ } e) \text{ } xs \longleftrightarrow xs ! V = \text{Addr } a \wedge \text{el-loc-ok } e \text{ } xs \wedge \text{unmod } e \text{ } V$
 $| \text{el-loc-ok } (e;;e') \text{ } xs \longleftrightarrow \text{el-loc-ok } e \text{ } xs \wedge \text{el-loc-ok } e' \text{ } xs$
 $| \text{el-loc-ok } (\text{if } (b) \text{ } e \text{ } \text{else } e') \text{ } xs \longleftrightarrow \text{el-loc-ok } b \text{ } xs \wedge \text{el-loc-ok } e \text{ } xs \wedge \text{el-loc-ok } e' \text{ } xs$
 $| \text{el-loc-ok } (\text{while } (b) \text{ } c) \text{ } xs \longleftrightarrow \text{el-loc-ok } b \text{ } xs \wedge \text{el-loc-ok } c \text{ } xs$
 $| \text{el-loc-ok } (\text{throw } e) \text{ } xs \longleftrightarrow \text{el-loc-ok } e \text{ } xs$
 $| \text{el-loc-ok } (\text{try } e \text{ } \text{catch}(C \text{ } V) \text{ } e') \text{ } xs \longleftrightarrow \text{el-loc-ok } e \text{ } xs \wedge \text{el-loc-ok } e' \text{ } xs$

$| \text{els-loc-ok } [] \text{ } xs \longleftrightarrow \text{True}$
 $| \text{els-loc-ok } (e \# es) \text{ } xs \longleftrightarrow \text{el-loc-ok } e \text{ } xs \wedge \text{els-loc-ok } es \text{ } xs$

lemma *el-loc-okI*: $\llbracket \neg \text{contains-insync } e; \text{syncvars } e; \mathcal{B} \text{ } e \text{ } n \rrbracket \Longrightarrow \text{el-loc-ok } e \text{ } xs$
and *els-loc-okI*: $\llbracket \neg \text{contains-insyncs } es; \text{syncvarss } es; \mathcal{B}s \text{ } es \text{ } n \rrbracket \Longrightarrow \text{els-loc-ok } es \text{ } xs$
by(*induct e and es arbitrary: xs n and xs n rule: el-loc-ok.induct els-loc-ok.induct*)(*auto intro: fv-B-unmod*)

lemma *el-loc-ok-compE1*: $\llbracket \neg \text{contains-insync } e; \text{fv } e \subseteq \text{set } Vs \rrbracket \Longrightarrow \text{el-loc-ok } (\text{compE1 } Vs \text{ } e) \text{ } xs$
and *els-loc-ok-compEs1*: $\llbracket \neg \text{contains-insyncs } es; \text{fvs } es \subseteq \text{set } Vs \rrbracket \Longrightarrow \text{els-loc-ok } (\text{compEs1 } Vs \text{ } es)$
xs
by(*auto intro: el-loc-okI els-loc-okI syncvars-compE1 syncvarss-compEs1 B Bs simp del: compEs1-conv-map*)

lemma shows *el-loc-ok-not-contains-insync-local-change*:
 $\llbracket \neg \text{contains-insync } e; \text{el-loc-ok } e \text{ } xs \rrbracket \Longrightarrow \text{el-loc-ok } e \text{ } xs'$
and *els-loc-ok-not-contains-insyncs-local-change*:
 $\llbracket \neg \text{contains-insyncs } es; \text{els-loc-ok } es \text{ } xs \rrbracket \Longrightarrow \text{els-loc-ok } es \text{ } xs'$
by(*induct e and es arbitrary: xs xs' and xs xs' rule: el-loc-ok.induct els-loc-ok.induct*)(*fastforce*)+

lemma *el-loc-ok-update*: $\llbracket \mathcal{B} \text{ } e \text{ } n; V < n \rrbracket \Longrightarrow \text{el-loc-ok } e \text{ } (xs[V := v]) = \text{el-loc-ok } e \text{ } xs$
and *els-loc-ok-update*: $\llbracket \mathcal{B}s \text{ } es \text{ } n; V < n \rrbracket \Longrightarrow \text{els-loc-ok } es \text{ } (xs[V := v]) = \text{els-loc-ok } es \text{ } xs$
apply(*induct e and es arbitrary: n xs and n xs rule: el-loc-ok.induct els-loc-ok.induct*)
apply(*auto simp add: list-update-swap*)
done

lemma *els-loc-ok-map-Val [simp]*:
 $\text{els-loc-ok } (\text{map } Val \text{ } vs) \text{ } xs$
by(*induct vs*) *auto*

lemma *els-loc-ok-map-Val-append [simp]*:
 $\text{els-loc-ok } (\text{map } Val \text{ } vs @ es) \text{ } xs = \text{els-loc-ok } es \text{ } xs$
by(*induct vs*) *auto*

lemma *el-loc-ok-extRet2J [simp]*:
 $\text{el-loc-ok } e \text{ } xs \Longrightarrow \text{el-loc-ok } (\text{extRet2J } e \text{ } va) \text{ } xs$
by(*cases va*) *auto*

definition *el-loc-ok1* :: $((\text{nat}, \text{nat}, 'addr) \text{exp} \times 'addr \text{locals1}) \times ((\text{nat}, \text{nat}, 'addr) \text{exp} \times 'addr \text{locals1})$
 $\text{list} \Rightarrow \text{bool}$

where $el\text{-loc-ok1} = (\lambda((e, xs), exs). el\text{-loc-ok } e \text{ } xs \wedge sync\text{-ok } e \wedge (\forall (e,xs) \in set \text{ } exs. el\text{-loc-ok } e \text{ } xs \wedge sync\text{-ok } e))$

lemma $el\text{-loc-ok1-simps}$:

$el\text{-loc-ok1 } ((e, xs), exs) = (el\text{-loc-ok } e \text{ } xs \wedge sync\text{-ok } e \wedge (\forall (e,xs) \in set \text{ } exs. el\text{-loc-ok } e \text{ } xs \wedge sync\text{-ok } e))$
by($simp$ add : $el\text{-loc-ok1-def}$)

lemma $el\text{-loc-ok-blocks1}$ [$simp$]:

$el\text{-loc-ok } (blocks1 \text{ } n \text{ } Ts \text{ } body) \text{ } xs = el\text{-loc-ok } body \text{ } xs$
by($induct$ $n \text{ } Ts \text{ } body$ $rule$: $blocks1.induct$) $auto$

lemma $sync\text{-oks-blocks1}$ [$simp$]: $sync\text{-ok } (blocks1 \text{ } n \text{ } Ts \text{ } e) = sync\text{-ok } e$

by($induct$ $n \text{ } Ts \text{ } e$ $rule$: $blocks1.induct$) $auto$

lemma **assumes** fin : $final \text{ } e'$

shows $el\text{-loc-ok-inline-call}$: $el\text{-loc-ok } e \text{ } xs \implies el\text{-loc-ok } (inline\text{-call } e' \text{ } e) \text{ } xs$
and $els\text{-loc-ok-inline-calls}$: $els\text{-loc-ok } es \text{ } xs \implies els\text{-loc-ok } (inline\text{-calls } e' \text{ } es) \text{ } xs$
apply($induct$ e **and** es $arbitrary$: xs **and** xs $rule$: $el\text{-loc-ok.induct}$ $els\text{-loc-ok.induct}$)
apply($insert \text{ } fin$)
apply($auto$ $simp$ add : $unmod\text{-inline-call}$)
done

lemma **assumes** $sync\text{-ok } e'$

shows $sync\text{-ok-inline-call}$: $sync\text{-ok } e \implies sync\text{-ok } (inline\text{-call } e' \text{ } e)$
and $sync\text{-oks-inline-calls}$: $sync\text{-oks } es \implies sync\text{-oks } (inline\text{-calls } e' \text{ } es)$
apply($induct$ e **and** es $rule$: $sync\text{-ok.induct}$ $sync\text{-oks.induct}$)
apply($insert \text{ } \langle sync\text{-ok } e' \rangle$)
apply $auto$
done

lemma $bisim\text{-sync-ok}$:

$bisim \text{ } Vs \text{ } e \text{ } e' \text{ } xs \implies sync\text{-ok } e$
 $bisim \text{ } Vs \text{ } e \text{ } e' \text{ } xs \implies sync\text{-ok } e'$

and $bisims\text{-sync-oks}$:

$bisims \text{ } Vs \text{ } es \text{ } es' \text{ } xs \implies sync\text{-oks } es$
 $bisims \text{ } Vs \text{ } es \text{ } es' \text{ } xs \implies sync\text{-oks } es'$

apply($induct$ $rule$: $bisim\text{-bisims.inducts}$)

apply($auto$ $intro$: $not\text{-contains-insync-sync-ok}$ $not\text{-contains-insyncs-sync-oks}$ $simp$ del : $compEs1\text{-conv-map}$)
done

lemma **assumes** $final \text{ } e'$

shows $expr\text{-locks-inline-call-final}$:
 $expr\text{-locks } (inline\text{-call } e' \text{ } e) = expr\text{-locks } e$
and $expr\text{-lockss-inline-calls-final}$:
 $expr\text{-lockss } (inline\text{-calls } e' \text{ } es) = expr\text{-lockss } es$
apply($induct$ e **and** es $rule$: $expr\text{-locks.induct}$ $expr\text{-lockss.induct}$)
apply($insert \text{ } \langle final \text{ } e' \rangle$)
apply($auto$ $simp$ add : $is\text{-vals-conv}$ $intro$: ext)
done

lemma $lock\text{-oks1I}$:

$\llbracket \bigwedge t \text{ } l. ts \text{ } t = None \implies has\text{-locks } (ls \text{ } \$ \text{ } l) \text{ } t = 0;$
 $\bigwedge t \text{ } e \text{ } xs \text{ } ln \text{ } l. ts \text{ } t = \llbracket ((e, x), exs), ln \rrbracket \implies has\text{-locks } (ls \text{ } \$ \text{ } l) \text{ } t + ln \text{ } \$ \text{ } l = expr\text{-locks } e \text{ } l +$

$expr\text{-}lockss (map\ fst\ exs)\ l\]$
 $\implies lock\text{-}oks1\ ls\ ts$
apply(*fastforce simp add: lock-oks1-def*)
done

lemma *lock-oks1E*:

$\llbracket lock\text{-}oks1\ ls\ ts;$
 $\forall t.\ ts\ t = None \implies (\forall l.\ has\text{-}locks\ (ls\ \$\ l)\ t = 0) \implies Q;$
 $\forall t\ e\ x\ exs\ ln.\ ts\ t = \llbracket (((e,\ x),\ exs),\ ln) \rrbracket \implies (\forall l.\ has\text{-}locks\ (ls\ \$\ l)\ t + ln\ \$\ l = expr\text{-}locks\ e\ l +$
 $expr\text{-}lockss\ (map\ fst\ exs)\ l) \implies Q \rrbracket$
 $\implies Q$
by(*fastforce simp add: lock-oks1-def*)

lemma *lock-oks1D1*:

$\llbracket lock\text{-}oks1\ ls\ ts; ts\ t = None \rrbracket \implies \forall l.\ has\text{-}locks\ (ls\ \$\ l)\ t = 0$
apply(*simp add: lock-oks1-def*)
apply(*erule-tac x=t in allE*)
apply(*auto*)
done

lemma *lock-oks1D2*:

$\bigwedge ln.\ \llbracket lock\text{-}oks1\ ls\ ts; ts\ t = \llbracket (((e,\ x),\ exs),\ ln) \rrbracket \rrbracket$
 $\implies \forall l.\ has\text{-}locks\ (ls\ \$\ l)\ t + ln\ \$\ l = expr\text{-}locks\ e\ l + expr\text{-}lockss\ (map\ fst\ exs)\ l$
apply(*fastforce simp add: lock-oks1-def*)
done

lemma *lock-oks1-thr-updI*:

$\bigwedge ln.\ \llbracket lock\text{-}oks1\ ls\ ts; ts\ t = \llbracket (((e,\ xs),\ exs),\ ln) \rrbracket;$
 $\forall l.\ expr\text{-}locks\ e\ l + expr\text{-}lockss\ (map\ fst\ exs)\ l = expr\text{-}locks\ e'\ l + expr\text{-}lockss\ (map\ fst\ exs')\ l \rrbracket$
 $\implies lock\text{-}oks1\ ls\ (ts(t \mapsto (((e',\ xs'),\ exs'),\ ln)))$
by(*rule lock-oks1I*)(*auto split: if-split-asm dest: lock-oks1D2 lock-oks1D1*)

definition *mbisim-Red1'-Red1* ::

$((\text{'addr}, \text{'thread-id}, (\text{'addr}\ expr1 \times \text{'addr}\ locals1) \times (\text{'addr}\ expr1 \times \text{'addr}\ locals1)\ list, \text{'heap}, \text{'addr})$
 $state,$
 $(\text{'addr}, \text{'thread-id}, (\text{'addr}\ expr1 \times \text{'addr}\ locals1) \times (\text{'addr}\ expr1 \times \text{'addr}\ locals1)\ list, \text{'heap}, \text{'addr})$
 $state) bisim$

where

$mbisim\text{-}Red1'\text{-}Red1\ s1\ s2 =$
 $(s1 = s2 \wedge lock\text{-}oks1\ (locks\ s1)\ (thr\ s1) \wedge ts\text{-}ok\ (\lambda t\ exxs\ h.\ el\text{-}loc\text{-}ok1\ exxs)\ (thr\ s1)\ (shr\ s1))$

lemma *sync-ok-blocks*:

$\llbracket length\ vs = length\ pns; length\ Ts = length\ pns \rrbracket$
 $\implies sync\text{-}ok\ (blocks\ pns\ Ts\ vs\ body) = sync\text{-}ok\ body$
by(*induct pns Ts vs body rule: blocks.induct*) *auto*

context *J1-heap-base begin*

lemma *red1-True-into-red1-False*:

$\llbracket True, P, t \vdash 1 \langle e, s \rangle \text{-}ta \rightarrow \langle e', s' \rangle; el\text{-}loc\text{-}ok\ e\ (lcl\ s) \rrbracket$
 $\implies False, P, t \vdash 1 \langle e, s \rangle \text{-}ta \rightarrow \langle e', s' \rangle \vee (\exists l.\ ta = \{\{UnlockFail \rightarrow l\}\} \wedge expr\text{-}locks\ e\ l > 0)$
and *reds1-True-into-reds1-False*:
 $\llbracket True, P, t \vdash 1 \langle es, s \rangle \text{-}ta \rightarrow \langle es', s' \rangle; els\text{-}loc\text{-}ok\ es\ (lcl\ s) \rrbracket$

$\implies \text{False}, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \vee (\exists l. ta = \{\!| \text{UnlockFail} \rightarrow l \!| \} \wedge \text{expr-lockss } es \ l > 0)$
apply(*induct rule: red1-reds1.inducts*)
apply(*auto intro: red1-reds1.intros split: if-split-asm*)
done

lemma *Red1-True-into-Red1-False:*

assumes $\text{True}, P, t \vdash 1 \langle ex/exs, shr \ s \rangle -ta \rightarrow \langle ex'/exs', m' \rangle$
and $el\text{-loc-ok1} \ (ex, exs)$
shows $\text{False}, P, t \vdash 1 \langle ex/exs, shr \ s \rangle -ta \rightarrow \langle ex'/exs', m' \rangle \vee$
 $(\exists l. ta = \{\!| \text{UnlockFail} \rightarrow l \!| \} \wedge \text{expr-lockss} \ (fst \ ex \ \# \ map \ fst \ exs) \ l > 0)$

using *assms*

by(*cases*)(*auto dest: Red1.intros red1-True-into-red1-False simp add: el-loc-ok1-def ta-upd-simps*)

lemma shows *red1-preserves-el-loc-ok:*

$\llbracket uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{sync-ok } e; el\text{-loc-ok } e \ (lcl \ s) \rrbracket \implies el\text{-loc-ok } e' \ (lcl \ s')$

and *reds1-preserves-els-loc-ok:*

$\llbracket uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{sync-oks } es; els\text{-loc-ok } es \ (lcl \ s) \rrbracket \implies els\text{-loc-ok } es' \ (lcl \ s')$

proof(*induct rule: red1-reds1.inducts*)

case (*Synchronized1Red2* $e \ s \ ta \ e' \ s' \ V \ a$)

from $\langle el\text{-loc-ok} \ (insync \ \vee \ (a) \ e) \ (lcl \ s) \rangle$

have $el\text{-loc-ok } e \ (lcl \ s) \ unmod \ e \ V \ lcl \ s \ ! \ V = \text{Addr } a$ **by** *auto*

from $\langle \text{sync-ok} \ (insync \ \vee \ (a) \ e) \rangle$ **have** $\text{sync-ok } e$ **by** *simp*

hence $el\text{-loc-ok } e' \ (lcl \ s')$

using $\langle el\text{-loc-ok } e \ (lcl \ s) \rangle$

by(*rule Synchronized1Red2*)

moreover from $\langle uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \langle unmod \ e \ V \rangle$ **have** $unmod \ e' \ V$

by(*rule red1-unmod-preserved*)

moreover from $red1\text{-preserves-unmod}[OF \ \langle uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \langle unmod \ e \ V \rangle] \langle lcl \ s \ ! \ V = \text{Addr } a \rangle$

have $lcl \ s' \ ! \ V = \text{Addr } a$ **by** *simp*

ultimately show *?case* **by** *auto*

qed(*auto elim: el-loc-ok-not-contains-insync-local-change els-loc-ok-not-contains-insyncs-local-change*)

lemma *red1-preserves-sync-ok:* $\llbracket uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{sync-ok } e \rrbracket \implies \text{sync-ok } e'$

and *reds1-preserves-sync-oks:* $\llbracket uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{sync-oks } es \rrbracket \implies \text{sync-oks } es'$

by(*induct rule: red1-reds1.inducts*)(*auto elim: not-contains-insync-sync-ok*)

lemma *Red1-preserves-el-loc-ok1:*

assumes $wf: wf\text{-J1-prog } P$

shows $\llbracket uf, P, t \vdash 1 \langle ex/exs, m \rangle -ta \rightarrow \langle ex'/exs', m' \rangle; el\text{-loc-ok1} \ (ex, exs) \rrbracket \implies el\text{-loc-ok1} \ (ex', exs')$

apply(*erule Red1.cases*)

apply(*auto simp add: el-loc-ok1-def dest: red1-preserves-el-loc-ok red1-preserves-sync-ok intro: el-loc-ok-inline-call sync-ok-inline-call*)

apply(*fastforce dest!: sees-wf-mdecl[OF wf] simp add: wf-mdecl-def intro!: el-loc-okI dest: WT1-not-contains-insync intro: not-contains-insync-sync-ok*)

done

lemma assumes $wf: wf\text{-J1-prog } P$

shows *red1-el-loc-ok1-new-thread:*

$\llbracket uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{NewThread } t' \ (C, M, a) \ h \in \text{set } \{\!| ta \!| \}_t \rrbracket$

$\implies el\text{-loc-ok1} \ ((\{0:Class \ (fst \ (method \ P \ C \ M))=None; \ the \ (snd \ (snd \ (snd \ (method \ P \ C \ M))))\}, xs), \llbracket$

and *reds1-el-loc-ok1-new-thread*:
 $\llbracket uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; NewThread t' (C, M, a) h \in set \{ta\}_t \rrbracket$
 $\implies el\text{-loc}\text{-ok1} (\{0:Class (fst (method P C M))=None; the (snd (snd (snd (method P C M))))\},$
 $xs), \llbracket \rrbracket$

proof(*induct rule: red1-reds1.inducts*)
case *Red1CallExternal* **thus** ?*case*
apply(*auto dest!: red-external-new-thread-sees[OF wf] simp add: el-loc-ok1-simps*)
apply(*auto dest!: sees-wf-mdecl[OF wf] WT1-not-contains-insync simp add: wf-mdecl-def intro!:*
el-loc-ok1 not-contains-insync-sync-ok)
done
qed *auto*

lemma *Red1-el-loc-ok1-new-thread*:
assumes *wf: wf-J1-prog P*
shows $\llbracket uf, P, t \vdash 1 \langle ex/exs, m \rangle -ta \rightarrow \langle ex'/exs', m' \rangle; NewThread t' exexs m' \in set \{ta\}_t \rrbracket$
 $\implies el\text{-loc}\text{-ok1} exexs$

by(*erule Red1.cases*)(*fastforce elim: red1-el-loc-ok1-new-thread[OF wf] simp add: ta-upd-simps*)+

lemma *Red1-el-loc-ok*:
assumes *wf: wf-J1-prog P*
shows *lifting-wf final-expr1 (mred1g uf P) ($\lambda t exexs h. el\text{-loc}\text{-ok1} exexs$)*

by(*unfold-locales*)(*auto elim: Red1-preserves-el-loc-ok1[OF wf] Red1-el-loc-ok1-new-thread[OF wf]*)

lemma *mred1-eq-mred1'*:
assumes *lok: lock-oks1 (locks s) (thr s)*
and *elo: ts-ok ($\lambda t exexs h. el\text{-loc}\text{-ok1} exexs$) (thr s) (shr s)*
and *tst: thr s t = [(exexs, no-wait-locks)]*
and *aoe: Red1-mthr.actions-ok s t ta*
shows *mred1 P t (exexs, shr s) ta = mred1' P t (exexs, shr s) ta*

proof(*intro ext iffI*)
fix *xm'*
assume *mred1 P t (exexs, shr s) ta xm'*
moreover obtain *ex exs where exexs [simp]: exexs = (ex, exs) by(cases exexs)*
moreover obtain *ex' exs' m' where xm' [simp]: xm' = ((ex', exs'), m') by(cases xm') auto*
ultimately have *red: True, P, t $\vdash 1 \langle ex/exs, shr s \rangle -ta \rightarrow \langle ex'/exs', m' \rangle$ by simp*
from *elo tst have el-loc-ok1 (ex, exs) by(auto dest: ts-okD)*
from *Red1-True-into-Red1-False[OF red this]*
have *False, P, t $\vdash 1 \langle ex/exs, shr s \rangle -ta \rightarrow \langle ex'/exs', m' \rangle$*

proof
assume $\exists l. ta = \{UnlockFail \rightarrow l\} \wedge 0 < expr\text{-lockss} (fst\ ex \# map\ fst\ exs)\ l$
then obtain *l where ta: ta = {UnlockFail \rightarrow l}*
and *el: expr-lockss (fst ex # map fst exs) l > 0 by blast*
from *aoe have lock-actions-ok (locks s \$ l) t ({ta}_l \$ l)*
by(*auto simp add: lock-ok-las-def*)
with *ta have has-locks (locks s \$ l) t = 0 by simp*
with *lok tst have expr-lockss (map fst (ex # exs)) l = 0*
by(*cases ex*)(*auto 4 6 simp add: lock-oks1-def*)
with *el have False by simp*
thus ?*thesis* ..

qed
thus *mred1' P t (exexs, shr s) ta xm' by simp*

next
fix *xm'*
assume *mred1' P t (exexs, shr s) ta xm'*

thus $mred1\ P\ t\ (exxs,\ shr\ s)\ ta\ xm'$
by($cases\ xm'$)($auto\ simp\ add:\ split\ beta\ intro:\ Red1\ False\ into\ Red1\ True$)
qed

lemma $Red1\ mthr\ eq\ Red1\ mthr'$:

assumes $lok:\ lock\ oks1\ (locks\ s)\ (thr\ s)$

and $elo:\ ts\ ok\ (\lambda t\ exxs\ h.\ el\ loc\ ok1\ exxs)\ (thr\ s)\ (shr\ s)$

shows $Red1\ mthr.\ redT\ True\ P\ s = Red1\ mthr.\ redT\ False\ P\ s$

proof($intro\ ext$)

fix $tta\ s'$

show $Red1\ mthr.\ redT\ True\ P\ s\ tta\ s' = Red1\ mthr.\ redT\ False\ P\ s\ tta\ s'$ (**is** $?lhs = ?rhs$)

proof

assume $?lhs\ \mathbf{thus}\ ?rhs$

proof $cases$

case ($redT\ normal\ t\ x\ ta\ x'\ m'$)

from $\langle mred1\ P\ t\ (x,\ shr\ s)\ ta\ (x',\ m') \rangle$ **have** $mred1'\ P\ t\ (x,\ shr\ s)\ ta\ (x',\ m')$

unfolding $mred1\ eq\ mred1'$ [$OF\ lok\ elo\ \langle thr\ s\ t = [(x,\ no\ wait\ locks)] \rangle\ \langle Red1\ mthr.\ actions\ ok\ s\ t\ ta \rangle$].

thus $?thesis\ \mathbf{using}\ redT\ normal(\beta-)\ \mathbf{unfolding}\ \langle tta = (t,\ ta) \rangle ..$

next

case ($redT\ acquire\ t\ x\ ln\ n$)

from $this(2-)$ **show** $?thesis\ \mathbf{unfolding}\ redT\ acquire(1) ..$

qed

next

assume $?rhs\ \mathbf{thus}\ ?lhs$

proof($cases$)

case ($redT\ normal\ t\ x\ ta\ x'\ m'$)

from $\langle mred1'\ P\ t\ (x,\ shr\ s)\ ta\ (x',\ m') \rangle$ **have** $mred1\ P\ t\ (x,\ shr\ s)\ ta\ (x',\ m')$

unfolding $mred1\ eq\ mred1'$ [$OF\ lok\ elo\ \langle thr\ s\ t = [(x,\ no\ wait\ locks)] \rangle\ \langle Red1\ mthr.\ actions\ ok\ s\ t\ ta \rangle$].

thus $?thesis\ \mathbf{using}\ redT\ normal(\beta-)\ \mathbf{unfolding}\ \langle tta = (t,\ ta) \rangle ..$

next

case ($redT\ acquire\ t\ x\ ln\ n$)

from $this(2-)$ **show** $?thesis\ \mathbf{unfolding}\ redT\ acquire(1) ..$

qed

qed

qed

lemma **assumes** $wf:\ wf\ J1\ prog\ P$

shows $expr\ locks\ new\ thread1$:

$\llbracket uf, P, t \vdash 1\ \langle e, s \rangle - TA \rightarrow \langle e', s' \rangle; NewThread\ t' (ex, exs)\ h \in set\ (map\ (convert\ new\ thread\ action\ (extNTA2J1\ P))\ \{\{TA\}_t\}) \rrbracket$

$\implies expr\ lockss\ (map\ fst\ (ex\ \# \ exs)) = (\lambda ad.\ 0)$

and $expr\ lockss\ new\ thread1$:

$\llbracket uf, P, t \vdash 1\ \langle es, s \rangle [-TA \rightarrow] \langle es', s' \rangle; NewThread\ t' (ex, exs)\ h \in set\ (map\ (convert\ new\ thread\ action\ (extNTA2J1\ P))\ \{\{TA\}_t\}) \rrbracket$

$\implies expr\ lockss\ (map\ fst\ (ex\ \# \ exs)) = (\lambda ad.\ 0)$

proof($induct\ rule:\ red1\ reds1.\ inducts$)

case ($Red1CallExternal\ s\ a\ T\ M\ vs\ ta\ va\ h'\ e'\ s'$)

then obtain $C\ fs\ ad$ **where** $subThread:\ P \vdash C \preceq^* Thread$ **and** $ext:\ extNTA2J1\ P\ (C,\ run,\ ad) = (ex,\ exs)$

by($fastforce\ dest:\ red\ external\ new\ thread\ sub\ thread$)

from $sub\ Thread\ sees\ run[OF\ wf\ subThread]$ **obtain** $D\ body$

where $sees:\ P \vdash C\ sees\ run:\ \square \rightarrow Void = \lfloor body \rfloor$ **in** D **by** $auto$

from *sees-wf-mdecl*[*OF wf this*] **obtain** T **where** $P, [\text{Class } D] \vdash 1 \text{ body} :: T$
by(*auto simp add: wf-mdecl-def*)
hence $\neg \text{contains-insync body}$ **by**(*rule WT1-not-contains-insync*)
hence *expr-locks* $\text{body} = (\lambda ad. 0)$ **by**(*auto simp add: contains-insync-conv fun-eq-iff*)
with *sees ext* **show** *?case* **by**(*auto*)
qed *auto*

lemma *assumes wf: wf-J1-prog P*

shows *red1-update-expr-locks*:

$\llbracket \text{False}, P, t \vdash 1 \langle e, s \rangle \text{---} \text{ta} \rightarrow \langle e', s' \rangle; \text{sync-ok } e; \text{el-loc-ok } e \text{ (lcl } s) \rrbracket$
 $\implies \text{upd-expr-locks (int o expr-locks } e) \llbracket \text{ta} \rrbracket_l = \text{int o expr-locks } e'$

and *reds1-update-expr-lockss*:

$\llbracket \text{False}, P, t \vdash 1 \langle es, s \rangle \text{---} \text{ta} \rightarrow \langle es', s' \rangle; \text{sync-oks } es; \text{els-loc-ok } es \text{ (lcl } s) \rrbracket$
 $\implies \text{upd-expr-locks (int o expr-lockss } es) \llbracket \text{ta} \rrbracket_l = \text{int o expr-lockss } es'$

proof –

have $\llbracket \text{False}, P, t \vdash 1 \langle e, s \rangle \text{---} \text{ta} \rightarrow \langle e', s' \rangle; \text{sync-ok } e; \text{el-loc-ok } e \text{ (lcl } s) \rrbracket$
 $\implies \text{upd-expr-locks } (\lambda ad. 0) \llbracket \text{ta} \rrbracket_l = (\lambda ad. (\text{int o expr-locks } e') ad - (\text{int o expr-locks } e) ad)$

and $\llbracket \text{False}, P, t \vdash 1 \langle es, s \rangle \text{---} \text{ta} \rightarrow \langle es', s' \rangle; \text{sync-oks } es; \text{els-loc-ok } es \text{ (lcl } s) \rrbracket$

$\implies \text{upd-expr-locks } (\lambda ad. 0) \llbracket \text{ta} \rrbracket_l = (\lambda ad. (\text{int o expr-lockss } es') ad - (\text{int o expr-lockss } es) ad)$

proof(*induct rule: red1-reds1.inducts*)

case *Red1CallExternal thus ?case*

by(*auto simp add: fun-eq-iff contains-insync-conv contains-insyncs-conv finfun-upd-apply elim!*:
red-external.cases)

qed(*fastforce simp add: fun-eq-iff contains-insync-conv contains-insyncs-conv finfun-upd-apply*)+

hence $\llbracket \text{False}, P, t \vdash 1 \langle e, s \rangle \text{---} \text{ta} \rightarrow \langle e', s' \rangle; \text{sync-ok } e; \text{el-loc-ok } e \text{ (lcl } s) \rrbracket$

$\implies \text{upd-expr-locks } (\lambda ad. 0 + (\text{int o expr-locks } e) ad) \llbracket \text{ta} \rrbracket_l = \text{int o expr-locks } e'$

and $\llbracket \text{False}, P, t \vdash 1 \langle es, s \rangle \text{---} \text{ta} \rightarrow \langle es', s' \rangle; \text{sync-oks } es; \text{els-loc-ok } es \text{ (lcl } s) \rrbracket$

$\implies \text{upd-expr-locks } (\lambda ad. 0 + (\text{int o expr-lockss } es) ad) \llbracket \text{ta} \rrbracket_l = \text{int o expr-lockss } es'$

by(*fastforce simp only: upd-expr-locks-add*)+

thus $\llbracket \text{False}, P, t \vdash 1 \langle e, s \rangle \text{---} \text{ta} \rightarrow \langle e', s' \rangle; \text{sync-ok } e; \text{el-loc-ok } e \text{ (lcl } s) \rrbracket$

$\implies \text{upd-expr-locks (int o expr-locks } e) \llbracket \text{ta} \rrbracket_l = \text{int o expr-locks } e'$

and $\llbracket \text{False}, P, t \vdash 1 \langle es, s \rangle \text{---} \text{ta} \rightarrow \langle es', s' \rangle; \text{sync-oks } es; \text{els-loc-ok } es \text{ (lcl } s) \rrbracket$

$\implies \text{upd-expr-locks (int o expr-lockss } es) \llbracket \text{ta} \rrbracket_l = \text{int o expr-lockss } es'$

by(*auto simp add: o-def*)

qed

lemma *Red1'-preserves-lock-oks*:

assumes *wf: wf-J1-prog P*

and *Red: Red1-mthr.redT False P s1 ta1 s1'*

and *loks: lock-oks1 (locks s1) (thr s1)*

and *sync: ts-ok* $(\lambda t \text{ exes } h. \text{el-loc-ok1 } \text{exes}) \text{ (thr } s1) \text{ (shr } s1)$

shows *lock-oks1 (locks s1') (thr s1')*

using *Red*

proof(*cases rule: Red1-mthr.redT.cases*)

case (*redT-normal t x ta x' m'*)

note [*simp*] = $\langle \text{ta1} = (t, \text{ta}) \rangle$

obtain *ex exs* **where** $x: x = (ex, exs)$ **by** (*cases x*)

obtain *ex' exs'* **where** $x': x' = (ex', exs')$ **by** (*cases x'*)

note *thrst* = $\langle \text{thr } s1 \text{ } t = \llbracket (x, \text{no-wait-locks}) \rrbracket \rangle$

note *aoe* = $\langle \text{Red1-mthr.actions-ok } s1 \text{ } t \text{ } \text{ta} \rangle$

from $\langle \text{mred1}' P \text{ } t \text{ } (x, \text{shr } s1) \text{ } \text{ta} \text{ } (x', m') \rangle$

have *red: False, P, t* $\vdash 1 \langle \text{ex/exs, shr } s1 \rangle \text{---} \text{ta} \rightarrow \langle \text{ex'/exs}', m' \rangle$

unfolding $x \text{ } x'$ **by** *simp-all*

```

note  $s1' = \langle redT\text{-upd } s1 \ t \ ta \ x' \ m' \ s1' \rangle$ 
moreover from  $red$ 
have  $lock\text{-oks1} \ (locks \ s1') \ (thr \ s1')$ 
proof cases
  case  $(red1Red \ e \ x \ TA \ e' \ x')$ 
    note  $[simp] = \langle ex = (e, x) \rangle \langle ta = extTA2J1 \ P \ TA \rangle \langle ex' = (e', x') \rangle \langle exs' = exs \rangle$ 
    and  $red = \langle False, P, t \vdash 1 \ \langle e, (shr \ s1, x) \rangle - TA \rightarrow \langle e', (m', x') \rangle \rangle$ 
    { fix  $t'$ 
      assume  $None: ((redT\text{-updTs} \ (thr \ s1) \ (map \ (convert\text{-new-thread-action} \ (extNTA2J1 \ P)) \ \{\!\!\{TA\!\!\}_t)))(t$ 
       $\mapsto (((e', x'), exs), redT\text{-updLns} \ (locks \ s1) \ t \ (snd \ (the \ (thr \ s1 \ t))) \ \{\!\!\{TA\!\!\}_l)) \ t' = None$ 
      { fix  $l$ 
        from  $aoe$  have  $lock\text{-actions-ok} \ (locks \ s1 \ \$ \ l) \ t \ (\{\!\!\{ta\!\!\}_l \ \$ \ l) \ \mathbf{by} \ (auto \ simp \ add: \ lock\text{-ok-las-def})$ 
        with  $None$  have  $has\text{-locks} \ ((redT\text{-updLns} \ (locks \ s1) \ t \ \{\!\!\{ta\!\!\}_l) \ \$ \ l) \ t' = has\text{-locks} \ (locks \ s1 \ \$ \ l) \ t'$ 
         $\ \mathbf{by} \ (auto \ split: \ if\text{-split-asm})$ 
        also from  $loks \ None$  have  $has\text{-locks} \ (locks \ s1 \ \$ \ l) \ t' = 0 \ \mathbf{unfolding} \ lock\text{-oks1-def}$ 
         $\ \mathbf{by} \ (force \ split: \ if\text{-split-asm} \ dest!: \ redT\text{-updTs-None})$ 
        finally have  $has\text{-locks} \ (upd\text{-locks} \ (locks \ s1 \ \$ \ l) \ t \ (\{\!\!\{TA\!\!\}_l \ \$ \ l)) \ t' = 0 \ \mathbf{by} \ simp \ }$ 
        hence  $\forall l. has\text{-locks} \ (upd\text{-locks} \ (locks \ s1 \ \$ \ l) \ t \ (\{\!\!\{TA\!\!\}_l \ \$ \ l)) \ t' = 0 \ \dots \}$ 
      }
      moreover {
        fix  $t' \ eX \ eXS \ LN$ 
        assume  $Some: ((redT\text{-updTs} \ (thr \ s1) \ (map \ (convert\text{-new-thread-action} \ (extNTA2J1 \ P)) \ \{\!\!\{TA\!\!\}_t)))(t$ 
         $\mapsto (((e', x'), exs), redT\text{-updLns} \ (locks \ s1) \ t \ (snd \ (the \ (thr \ s1 \ t))) \ \{\!\!\{TA\!\!\}_l)) \ t' = [((eX, eXS), LN)]$ 
        { fix  $l$ 
          from  $aoe$  have  $lao: lock\text{-actions-ok} \ (locks \ s1 \ \$ \ l) \ t \ (\{\!\!\{ta\!\!\}_l \ \$ \ l) \ \mathbf{by} \ (auto \ simp \ add: \ lock\text{-ok-las-def})$ 
          have  $has\text{-locks} \ ((redT\text{-updLns} \ (locks \ s1) \ t \ \{\!\!\{ta\!\!\}_l) \ \$ \ l) \ t' + LN \ \$ \ l = expr\text{-lockss} \ (map \ fst \ (eX \ \#$ 
           $eXS)) \ l$ 
          proof  $(cases \ t = t')$ 
            case  $True$ 
            from  $loks \ thrst \ x$ 
            have  $has\text{-locks} \ (locks \ s1 \ \$ \ l) \ t = expr\text{-locks} \ e \ l + expr\text{-lockss} \ (map \ fst \ exs) \ l$ 
             $\ \mathbf{by} \ (force \ simp \ add: \ lock\text{-oks1-def})$ 
            hence  $lock\text{-expr-locks-ok} \ (locks \ s1 \ \$ \ l) \ t \ 0 \ (int \ (expr\text{-locks} \ e \ l + expr\text{-lockss} \ (map \ fst \ exs) \ l))$ 
             $\ \mathbf{by} \ (simp \ add: \ lock\text{-expr-locks-ok-def})$ 
            with  $lao$  have  $lock\text{-expr-locks-ok} \ (upd\text{-locks} \ (locks \ s1 \ \$ \ l) \ t \ (\{\!\!\{ta\!\!\}_l \ \$ \ l)) \ t \ (upd\text{-threadRs} \ 0$ 
             $(locks \ s1 \ \$ \ l) \ t \ (\{\!\!\{ta\!\!\}_l \ \$ \ l))$ 
             $(upd\text{-expr-lock-actions} \ (int \ (expr\text{-locks} \ e \ l + expr\text{-lockss} \ (map \ fst \ exs) \ l)) \ (\{\!\!\{ta\!\!\}_l \ \$ \ l))$ 
             $\ \mathbf{by} \ (rule \ upd\text{-locks-upd-expr-lock-preserve-lock-expr-locks-ok})$ 
            moreover from  $sync \ thrst \ x$  have  $sync\text{-ok} \ e \ el\text{-loc-ok} \ e \ x$ 
             $\ \mathbf{unfolding} \ el\text{-loc-ok1-def} \ \mathbf{by} \ (auto \ dest: \ ts-okD)$ 
            with  $red1\text{-update-expr-locks}[OF \ wf \ red]$ 
            have  $upd\text{-expr-locks} \ (int \circ expr\text{-locks} \ e) \ \{\!\!\{TA\!\!\}_l = int \circ expr\text{-locks} \ e' \ \mathbf{by} \ (simp)$ 
            hence  $upd\text{-expr-lock-actions} \ (int \ (expr\text{-locks} \ e \ l)) \ (\{\!\!\{TA\!\!\}_l \ \$ \ l) = int \ (expr\text{-locks} \ e' \ l)$ 
             $\ \mathbf{by} \ (simp \ add: \ upd\text{-expr-locks-def} \ fun\text{-eq-iff})$ 
            ultimately show  $?thesis \ \mathbf{using} \ lao \ Some \ thrst \ x \ True$ 
             $\ \mathbf{by} \ (auto \ simp \ add: \ lock\text{-expr-locks-ok-def} \ upd\text{-expr-locks-def})$ 
          }
        }
      }
      next
      case  $False$ 
      from  $aoe$  have  $tok: thread\text{-oks} \ (thr \ s1) \ \{\!\!\{ta\!\!\}_t \ \mathbf{by} \ auto$ 
      show  $?thesis$ 
      proof  $(cases \ thr \ s1 \ t' = None)$ 
      case  $True$ 
      with  $Some \ tok \ False$  obtain  $m$ 
      where  $nt: NewThread \ t' \ (eX, eXS) \ m \in set \ (map \ (convert\text{-new-thread-action} \ (extNTA2J1$ 
       $P)) \ \{\!\!\{TA\!\!\}_t)$ 

```

```

    and [simp]: LN = no-wait-locks by(auto dest: redT-updTs-new-thread)
    note expr-locks-new-thread1[OF wf red nt]
    moreover from loks True have has-locks (locks s1 $ l) t' = 0
      by(force simp add: lock-oks1-def)
    ultimately show ?thesis using lao False by simp
  next
    case False
    with Some ⟨t ≠ t'⟩ tok
    have thr s1 t' = [((eX, eXS), LN)] by(fastforce dest: redT-updTs-Some[OF - tok])
    with loks tok lao ⟨t ≠ t'⟩ show ?thesis by(cases eX)(auto simp add: lock-oks1-def)
  qed
qed }
  hence ∀ l. has-locks ((redT-updLs (locks s1) t {ta}l) $ l) t' + LN $ l = expr-lockss (map fst (eX
# eXS)) l .. }
  ultimately show ?thesis using s1' unfolding lock-oks1-def x' by(clarsimp simp del: fun-upd-apply)
next
  case (red1Call e a M vs U Ts T body D x)
  from wf ⟨P ⊢ class-type-of U sees M: Ts → T = [body] in D⟩
  obtain T' where P, Class D # Ts ⊢ 1 body :: T'
    by(auto simp add: wf-mdecl-def dest!: sees-wf-mdecl)
  hence expr-locks (blocks1 0 (Class D # Ts) body) = (λl. 0)
  by(auto simp add: expr-locks-blocks1 contains-insync-conv fun-eq-iff dest!: WT1-not-contains-insync)
  thus ?thesis using red1Call thrst loks s1'
    unfolding lock-oks1-def x' x
    by auto force+
next
  case (red1Return e' x' e x)
  thus ?thesis using thrst loks s1'
    unfolding lock-oks1-def x' x
    apply(auto simp add: redT-updWs-def elim!: rtrancl3p-cases)
    apply(erule-tac x=t in allE)
    apply(erule conjE)
    apply(erule disjE)
    apply(force simp add: expr-locks-inline-call-final ac-simps)
    apply(fastforce simp add: expr-locks-inline-call-final)
    apply hypsubst-thin
    apply(erule-tac x=ta in allE)
    apply fastforce
  done
qed
  moreover from sync ⟨mred1' P t (x, shr s1) ta (x', m')⟩ thrst aoe s1'
  have ts-ok (λt exes h. el-loc-ok1 exes) (thr s1') (shr s1')
    by(auto intro: lifting-wf.redT-updTs-preserves[OF Red1-el-loc-ok[OF wf]])
  ultimately show ?thesis by simp
next
  case (redT-acquire t x n ln)
  thus ?thesis using loks unfolding lock-oks1-def
    apply auto
    apply force
    apply(case-tac ln $ l::nat)
    apply simp
    apply(erule allE)
    apply(erule conjE)
    apply(erule allE)+

```

```

apply(erule (1) impE)
apply(erule-tac x=l in allE)
apply fastforce
apply(erule may-acquire-allE)
apply(erule allE)
apply(erule-tac x=l in allE)
apply(erule impE)
apply simp
apply(simp only: has-locks-acquire-locks-conv)
apply(erule conjE)
apply(erule allE)+
apply(erule (1) impE)
apply(erule-tac x=l in allE)
apply simp
done

```

qed

lemma *Red1'-Red1-bisimulation*:

assumes wf: wf-J1-prog P

shows bisimulation (Red1-mthr.redT False P) (Red1-mthr.redT True P) mbisim-Red1'-Red1 (=)

proof

fix s1 s2 tl1 s1'

assume mbisim-Red1'-Red1 s1 s2 **and** Red1-mthr.redT False P s1 tl1 s1'

thus $\exists s2' tl2. \text{Red1-mthr.redT True P } s2 \text{ } tl2 \text{ } s2' \wedge \text{mbisim-Red1'-Red1 } s1' \text{ } s2' \wedge tl1 = tl2$

by(cases tl1)(auto simp add: mbisim-Red1'-Red1-def Red1-mthr-eq-Red1-mthr' simp del: split-paired-Ex
elim: Red1'-preserves-lock-oks[OF wf] lifting-wf.redT-preserves[OF Red1-el-loc-ok, OF wf])

next

fix s1 s2 tl2 s2'

assume mbisim-Red1'-Red1 s1 s2 Red1-mthr.redT True P s2 tl2 s2'

thus $\exists s1' tl1. \text{Red1-mthr.redT False P } s1 \text{ } tl1 \text{ } s1' \wedge \text{mbisim-Red1'-Red1 } s1' \text{ } s2' \wedge tl1 = tl2$

by(cases tl2)(auto simp add: mbisim-Red1'-Red1-def Red1-mthr-eq-Red1-mthr' simp del: split-paired-Ex
elim: Red1'-preserves-lock-oks[OF wf] lifting-wf.redT-preserves[OF Red1-el-loc-ok, OF wf])

qed

lemma *Red1'-Red1-bisimulation-final*:

wf-J1-prog P

\implies bisimulation-final (Red1-mthr.redT False P) (Red1-mthr.redT True P)

mbisim-Red1'-Red1 (=) Red1-mthr.mfinal Red1-mthr.mfinal

apply(intro-locales)

apply(erule Red1'-Red1-bisimulation)

apply(unfold-locales)

apply(auto simp add: mbisim-Red1'-Red1-def)

done

lemma *bisim-J1-J1-start*:

assumes wf: wf-J1-prog P

and wf-start: wf-start-state P C M vs

shows mbisim-Red1'-Red1 (J1-start-state P C M vs) (J1-start-state P C M vs)

proof –

from wf-start **obtain** Ts T body D

where sees: P \vdash C sees M:Ts \rightarrow T= \lfloor body \rfloor in D

and conf: P,start-heap \vdash vs $\lfloor \leq \rfloor$ Ts

by cases

let ?e = blocks1 0 (Class C#Ts) body

```

let ?xs = Null # vs @ replicate (max-vars body) undefined-value

from sees-wf-mdecl[OF wf sees] obtain T'
  where B: B body (Suc (length Ts))
  and wt: P, Class D # Ts ⊢ 1 body :: T'
  and da: D body [..length Ts]
  and sv: syncvars body
  by(auto simp add: wf-mdecl-def)

from wt have expr-locks ?e = (λ-. 0) by(auto intro: WT1-expr-locks)
thus ?thesis using da sees sv B
  unfolding start-state-def
  by(fastforce simp add: mbisim-Red1'-Red1-def lock-oks1-def el-loc-ok1-def contains-insync-conv
intro!: ts-okI expr-locks-sync-ok split: if-split-asm intro: el-loc-okI)
qed

```

```

lemma Red1'-Red1-bisim-into-weak:
  assumes wf: wf-J1-prog P
  shows bisimulation-into-delay (Red1-mthr.redT False P) (Red1-mthr.redT True P) mbisim-Red1'-Red1
  (=) (Red1-mthr.mτmove P) (Red1-mthr.mτmove P)

```

```

proof -
  interpret b: bisimulation Red1-mthr.redT False P Red1-mthr.redT True P mbisim-Red1'-Red1 (=)
  by(rule Red1'-Red1-bisimulation[OF wf])
  show ?thesis by(unfold-locales)(simp add: mbisim-Red1'-Red1-def)
qed

```

end

```

sublocale J1-heap-base < Red1-mthr:
  if-τmultithreaded-wf
  final-expr1
  mred1g uf P
  convert-RA
  τMOVE1 P
  for uf P
by(unfold-locales)

```

context J1-heap-base **begin**

```

abbreviation if-lock-oks1 ::
  ('addr, 'thread-id) locks
  ⇒ ('addr, 'thread-id, (status × (('a, 'b, 'addr) exp × 'c) × (('a, 'b, 'addr) exp × 'c) list)) thread-info
  ⇒ bool

```

```

where
  if-lock-oks1 ls ts ≡ lock-oks1 ls (init-fin-descend-thr ts)

```

```

definition if-mbisim-Red1'-Red1 ::
  (('addr, 'thread-id, status × (('addr expr1 × 'addr locals1) × ('addr expr1 × 'addr locals1) list), 'heap, 'addr)
  state,
  ('addr, 'thread-id, status × (('addr expr1 × 'addr locals1) × ('addr expr1 × 'addr locals1) list), 'heap, 'addr)
  state) bisim

```

```

where
  if-mbisim-Red1'-Red1 s1 s2 ←→
  s1 = s2 ∧ if-lock-oks1 (locks s1) (thr s1) ∧ ts-ok (init-fin-lift (λt exers h. el-loc-ok1 exers)) (thr s1)

```

(shr s1)

lemma *if-mbisim-Red1'-Red1-imp-mbisim-Red1'-Red1*:

if-mbisim-Red1'-Red1 s1 s2 \implies mbisim-Red1'-Red1 (init-fin-descend-state s1) (init-fin-descend-state s2)

by(*auto simp add: mbisim-Red1'-Red1-def if-mbisim-Red1'-Red1-def ts-ok-init-fin-descend-state*)

lemma *if-Red1-mthr-imp-if-Red1-mthr'*:

assumes *lok: if-lock-oks1 (locks s) (thr s)*

and *elo: ts-ok (init-fin-lift (λt exexs h. el-loc-ok1 exexs)) (thr s) (shr s)*

and *Red: Red1-mthr.if.redT uf P s tta s'*

shows *Red1-mthr.if.redT (\neg uf) P s tta s'*

using *Red*

proof(*cases*)

case (*redT-acquire t x ln n*)

from *this(2-)* **show** *?thesis unfolding redT-acquire(1) ..*

next

case (*redT-normal t x ta x' m'*)

note *aok = \langle Red1-mthr.if.actions-ok s t ta*

and *tst = \langle thr s t = \lfloor (x, no-wait-locks) \rfloor*

from *\langle Red1-mthr.init-fin uf P t (x, shr s) ta (x', m')*

have *Red1-mthr.init-fin (\neg uf) P t (x, shr s) ta (x', m')*

proof(*cases*)

case *InitialThreadAction* **show** *?thesis unfolding InitialThreadAction ..*

next

case (*ThreadFinishAction exexs*)

from *\langle final-expr1 exexs* **show** *?thesis unfolding ThreadFinishAction ..*

next

case (*NormalAction exexs ta' exexs'*)

let *?s = init-fin-descend-state s*

from *lok* **have** *lock-oks1 (locks ?s) (thr ?s)* **by**(*simp*)

moreover from *elo* **have** *elo: ts-ok (λt exexs h. el-loc-ok1 exexs) (thr ?s) (shr ?s)*

by(*simp add: ts-ok-init-fin-descend-state*)

moreover from *tst* $\langle x = (Running, exexs) \rangle$

have *thr ?s t = \lfloor (exexs, no-wait-locks) \rfloor* **by** *simp*

moreover from *aok* **have** *Red1-mthr.actions-ok ?s t ta'*

using $\langle ta = convert-TA-initial (convert-obs-initial ta') \rangle$ **by** *auto*

ultimately have *mred1 P t (exexs, shr ?s) ta' = mred1' P t (exexs, shr ?s) ta'*

by(*rule mred1-eq-mred1'*)

with $\langle mred1g uf P t (exexs, shr s) ta' (exexs', m') \rangle$

have *mred1g (\neg uf) P t (exexs, shr s) ta' (exexs', m')*

by(*cases uf*) *simp-all*

thus *?thesis unfolding NormalAction(1-3)* **by**(*rule Red1-mthr.init-fin.NormalAction*)

qed

thus *?thesis using tst aok \langle redT-upd s t ta x' m' s' unfolding \langle tta = (t, ta) \rangle ..*

qed

lemma *if-Red1-mthr-eq-if-Red1-mthr'*:

assumes *lok: if-lock-oks1 (locks s) (thr s)*

and *elo: ts-ok (init-fin-lift (λt exexs h. el-loc-ok1 exexs)) (thr s) (shr s)*

shows *Red1-mthr.if.redT True P s = Red1-mthr.if.redT False P s*

using *if-Red1-mthr-imp-if-Red1-mthr'[OF assms, of True P, simplified]*

if-Red1-mthr-imp-if-Red1-mthr'[OF assms, of False P, simplified]

by(*blast del: equalityI*)

lemma *if-Red1-el-loc-ok:*

assumes *wf: wf-J1-prog P*

shows *lifting-wf Red1-mthr.init-fin-final (Red1-mthr.init-fin uf P) (init-fin-lift (λt exexs h. el-loc-ok1 exexs))*

by(*rule lifting-wf.lifting-wf-init-fin-lift*)(*rule Red1-el-loc-ok[OF wf]*)

lemma *if-Red1'-preserves-if-lock-oks:*

assumes *wf: wf-J1-prog P*

and *Red: Red1-mthr.if.redT False P s1 ta1 s1'*

and *loks: if-lock-oks1 (locks s1) (thr s1)*

and *sync: ts-ok (init-fin-lift (λt exexs h. el-loc-ok1 exexs)) (thr s1) (shr s1)*

shows *if-lock-oks1 (locks s1') (thr s1')*

proof –

let *?s1 = init-fin-descend-state s1*

let *?s1' = init-fin-descend-state s1'*

from *loks have loks': lock-oks1 (locks ?s1) (thr ?s1) by simp*

from *sync have sync': ts-ok (λt exexs h. el-loc-ok1 exexs) (thr ?s1) (shr ?s1)*

by(*simp add: ts-ok-init-fin-descend-state*)

from *Red show ?thesis*

proof(*cases*)

case (*redT-acquire t x n ln*)

hence *Red1-mthr.redT False P ?s1 (t, K\$ [], [], [], [], [], convert-RA ln) ?s1'*

by(*cases x*)(*auto intro!: Red1-mthr.redT.redT-acquire simp add: init-fin-descend-thr-def*)

with *wf have lock-oks1 (locks ?s1') (thr ?s1')* **using** *loks' sync'* **by**(*rule Red1'-preserves-lock-oks*)

thus *?thesis by simp*

next

case (*redT-normal t sx ta sx' m'*)

note *tst = ⟨thr s1 t = [(sx, no-wait-locks)]⟩*

from *⟨Red1-mthr.init-fin False P t (sx, shr s1) ta (sx', m')⟩*

show *?thesis*

proof(*cases*)

case (*InitialThreadAction x*) **thus** *?thesis using redT-normal loks*

by(*cases x*)(*auto 4 3 simp add: init-fin-descend-thr-def redT-updLns-def expand-finfun-eq fun-eq-iff*)

intro: lock-oks1-thr-updI)

next

case (*ThreadFinishAction x*) **thus** *?thesis using redT-normal loks*

by(*cases x*)(*auto 4 3 simp add: init-fin-descend-thr-def redT-updLns-def expand-finfun-eq fun-eq-iff*)

intro: lock-oks1-thr-updI)

next

case (*NormalAction x ta' x'*)

note *ta = ⟨ta = convert-TA-initial (convert-obs-initial ta')⟩*

from *⟨mred1' P t (x, shr s1) ta' (x', m')⟩*

have *mred1' P t (x, shr ?s1) ta' (x', m')* **by** *simp*

moreover **have** *tst': thr ?s1 t = [(x, no-wait-locks)]*

using *tst* *⟨sx = (Running, x)⟩ by simp*

moreover **have** *Red1-mthr.actions-ok ?s1 t ta'*

using *ta* *⟨Red1-mthr.if.actions-ok s1 t ta⟩ by simp*

moreover **from** *⟨redT-upd s1 t ta sx' m' s1'⟩ tst tst' ta* *⟨sx' = (Running, x')⟩*

have *redT-upd ?s1 t ta' x' m' ?s1'* **by** *auto*

ultimately **have** *Red1-mthr.redT False P ?s1 (t, ta') ?s1' ..*

with *wf have lock-oks1 (locks ?s1') (thr ?s1')* **using** *loks' sync'* **by**(*rule Red1'-preserves-lock-oks*)

thus *?thesis by simp*

qed
 qed
 qed

lemma *Red1'-Red1-if-bisimulation*:

assumes *wf*: *wf-J1-prog P*

shows *bisimulation (Red1-mthr.if.redT False P) (Red1-mthr.if.redT True P) if-mbisim-Red1'-Red1 (=)*

proof

fix *s1 s2 tl1 s1'*

assume *if-mbisim-Red1'-Red1 s1 s2 and Red1-mthr.if.redT False P s1 tl1 s1'*

thus $\exists s2' tl2. Red1-mthr.if.redT True P s2 tl2 s2' \wedge if-mbisim-Red1'-Red1 s1' s2' \wedge tl1 = tl2$

by(*cases tl1*)(*auto simp add: if-mbisim-Red1'-Red1-def if-Red1-mthr-eq-if-Red1-mthr' simp del: split-paired-Ex elim: if-Red1'-preserves-if-lock-oks[OF wf] lifting-wf.redT-preserves[OF if-Red1-el-loc-ok, OF wf]*)

next

fix *s1 s2 tl2 s2'*

assume *if-mbisim-Red1'-Red1 s1 s2 Red1-mthr.if.redT True P s2 tl2 s2'*

thus $\exists s1' tl1. Red1-mthr.if.redT False P s1 tl1 s1' \wedge if-mbisim-Red1'-Red1 s1' s2' \wedge tl1 = tl2$

by(*cases tl2*)(*auto simp add: if-mbisim-Red1'-Red1-def if-Red1-mthr-eq-if-Red1-mthr' simp del: split-paired-Ex elim: if-Red1'-preserves-if-lock-oks[OF wf] lifting-wf.redT-preserves[OF if-Red1-el-loc-ok, OF wf]*)

qed

lemma *if-bisim-J1-J1-start*:

assumes *wf*: *wf-J1-prog P*

and *wf-start*: *wf-start-state P C M vs*

shows *if-mbisim-Red1'-Red1 (init-fin-lift-state status (J1-start-state P C M vs)) (init-fin-lift-state status (J1-start-state P C M vs))*

proof –

from *assms have mbisim-Red1'-Red1 (J1-start-state P C M vs) (J1-start-state P C M vs) by(rule bisim-J1-J1-start)*

thus *?thesis*

by(*simp add: if-mbisim-Red1'-Red1-def mbisim-Red1'-Red1-def*)(*simp add: init-fin-lift-state-conv-simps init-fin-descend-thr-def thr-init-fin-list-state' o-def map-option.compositionality map-option.identity split-beta*)

qed

lemma *if-Red1'-Red1-bisim-into-weak*:

assumes *wf*: *wf-J1-prog P*

shows *bisimulation-into-delay (Red1-mthr.if.redT False P) (Red1-mthr.if.redT True P) if-mbisim-Red1'-Red1 (=) (Red1-mthr.if.mτmove P) (Red1-mthr.if.mτmove P)*

proof –

interpret *b*: *bisimulation Red1-mthr.if.redT False P Red1-mthr.if.redT True P if-mbisim-Red1'-Red1 (=)*

by(*rule Red1'-Red1-if-bisimulation[OF wf]*)

show *?thesis by(unfold-locales)(simp add: if-mbisim-Red1'-Red1-def)*

qed

lemma *if-Red1'-Red1-bisimulation-final*:

wf-J1-prog P

\implies *bisimulation-final (Red1-mthr.if.redT False P) (Red1-mthr.if.redT True P)*

if-mbisim-Red1'-Red1 (=) Red1-mthr.if.mfinal Red1-mthr.if.mfinal

apply(*intro-locales*)

apply(*erule Red1'-Red1-if-bisimulation*)


```

apply(unfold-locales)
apply(auto simp add: if-mbisim-Red1'-Red1-def)
done

end

end

```

7.24 Semantic Correctness of Stage 1

```

theory Correctness1 imports

```

```

  J0J1Bisim

```

```

  ../J/DefAssPreservation

```

```

begin

```

```

lemma finals-map-Val [simp]: finals (map Val vs)
by(simp add: finals-iff)

```

```

context J-heap-base begin

```

```

lemma  $\tau$ red0r-preserves-defass:

```

```

  assumes wf: wf-J-prog P

```

```

  shows  $\llbracket \tau$ red0r extTA P t h (e, xs) (e', xs');  $\mathcal{D} e \llbracket \text{dom } xs \rrbracket \rrbracket \implies \mathcal{D} e' \llbracket \text{dom } xs' \rrbracket \rrbracket$ 
by(induct rule: rtranclp-induct2)(auto dest: red-preserves-defass[OF wf])

```

```

lemma  $\tau$ red0t-preserves-defass:

```

```

  assumes wf: wf-J-prog P

```

```

  shows  $\llbracket \tau$ red0t extTA P t h (e, xs) (e', xs');  $\mathcal{D} e \llbracket \text{dom } xs \rrbracket \rrbracket \implies \mathcal{D} e' \llbracket \text{dom } xs' \rrbracket \rrbracket$ 
by(rule  $\tau$ red0r-preserves-defass[OF wf])(rule tranclp-into-rtranclp)

```

```

end

```

```

lemma LAss-lem:

```

```

 $\llbracket x \in \text{set } xs; \text{size } xs \leq \text{size } ys \rrbracket$ 

```

```

 $\implies m1 \subseteq_m m2(xs \mapsto ys) \implies m1(x \mapsto y) \subseteq_m m2(xs \mapsto ys[\text{index } xs \ x := y])$ 

```

```

apply(simp add:map-le-def)

```

```

apply(simp add:fun-upds-apply index-less-aux eq-sym-conv)

```

```

done

```

```

lemma Block-lem:

```

```

fixes l :: 'a  $\rightarrow$  'b

```

```

assumes 0: l  $\subseteq_m$  [Vs  $\mapsto$ ] ls]

```

```

  and 1: l'  $\subseteq_m$  [Vs  $\mapsto$ ] ls', V  $\mapsto$  v]

```

```

  and hidden: V  $\in$  set Vs  $\implies$  ls ! index Vs V = ls' ! index Vs V

```

```

  and size: size ls = size ls'   size Vs < size ls'

```

```

shows l'(V := l V)  $\subseteq_m$  [Vs  $\mapsto$ ] ls']

```

```

proof -

```

```

  have l'(V := l V)  $\subseteq_m$  [Vs  $\mapsto$ ] ls', V  $\mapsto$  v](V := l V)

```

```

    using 1 by(rule map-le-upd)

```

```

  also have ... = [Vs  $\mapsto$ ] ls'](V := l V) by simp

```

```

  also have ...  $\subseteq_m$  [Vs  $\mapsto$ ] ls']

```

```

  proof (cases l V)

```

```

    case None thus ?thesis by simp

```

```

next
  case (Some w)
  hence [Vs [↦] ls] V = Some w
    using 0 by (force simp add: map-le-def split:if-splits)
  hence VinVs: V ∈ set Vs and w: w = ls ! index Vs V
    using size by (auto simp add: fun-upds-apply split:if-splits)
  hence w = ls' ! index Vs V using hidden[OF VinVs] by simp
  hence [Vs [↦] ls'](V := l V) = [Vs [↦] ls']
    using Some size VinVs by (simp add: index-less-aux map-upds-upd-conv-index)
  thus ?thesis by simp
qed
finally show ?thesis .
qed

```

7.24.1 Correctness proof

```

locale J0-J1-heap-base =
  J?: J-heap-base +
  J1?: J1-heap-base +
  constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
  and thread-id2addr :: 'thread-id ⇒ 'addr
  and empty-heap :: 'heap
  and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
  and typeof-addr :: 'heap ⇒ 'addr → htype
  and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
  and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
begin

```

lemma *ta-bisim01-extTA2J0-extTA2J1*:

```

  assumes wf: wf-J-prog P
  and nt: ∧ n T C M a h. [ n < length {ta}_t; {ta}_t ! n = NewThread T (C, M, a) h ]
    ⇒ typeof-addr h a = [Class-type C] ∧ (∃ T meth D. P ⊢ C sees M:[] → T = [meth] in D)
  shows ta-bisim01 (extTA2J0 P ta) (extTA2J1 (compP1 P) ta)
apply (simp add: ta-bisim-def ta-upd-simps)
apply (auto intro!: list-all2-all-nthI)
apply (case-tac {ta}_t ! n)
  apply (auto simp add: bisim-red0-Red1-def)
apply (drule (1) nt)
apply (clarify)
apply (erule bisim-list-extTA2J0-extTA2J1 [OF wf, simplified])
done

```

lemma *red-external-ta-bisim01*:

```

[ wf-J-prog P; P, t ⊢ ⟨a · M(vs), h⟩ -ta → ext ⟨va, h'⟩ ] ⇒ ta-bisim01 (extTA2J0 P ta) (extTA2J1
(compP1 P) ta)
apply (rule ta-bisim01-extTA2J0-extTA2J1, assumption)
apply (drule (1) red-external-new-thread-sees, auto simp add: in-set-conv-nth)
apply (drule red-ext-new-thread-heap, auto simp add: in-set-conv-nth)
done

```

lemmas *τred1t-expr* =

```

NewArray-τred1t-xt Cast-τred1t-xt InstanceOf-τred1t-xt BinOp-τred1t-xt1 BinOp-τred1t-xt2 LAss-τred1t
AAcc-τred1t-xt1 AAcc-τred1t-xt2 AAss-τred1t-xt1 AAss-τred1t-xt2 AAss-τred1t-xt3
ALength-τred1t-xt FAcc-τred1t-xt FAss-τred1t-xt1 FAss-τred1t-xt2

```

CAS- τ red1t-xt1 CAS- τ red1t-xt2 CAS- τ red1t-xt3 Call- τ red1t-obj
Call- τ red1t-param Block-None- τ red1t-xt Block- τ red1t-Some Sync- τ red1t-xt InSync- τ red1t-xt
Seq- τ red1t-xt Cond- τ red1t-xt Throw- τ red1t-xt Try- τ red1t-xt

lemmas τ red1r-expr =

NewArray- τ red1r-xt Cast- τ red1r-xt InstanceOf- τ red1r-xt BinOp- τ red1r-xt1 BinOp- τ red1r-xt2 LAss- τ red1r
AAcc- τ red1r-xt1 AAcc- τ red1r-xt2 AAss- τ red1r-xt1 AAss- τ red1r-xt2 AAss- τ red1r-xt3
ALength- τ red1r-xt FAcc- τ red1r-xt FAss- τ red1r-xt1 FAss- τ red1r-xt2
CAS- τ red1r-xt1 CAS- τ red1r-xt2 CAS- τ red1r-xt3 Call- τ red1r-obj
Call- τ red1r-param Block-None- τ red1r-xt Block- τ red1r-Some Sync- τ red1r-xt InSync- τ red1r-xt
Seq- τ red1r-xt Cond- τ red1r-xt Throw- τ red1r-xt Try- τ red1r-xt

definition *sim-move01* ::

'addr J1-prog \Rightarrow *'thread-id* \Rightarrow (*'addr, 'thread-id, 'heap*) *J0-thread-action* \Rightarrow *'addr expr* \Rightarrow *'addr expr1*
 \Rightarrow *'heap*
 \Rightarrow *'addr locals1* \Rightarrow (*'addr, 'thread-id, 'heap*) *external-thread-action* \Rightarrow *'addr expr1* \Rightarrow *'heap* \Rightarrow *'addr*
locals1 \Rightarrow *bool*

where

sim-move01 *P t ta0 e0 e h xs ta e' h' xs'* \longleftrightarrow \neg *final e0* \wedge
 (*if* τ *move0* *P h e0* *then* *h' = h* \wedge *ta0 = ε* \wedge *ta = ε* \wedge τ *red1't P t h (e, xs) (e', xs')*
else ta-bisim01 ta0 (extTA2J1 P ta) \wedge
 (*if* *call e0 = None* \vee *call1 e = None*
then (\exists *e'' xs''*. τ *red1'r P t h (e, xs) (e'', xs'')* \wedge *False, P, t \vdash 1* $\langle e'', (h, xs'') \rangle$ \rightarrow $\langle e', (h', xs') \rangle$)
 \wedge
 \neg τ *move1 P h e''*)
else False, P, t \vdash 1 $\langle e, (h, xs) \rangle$ \rightarrow $\langle e', (h', xs') \rangle$ \wedge \neg τ *move1 P h e*)

definition *sim-moves01* ::

'addr J1-prog \Rightarrow *'thread-id* \Rightarrow (*'addr, 'thread-id, 'heap*) *J0-thread-action* \Rightarrow *'addr expr list* \Rightarrow *'addr*
expr1 list \Rightarrow *'heap*
 \Rightarrow *'addr locals1* \Rightarrow (*'addr, 'thread-id, 'heap*) *external-thread-action* \Rightarrow *'addr expr1 list* \Rightarrow *'heap* \Rightarrow
'addr locals1 \Rightarrow *bool*

where

sim-moves01 *P t ta0 es0 es h xs ta es' h' xs'* \longleftrightarrow \neg *finals es0* \wedge
 (*if* τ *moves0* *P h es0* *then* *h' = h* \wedge *ta0 = ε* \wedge *ta = ε* \wedge τ *reds1't P t h (es, xs) (es', xs')*
else ta-bisim01 ta0 (extTA2J1 P ta) \wedge
 (*if* *calls es0 = None* \vee *calls1 es = None*
then (\exists *es'' xs''*. τ *reds1'r P t h (es, xs) (es'', xs'')* \wedge *False, P, t \vdash 1* $\langle es'', (h, xs'') \rangle$ \rightarrow $\langle es', (h',$
*xs') \rangle \wedge
 \neg τ *moves1 P h es''*)
else False, P, t \vdash 1 $\langle es, (h, xs) \rangle$ \rightarrow $\langle es', (h', xs') \rangle$ \wedge \neg τ *moves1 P h es*)*

declare τ red1t-expr [elim!] τ red1r-expr[elim!]

lemma *sim-move01-expr*:

assumes *sim-move01 P t ta0 e0 e h xs ta e' h' xs'*

shows

sim-move01 P t ta0 (newA T[e0]) (newA T[e]) h xs ta (newA T[e']) h' xs'
sim-move01 P t ta0 (Cast T e0) (Cast T e) h xs ta (Cast T e') h' xs'
sim-move01 P t ta0 (e0 instanceof T) (e instanceof T) h xs ta (e' instanceof T) h' xs'
sim-move01 P t ta0 (e0 «bop» e2) (e «bop» e2') h xs ta (e' «bop» e2') h' xs'
sim-move01 P t ta0 (Val v «bop» e0) (Val v «bop» e) h xs ta (Val v «bop» e') h' xs'
sim-move01 P t ta0 (V := e0) (V := e) h xs ta (V := e') h' xs'
sim-move01 P t ta0 (e0[e2]) (e[e2']) h xs ta (e'[e2']) h' xs'

$sim-move01 P t ta0 (Val v[e0]) (Val v[e]) h xs ta (Val v[e']) h' xs'$
 $sim-move01 P t ta0 (e0[e2] := e3) (e[e2'] := e3') h xs ta (e'[e2'] := e3') h' xs'$
 $sim-move01 P t ta0 (Val v[e0] := e3) (Val v[e] := e3') h xs ta (Val v[e'] := e3') h' xs'$
 $sim-move01 P t ta0 (AAss (Val v) (Val v') e0) (AAss (Val v) (Val v') e) h xs ta (AAss (Val v) (Val v') e') h' xs'$
 $sim-move01 P t ta0 (e0.length) (e.length) h xs ta (e'.length) h' xs'$
 $sim-move01 P t ta0 (e0.F\{D\}) (e.F\{D'\}) h xs ta (e'.F\{D'\}) h' xs'$
 $sim-move01 P t ta0 (FAss e0 F D e2) (FAss e F' D' e2') h xs ta (FAss e' F' D' e2') h' xs'$
 $sim-move01 P t ta0 (FAss (Val v) F D e0) (FAss (Val v) F' D' e) h xs ta (FAss (Val v) F' D' e') h' xs'$
 $sim-move01 P t ta0 (CompareAndSwap e0 D F e2 e3) (CompareAndSwap e D F e2' e3') h xs ta (CompareAndSwap e' D F e2' e3') h' xs'$
 $sim-move01 P t ta0 (CompareAndSwap (Val v) D F e0 e3) (CompareAndSwap (Val v) D F e e3') h xs ta (CompareAndSwap (Val v) D F e' e3') h' xs'$
 $sim-move01 P t ta0 (CompareAndSwap (Val v) D F (Val v') e0) (CompareAndSwap (Val v) D F (Val v') e) h xs ta (CompareAndSwap (Val v) D F (Val v') e') h' xs'$
 $sim-move01 P t ta0 (e0.M(es)) (e.M(es')) h xs ta (e'.M(es')) h' xs'$
 $sim-move01 P t ta0 (\{V:T=vo; e0\}) (\{V':T=None; e\}) h xs ta (\{V':T=None; e'\}) h' xs'$
 $sim-move01 P t ta0 (sync(e0) e2) (sync_{V'}(e) e2') h xs ta (sync_{V'}(e') e2') h' xs'$
 $sim-move01 P t ta0 (insync(a) e0) (insync_{V'}(a') e) h xs ta (insync_{V'}(a') e') h' xs'$
 $sim-move01 P t ta0 (e0;;e2) (e;;e2') h xs ta (e';;e2') h' xs'$
 $sim-move01 P t ta0 (if (e0) e2 else e3) (if (e) e2' else e3') h xs ta (if (e') e2' else e3') h' xs'$
 $sim-move01 P t ta0 (throw e0) (throw e) h xs ta (throw e') h' xs'$
 $sim-move01 P t ta0 (try e0 catch(C V) e2) (try e catch(C' V') e2') h xs ta (try e' catch(C' V') e2') h' xs'$

using *assms*

apply(*simp-all add: sim-move01-def final-iff $\tau red1r$ -Val $\tau red1t$ -Val split: if-split-asm split del: if-split*)

apply(*fastforce simp add: final-iff $\tau red1r$ -Val $\tau red1t$ -Val split!: if-splits intro: red1-reds1.intros*)+

done

lemma *sim-moves01-expr:*

$sim-move01 P t ta0 e0 e h xs ta e' h' xs' \implies sim-moves01 P t ta0 (e0 \# es2) (e \# es2') h xs ta (e' \# es2') h' xs'$

$sim-moves01 P t ta0 es0 es h xs ta es' h' xs' \implies sim-moves01 P t ta0 (Val v \# es0) (Val v \# es) h xs ta (Val v \# es') h' xs'$

apply(*simp-all add: sim-move01-def sim-moves01-def final-iff finals-iff Cons-eq-append-conv $\tau red1t$ -Val $\tau red1r$ -Val split: if-split-asm split del: if-split*)

apply(*auto simp add: Cons-eq-append-conv $\tau red1t$ -Val $\tau red1r$ -Val split!: if-splits intro: List1Red1 List1Red2 $\tau red1t$ -inj- $\tau reds1t$ $\tau red1r$ -inj- $\tau reds1r$ $\tau reds1t$ -cons- $\tau reds1t$ $\tau reds1r$ -cons- $\tau reds1r$*)

apply(*force elim!: $\tau red1r$ -inj- $\tau reds1r$ List1Red1*)

apply(*force elim!: $\tau red1r$ -inj- $\tau reds1r$ List1Red1*)

apply(*force elim!: $\tau red1r$ -inj- $\tau reds1r$ List1Red1*)

apply(*force elim!: $\tau red1r$ -inj- $\tau reds1r$ List1Red1*)

apply(*force elim!: $\tau reds1r$ -cons- $\tau reds1r$ intro!: List1Red2*)

apply(*force elim!: $\tau reds1r$ -cons- $\tau reds1r$ intro!: List1Red2*)

done

lemma *sim-move01-CallParams:*

$sim-moves01 P t ta0 es0 es h xs ta es' h' xs'$

$\implies sim-move01 P t ta0 (Val v.M(es0)) (Val v.M(es)) h xs ta (Val v.M(es')) h' xs'$

apply(*clarsimp simp add: sim-move01-def sim-moves01-def $\tau reds1r$ -map-Val $\tau reds1t$ -map-Val is-vals-conv split: if-split-asm split del: if-split*)

apply(*fastforce simp add: sim-move01-def sim-moves01-def $\tau reds1r$ -map-Val $\tau reds1t$ -map-Val intro: Call- $\tau red1r$ -param Call1Params*)

apply(*rule conjI*, *fastforce*)
apply(*split if-split*)
apply(*rule conjI*)
apply(*clarsimp simp add: finals-iff*)
apply(*clarify*)
apply(*split if-split*)
apply(*rule conjI*)
apply(*simp del: call.simps calls.simps call1.simps calls1.simps*)
apply(*fastforce simp add: sim-move01-def sim-moves01-def τ red1r-Val τ red1t-Val τ reds1r-map-Val-Throw*
intro: Call- τ red1r-param Call1Params split: if-split-asm)
apply(*fastforce split: if-split-asm simp add: is-vals-conv τ reds1r-map-Val τ reds1r-map-Val-Throw*)
apply(*rule conjI*, *fastforce*)
apply(*fastforce simp add: sim-move01-def sim-moves01-def τ red1r-Val τ red1t-Val τ reds1t-map-Val*
 τ reds1r-map-Val is-vals-conv intro: Call- τ red1r-param Call1Params split: if-split-asm)
done

lemma *sim-move01-reds:*

$\llbracket (h', a) \in \text{allocate } h \text{ (Class-type } C); ta0 = \{\!\{ \text{NewHeapElem } a \text{ (Class-type } C) \}\!\}; ta = \{\!\{ \text{NewHeapElem } a \text{ (Class-type } C) \}\!\} \rrbracket$
 $\implies \text{sim-move01 } P \ t \ ta0 \ (new \ C) \ (new \ C) \ h \ xs \ ta \ (addr \ a) \ h' \ xs$
 $\text{allocate } h \text{ (Class-type } C) = \{\!\{ \}\!\} \implies \text{sim-move01 } P \ t \ \varepsilon \ (new \ C) \ (new \ C) \ h \ xs \ \varepsilon \ (THROW \ OutOfMemory) \ h \ xs$
 $\llbracket (h', a) \in \text{allocate } h \text{ (Array-type } T \text{ (nat (sint } i))); 0 \leq i; ta0 = \{\!\{ \text{NewHeapElem } a \text{ (Array-type } T \text{ (nat (sint } i)) \}\!\}; ta = \{\!\{ \text{NewHeapElem } a \text{ (Array-type } T \text{ (nat (sint } i)) \}\!\} \rrbracket$
 $\implies \text{sim-move01 } P \ t \ ta0 \ (newA \ T[Val \ (Intg \ i)]) \ (newA \ T[Val \ (Intg \ i)]) \ h \ xs \ ta \ (addr \ a) \ h' \ xs$
 $i < s \ 0 \implies \text{sim-move01 } P \ t \ \varepsilon \ (newA \ T[Val \ (Intg \ i)]) \ (newA \ T[Val \ (Intg \ i)]) \ h \ xs \ \varepsilon \ (THROW \ NegativeArraySize) \ h \ xs$
 $\llbracket \text{allocate } h \text{ (Array-type } T \text{ (nat (sint } i))) = \{\!\{ \}\!\}; 0 \leq i \rrbracket$
 $\implies \text{sim-move01 } P \ t \ \varepsilon \ (newA \ T[Val \ (Intg \ i)]) \ (newA \ T[Val \ (Intg \ i)]) \ h \ xs \ \varepsilon \ (THROW \ OutOfMemory) \ h \ xs$
 $\llbracket \text{typeof}_h \ v = \lfloor U \rfloor; P \vdash U \leq T \rrbracket$
 $\implies \text{sim-move01 } P \ t \ \varepsilon \ (Cast \ T \ (Val \ v)) \ (Cast \ T \ (Val \ v)) \ h \ xs \ \varepsilon \ (Val \ v) \ h \ xs$
 $\llbracket \text{typeof}_h \ v = \lfloor U \rfloor; \neg P \vdash U \leq T \rrbracket$
 $\implies \text{sim-move01 } P \ t \ \varepsilon \ (Cast \ T \ (Val \ v)) \ (Cast \ T \ (Val \ v)) \ h \ xs \ \varepsilon \ (THROW \ ClassCast) \ h \ xs$
 $\llbracket \text{typeof}_h \ v = \lfloor U \rfloor; b \longleftrightarrow v \neq Null \wedge P \vdash U \leq T \rrbracket$
 $\implies \text{sim-move01 } P \ t \ \varepsilon \ ((Val \ v) \ \text{instanceof } T) \ ((Val \ v) \ \text{instanceof } T) \ h \ xs \ \varepsilon \ (Val \ (Bool \ b)) \ h \ xs$
 $\text{binop } bop \ v1 \ v2 = Some \ (Inl \ v) \implies \text{sim-move01 } P \ t \ \varepsilon \ ((Val \ v1) \ \llbracket bop \rrbracket \ (Val \ v2)) \ (Val \ v1 \ \llbracket bop \rrbracket \ Val \ v2) \ h \ xs \ \varepsilon \ (Val \ v) \ h \ xs$
 $\text{binop } bop \ v1 \ v2 = Some \ (Inr \ a) \implies \text{sim-move01 } P \ t \ \varepsilon \ ((Val \ v1) \ \llbracket bop \rrbracket \ (Val \ v2)) \ (Val \ v1 \ \llbracket bop \rrbracket \ Val \ v2) \ h \ xs \ \varepsilon \ (Throw \ a) \ h \ xs$
 $\llbracket xs[V] = v; V < size \ xs \rrbracket \implies \text{sim-move01 } P \ t \ \varepsilon \ (Var \ V') \ (Var \ V) \ h \ xs \ \varepsilon \ (Val \ v) \ h \ xs$
 $V < length \ xs \implies \text{sim-move01 } P \ t \ \varepsilon \ (V' := Val \ v) \ (V := Val \ v) \ h \ xs \ \varepsilon \ unit \ h \ (xs[V := v])$
 $\text{sim-move01 } P \ t \ \varepsilon \ (null[Val \ v]) \ (null[Val \ v]) \ h \ xs \ \varepsilon \ (THROW \ NullPointer) \ h \ xs$
 $\llbracket \text{typeof-addr } h \ a = \lfloor \text{Array-type } T \ n \rfloor; i < s \ 0 \vee \text{sint } i \geq \text{int } n \rrbracket$
 $\implies \text{sim-move01 } P \ t \ \varepsilon \ (addr \ a[Val \ (Intg \ i)]) \ ((addr \ a)[Val \ (Intg \ i)]) \ h \ xs \ \varepsilon \ (THROW \ ArrayIndexOutOfBounds) \ h \ xs$
 $\llbracket \text{typeof-addr } h \ a = \lfloor \text{Array-type } T \ n \rfloor; 0 \leq i; \text{sint } i < \text{int } n; \text{heap-read } h \ a \ (ACell \ (nat \ (\text{sint } i))) \ v; ta0 = \{\!\{ \text{ReadMem } a \ (ACell \ (nat \ (\text{sint } i))) \ v \}\!\}; ta = \{\!\{ \text{ReadMem } a \ (ACell \ (nat \ (\text{sint } i))) \ v \}\!\} \rrbracket$
 $\implies \text{sim-move01 } P \ t \ ta0 \ (addr \ a[Val \ (Intg \ i)]) \ ((addr \ a)[Val \ (Intg \ i)]) \ h \ xs \ ta \ (Val \ v) \ h \ xs$
 $\text{sim-move01 } P \ t \ \varepsilon \ (null[Val \ v] := Val \ v') \ (null[Val \ v] := Val \ v') \ h \ xs \ \varepsilon \ (THROW \ NullPointer) \ h \ xs$
 $\llbracket \text{typeof-addr } h \ a = \lfloor \text{Array-type } T \ n \rfloor; i < s \ 0 \vee \text{sint } i \geq \text{int } n \rrbracket$

$\implies \text{sim-move01 } P t \varepsilon (\text{AAss } (\text{addr } a) (\text{Val } (\text{Intg } i)) (\text{Val } v)) (\text{AAss } (\text{addr } a) (\text{Val } (\text{Intg } i)) (\text{Val } v))$
 $h \text{ xs } \varepsilon (\text{THROW ArrayIndexOutOfBounds}) h \text{ xs}$
 $\llbracket \text{typeof-addr } h a = \lfloor \text{Array-type } T n \rfloor; 0 \leq s i; \text{sint } i < \text{int } n; \text{typeof}_h v = \lfloor U \rfloor; \neg (P \vdash U \leq T) \rrbracket$
 $\implies \text{sim-move01 } P t \varepsilon (\text{AAss } (\text{addr } a) (\text{Val } (\text{Intg } i)) (\text{Val } v)) (\text{AAss } (\text{addr } a) (\text{Val } (\text{Intg } i)) (\text{Val } v))$
 $h \text{ xs } \varepsilon (\text{THROW ArrayStore}) h \text{ xs}$
 $\llbracket \text{typeof-addr } h a = \lfloor \text{Array-type } T n \rfloor; 0 \leq s i; \text{sint } i < \text{int } n; \text{typeof}_h v = \text{Some } U; P \vdash U \leq T;$
 $\text{heap-write } h a (\text{ACell } (\text{nat } (\text{sint } i))) v h';$
 $\text{ta0} = \llbracket \text{WriteMem } a (\text{ACell } (\text{nat } (\text{sint } i))) v \rrbracket; \text{ta} = \llbracket \text{WriteMem } a (\text{ACell } (\text{nat } (\text{sint } i))) v \rrbracket \rrbracket$
 $\implies \text{sim-move01 } P t \text{ta0} (\text{AAss } (\text{addr } a) (\text{Val } (\text{Intg } i)) (\text{Val } v)) (\text{AAss } (\text{addr } a) (\text{Val } (\text{Intg } i)) (\text{Val } v))$
 $h \text{ xs } \text{ta unit } h' \text{ xs}$
 $\text{typeof-addr } h a = \lfloor \text{Array-type } T n \rfloor \implies \text{sim-move01 } P t \varepsilon (\text{addr } a \cdot \text{length}) (\text{addr } a \cdot \text{length}) h \text{ xs } \varepsilon$
 $(\text{Val } (\text{Intg } (\text{word-of-int } (\text{int } n)))) h \text{ xs}$
 $\text{sim-move01 } P t \varepsilon (\text{null} \cdot \text{length}) (\text{null} \cdot \text{length}) h \text{ xs } \varepsilon (\text{THROW NullPointer}) h \text{ xs}$

$\llbracket \text{heap-read } h a (\text{CField } D F) v; \text{ta0} = \llbracket \text{ReadMem } a (\text{CField } D F) v \rrbracket; \text{ta} = \llbracket \text{ReadMem } a (\text{CField } D F) v \rrbracket \rrbracket$
 $\implies \text{sim-move01 } P t \text{ta0} (\text{addr } a \cdot F\{D\}) (\text{addr } a \cdot F\{D\}) h \text{ xs } \text{ta} (\text{Val } v) h \text{ xs}$
 $\text{sim-move01 } P t \varepsilon (\text{null} \cdot F\{D\}) (\text{null} \cdot F\{D\}) h \text{ xs } \varepsilon (\text{THROW NullPointer}) h \text{ xs}$
 $\llbracket \text{heap-write } h a (\text{CField } D F) v h'; \text{ta0} = \llbracket \text{WriteMem } a (\text{CField } D F) v \rrbracket; \text{ta} = \llbracket \text{WriteMem } a (\text{CField } D F) v \rrbracket \rrbracket$
 $\implies \text{sim-move01 } P t \text{ta0} (\text{addr } a \cdot F\{D\} := \text{Val } v) (\text{addr } a \cdot F\{D\} := \text{Val } v) h \text{ xs } \text{ta unit } h' \text{ xs}$
 $\text{sim-move01 } P t \varepsilon (\text{null} \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v, \text{Val } v')) (\text{null} \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v, \text{Val } v'))$
 $h \text{ xs } \varepsilon (\text{THROW NullPointer}) h \text{ xs}$
 $\llbracket \text{heap-read } h a (\text{CField } D F) v''; \text{heap-write } h a (\text{CField } D F) v' h'; v'' = v;$
 $\text{ta0} = \llbracket \text{ReadMem } a (\text{CField } D F) v'', \text{WriteMem } a (\text{CField } D F) v' \rrbracket; \text{ta} = \llbracket \text{ReadMem } a (\text{CField } D F) v'', \text{WriteMem } a (\text{CField } D F) v' \rrbracket \rrbracket$
 $\implies \text{sim-move01 } P t \text{ta0} (\text{addr } a \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v, \text{Val } v')) (\text{addr } a \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v, \text{Val } v'))$
 $h \text{ xs } \text{ta true } h' \text{ xs}$
 $\llbracket \text{heap-read } h a (\text{CField } D F) v''; v'' \neq v;$
 $\text{ta0} = \llbracket \text{ReadMem } a (\text{CField } D F) v'' \rrbracket; \text{ta} = \llbracket \text{ReadMem } a (\text{CField } D F) v'' \rrbracket \rrbracket$
 $\implies \text{sim-move01 } P t \text{ta0} (\text{addr } a \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v, \text{Val } v')) (\text{addr } a \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v, \text{Val } v'))$
 $h \text{ xs } \text{ta false } h \text{ xs}$
 $\text{sim-move01 } P t \varepsilon (\text{null} \cdot F\{D\} := \text{Val } v) (\text{null} \cdot F\{D\} := \text{Val } v) h \text{ xs } \varepsilon (\text{THROW NullPointer}) h \text{ xs}$
 $\text{sim-move01 } P t \varepsilon (\{V':T=vo; \text{Val } u\}) (\{V':T=None; \text{Val } u\}) h \text{ xs } \varepsilon (\text{Val } u) h \text{ xs}$
 $V < \text{length } \text{xs} \implies \text{sim-move01 } P t \varepsilon (\text{sync}(\text{null}) e0) (\text{sync}_V(\text{null}) e1) h \text{ xs } \varepsilon (\text{THROW NullPointer})$
 $h (\text{xs}[V := \text{Null}])$
 $\text{sim-move01 } P t \varepsilon (\text{Val } v;; e0) (\text{Val } v;; e1) h \text{ xs } \varepsilon e1 h \text{ xs}$
 $\text{sim-move01 } P t \varepsilon (\text{if } (\text{true}) e0 \text{ else } e0') (\text{if } (\text{true}) e1 \text{ else } e1') h \text{ xs } \varepsilon e1 h \text{ xs}$
 $\text{sim-move01 } P t \varepsilon (\text{if } (\text{false}) e0 \text{ else } e0') (\text{if } (\text{false}) e1 \text{ else } e1') h \text{ xs } \varepsilon e1' h \text{ xs}$
 $\text{sim-move01 } P t \varepsilon (\text{throw null}) (\text{throw null}) h \text{ xs } \varepsilon (\text{THROW NullPointer}) h \text{ xs}$
 $\text{sim-move01 } P t \varepsilon (\text{try } (\text{Val } v) \text{ catch}(C V') e0) (\text{try } (\text{Val } v) \text{ catch}(C V) e1) h \text{ xs } \varepsilon (\text{Val } v) h \text{ xs}$
 $\llbracket \text{typeof-addr } h a = \lfloor \text{Class-type } D \rfloor; P \vdash D \preceq^* C; V < \text{length } \text{xs} \rrbracket$
 $\implies \text{sim-move01 } P t \varepsilon (\text{try } (\text{Throw } a) \text{ catch}(C V') e0) (\text{try } (\text{Throw } a) \text{ catch}(C V) e1) h \text{ xs } \varepsilon$
 $(\{V:\text{Class } C=\text{None}; e1\}) h (\text{xs}[V := \text{Addr } a])$
 $\text{sim-move01 } P t \varepsilon (\text{newA } T[\text{Throw } a]) (\text{newA } T[\text{Throw } a]) h \text{ xs } \varepsilon (\text{Throw } a) h \text{ xs}$
 $\text{sim-move01 } P t \varepsilon (\text{Cast } T (\text{Throw } a)) (\text{Cast } T (\text{Throw } a)) h \text{ xs } \varepsilon (\text{Throw } a) h \text{ xs}$
 $\text{sim-move01 } P t \varepsilon ((\text{Throw } a) \text{ instanceof } T) ((\text{Throw } a) \text{ instanceof } T) h \text{ xs } \varepsilon (\text{Throw } a) h \text{ xs}$
 $\text{sim-move01 } P t \varepsilon ((\text{Throw } a) \llbracket \text{bop} \rrbracket e0) ((\text{Throw } a) \llbracket \text{bop} \rrbracket e1) h \text{ xs } \varepsilon (\text{Throw } a) h \text{ xs}$
 $\text{sim-move01 } P t \varepsilon (\text{Val } v \llbracket \text{bop} \rrbracket (\text{Throw } a)) (\text{Val } v \llbracket \text{bop} \rrbracket (\text{Throw } a)) h \text{ xs } \varepsilon (\text{Throw } a) h \text{ xs}$
 $\text{sim-move01 } P t \varepsilon (V' := \text{Throw } a) (V := \text{Throw } a) h \text{ xs } \varepsilon (\text{Throw } a) h \text{ xs}$
 $\text{sim-move01 } P t \varepsilon (\text{Throw } a[e0]) (\text{Throw } a[e1]) h \text{ xs } \varepsilon (\text{Throw } a) h \text{ xs}$
 $\text{sim-move01 } P t \varepsilon (\text{Val } v[\text{Throw } a]) (\text{Val } v[\text{Throw } a]) h \text{ xs } \varepsilon (\text{Throw } a) h \text{ xs}$
 $\text{sim-move01 } P t \varepsilon (\text{Throw } a[e0] := e0') (\text{Throw } a[e1] := e1') h \text{ xs } \varepsilon (\text{Throw } a) h \text{ xs}$
 $\text{sim-move01 } P t \varepsilon (\text{Val } v[\text{Throw } a] := e0) (\text{Val } v[\text{Throw } a] := e1) h \text{ xs } \varepsilon (\text{Throw } a) h \text{ xs}$

$sim-move01 P t \varepsilon (Val v \lfloor Val v \rfloor := Throw a) (Val v \lfloor Val v \rfloor := Throw a) h xs \varepsilon (Throw a) h xs$
 $sim-move01 P t \varepsilon (Throw a \cdot length) (Throw a \cdot length) h xs \varepsilon (Throw a) h xs$
 $sim-move01 P t \varepsilon (Throw a \cdot F\{D\}) (Throw a \cdot F\{D\}) h xs \varepsilon (Throw a) h xs$
 $sim-move01 P t \varepsilon (Throw a \cdot F\{D\} := e0) (Throw a \cdot F\{D\} := e1) h xs \varepsilon (Throw a) h xs$
 $sim-move01 P t \varepsilon (Val v \cdot F\{D\} := Throw a) (Val v \cdot F\{D\} := Throw a) h xs \varepsilon (Throw a) h xs$
 $sim-move01 P t \varepsilon (Throw a \cdot compareAndSwap(D \cdot F, e2, e3)) (Throw a \cdot compareAndSwap(D \cdot F, e2', e3')) h xs \varepsilon (Throw a) h xs$
 $sim-move01 P t \varepsilon (Val v \cdot compareAndSwap(D \cdot F, Throw a, e3)) (Val v \cdot compareAndSwap(D \cdot F, Throw a, e3')) h xs \varepsilon (Throw a) h xs$
 $sim-move01 P t \varepsilon (Val v \cdot compareAndSwap(D \cdot F, Val v', Throw a)) (Val v \cdot compareAndSwap(D \cdot F, Val v', Throw a)) h xs \varepsilon (Throw a) h xs$
 $sim-move01 P t \varepsilon (Throw a \cdot M(es0)) (Throw a \cdot M(es1)) h xs \varepsilon (Throw a) h xs$
 $sim-move01 P t \varepsilon (\{V': T=vo; Throw a\}) (\{V': T=None; Throw a\}) h xs \varepsilon (Throw a) h xs$
 $sim-move01 P t \varepsilon (sync(Throw a) e0) (sync_V(Throw a) e1) h xs \varepsilon (Throw a) h xs$
 $sim-move01 P t \varepsilon (Throw a;;e0) (Throw a;;e1) h xs \varepsilon (Throw a) h xs$
 $sim-move01 P t \varepsilon (if (Throw a) e0 else e0') (if (Throw a) e1 else e1') h xs \varepsilon (Throw a) h xs$
 $sim-move01 P t \varepsilon (throw (Throw a)) (throw (Throw a)) h xs \varepsilon (Throw a) h xs$
apply(simp-all add: sim-move01-def ta-bisim-def split: if-split-asm split del: if-split)
apply(fastforce intro: red1-reds1.intros)+
done

lemma *sim-move01-ThrowParams*:

$sim-move01 P t \varepsilon (Val v \cdot M(map Val vs @ Throw a \# es0)) (Val v \cdot M(map Val vs @ Throw a \# es1)) h xs \varepsilon (Throw a) h xs$
apply(simp add: sim-move01-def split del: if-split)
apply(rule conjI, fastforce)
apply(split if-split)
apply(rule conjI)
apply(fastforce intro: red1-reds1.intros)
apply(fastforce simp add: sim-move01-def intro: red1-reds1.intros)
done

lemma *sim-move01-CallNull*:

$sim-move01 P t \varepsilon (null \cdot M(map Val vs)) (null \cdot M(map Val vs)) h xs \varepsilon (THROW NullPointer) h xs$
by(fastforce simp add: sim-move01-def map-eq-append-conv intro: red1-reds1.intros)

lemma *sim-move01-SyncLocks*:

$\llbracket V < length xs; ta0 = \{\!| Lock \rightarrow a, SyncLock a \!|\}; ta = \{\!| Lock \rightarrow a, SyncLock a \!|\} \rrbracket$
 $\implies sim-move01 P t ta0 (sync(addr a) e0) (sync_V(addr a) e1) h xs ta (insync_V(a) e1) h (xs[V := Addr a])$
 $\llbracket xs ! V = Addr a'; V < length xs; ta0 = \{\!| Unlock \rightarrow a', SyncUnlock a' \!|\}; ta = \{\!| Unlock \rightarrow a', SyncUnlock a' \!|\} \rrbracket$
 $\implies sim-move01 P t ta0 (insync(a') (Val v)) (insync_V(a) (Val v)) h xs ta (Val v) h xs$
 $\llbracket xs ! V = Addr a'; V < length xs; ta0 = \{\!| Unlock \rightarrow a', SyncUnlock a' \!|\}; ta = \{\!| Unlock \rightarrow a', SyncUnlock a' \!|\} \rrbracket$
 $\implies sim-move01 P t ta0 (insync(a') (Throw a')) (insync_V(a) (Throw a')) h xs ta (Throw a') h xs$
by(fastforce simp add: sim-move01-def ta-bisim-def expand-funfun-eq fun-eq-iff funfun-upd-apply ta-upd-simps intro: red1-reds1.intros[simplified] split: if-split-asm)+

lemma *sim-move01-TryFail*:

$\llbracket typeof-addr h a = \lfloor Class-type D \rfloor; \neg P \vdash D \preceq^* C \rrbracket$
 $\implies sim-move01 P t \varepsilon (try (Throw a) catch(C V') e0) (try (Throw a) catch(C V) e1) h xs \varepsilon (Throw a) h xs$
by(auto simp add: sim-move01-def intro!: Red1TryFail)

lemma *sim-move01-BlockSome*:

```

[[ sim-move01 P t ta0 e0 e h (xs[V := v]) ta e' h' xs'; V < length xs ]]
  ==> sim-move01 P t ta0 ({V':T=[v]; e0}) ({V:T=[v]; e}) h xs ta ({V:T=None; e'}) h' xs'
  V < length xs ==> sim-move01 P t ε ({V':T=[v]; Val u}) ({V:T=[v]; Val u}) h xs ε (Val u) h
(xs[V := v])
  V < length xs ==> sim-move01 P t ε ({V':T=[v]; Throw a}) ({V:T=[v]; Throw a}) h xs ε (Throw
a) h (xs[V := v])
apply(auto simp add: sim-move01-def)
apply(split if-split-asm)
apply(fastforce intro: intro: converse-rtranclp-into-rtranclp Block1Some Block1Red Block-τred1r-Some)
apply(fastforce intro: intro: converse-rtranclp-into-rtranclp Block1Some Block1Red Block-τred1r-Some)
apply(fastforce simp add: sim-move01-def intro!: τred1t-2step[OF Block1Some] τred1r-1step[OF Block1Some]
Red1Block Block1Throw)+
done

```

lemmas *sim-move01-intros* =

```

sim-move01-expr sim-move01-reds sim-move01-ThrowParams sim-move01-CallNull sim-move01-TryFail
sim-move01-BlockSome sim-move01-CallParams

```

declare *sim-move01-intros*[intro]

lemma *sim-move01-preserves-len*: *sim-move01 P t ta0 e0 e h xs ta e' h' xs' ==> length xs' = length xs*
by(fastforce simp add: sim-move01-def split: if-split-asm dest: τred1r-preserves-len τred1t-preserves-len red1-preserves-len)

lemma *sim-move01-preserves-unmod*:

```

[[ sim-move01 P t ta0 e0 e h xs ta e' h' xs'; unmod e i; i < length xs ]] ==> xs' ! i = xs ! i
apply(auto simp add: sim-move01-def split: if-split-asm dest: τred1t-preserves-unmod)
apply(frule (2) τred1'r-preserves-unmod)
apply(frule (1) τred1r-unmod-preserved)
apply(frule τred1r-preserves-len)
apply(auto dest: red1-preserves-unmod)
apply(frule (2) τred1'r-preserves-unmod)
apply(frule (1) τred1r-unmod-preserved)
apply(frule τred1r-preserves-len)
apply(auto dest: red1-preserves-unmod)
done

```

lemma *assumes wf: wf-J-prog P*

shows *red1-simulates-red-aux*:

```

[[ extTA2J0 P,P,t ⊢ ⟨e1, S⟩ -TA→ ⟨e1', S'⟩; bisim vs e1 e2 XS; fv e1 ⊆ set vs;
  lcl S ⊆m [vs [↦] XS]; length vs + max-vars e1 ≤ length XS;
  ∀ aMvs. call e1 = [aMvs] → synthesized-call P (hp S) aMvs ]]
  ==> ∃ ta e2' XS'. sim-move01 (compP1 P) t TA e1 e2 (hp S) XS ta e2' (hp S') XS' ∧ bisim vs e1'
e2' XS' ∧ lcl S' ⊆m [vs [↦] XS']
  (is [[ -; -; -; -; ?synth e1 S ]] ==> ?concl e1 e2 S XS e1' S' TA vs)

```

and *reds1-simulates-reds-aux*:

```

[[ extTA2J0 P,P,t ⊢ ⟨es1, S⟩ [-TA→] ⟨es1', S'⟩; bisims vs es1 es2 XS; fvs es1 ⊆ set vs;
  lcl S ⊆m [vs [↦] XS]; length vs + max-varss es1 ≤ length XS;
  ∀ aMvs. calls es1 = [aMvs] → synthesized-call P (hp S) aMvs ]]
  ==> ∃ ta es2' xs'. sim-moves01 (compP1 P) t TA es1 es2 (hp S) XS ta es2' (hp S') xs' ∧ bisims vs
es1' es2' xs' ∧ lcl S' ⊆m [vs [↦] xs']

```


(is [-; -; -; -; ?synths es1 S] \implies ?concls es1 es2 S XS es1' S' TA vs)

proof(*induct arbitrary: vs e2 XS and vs es2 XS rule: red-reds.inducts*)

case (BinOpRed1 e s ta e' s' bop e2 Vs E2 xs)

note IH = $\langle \bigwedge vs e2 XS. \llbracket bisim\ vs\ e\ e2\ XS; fv\ e \subseteq set\ vs; lcl\ s \subseteq_m [vs\ \mapsto] XS \rrbracket; length\ vs + max\text{-}vars\ e \leq length\ XS;$

$\llbracket ?synth\ e\ s \rrbracket \implies ?concl\ e\ e2\ s\ XS\ e'\ s'\ ta\ vs$

from $\langle extTA2J0\ P,P,t \vdash \langle e,s \rangle -ta \rightarrow \langle e',s' \rangle \rangle$ **have** $\neg is\text{-}val\ e$ **by** *auto*

with $\langle bisim\ Vs\ (e\ \langle bop \rangle\ e2)\ E2\ xs \rangle$ **obtain** E

where E2 = E $\langle bop \rangle$ compE1 Vs e2 **and** bisim Vs e E xs **and** $\neg contains\text{-}insync\ e2$ **by** *auto*

with IH[*of* Vs E xs] $\langle fv\ (e\ \langle bop \rangle\ e2) \subseteq set\ Vs \rangle \langle lcl\ s \subseteq_m [Vs\ \mapsto] xs \rangle \langle \neg is\text{-}val\ e \rangle$

$\langle length\ Vs + max\text{-}vars\ (e\ \langle bop \rangle\ e2) \leq length\ xs \rangle \langle ?synth\ (e\ \langle bop \rangle\ e2)\ s \rangle \langle extTA2J0\ P,P,t \vdash \langle e,s \rangle -ta \rightarrow \langle e',s' \rangle \rangle$

show ?case **by**(cases is-val e[^](fastforce elim!: sim-move01-expr)+

next

case (BinOpRed2 e s ta e' s' v bop Vs E2 xs)

note IH = $\langle \bigwedge vs e2 XS. \llbracket bisim\ vs\ e\ e2\ XS; fv\ e \subseteq set\ vs; lcl\ s \subseteq_m [vs\ \mapsto] XS \rrbracket; length\ vs + max\text{-}vars\ e \leq length\ XS;$

$\llbracket ?synth\ e\ s \rrbracket \implies ?concl\ e\ e2\ s\ XS\ e'\ s'\ ta\ vs$

from $\langle bisim\ Vs\ (Val\ v\ \langle bop \rangle\ e)\ E2\ xs \rangle$ **obtain** E

where E2 = Val v $\langle bop \rangle$ E **and** bisim Vs e E xs **by** *auto*

with IH[*of* Vs E xs] $\langle fv\ (Val\ v\ \langle bop \rangle\ e) \subseteq set\ Vs \rangle \langle lcl\ s \subseteq_m [Vs\ \mapsto] xs \rangle$

$\langle length\ Vs + max\text{-}vars\ (Val\ v\ \langle bop \rangle\ e) \leq length\ xs \rangle \langle ?synth\ (Val\ v\ \langle bop \rangle\ e)\ s \rangle \langle extTA2J0\ P,P,t \vdash \langle e,s \rangle -ta \rightarrow \langle e',s' \rangle \rangle$

show ?case **by**(fastforce elim!: sim-move01-expr)

next

case RedVar **thus** ?case

by(fastforce simp add: index-less-aux map-le-def fun-upds-apply intro!: exI dest: bspec)

next

case RedLAss **thus** ?case

by(fastforce intro: index-less-aux LAss-lem intro!: exI simp del: fun-upd-apply)

next

case (AAccRed1 a s ta a' s' i Vs E2 xs)

note IH = $\langle \bigwedge vs e2 XS. \llbracket bisim\ vs\ a\ e2\ XS; fv\ a \subseteq set\ vs; lcl\ s \subseteq_m [vs\ \mapsto] XS \rrbracket; length\ vs + max\text{-}vars\ a \leq length\ XS;$

$\llbracket ?synth\ a\ s \rrbracket \implies ?concl\ a\ e2\ s\ XS\ a'\ s'\ ta\ vs$

from $\langle extTA2J0\ P,P,t \vdash \langle a,s \rangle -ta \rightarrow \langle a',s' \rangle \rangle$ **have** $\neg is\text{-}val\ a$ **by** *auto*

with $\langle bisim\ Vs\ (a[i])\ E2\ xs \rangle$ **obtain** E

where E2 = E | compE1 Vs i | **and** bisim Vs a E xs **and** $\neg contains\text{-}insync\ i$ **by** *auto*

with IH[*of* Vs E xs] $\langle fv\ (a[i]) \subseteq set\ Vs \rangle \langle lcl\ s \subseteq_m [Vs\ \mapsto] xs \rangle \langle \neg is\text{-}val\ a \rangle$

$\langle length\ Vs + max\text{-}vars\ (a[i]) \leq length\ xs \rangle \langle ?synth\ (a[i])\ s \rangle \langle extTA2J0\ P,P,t \vdash \langle a,s \rangle -ta \rightarrow \langle a',s' \rangle \rangle$

show ?case **by**(cases is-val a[^](fastforce elim!: sim-move01-expr)+

next

case (AAccRed2 i s ta i' s' a Vs E2 xs)

note IH = $\langle \bigwedge vs e2 XS. \llbracket bisim\ vs\ i\ e2\ XS; fv\ i \subseteq set\ vs; lcl\ s \subseteq_m [vs\ \mapsto] XS \rrbracket; length\ vs + max\text{-}vars\ i \leq length\ XS;$

$\llbracket ?synth\ i\ s \rrbracket \implies ?concl\ i\ e2\ s\ XS\ i'\ s'\ ta\ vs$

from $\langle bisim\ Vs\ (Val\ a[i])\ E2\ xs \rangle$ **obtain** E

where E2 = Val a[E] **and** bisim Vs i E xs **by** *auto*

with IH[*of* Vs E xs] $\langle fv\ (Val\ a[i]) \subseteq set\ Vs \rangle \langle lcl\ s \subseteq_m [Vs\ \mapsto] xs \rangle$

$\langle length\ Vs + max\text{-}vars\ (Val\ a[i]) \leq length\ xs \rangle \langle ?synth\ (Val\ a[i])\ s \rangle \langle extTA2J0\ P,P,t \vdash \langle i,s \rangle -ta \rightarrow \langle i',s' \rangle \rangle$

show ?case **by**(fastforce elim!: sim-move01-expr)

next

case RedAAcc **thus** ?case **by**(auto simp del: split-paired-Ex)

next

case (*AAssRed1* $a\ s\ ta\ a'\ s'\ i\ e\ Vs\ E2\ xs$)
note $IH = \langle \bigwedge vs\ e2\ XS. \llbracket bisim\ vs\ a\ e2\ XS; fv\ a \subseteq set\ vs; lcl\ s \subseteq_m [vs\ \mapsto] XS \rrbracket; length\ vs + max\text{-}vars\ a \leq length\ XS;$
 $\llbracket ?synth\ a\ s \rrbracket \implies ?concl\ a\ e2\ s\ XS\ a'\ s'\ ta\ vs \rangle$
from $\langle extTA2J0\ P,P,t \vdash \langle a,s \rangle -ta \rightarrow \langle a',s' \rangle \rangle$ **have** $\neg is\text{-}val\ a$ **by** *auto*
with $\langle bisim\ Vs\ (a[i] := e)\ E2\ xs \rangle$ **obtain** E
where $E2: E2 = E[compE1\ Vs\ i] := compE1\ Vs\ e$ **and** $bisim\ Vs\ a\ E\ xs$
and $sync: \neg contains\text{-}insync\ i \neg contains\text{-}insync\ e$ **by** *auto*
with $IH[of\ Vs\ E\ xs] \langle fv\ (a[i] := e) \subseteq set\ Vs \rangle \langle lcl\ s \subseteq_m [Vs\ \mapsto] xs \rangle \langle \neg is\text{-}val\ a \rangle \langle extTA2J0\ P,P,t \vdash \langle a,s \rangle -ta \rightarrow \langle a',s' \rangle \rangle$
 $\langle length\ Vs + max\text{-}vars\ (a[i] := e) \leq length\ xs \rangle \langle ?synth\ (a[i] := e)\ s \rangle$
obtain $ta'\ e2'\ xs'$
where $IH': sim\text{-}move01\ (compP1\ P)\ t\ ta\ a\ E\ (hp\ s)\ xs\ ta'\ e2'\ (hp\ s')\ xs'$
 $bisim\ Vs\ a'\ e2'\ xs'\ lcl\ s' \subseteq_m [Vs\ \mapsto] xs'$
by *auto*
show $?case$
proof(*cases is-val a'*)
case *True*
from $\langle fv\ (a[i] := e) \subseteq set\ Vs \rangle$ $sync$
have $bisim\ Vs\ i\ (compE1\ Vs\ i)\ xs'\ bisim\ Vs\ e\ (compE1\ Vs\ e)\ xs'$ **by** *auto*
with $IH'\ E2\ True\ sync\ \langle \neg is\text{-}val\ a \rangle \langle extTA2J0\ P,P,t \vdash \langle a,s \rangle -ta \rightarrow \langle a',s' \rangle \rangle$ **show** $?thesis$
by(*cases is-val i*)(*fastforce elim!: sim-move01-expr*)+
next
case *False* **with** $IH'\ E2\ sync\ \langle \neg is\text{-}val\ a \rangle \langle extTA2J0\ P,P,t \vdash \langle a,s \rangle -ta \rightarrow \langle a',s' \rangle \rangle$
show $?thesis$ **by**(*fastforce elim!: sim-move01-expr*)
qed
next
case (*AAssRed2* $i\ s\ ta\ i'\ s'\ a\ e\ Vs\ E2\ xs$)
note $IH = \langle \bigwedge vs\ e2\ XS. \llbracket bisim\ vs\ i\ e2\ XS; fv\ i \subseteq set\ vs; lcl\ s \subseteq_m [vs\ \mapsto] XS \rrbracket; length\ vs + max\text{-}vars\ i \leq length\ XS;$
 $\llbracket ?synth\ i\ s \rrbracket \implies ?concl\ i\ e2\ s\ XS\ i'\ s'\ ta\ vs \rangle$
from $\langle extTA2J0\ P,P,t \vdash \langle i,s \rangle -ta \rightarrow \langle i',s' \rangle \rangle$ **have** $\neg is\text{-}val\ i$ **by** *auto*
with $\langle bisim\ Vs\ (Val\ a[i] := e)\ E2\ xs \rangle$ **obtain** E
where $E2 = Val\ a[E] := compE1\ Vs\ e$ **and** $bisim\ Vs\ i\ E\ xs$ **and** $\neg contains\text{-}insync\ e$ **by** *auto*
with $IH[of\ Vs\ E\ xs] \langle fv\ (Val\ a[i] := e) \subseteq set\ Vs \rangle \langle lcl\ s \subseteq_m [Vs\ \mapsto] xs \rangle \langle \neg is\text{-}val\ i \rangle \langle extTA2J0\ P,P,t \vdash \langle i,s \rangle -ta \rightarrow \langle i',s' \rangle \rangle$
 $\langle length\ Vs + max\text{-}vars\ (Val\ a[i] := e) \leq length\ xs \rangle \langle ?synth\ (Val\ a[i] := e)\ s \rangle$
show $?case$ **by**(*cases is-val i'*)(*fastforce elim!: sim-move01-expr*)+
next
case (*AAssRed3* $e\ s\ ta\ e'\ s'\ a\ i\ Vs\ E2\ xs$)
note $IH = \langle \bigwedge vs\ e2\ XS. \llbracket bisim\ vs\ e\ e2\ XS; fv\ e \subseteq set\ vs; lcl\ s \subseteq_m [vs\ \mapsto] XS \rrbracket; length\ vs + max\text{-}vars\ e \leq length\ XS;$
 $\llbracket ?synth\ e\ s \rrbracket \implies ?concl\ e\ e2\ s\ XS\ e'\ s'\ ta\ vs \rangle$
from $\langle bisim\ Vs\ (Val\ a[Val\ i] := e)\ E2\ xs \rangle$ **obtain** E
where $E2 = Val\ a[Val\ i] := E$ **and** $bisim\ Vs\ e\ E\ xs$ **by** *auto*
with $IH[of\ Vs\ E\ xs] \langle fv\ (Val\ a[Val\ i] := e) \subseteq set\ Vs \rangle \langle lcl\ s \subseteq_m [Vs\ \mapsto] xs \rangle \langle extTA2J0\ P,P,t \vdash \langle e,s \rangle -ta \rightarrow \langle e',s' \rangle \rangle$
 $\langle length\ Vs + max\text{-}vars\ (Val\ a[Val\ i] := e) \leq length\ xs \rangle \langle ?synth\ (Val\ a[Val\ i] := e)\ s \rangle$
show $?case$ **by**(*fastforce elim!: sim-move01-expr*)
next
case *RedAAssStore* **thus** $?case$ **by**(*auto intro!: exI*)
next
case (*FAssRed1* $e\ s\ ta\ e'\ s'\ F\ D\ e2\ Vs\ E2\ xs$)

note $IH = \langle \bigwedge vs e2 XS. \llbracket bisim\ vs\ e\ e2\ XS; fv\ e \subseteq set\ vs; lcl\ s \subseteq_m [vs\ [\mapsto]\ XS]; length\ vs + max\ vars\ e \leq length\ XS; \rrbracket$

$\llbracket ?synth\ e\ s \rrbracket \implies ?concl\ e\ e2\ s\ XS\ e'\ s'\ ta\ vs$

from $\langle extTA2J0\ P,P,t \vdash \langle e,s \rangle -ta \rightarrow \langle e',s' \rangle \rangle$ **have** $\neg is\ val\ e$ **by** *auto*

with $\langle bisim\ Vs\ (e \cdot F\{D\} := e2)\ E2\ xs \rangle$ **obtain** E

where $E2 = E \cdot F\{D\} := compE1\ Vs\ e2$ **and** $bisim\ Vs\ e\ E\ xs$ **and** $\neg contains\ insync\ e2$ **by** *auto*

with IH [of $Vs\ E\ xs$] $\langle fv\ (e \cdot F\{D\} := e2) \subseteq set\ Vs \rangle \langle lcl\ s \subseteq_m [Vs\ [\mapsto]\ xs] \rangle \langle \neg is\ val\ e \rangle \langle extTA2J0\ P,P,t \vdash \langle e,s \rangle -ta \rightarrow \langle e',s' \rangle \rangle$

$\langle length\ Vs + max\ vars\ (e \cdot F\{D\} := e2) \leq length\ xs \rangle \langle ?synth\ (e \cdot F\{D\} := e2)\ s \rangle$

show $?case\ by(cases\ is\ val\ e^{\wedge}(fastforce\ elim!: sim\ move01\ expr)+$

next

case $(FAssRed2\ e\ s\ ta\ e'\ s'\ v\ F\ D\ Vs\ E2\ xs)$

note $IH = \langle \bigwedge vs e2 XS. \llbracket bisim\ vs\ e\ e2\ XS; fv\ e \subseteq set\ vs; lcl\ s \subseteq_m [vs\ [\mapsto]\ XS]; length\ vs + max\ vars\ e \leq length\ XS; \rrbracket$

$\llbracket ?synth\ e\ s \rrbracket \implies ?concl\ e\ e2\ s\ XS\ e'\ s'\ ta\ vs$

from $\langle bisim\ Vs\ (Val\ v \cdot F\{D\} := e)\ E2\ xs \rangle$ **obtain** E

where $E2 = Val\ v \cdot F\{D\} := E$ **and** $bisim\ Vs\ e\ E\ xs$ **by** *auto*

with IH [of $Vs\ E\ xs$] $\langle fv\ (Val\ v \cdot F\{D\} := e) \subseteq set\ Vs \rangle \langle lcl\ s \subseteq_m [Vs\ [\mapsto]\ xs] \rangle \langle extTA2J0\ P,P,t \vdash \langle e,s \rangle -ta \rightarrow \langle e',s' \rangle \rangle$

$\langle length\ Vs + max\ vars\ (Val\ v \cdot F\{D\} := e) \leq length\ xs \rangle \langle ?synth\ (Val\ v \cdot F\{D\} := e)\ s \rangle$

show $?case\ by(fastforce\ elim!: sim\ move01\ expr)$

next

case $(CASRed1\ e\ s\ ta\ e'\ s'\ D\ F\ e2\ e3\ Vs\ E2\ xs)$

note $IH = \langle \bigwedge vs e2 XS. \llbracket bisim\ vs\ e\ e2\ XS; fv\ e \subseteq set\ vs; lcl\ s \subseteq_m [vs\ [\mapsto]\ XS]; length\ vs + max\ vars\ e \leq length\ XS; \rrbracket$

$\llbracket ?synth\ e\ s \rrbracket \implies ?concl\ e\ e2\ s\ XS\ e'\ s'\ ta\ vs$

from $\langle extTA2J0\ P,P,t \vdash \langle e,s \rangle -ta \rightarrow \langle e',s' \rangle \rangle$ **have** $\neg is\ val\ e$ **by** *auto*

with $\langle bisim\ Vs\ -\ E2\ xs \rangle$ **obtain** E

where $E2: E2 = E \cdot compareAndSwap(D \cdot F, compE1\ Vs\ e2, compE1\ Vs\ e3)$ **and** $bisim\ Vs\ e\ E\ xs$

and $sync: \neg contains\ insync\ e2 \neg contains\ insync\ e3$ **by** *(auto)*

with IH [of $Vs\ E\ xs$] $\langle fv\ - \subseteq set\ Vs \rangle \langle lcl\ s \subseteq_m [Vs\ [\mapsto]\ xs] \rangle \langle \neg is\ val\ e \rangle \langle extTA2J0\ P,P,t \vdash \langle e,s \rangle -ta \rightarrow \langle e',s' \rangle \rangle$

$\langle length\ Vs + max\ vars\ - \leq length\ xs \rangle \langle ?synth\ -\ s \rangle$

obtain $ta'\ e2'\ xs'$

where $IH': sim\ move01\ (compP1\ P)\ t\ ta\ e\ E\ (hp\ s)\ xs\ ta'\ e2'\ (hp\ s')\ xs'$

$bisim\ Vs\ e'\ e2'\ xs'\ lcl\ s' \subseteq_m [Vs\ [\mapsto]\ xs']$

by *auto*

show $?case$

proof $(cases\ is\ val\ e')$

case *True*

from $\langle fv\ - \subseteq set\ Vs \rangle\ sync$

have $bisim\ Vs\ e2\ (compE1\ Vs\ e2)\ xs'\ bisim\ Vs\ e3\ (compE1\ Vs\ e3)\ xs'$ **by** *auto*

with $IH'\ E2\ True\ sync\ \langle \neg is\ val\ e \rangle \langle extTA2J0\ P,P,t \vdash \langle e,s \rangle -ta \rightarrow \langle e',s' \rangle \rangle$ **show** $?thesis$

by $(cases\ is\ val\ e2)(fastforce\ elim!: sim\ move01\ expr)+$

next

case *False* **with** $IH'\ E2\ sync\ \langle \neg is\ val\ e \rangle \langle extTA2J0\ P,P,t \vdash \langle e,s \rangle -ta \rightarrow \langle e',s' \rangle \rangle$

show $?thesis\ by(fastforce\ elim!: sim\ move01\ expr)$

qed

next

case $(CASRed2\ e\ s\ ta\ e'\ s'\ v\ D\ F\ e3\ Vs\ E2\ xs)$

note $IH = \langle \bigwedge vs e2 XS. \llbracket bisim\ vs\ e\ e2\ XS; fv\ e \subseteq set\ vs; lcl\ s \subseteq_m [vs\ [\mapsto]\ XS]; length\ vs + max\ vars\ e \leq length\ XS; \rrbracket$

$\llbracket ?synth\ e\ s \rrbracket \implies ?concl\ e\ e2\ s\ XS\ e'\ s'\ ta\ vs$

from $\langle extTA2J0\ P,P,t \vdash \langle e,s \rangle -ta \rightarrow \langle e',s' \rangle \rangle$ **have** $\neg is\ val\ e$ **by** *auto*

with $\langle \text{bisim } Vs - E2 \text{ } xs \rangle$ **obtain** E
where $E2 = \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, E, \text{comp}E1 \text{ } Vs \text{ } e3)$ **and** $\text{bisim } Vs \text{ } e \text{ } E \text{ } xs$ **and** \neg
 $\text{contains-insync } e3$ **by** (auto)
with $IH[\text{of } Vs \text{ } E \text{ } xs] \langle fv - \subseteq \text{set } Vs \rangle \langle \text{lcl } s \subseteq_m [Vs \text{ } \mapsto] \text{ } xs \rangle \langle \neg \text{is-val } e \rangle \langle \text{extTA2J0 } P, P, t \vdash \langle e, s \rangle$
 $-\text{ta} \rightarrow \langle e', s' \rangle \rangle$
 $\langle \text{length } Vs + \text{max-vars} - \leq \text{length } xs \rangle \langle ?\text{synth} - s \rangle$
show $?case$ **by** $(\text{cases is-val } e^\wedge)(\text{fastforce elim!}: \text{sim-move01-expr})+$
next
case $(\text{CASRed3 } e \text{ } s \text{ } ta \text{ } e' \text{ } s' \text{ } v \text{ } D \text{ } F \text{ } v' \text{ } Vs \text{ } E2 \text{ } xs)$
note $IH = \langle \bigwedge vs \text{ } e2 \text{ } XS. \llbracket \text{bisim } vs \text{ } e \text{ } e2 \text{ } XS; fv \text{ } e \subseteq \text{set } vs; \text{lcl } s \subseteq_m [vs \text{ } \mapsto] \text{ } XS \rrbracket; \text{length } vs + \text{max-vars}$
 $e \leq \text{length } XS;$
 $?synth \text{ } e \text{ } s \rrbracket \implies ?concl \text{ } e \text{ } e2 \text{ } s \text{ } XS \text{ } e' \text{ } s' \text{ } ta \text{ } vs \rangle$
from $\langle \text{bisim } Vs - E2 \text{ } xs \rangle$ **obtain** E
where $E2 = \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', E)$ **and** $\text{bisim } Vs \text{ } e \text{ } E \text{ } xs$ **by** auto
with $IH[\text{of } Vs \text{ } E \text{ } xs] \langle fv - \subseteq \text{set } Vs \rangle \langle \text{lcl } s \subseteq_m [Vs \text{ } \mapsto] \text{ } xs \rangle \langle \text{extTA2J0 } P, P, t \vdash \langle e, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle \rangle$
 $\langle \text{length } Vs + \text{max-vars} - \leq \text{length } xs \rangle \langle ?\text{synth} - s \rangle$
show $?case$ **by** $(\text{fastforce elim!}: \text{sim-move01-expr})$
next
case $(\text{CallObj } e \text{ } s \text{ } ta \text{ } e' \text{ } s' \text{ } M \text{ } es \text{ } Vs \text{ } E2 \text{ } xs)$
note $IH = \langle \bigwedge vs \text{ } e2 \text{ } XS. \llbracket \text{bisim } vs \text{ } e \text{ } e2 \text{ } XS; fv \text{ } e \subseteq \text{set } vs; \text{lcl } s \subseteq_m [vs \text{ } \mapsto] \text{ } XS \rrbracket; \text{length } vs + \text{max-vars}$
 $e \leq \text{length } XS;$
 $?synth \text{ } e \text{ } s \rrbracket \implies ?concl \text{ } e \text{ } e2 \text{ } s \text{ } XS \text{ } e' \text{ } s' \text{ } ta \text{ } vs \rangle$
from $\langle \text{extTA2J0 } P, P, t \vdash \langle e, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle \rangle$ **have** $\neg \text{is-val } e$ **by** auto
with $\langle \text{bisim } Vs \text{ } (e \cdot M(es)) \text{ } E2 \text{ } xs \rangle$ **obtain** E
where $E2 = E \cdot M(\text{compEs1 } Vs \text{ } es)$ **and** $\text{bisim } Vs \text{ } e \text{ } E \text{ } xs$ **and** $\neg \text{contains-insyncs } es$
by $(\text{auto simp add: compEs1-conv-map})$
with $IH[\text{of } Vs \text{ } E \text{ } xs] \langle fv \text{ } (e \cdot M(es)) \subseteq \text{set } Vs \rangle \langle \text{lcl } s \subseteq_m [Vs \text{ } \mapsto] \text{ } xs \rangle$
 $\langle \text{length } Vs + \text{max-vars } (e \cdot M(es)) \leq \text{length } xs \rangle \langle ?\text{synth } (e \cdot M(es)) \text{ } s \rangle$
show $?case$ **by** $(\text{cases is-val } e^\wedge)(\text{fastforce elim!}: \text{sim-move01-expr split: if-split-asm})+$
next
case $(\text{CallParams } es \text{ } s \text{ } ta \text{ } es' \text{ } s' \text{ } v \text{ } M \text{ } Vs \text{ } E2 \text{ } xs)$
note $IH = \langle \bigwedge vs \text{ } es2 \text{ } XS. \llbracket \text{bisims } vs \text{ } es \text{ } es2 \text{ } XS; fvs } es \subseteq \text{set } vs; \text{lcl } s \subseteq_m [vs \text{ } \mapsto] \text{ } XS \rrbracket; \text{length } vs +$
 $\text{max-varss } es \leq \text{length } XS;$
 $?synths \text{ } es \text{ } s \rrbracket \implies ?concls \text{ } es \text{ } es2 \text{ } s \text{ } XS \text{ } es' \text{ } s' \text{ } ta \text{ } vs \rangle$
from $\langle \text{bisim } Vs \text{ } (\text{Val } v \cdot M(es)) \text{ } E2 \text{ } xs \rangle$ **obtain** Es
where $E2 = \text{Val } v \cdot M(Es)$ **and** $\text{bisims } Vs \text{ } es \text{ } Es \text{ } xs$ **by** auto
moreover from $\langle \text{extTA2J0 } P, P, t \vdash \langle es, s \rangle [-\text{ta} \rightarrow] \langle es', s' \rangle \rangle$ **have** $\neg \text{is-vals } es$ **by** auto
with $\langle ?\text{synth } (\text{Val } v \cdot M(es)) \text{ } s \rangle$ **have** $?synths \text{ } es \text{ } s$ **by** (auto)
moreover note $IH[\text{of } Vs \text{ } Es \text{ } xs] \langle fv \text{ } (\text{Val } v \cdot M(es)) \subseteq \text{set } Vs \rangle \langle \text{lcl } s \subseteq_m [Vs \text{ } \mapsto] \text{ } xs \rangle$
 $\langle \text{length } Vs + \text{max-vars } (\text{Val } v \cdot M(es)) \leq \text{length } xs \rangle$
ultimately show $?case$ **by** $(\text{fastforce elim!}: \text{sim-move01-CallParams})$
next
case $(\text{RedCall } s \text{ } a \text{ } U \text{ } M \text{ } Ts \text{ } T \text{ } pns \text{ } body \text{ } D \text{ } vs \text{ } Vs \text{ } E2 \text{ } xs)$
from $\langle \text{typeof-addr } (hp \text{ } s) \text{ } a = \lfloor U \rfloor \rangle$
have $\text{call } (\text{addr } a \cdot M(\text{map } \text{Val } vs)) = \lfloor (a, M, vs) \rfloor$ **by** auto
with $\langle ?\text{synth } (\text{addr } a \cdot M(\text{map } \text{Val } vs)) \text{ } s \rangle$ **have** $\text{synthesized-call } P \text{ } (hp \text{ } s) \text{ } (a, M, vs)$ **by** auto
with $\langle \text{typeof-addr } (hp \text{ } s) \text{ } a = \lfloor U \rfloor \rangle \langle P \vdash \text{class-type-of } U \text{ sees } M: Ts \rightarrow T = \lfloor (pns, body) \rfloor \text{ in } D \rangle$
have False **by** $(\text{auto simp add: synthesized-call-conv dest: sees-method-fun})$
thus $?case \dots$
next
case $(\text{RedCallExternal } s \text{ } a \text{ } T \text{ } M \text{ } Ts \text{ } Tr \text{ } D \text{ } vs \text{ } ta \text{ } va \text{ } h' \text{ } ta' \text{ } e' \text{ } s' \text{ } Vs \text{ } E2 \text{ } xs)$
from $\langle \text{bisim } Vs \text{ } (\text{addr } a \cdot M(\text{map } \text{Val } vs)) \text{ } E2 \text{ } xs \rangle$ **have** $E2 = \text{addr } a \cdot M(\text{map } \text{Val } vs)$ **by** auto
moreover note $\langle P \vdash \text{class-type-of } T \text{ sees } M: Ts \rightarrow Tr = \text{Native in } D \rangle \langle \text{typeof-addr } (hp \text{ } s) \text{ } a = \lfloor T \rfloor \rangle$
 $\langle ta' = \text{extTA2J0 } P \text{ } ta \rangle$

$\langle e' = \text{extRet2J} (\text{addr } a \cdot M(\text{map Val } vs)) \text{ va} \rangle \langle s' = (h', \text{lcl } s) \rangle \langle P, t \vdash \langle a \cdot M(vs), \text{hp } s \rangle -\text{ta} \rightarrow \text{ext} \langle \text{va}, h' \rangle \rangle$
 $\langle \text{lcl } s \subseteq_m [Vs \mapsto] xs \rangle$
moreover from $wf \langle P, t \vdash \langle a \cdot M(vs), \text{hp } s \rangle -\text{ta} \rightarrow \text{ext} \langle \text{va}, h' \rangle \rangle$
have $\text{ta-bisim01} (\text{extTA2J0 } P \text{ ta}) (\text{extTA2J1} (\text{compP1 } P) \text{ ta})$ **by** $(\text{rule red-external-ta-bisim01})$
moreover from $\langle P, t \vdash \langle a \cdot M(vs), \text{hp } s \rangle -\text{ta} \rightarrow \text{ext} \langle \text{va}, h' \rangle \rangle \langle \text{typeof-addr} (\text{hp } s) \text{ a} = \lfloor T \rfloor \rangle$
 $\langle P \vdash \text{class-type-of } T \text{ sees } M: Ts \rightarrow Tr = \text{Native in } D \rangle$
have $\tau \text{external-defs } D \text{ M} \implies h' = \text{hp } s \wedge \text{ta} = \varepsilon$
by $(\text{fastforce dest: } \tau \text{external'-red-external-heap-unchanged } \tau \text{external'-red-external-TA-empty simp add: } \tau \text{external'-def } \tau \text{external-def})$
ultimately show $?case$
by $(\text{cases va})(\text{fastforce intro!: exI}[\text{where } x = \text{ta}] \text{ intro: Red1CallExternal simp add: map-eq-append-conv sim-move01-def dest: sees-method-fun simp del: split-paired-Ex})+$
next
case $(\text{BlockRed } e \text{ h } x \text{ V } \text{vo } \text{ta } e' \text{ h}' \text{ x}' \text{ T } Vs \text{ E2 } xs)$
note $IH = \langle \bigwedge vs \text{ e2 } XS. \llbracket \text{bisim } vs \text{ e } \text{e2 } XS; \text{fv } e \subseteq \text{set } vs; \text{lcl } (h, x(V := \text{vo})) \subseteq_m [vs \mapsto] XS; \text{length } vs + \text{max-vars } e \leq \text{length } XS; ?\text{synth } e (h, (x(V := \text{vo}))) \rrbracket \implies ?\text{concl } e \text{e2 } (h, x(V := \text{vo})) \text{ XS } e' (h', x') \text{ ta } vs \rangle$
note $\text{red} = \langle \text{extTA2J0 } P, P, t \vdash \langle e, (h, x(V := \text{vo})) \rangle -\text{ta} \rightarrow \langle e', (h', x') \rangle \rangle$
note $\text{len} = \langle \text{length } Vs + \text{max-vars } \{V:T=\text{vo}; e\} \leq \text{length } xs \rangle$
from $\langle \text{fv } \{V:T=\text{vo}; e\} \subseteq \text{set } Vs \rangle$ **have** $\text{fv: } \text{fv } e \subseteq \text{set } (Vs@[V])$ **by** auto
from $\langle \text{bisim } Vs \{V:T=\text{vo}; e\} \text{ E2 } xs \rangle$ **show** $?case$
proof $(\text{cases rule: bisim-cases}(?)[\text{consumes } 1, \text{case-names } \text{BlockNone } \text{BlockSome } \text{BlockSomeNone}])$
case $(\text{BlockNone } E')$
with $\text{red } IH[\text{of } Vs@[V] \text{ E}' xs] \text{fv} \langle \text{lcl } (h, x) \subseteq_m [Vs \mapsto] xs \rangle$
 $\langle \text{length } Vs + \text{max-vars } \{V:T=\text{vo}; e\} \leq \text{length } xs \rangle \langle ?\text{synth } \{V:T=\text{vo}; e\} (h, x) \rangle$
obtain $TA' \text{ e2}' xs'$ **where** $\text{red}' : \text{sim-move01} (\text{compP1 } P) \text{ t ta } e \text{ E}' h \text{ xs } TA' \text{ e2}' h' xs'$
and $\text{bisim}' : \text{bisim} (Vs @ [V]) e' \text{e2}' xs' x' \subseteq_m [Vs @ [V] \mapsto] xs'$ **by** auto
from $\text{red}' \langle \text{length } Vs + \text{max-vars } \{V:T=\text{vo}; e\} \leq \text{length } xs \rangle$
have $\text{length } (Vs@[V]) + \text{max-vars } e \leq \text{length } xs'$
by $(\text{fastforce simp add: sim-move01-def dest: red1-preserves-len } \tau \text{red1t-preserves-len } \tau \text{red1r-preserves-len split: if-split-asm})$
with $\langle x' \subseteq_m [Vs @ [V] \mapsto] xs' \rangle$ **have** $x' \subseteq_m [Vs \mapsto] xs', V \mapsto xs' ! \text{length } Vs$ **by** (simp)
moreover
{ **assume** $V \in \text{set } Vs$
hence $\text{hidden} (Vs @ [V]) (\text{index } Vs \text{ V})$ **by** $(\text{rule hidden-index})$
with $\langle \text{bisim} (Vs @ [V]) e \text{ E}' xs \rangle$ **have** $\text{unmod } E' (\text{index } Vs \text{ V})$
by $-(\text{rule hidden-bisim-unmod})$
moreover from $\langle \text{length } Vs + \text{max-vars } \{V:T=\text{vo}; e\} \leq \text{length } xs \rangle \langle V \in \text{set } Vs \rangle$
have $\text{index } Vs \text{ V} < \text{length } xs$ **by** $(\text{auto intro: index-less-aux})$
ultimately have $xs ! \text{index } Vs \text{ V} = xs' ! \text{index } Vs \text{ V}$
using $\text{sim-move01-preserves-unmod}[OF \text{red}']$ **by** (simp) **}**
moreover from red' **have** $\text{length } xs = \text{length } xs'$ **by** $(\text{rule sim-move01-preserves-len}[\text{symmetric}])$
ultimately have $\text{rel: } x'(V := x \text{ V}) \subseteq_m [Vs \mapsto] xs'$
using $\langle \text{lcl } (h, x) \subseteq_m [Vs \mapsto] xs \rangle \langle \text{length } Vs + \text{max-vars } \{V:T=\text{vo}; e\} \leq \text{length } xs \rangle$
by $(\text{auto intro: Block-lem})$
show $?thesis$
proof $(\text{cases } x' \text{ V})$
case None
with $\text{red}' \text{ bisim}' \text{ BlockNone } \text{len}$
show $?thesis$ **by** $(\text{fastforce simp del: split-paired-Ex fun-upd-apply intro: rel})$
next
case $(\text{Some } v)$
moreover

with $\langle x' \subseteq_m [Vs @ [V] [\mapsto] xs'] \rangle$ **have** $[Vs @ [V] [\mapsto] xs'] V = [v]$
by(*auto simp add: map-le-def dest: bspec*)
moreover
from $\langle \text{length } Vs + \text{max-vars } \{V:T=vo; e\} \leq \text{length } xs \rangle$ **have** $\text{length } Vs < \text{length } xs$ **by** *auto*
ultimately have $xs' ! \text{length } Vs = v$ **using** $\langle \text{length } xs = \text{length } xs' \rangle$ **by**(*simp*)
with *red' bisim' BlockNone Some len*
show *?thesis* **by**(*fastforce simp del: fun-upd-apply intro: rel*)
qed
next
case (*BlockSome E' v*)
with *red IH[of Vs@[V] E' xs[length Vs := v]] fv* $\langle \text{lcl } (h, x) \subseteq_m [Vs [\mapsto] xs] \rangle$
 $\langle \text{length } Vs + \text{max-vars } \{V:T=vo; e\} \leq \text{length } xs \rangle$ $\langle ?\text{synth } \{V:T=vo; e\} (h, x) \rangle$
obtain $TA' e2' xs'$ **where** *red': sim-move01 (compP1 P) t ta e E' h (xs[length Vs := v]) TA' e2'*
 $h' xs'$
and *bisim': bisim (Vs @ [V]) e' e2' xs' x' \subseteq_m [Vs @ [V] [\mapsto] xs']* **by** *auto*
from *red'* $\langle \text{length } Vs + \text{max-vars } \{V:T=vo; e\} \leq \text{length } xs \rangle$
have $\text{length } (Vs@[V]) + \text{max-vars } e \leq \text{length } xs'$ **by**(*auto dest: sim-move01-preserves-len*)
with $\langle x' \subseteq_m [Vs @ [V] [\mapsto] xs'] \rangle$ **have** $x' \subseteq_m [Vs [\mapsto] xs', V \mapsto xs' ! \text{length } Vs]$ **by**(*simp*)
moreover
{ assume $V \in \text{set } Vs$
hence *hidden (Vs @ [V]) (index Vs V)* **by**(*rule hidden-index*)
with $\langle \text{bisim } (Vs @ [V]) e E' (xs[\text{length } Vs := v]) \rangle$ **have** *unmod E' (index Vs V)*
by $-(\text{rule hidden-bisim-unmod})$
moreover from $\langle \text{length } Vs + \text{max-vars } \{V:T=vo; e\} \leq \text{length } xs \rangle$ $\langle V \in \text{set } Vs \rangle$
have $\text{index } Vs V < \text{length } xs$ **by**(*auto intro: index-less-aux*)
moreover from $\langle \text{length } Vs + \text{max-vars } \{V:T=vo; e\} \leq \text{length } xs \rangle$ $\langle V \in \text{set } Vs \rangle$
have $(xs[\text{length } Vs := v]) ! \text{index } Vs V = xs' ! \text{index } Vs V$ **by**(*simp*)
ultimately have $xs ! \text{index } Vs V = xs' ! \text{index } Vs V$
using *sim-move01-preserves-unmod[OF red', of index Vs V]* **by**(*simp*) **}**
moreover from *red'* **have** $\text{length } xs = \text{length } xs'$ **by**(*auto dest: sim-move01-preserves-len*)
ultimately have *rel: x'(V := x V) \subseteq_m [Vs [\mapsto] xs']*
using $\langle \text{lcl } (h, x) \subseteq_m [Vs [\mapsto] xs] \rangle$ $\langle \text{length } Vs + \text{max-vars } \{V:T=vo; e\} \leq \text{length } xs \rangle$
by(*auto intro: Block-lem*)
from *BlockSome red* **obtain** v' **where** *Some: x' V = [v']* **by**(*auto dest!: red-lcl-incr*)
with $\langle x' \subseteq_m [Vs @ [V] [\mapsto] xs'] \rangle$ **have** $[Vs @ [V] [\mapsto] xs'] V = [v']$
by(*auto simp add: map-le-def dest: bspec*)
moreover
from $\langle \text{length } Vs + \text{max-vars } \{V:T=vo; e\} \leq \text{length } xs \rangle$ **have** $\text{length } Vs < \text{length } xs$ **by** *auto*
ultimately have $xs' ! \text{length } Vs = v'$ **using** $\langle \text{length } xs = \text{length } xs' \rangle$ **by**(*simp*)
with *red' bisim' BlockSome Some* $\langle \text{length } Vs < \text{length } xs \rangle$
show *?thesis* **by**(*fastforce simp del: fun-upd-apply intro: rel*)
next
case (*BlockSomeNone E'*)
with *red IH[of Vs@[V] E' xs] fv* $\langle \text{lcl } (h, x) \subseteq_m [Vs [\mapsto] xs] \rangle$
 $\langle \text{length } Vs + \text{max-vars } \{V:T=vo; e\} \leq \text{length } xs \rangle$ $\langle ?\text{synth } \{V:T=vo; e\} (h, x) \rangle$
obtain $TA' e2' xs'$ **where** *red': sim-move01 (compP1 P) t ta e E' h xs TA' e2' h' xs'*
and *IH': bisim (Vs @ [V]) e' e2' xs' x' \subseteq_m [Vs @ [V] [\mapsto] xs']* **by** *auto*
from *red'* $\langle \text{length } Vs + \text{max-vars } \{V:T=vo; e\} \leq \text{length } xs \rangle$
have $\text{length } (Vs@[V]) + \text{max-vars } e \leq \text{length } xs'$ **by**(*auto dest: sim-move01-preserves-len*)
with $\langle x' \subseteq_m [Vs @ [V] [\mapsto] xs'] \rangle$ **have** $x' \subseteq_m [Vs [\mapsto] xs', V \mapsto xs' ! \text{length } Vs]$ **by**(*simp*)
moreover
{ assume $V \in \text{set } Vs$
hence *hidden (Vs @ [V]) (index Vs V)* **by**(*rule hidden-index*)
with $\langle \text{bisim } (Vs @ [V]) e E' xs \rangle$ **have** *unmod E' (index Vs V)*

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    by  $\neg$ (rule hidden-bisim-unmod)
  moreover from  $\langle \text{length } Vs + \text{max-vars } \{V:T=vo; e\} \leq \text{length } xs \rangle \langle V \in \text{set } Vs \rangle$ 
  have  $\text{index } Vs V < \text{length } xs$  by(auto intro: index-less-aux)
  moreover from  $\langle \text{length } Vs + \text{max-vars } \{V:T=vo; e\} \leq \text{length } xs \rangle \langle V \in \text{set } Vs \rangle$ 
  have  $(xs[\text{length } Vs := v]) ! \text{index } Vs V = xs ! \text{index } Vs V$  by(simp)
  ultimately have  $xs ! \text{index } Vs V = xs' ! \text{index } Vs V$ 
    using sim-move01-preserves-unmod[OF red', of index Vs V] by(simp) }
  moreover from red' have  $\text{length } xs = \text{length } xs'$  by(auto dest: sim-move01-preserves-len)
  ultimately have  $\text{rel: } x'(V := x V) \subseteq_m [Vs \mapsto] xs'$ 
    using  $\langle \text{lcl } (h, x) \subseteq_m [Vs \mapsto] xs \rangle \langle \text{length } Vs + \text{max-vars } \{V:T=vo; e\} \leq \text{length } xs \rangle$ 
    by(auto intro: Block-lem)
  from BlockSomeNone red obtain  $v'$  where  $\text{Some: } x' V = [v']$  by(auto dest!: red-lcl-incr)
  with  $\langle x' \subseteq_m [Vs @ [V] \mapsto] xs' \rangle$  have  $[Vs @ [V] \mapsto] V = [v']$ 
    by(auto simp add: map-le-def dest: bspec)
  moreover
  from  $\langle \text{length } Vs + \text{max-vars } \{V:T=vo; e\} \leq \text{length } xs \rangle$  have  $\text{length } Vs < \text{length } xs$  by auto
  ultimately have  $xs' ! \text{length } Vs = v'$  using  $\langle \text{length } xs = \text{length } xs' \rangle$  by(simp)
  with red' IH' BlockSomeNone Some  $\langle \text{length } Vs < \text{length } xs \rangle$ 
  show ?thesis by(fastforce simp del: fun-upd-apply intro: rel)
qed
next
case (RedBlock V T vo u s Vs E2 xs)
from  $\langle \text{bisim } Vs \{V:T=vo; \text{Val } u\} E2 xs \rangle$  obtain  $vo'$ 
  where [simp]:  $E2 = \{ \text{length } Vs:T=vo'; \text{Val } u \}$  by auto
from RedBlock show ?case
proof(cases vo)
  case (Some v)
  with  $\langle \text{bisim } Vs \{V:T=vo; \text{Val } u\} E2 xs \rangle$ 
  have  $vo': \text{case } vo' \text{ of None} \Rightarrow xs ! \text{length } Vs = v \mid \text{Some } v' \Rightarrow v = v'$  by auto
  have sim-move01 (compP1 P)  $t \in \{V:T=vo; \text{Val } u\} E2 (hp s) xs \in (\text{Val } u) (hp s) (xs[\text{length } Vs := v])$ 
  proof(cases vo')
    case None with vo'
    have  $xs[\text{length } Vs := v] = xs$  by auto
    thus ?thesis using Some None by auto
  next
    case Some
    from  $\langle \text{length } Vs + \text{max-vars } \{V:T=vo; \text{Val } u\} \leq \text{length } xs \rangle$  have  $\text{length } Vs < \text{length } xs$  by simp
    with vo' Some show ?thesis using  $\langle vo = \text{Some } v \rangle$  by auto
  qed
  thus ?thesis using RedBlock by fastforce
qed fastforce
next
case SynchronizedNull thus ?case by fastforce
next
case (LockSynchronized a e s Vs E2 xs)
from  $\langle \text{bisim } Vs (\text{sync}(\text{addr } a) e) E2 xs \rangle$ 
have  $E2: E2 = \text{sync}_{\text{length } Vs}(\text{addr } a) (\text{compE1 } (Vs@[fresh-var Vs]) e)$ 
  and  $\text{sync: } \neg \text{contains-insync } e$  by auto
moreover have  $\text{fresh-var } Vs \notin \text{set } Vs$  by(rule fresh-var-fresh)
with  $\langle \text{fv } (\text{sync}(\text{addr } a) e) \subseteq \text{set } Vs \rangle$  have  $\text{fresh-var } Vs \notin \text{fv } e$  by auto
from  $E2 \langle \text{fv } (\text{sync}(\text{addr } a) e) \subseteq \text{set } Vs \rangle$  sync
have  $\text{bisim } (Vs@[fresh-var Vs]) e (\text{compE1 } (Vs@[fresh-var Vs]) e) (xs[\text{length } Vs := \text{Addr } a])$ 
  by(auto intro!: compE1-bisim)

```

hence $\text{bisim } Vs \langle \text{insync}(a) e \rangle \langle \text{insync}_{\text{length } Vs} (a) (\text{compE1 } (Vs@[fresh-var Vs]) e) \rangle (xs[\text{length } Vs := \text{Addr } a])$

using $\langle \text{fresh-var } Vs \notin \text{fv } e \rangle \langle \text{length } Vs + \text{max-vars } (\text{sync}(\text{addr } a) e) \leq \text{length } xs \rangle$ **by** *auto*

moreover from $\langle \text{length } Vs + \text{max-vars } (\text{sync}(\text{addr } a) e) \leq \text{length } xs \rangle$

have $\text{False}, \text{compP1 } P, t \vdash 1 \langle \text{sync}_{\text{length } Vs} (\text{addr } a) (\text{compE1 } (Vs@[fresh-var Vs]) e), (hp s, xs) \rangle$
 $-\llbracket \text{Lock} \rightarrow a, \text{SyncLock } a \rrbracket \rightarrow$
 $\langle \text{insync}_{\text{length } Vs} (a) (\text{compE1 } (Vs@[fresh-var Vs]) e), (hp s, xs[\text{length } Vs := \text{Addr } a]) \rangle$

by $-(\text{rule } \text{Lock1Synchronized}, \text{auto})$

hence $\text{sim-move01 } (\text{compP1 } P) t \llbracket \text{Lock} \rightarrow a, \text{SyncLock } a \rrbracket (\text{sync}(\text{addr } a) e) E2 (hp s) xs \llbracket \text{Lock} \rightarrow a, \text{SyncLock } a \rrbracket \langle \text{insync}_{\text{length } Vs} (a) (\text{compE1 } (Vs@[fresh-var Vs]) e) \rangle (hp s) (xs[\text{length } Vs := \text{Addr } a])$

using $E2$ **by** $(\text{fastforce } \text{simp } \text{add: } \text{sim-move01-def } \text{ta-bisim-def})$

moreover have $\text{zip } Vs (xs[\text{length } Vs := \text{Addr } a]) = (\text{zip } Vs xs)[\text{length } Vs := (\text{arbitrary}, \text{Addr } a)]$
by $(\text{rule } \text{sym})(\text{simp } \text{add: } \text{update-zip})$

hence $\text{zip } Vs (xs[\text{length } Vs := \text{Addr } a]) = \text{zip } Vs xs$ **by** *simp*

with $\langle \text{lcl } s \subseteq_m [Vs \mapsto xs] \rangle$ **have** $\text{lcl } s \subseteq_m [Vs \mapsto xs[\text{length } Vs := \text{Addr } a]]$
by $(\text{auto } \text{simp } \text{add: } \text{map-le-def } \text{map-upds-def})$

ultimately show $?case$ **using** $\langle \text{lcl } s \subseteq_m [Vs \mapsto xs] \rangle$ **by** *fastforce*

next

case $(\text{SynchronizedRed2 } e s \text{ ta } e' s' a Vs E2 xs)$

note $IH = \langle \bigwedge vs e2 XS. \llbracket \text{bisim } vs e e2 XS; \text{fv } e \subseteq \text{set } vs; \text{lcl } s \subseteq_m [vs \mapsto XS]; \text{length } vs + \text{max-vars } e \leq \text{length } XS; \rangle$
 $?synth e s \rrbracket \implies ?concl e e2 s XS e' s' \text{ ta } vs$

from $\langle \text{bisim } Vs \langle \text{insync}(a) e \rangle E2 xs \rangle$ **obtain** E

where $E2: E2 = \text{insync}_{\text{length } Vs} (a) E$ **and** $\text{bisim}: \text{bisim } (Vs@[fresh-var Vs]) e E xs$
and $\text{xa}: xs ! \text{length } Vs = \text{Addr } a$ **by** *auto*

from $\langle \text{fv } (\text{insync}(a) e) \subseteq \text{set } Vs \rangle$ **fresh-var-fresh**[of Vs] **have** $\text{fv}: \text{fresh-var } Vs \notin \text{fv } e$ **by** *auto*

from $\langle \text{length } Vs + \text{max-vars } (\text{insync}(a) e) \leq \text{length } xs \rangle$ **have** $\text{length } Vs < \text{length } xs$ **by** *simp*

{ assume $\text{lcl } s (\text{fresh-var } Vs) \neq \text{None}$

then obtain v **where** $\text{lcl } s (\text{fresh-var } Vs) = [v]$ **by** *auto*

with $\langle \text{lcl } s \subseteq_m [Vs \mapsto xs] \rangle$ **have** $[Vs \mapsto xs] (\text{fresh-var } Vs) = [v]$
by $(\text{auto } \text{simp } \text{add: } \text{map-le-def } \text{dest: } \text{bspec})$

hence $\text{fresh-var } Vs \in \text{set } Vs$

by $(\text{auto } \text{simp } \text{add: } \text{map-upds-def } \text{set-zip } \text{dest!}: \text{map-of-SomeD })$

moreover have $\text{fresh-var } Vs \notin \text{set } Vs$ **by** $(\text{rule } \text{fresh-var-fresh})$

ultimately have False **by** *contradiction* **}**

hence $\text{lcl } s (\text{fresh-var } Vs) = \text{None}$ **by** $(\text{cases } \text{lcl } s (\text{fresh-var } Vs), \text{auto})$

hence $(\text{lcl } s)(\text{fresh-var } Vs := \text{None}) = \text{lcl } s$ **by** $(\text{auto } \text{intro: } \text{ext})$

moreover from $\langle \text{lcl } s \subseteq_m [Vs \mapsto xs] \rangle$

have $(\text{lcl } s)(\text{fresh-var } Vs := \text{None}) \subseteq_m [Vs \mapsto xs, \text{fresh-var } Vs \mapsto xs ! \text{length } Vs]$ **by** (simp)

ultimately have $\text{lcl } s \subseteq_m [Vs @ [fresh-var Vs] \mapsto xs]$
using $\langle \text{length } Vs < \text{length } xs \rangle$ **by** (auto)

with $IH[\text{of } Vs@[fresh-var Vs] E xs] \langle \text{fv } (\text{insync}(a) e) \subseteq \text{set } Vs \rangle$ bisim
 $\langle \text{length } Vs + \text{max-vars } (\text{insync}(a) e) \leq \text{length } xs \rangle \langle ?synth (\text{insync}(a) e) s \rangle$

obtain $TA' e2' xs'$ **where** $IH': \text{sim-move01 } (\text{compP1 } P) t \text{ ta } e E (hp s) xs TA' e2' (hp s') xs'$
 $\text{bisim } (Vs @ [fresh-var Vs]) e' e2' xs' \text{lcl } s' \subseteq_m [Vs @ [fresh-var Vs] \mapsto xs']$ **by** *auto*

from $\langle \text{extTA2J0 } P, P, t \vdash \langle e, s \rangle -\text{ta} \rightarrow \langle e', s' \rangle \rangle$ **have** $\text{dom } (\text{lcl } s') \subseteq \text{dom } (\text{lcl } s) \cup \text{fv } e$ **by** $(\text{auto } \text{dest: } \text{red-dom-lcl})$

with $\text{fv } \langle \text{lcl } s (\text{fresh-var } Vs) = \text{None} \rangle$ **have** $(\text{fresh-var } Vs) \notin \text{dom } (\text{lcl } s')$ **by** *auto*

hence $\text{lcl } s' (\text{fresh-var } Vs) = \text{None}$ **by** *auto*

moreover from IH' **have** $\text{length } xs = \text{length } xs'$ **by** $(\text{auto } \text{dest: } \text{sim-move01-preserves-len})$

moreover note $\langle \text{lcl } s' \subseteq_m [Vs @ [fresh-var Vs] \mapsto xs'] \rangle \langle \text{length } Vs < \text{length } xs \rangle$

ultimately have $\text{lcl } s' \subseteq_m [Vs \mapsto xs']$ **by** $(\text{auto } \text{simp } \text{add: } \text{map-le-def } \text{dest: } \text{bspec})$

moreover from $\text{bisim } \text{fv}$ **have** $\text{unmod } E (\text{length } Vs)$ **by** $(\text{auto } \text{intro: } \text{bisim-fv-unmod})$

with $IH' \langle \text{length } Vs < \text{length } xs \rangle$ **have** $xs ! \text{length } Vs = xs' ! \text{length } Vs$

by(*auto dest!*: *sim-move01-preserves-unmod*)
with *xsa* **have** $xs' ! \text{length } Vs = \text{Addr } a$ **by** *simp*
ultimately show ?*case* **using** *IH' E2* **by**(*fastforce*)
next
case (*UnlockSynchronized a v s Vs E2 xs*)
from $\langle \text{bisim } Vs (\text{insync}(a) \text{ Val } v) E2 \text{ xs} \rangle$ **have** $E2 = \text{insync}_{\text{length } Vs} (a) \text{ Val } v$
and *xsa*: $xs ! \text{length } Vs = \text{Addr } a$ **by** *auto*
moreover from $\langle \text{length } Vs + \text{max-vars } (\text{insync}(a) \text{ Val } v) \leq \text{length } xs \rangle$ *xsa*
have $\text{False}, \text{compP1 } P, t \vdash 1 \langle \text{insync}_{\text{length } Vs} (a) (\text{Val } v), (\text{hp } s, xs) \rangle - \{\!\! \{ \text{Unlock} \rightarrow a, \text{SyncUnlock } a \}\!\!\} \rightarrow$
 $\langle \text{Val } v, (\text{hp } s, xs) \rangle$
by-(*rule Unlock1Synchronized, auto*)
hence *sim-move01* (*compP1 P*) $t \{\!\! \{ \text{Unlock} \rightarrow a, \text{SyncUnlock } a \}\!\!\} (\text{insync}(a) \text{ Val } v) (\text{insync}_{\text{length } Vs} (a)$
 $\text{Val } v) (\text{hp } s) xs \{\!\! \{ \text{Unlock} \rightarrow a, \text{SyncUnlock } a \}\!\!\} (\text{Val } v) (\text{hp } s) xs$
by(*fastforce simp add: sim-move01-def ta-bisim-def*)
ultimately show ?*case* **using** $\langle \text{lcl } s \subseteq_m [Vs \mapsto] xs \rangle$ **by** *fastforce*
next
case (*RedWhile b c s Vs E2 xs*)
from $\langle \text{bisim } Vs (\text{while } (b) c) E2 \text{ xs} \rangle$ **have** $E2 = \text{while } (\text{compE1 } Vs b) (\text{compE1 } Vs c)$
and *sync*: $\neg \text{contains-insync } b \neg \text{contains-insync } c$ **by** *auto*
moreover have $\text{False}, \text{compP1 } P, t \vdash 1 \langle \text{while}(\text{compE1 } Vs b) (\text{compE1 } Vs c), (\text{hp } s, xs) \rangle$
 $-\varepsilon \rightarrow \langle \text{if } (\text{compE1 } Vs b) (\text{compE1 } Vs c);; \text{while}(\text{compE1 } Vs b) (\text{compE1 } Vs c) \rangle \text{ else unit},$
 $(\text{hp } s, xs) \rangle$
by(*rule Red1While*)
hence *sim-move01* (*compP1 P*) $t \varepsilon (\text{while } (b) c) (\text{while } (\text{compE1 } Vs b) (\text{compE1 } Vs c)) (\text{hp } s) xs \varepsilon$
 $\langle \text{if } (\text{compE1 } Vs b) (\text{compE1 } Vs c);; \text{while}(\text{compE1 } Vs b) (\text{compE1 } Vs c) \rangle \text{ else unit} \rangle (\text{hp } s) xs$
by(*auto simp add: sim-move01-def*)
moreover from $\langle \text{fv } (\text{while } (b) c) \subseteq \text{set } Vs \rangle$ *sync*
have *bisim* $Vs (\text{if } (b) (c);; \text{while } (b) c) \text{ else unit}$
 $(\text{if } (\text{compE1 } Vs b) (\text{compE1 } Vs (c);; \text{while}(b) c)) \text{ else } (\text{compE1 } Vs \text{unit})) xs$
by -(*rule bisimCond, auto*)
ultimately show ?*case* **using** $\langle \text{lcl } s \subseteq_m [Vs \mapsto] xs \rangle$
by(*simp*)((*rule exI*)+, *erule conjI*, *auto*)
next
case (*RedTryCatch s a D C V e2 Vs E2 xs*)
thus ?*case* **by**(*auto intro!*: *exI*)(*auto intro!*: *compE1-bisim*)
next
case *RedTryFail* **thus** ?*case* **by**(*auto intro!*: *exI*)
next
case (*ListRed1 e s ta e' s' es Vs ES2 xs*)
note $IH = \langle \bigwedge vs e2 XS. [\text{bisim } vs e e2 XS; \text{fv } e \subseteq \text{set } vs; \text{lcl } s \subseteq_m [vs \mapsto] XS]; \text{length } vs + \text{max-vars}$
 $e \leq \text{length } XS;$
 $\text{?synth } e s] \implies \text{?concl } e e2 s XS e' s' ta vs \rangle$
from $\langle \text{extTA2J0 } P, P, t \vdash \langle e, s \rangle - \text{ta} \rightarrow \langle e', s' \rangle \rangle$ **have** $\neg \text{is-val } e$ **by** *auto*
with $\langle \text{bisims } Vs (e \# es) ES2 \text{ xs} \rangle$ **obtain** E'
where *bisim* $Vs e E' xs$ **and** $ES2: ES2 = E' \# \text{compEs1 } Vs es$
and *sync*: $\neg \text{contains-insyncs } es$ **by**(*auto simp add: compEs1-conv-map*)
with $IH[\text{of } Vs E' xs] \langle \text{fvs } (e \# es) \subseteq \text{set } Vs \rangle \langle \text{lcl } s \subseteq_m [Vs \mapsto] xs \rangle \langle \text{extTA2J0 } P, P, t \vdash \langle e, s \rangle - \text{ta} \rightarrow$
 $\langle e', s' \rangle \rangle$
 $\langle \text{length } Vs + \text{max-varss } (e \# es) \leq \text{length } xs \rangle \langle \text{?synths } (e \# es) s \rangle \langle \neg \text{is-val } e \rangle$
show ?*case* **by**(*cases is-val e*)(*fastforce elim!*: *sim-moves01-expr split: if-split-asm*)+
next
case (*ListRed2 es s ta es' s' v Vs ES2 xs*)
thus ?*case* **by**(*fastforce elim!*: *bisims-cases elim!*: *sim-moves01-expr*)
next

```

case CallThrowParams thus ?case
  by(fastforce elim!: bisim-cases simp add: bisims-map-Val-Throw)
next
  case (BlockThrow V T vo a s Vs e2 xs) thus ?case
    by(cases vo)(fastforce elim!: bisim-cases)+
next
  case (SynchronizedThrow2 a ad s Vs E2 xs)
    from  $\langle \text{bisim } Vs \text{ (insync}(a) \text{ Throw } ad) \text{ E2 } xs \rangle$  have  $xs ! \text{ length } Vs = \text{Addr } a$  by auto
    with  $\langle \text{length } Vs + \text{max-vars (insync}(a) \text{ Throw } ad) \leq \text{length } xs \rangle$ 
    have False, compP1 P, t  $\vdash 1 \langle \text{insync}_{\text{length } Vs} (a) \text{ Throw } ad, (hp \ s, \ xs) \rangle - \{\!| \text{Unlock} \rightarrow a, \text{SyncUnlock } a \!|\} \rightarrow \langle \text{Throw } ad, (hp \ s, \ xs) \rangle$ 
    by  $-(\text{rule } \text{Synchronized1Throw2}, \text{auto})$ 
    hence sim-move01 (compP1 P) t  $\{\!| \text{Unlock} \rightarrow a, \text{SyncUnlock } a \!|\}$   $(\text{insync}(a) \text{ Throw } ad) (\text{insync}_{\text{length } Vs} (a) \text{ Throw } ad) (hp \ s) \ xs \{\!| \text{Unlock} \rightarrow a, \text{SyncUnlock } a \!|\} (\text{Throw } ad) (hp \ s) \ xs$ 
    by(fastforce simp add: sim-move01-def ta-bisim-def fun-eq-iff expand-finfun-eq finfun-upd-apply ta-upd-simps split: if-split-asm)
    moreover note  $\langle \text{lcl } s \subseteq_m [Vs \mapsto] xs \rangle \langle \text{bisim } Vs \text{ (insync}(a) \text{ Throw } ad) \text{ E2 } xs \rangle$ 
    ultimately show ?case by(fastforce)
next
  case InstanceOfRed thus ?case by(fastforce)
next
  case RedInstanceOf thus ?case by(auto intro!: exI)
next
  case InstanceOfThrow thus ?case by fastforce
qed(fastforce simp del: fun-upd-apply split: if-split-asm)+

end

declare max-dest [iff del]

declare split-paired-Ex [simp del]

primrec countInitBlock :: ('a, 'b, 'addr) exp  $\Rightarrow$  nat
  and countInitBlocks :: ('a, 'b, 'addr) exp list  $\Rightarrow$  nat
where
  countInitBlock (new C) = 0
  | countInitBlock (newA T [e]) = countInitBlock e
  | countInitBlock (Cast T e) = countInitBlock e
  | countInitBlock (e instanceof T) = countInitBlock e
  | countInitBlock (Val v) = 0
  | countInitBlock (Var V) = 0
  | countInitBlock (V := e) = countInitBlock e
  | countInitBlock (e «bop» e') = countInitBlock e + countInitBlock e'
  | countInitBlock (a [i]) = countInitBlock a + countInitBlock i
  | countInitBlock (AAss a i e) = countInitBlock a + countInitBlock i + countInitBlock e
  | countInitBlock (a.length) = countInitBlock a
  | countInitBlock (e.F {D}) = countInitBlock e
  | countInitBlock (FAss e F D e') = countInitBlock e + countInitBlock e'
  | countInitBlock (e.compareAndSwap(D.F, e', e'')) =
    countInitBlock e + countInitBlock e' + countInitBlock e''
  | countInitBlock (e.M(es)) = countInitBlock e + countInitBlocks es
  | countInitBlock ({V:T=vo; e}) = (case vo of None  $\Rightarrow$  0 | Some v  $\Rightarrow$  1) + countInitBlock e
  | countInitBlock (sync V' (e) e') = countInitBlock e + countInitBlock e'
  | countInitBlock (insync V' (ad) e) = countInitBlock e

```

$| \text{countInitBlock } (e; e') = \text{countInitBlock } e + \text{countInitBlock } e'$
 $| \text{countInitBlock } (\text{if } (e) e1 \text{ else } e2) = \text{countInitBlock } e + \text{countInitBlock } e1 + \text{countInitBlock } e2$
 $| \text{countInitBlock } (\text{while}(b) e) = \text{countInitBlock } b + \text{countInitBlock } e$
 $| \text{countInitBlock } (\text{throw } e) = \text{countInitBlock } e$
 $| \text{countInitBlock } (\text{try } e \text{ catch}(C V) e') = \text{countInitBlock } e + \text{countInitBlock } e'$

$| \text{countInitBlocks } [] = 0$
 $| \text{countInitBlocks } (e \# es) = \text{countInitBlock } e + \text{countInitBlocks } es$

context *J0-J1-heap-base* **begin**

lemmas $\tau\text{red0r-expr} =$

$\text{NewArray-}\tau\text{red0r-xt}$ $\text{Cast-}\tau\text{red0r-xt}$ $\text{InstanceOf-}\tau\text{red0r-xt}$ $\text{BinOp-}\tau\text{red0r-xt1}$ $\text{BinOp-}\tau\text{red0r-xt2}$ $\text{LAss-}\tau\text{red0r-xt}$
 $\text{AAss-}\tau\text{red0r-xt1}$ $\text{AAss-}\tau\text{red0r-xt2}$ $\text{AAss-}\tau\text{red0r-xt3}$ $\text{AAss-}\tau\text{red0r-xt2}$ $\text{AAss-}\tau\text{red0r-xt3}$
 $\text{ALength-}\tau\text{red0r-xt}$ $\text{FAcc-}\tau\text{red0r-xt}$ $\text{FAss-}\tau\text{red0r-xt1}$ $\text{FAss-}\tau\text{red0r-xt2}$
 $\text{CAS-}\tau\text{red0r-xt1}$ $\text{CAS-}\tau\text{red0r-xt2}$ $\text{CAS-}\tau\text{red0r-xt3}$ $\text{Call-}\tau\text{red0r-obj}$
 $\text{Call-}\tau\text{red0r-param}$ $\text{Block-}\tau\text{red0r-xt}$ $\text{Sync-}\tau\text{red0r-xt}$ $\text{InSync-}\tau\text{red0r-xt}$
 $\text{Seq-}\tau\text{red0r-xt}$ $\text{Cond-}\tau\text{red0r-xt}$ $\text{Throw-}\tau\text{red0r-xt}$ $\text{Try-}\tau\text{red0r-xt}$

lemmas $\tau\text{red0t-expr} =$

$\text{NewArray-}\tau\text{red0t-xt}$ $\text{Cast-}\tau\text{red0t-xt}$ $\text{InstanceOf-}\tau\text{red0t-xt}$ $\text{BinOp-}\tau\text{red0t-xt1}$ $\text{BinOp-}\tau\text{red0t-xt2}$ $\text{LAss-}\tau\text{red0t-xt}$
 $\text{AAss-}\tau\text{red0t-xt1}$ $\text{AAss-}\tau\text{red0t-xt2}$ $\text{AAss-}\tau\text{red0t-xt1}$ $\text{AAss-}\tau\text{red0t-xt2}$ $\text{AAss-}\tau\text{red0t-xt3}$
 $\text{ALength-}\tau\text{red0t-xt}$ $\text{FAcc-}\tau\text{red0t-xt}$ $\text{FAss-}\tau\text{red0t-xt1}$ $\text{FAss-}\tau\text{red0t-xt2}$
 $\text{CAS-}\tau\text{red0t-xt1}$ $\text{CAS-}\tau\text{red0t-xt2}$ $\text{CAS-}\tau\text{red0t-xt3}$ $\text{Call-}\tau\text{red0t-obj}$
 $\text{Call-}\tau\text{red0t-param}$ $\text{Block-}\tau\text{red0t-xt}$ $\text{Sync-}\tau\text{red0t-xt}$ $\text{InSync-}\tau\text{red0t-xt}$
 $\text{Seq-}\tau\text{red0t-xt}$ $\text{Cond-}\tau\text{red0t-xt}$ $\text{Throw-}\tau\text{red0t-xt}$ $\text{Try-}\tau\text{red0t-xt}$

declare $\tau\text{red0r-expr}$ [elim!]

declare $\tau\text{red0t-expr}$ [elim!]

definition *sim-move10* ::

$'\text{addr } J\text{-prog} \Rightarrow '\text{thread-id} \Rightarrow (' \text{addr}, '\text{thread-id}, '\text{heap}) \text{ external-thread-action} \Rightarrow '\text{addr } \text{expr1} \Rightarrow '\text{addr } \text{expr1} \Rightarrow '\text{addr } \text{expr}$
 $\Rightarrow '\text{heap} \Rightarrow '\text{addr locals} \Rightarrow (' \text{addr}, '\text{thread-id}, '\text{heap}) J0\text{-thread-action} \Rightarrow '\text{addr } \text{expr} \Rightarrow '\text{heap} \Rightarrow '\text{addr } \text{locals} \Rightarrow \text{bool}$

where

$\text{sim-move10 } P t \text{ ta1 } e1 e1' e h \text{ xs } \text{ ta } e' h' \text{ xs}' \longleftrightarrow \neg \text{final } e1 \wedge$
 $(\text{if } \tau\text{move1 } P h e1 \text{ then } (\tau\text{red0t } (\text{extTA2J0 } P) P t h (e, \text{xs}) (e', \text{xs}') \vee \text{countInitBlock } e1' < \text{countInitBlock } e1 \wedge e' = e \wedge \text{xs}' = \text{xs}) \wedge h' = h \wedge \text{ta1} = \varepsilon \wedge \text{ta} = \varepsilon$
 $\text{else } \text{ta-bisim01 } \text{ta} (\text{extTA2J1 } (\text{compP1 } P) \text{ ta1}) \wedge$
 $(\text{if } \text{call } e = \text{None} \vee \text{call1 } e1 = \text{None}$
 $\text{then } (\exists e'' \text{xs}''. \tau\text{red0r } (\text{extTA2J0 } P) P t h (e, \text{xs}) (e'', \text{xs}'') \wedge \text{extTA2J0 } P, P, t \vdash \langle e'', (h, \text{xs}'') \rangle$
 $\text{-ta} \rightarrow \langle e', (h', \text{xs}') \rangle \wedge \text{no-call } P h e'' \wedge \neg \tau\text{move0 } P h e'')$
 $\text{else } \text{extTA2J0 } P, P, t \vdash \langle e, (h, \text{xs}) \rangle \text{-ta} \rightarrow \langle e', (h', \text{xs}') \rangle \wedge \text{no-call } P h e \wedge \neg \tau\text{move0 } P h e))$

definition *sim-moves10* ::

$'\text{addr } J\text{-prog} \Rightarrow '\text{thread-id} \Rightarrow (' \text{addr}, '\text{thread-id}, '\text{heap}) \text{ external-thread-action} \Rightarrow '\text{addr } \text{expr1 list} \Rightarrow '\text{addr } \text{expr1 list}$
 $\Rightarrow '\text{addr } \text{expr list} \Rightarrow '\text{heap} \Rightarrow '\text{addr locals} \Rightarrow (' \text{addr}, '\text{thread-id}, '\text{heap}) J0\text{-thread-action} \Rightarrow '\text{addr } \text{expr list} \Rightarrow '\text{heap}$
 $\Rightarrow '\text{addr locals} \Rightarrow \text{bool}$

where

$\text{sim-moves10 } P t \text{ ta1 } es1 es1' es h \text{ xs } \text{ ta } es' h' \text{ xs}' \longleftrightarrow \neg \text{finals } es1 \wedge$
 $(\text{if } \tau\text{moves1 } P h es1 \text{ then } (\tau\text{reds0t } (\text{extTA2J0 } P) P t h (es, \text{xs}) (es', \text{xs}') \vee \text{countInitBlocks } es1' <$

$\text{countInitBlocks } es1 \wedge es' = es \wedge xs' = xs) \wedge h' = h \wedge ta1 = \varepsilon \wedge ta = \varepsilon$
 $\text{else } ta\text{-bisim01 } ta (\text{extTA2J1 } (\text{compP1 } P) ta1) \wedge$
 $(\text{if calls } es = \text{None} \vee \text{calls1 } es1 = \text{None}$
 $\text{then } (\exists es'' xs''. \tau\text{reds0r } (\text{extTA2J0 } P) P t h (es, xs) (es'', xs'') \wedge \text{extTA2J0 } P, P, t \vdash \langle es'', (h,$
 $xs'') \rangle [-ta \rightarrow] \langle es', (h', xs') \rangle \wedge \text{no-calls } P h es'' \wedge \neg \tau\text{moves0 } P h es'')$
 $\text{else } \text{extTA2J0 } P, P, t \vdash \langle es, (h, xs) \rangle [-ta \rightarrow] \langle es', (h', xs') \rangle \wedge \text{no-calls } P h es \wedge \neg \tau\text{moves0 } P h$
 $es))$

lemma *sim-move10-expr*:

assumes *sim-move10* $P t ta1 e1 e1' e h xs ta e' h' xs'$

shows

$\text{sim-move10 } P t ta1 (\text{newA } T[e1]) (\text{newA } T[e1']) (\text{newA } T[e]) h xs ta (\text{newA } T[e']) h' xs'$
 $\text{sim-move10 } P t ta1 (\text{Cast } T e1) (\text{Cast } T e1') (\text{Cast } T e) h xs ta (\text{Cast } T e') h' xs'$
 $\text{sim-move10 } P t ta1 (e1 \text{ instanceof } T) (e1' \text{ instanceof } T) (e \text{ instanceof } T) h xs ta (e' \text{ instanceof } T)$
 $h' xs'$
 $\text{sim-move10 } P t ta1 (e1 \ll\text{bop}\gg e2) (e1' \ll\text{bop}\gg e2) (e \ll\text{bop}\gg e2') h xs ta (e' \ll\text{bop}\gg e2') h' xs'$
 $\text{sim-move10 } P t ta1 (\text{Val } v \ll\text{bop}\gg e1) (\text{Val } v \ll\text{bop}\gg e1') (\text{Val } v \ll\text{bop}\gg e) h xs ta (\text{Val } v \ll\text{bop}\gg e') h'$
 xs'
 $\text{sim-move10 } P t ta1 (V := e1) (V := e1') (V' := e) h xs ta (V' := e') h' xs'$
 $\text{sim-move10 } P t ta1 (e1[e2]) (e1'[e2']) (e[e2]) h xs ta (e'[e2']) h' xs'$
 $\text{sim-move10 } P t ta1 (\text{Val } v[e1]) (\text{Val } v[e1']) (\text{Val } v[e]) h xs ta (\text{Val } v[e']) h' xs'$
 $\text{sim-move10 } P t ta1 (e1[e2] := e3) (e1'[e2'] := e3) (e[e2] := e3') h xs ta (e'[e2'] := e3') h' xs'$
 $\text{sim-move10 } P t ta1 (\text{Val } v[e1] := e3) (\text{Val } v[e1'] := e3) (\text{Val } v[e] := e3') h xs ta (\text{Val } v[e'] :=$
 $e3') h' xs'$
 $\text{sim-move10 } P t ta1 (\text{AAss } (\text{Val } v) (\text{Val } v') e1) (\text{AAss } (\text{Val } v) (\text{Val } v') e1') (\text{AAss } (\text{Val } v) (\text{Val } v')$
 $e) h xs ta (\text{AAss } (\text{Val } v) (\text{Val } v') e') h' xs'$
 $\text{sim-move10 } P t ta1 (e1 \cdot \text{length}) (e1' \cdot \text{length}) (e \cdot \text{length}) h xs ta (e' \cdot \text{length}) h' xs'$
 $\text{sim-move10 } P t ta1 (e1 \cdot F\{D\}) (e1' \cdot F\{D'\}) (e \cdot F\{D'\}) h xs ta (e' \cdot F\{D'\}) h' xs'$
 $\text{sim-move10 } P t ta1 (\text{FAss } e1 F D e2) (\text{FAss } e1' F D e2) (\text{FAss } e F' D' e2') h xs ta (\text{FAss } e' F' D'$
 $e2') h' xs'$
 $\text{sim-move10 } P t ta1 (\text{FAss } (\text{Val } v) F D e1) (\text{FAss } (\text{Val } v) F D e1') (\text{FAss } (\text{Val } v) F' D' e) h xs ta$
 $(\text{FAss } (\text{Val } v) F' D' e') h' xs'$
 $\text{sim-move10 } P t ta1 (\text{CompareAndSwap } e1 F D e2 e3) (\text{CompareAndSwap } e1' F D e2 e3) (\text{CompareAndSwap}$
 $e F' D' e2' e3') h xs ta (\text{CompareAndSwap } e' F' D' e2' e3') h' xs'$
 $\text{sim-move10 } P t ta1 (\text{CompareAndSwap } (\text{Val } v) F D e1 e3) (\text{CompareAndSwap } (\text{Val } v) F D e1' e3)$
 $(\text{CompareAndSwap } (\text{Val } v) F' D' e e3') h xs ta (\text{CompareAndSwap } (\text{Val } v) F' D' e' e3') h' xs'$
 $\text{sim-move10 } P t ta1 (\text{CompareAndSwap } (\text{Val } v) F D (\text{Val } v') e1) (\text{CompareAndSwap } (\text{Val } v) F D$
 $(\text{Val } v') e1') (\text{CompareAndSwap } (\text{Val } v) F' D' (\text{Val } v') e) h xs ta (\text{CompareAndSwap } (\text{Val } v) F' D'$
 $(\text{Val } v') e') h' xs'$
 $\text{sim-move10 } P t ta1 (e1 \cdot M(es)) (e1' \cdot M(es)) (e \cdot M(es')) h xs ta (e' \cdot M(es')) h' xs'$
 $\text{sim-move10 } P t ta1 (\text{sync}_V(e1) e2) (\text{sync}_V(e1') e2) (\text{sync}(e) e2') h xs ta (\text{sync}(e') e2') h' xs'$
 $\text{sim-move10 } P t ta1 (\text{insync}_V(a) e1) (\text{insync}_V(a) e1') (\text{insync}(a') e) h xs ta (\text{insync}(a') e') h' xs'$
 $\text{sim-move10 } P t ta1 (e1 ;; e2) (e1' ;; e2) (e ;; e2') h xs ta (e' ;; e2') h' xs'$
 $\text{sim-move10 } P t ta1 (\text{if } (e1) e2 \text{ else } e3) (\text{if } (e1') e2 \text{ else } e3) (\text{if } (e) e2' \text{ else } e3') h xs ta (\text{if } (e') e2'$
 $\text{else } e3') h' xs'$
 $\text{sim-move10 } P t ta1 (\text{throw } e1) (\text{throw } e1') (\text{throw } e) h xs ta (\text{throw } e') h' xs'$
 $\text{sim-move10 } P t ta1 (\text{try } e1 \text{ catch}(C V) e2) (\text{try } e1' \text{ catch}(C V) e2) (\text{try } e \text{ catch}(C' V') e2') h xs$
 $ta (\text{try } e' \text{ catch}(C' V') e2') h' xs'$

using *assms*

apply(*simp-all add: sim-move10-def final-iff split del: if-split split: if-split-asm*)

apply(*fastforce simp: $\tau\text{red0t-Val } \tau\text{red0r-Val}$ intro: red-reds.intros split!: if-splits*)+

done

lemma *sim-moves10-expr*:

$sim-move10 P t ta1 e1 e1' e h xs ta e' h' xs' \implies sim-moves10 P t ta1 (e1 \# es2) (e1' \# es2) (e \# es2') h xs ta (e' \# es2') h' xs'$

$sim-moves10 P t ta1 es1 es1' es h xs ta es' h' xs' \implies sim-moves10 P t ta1 (Val v \# es1) (Val v \# es1') (Val v \# es) h xs ta (Val v \# es') h' xs'$

unfolding $sim-moves10-def sim-move10-def final-iff finals-iff$

apply($simp-all$ add: $Cons-eq-append-conv split del: if-split split: if-split-asm$)

apply($safe intro!$: $if-split$)

apply($fastforce simp$ add: $is-vals-conv \tau reds0t-map-Val \tau reds0r-map-Val \tau red0t-Val \tau red0r-Val intro!$: $\tau red0r-inj-\tau reds0r \tau reds0r-cons-\tau reds0r \tau red0t-inj-\tau reds0t \tau reds0t-cons-\tau reds0t ListRed1 ListRed2 split: if-split-asm$)+

done

lemma $sim-move10-CallParams$:

$sim-moves10 P t ta1 es1 es1' es h xs ta es' h' xs'$

$\implies sim-move10 P t ta1 (Val v \cdot M(es1)) (Val v \cdot M(es1')) (Val v \cdot M(es)) h xs ta (Val v \cdot M(es')) h' xs'$

unfolding $sim-move10-def sim-moves10-def$

apply($simp split: if-split-asm split del: if-split add: is-vals-conv$)

apply($fastforce simp$ add: $\tau red0t-Val \tau red0r-Val \tau reds0t-map-Val \tau reds0r-map-Val intro$: $Call-\tau red0r-param Call-\tau red0t-param CallParams split: if-split-asm split del: if-split intro!$: $if-split$)

apply($rule conjI$)

apply $fastforce$

apply($rule if-intro$)

apply $fastforce$

apply($clarsimp split del: if-split$)

apply($rule if-intro$)

apply($clarsimp split: if-split-asm simp add: is-vals-conv$)

apply($rule exI conjI$)+

apply($erule Call-\tau red0r-param$)

apply($fastforce intro$: $CallParams$)

apply($rule exI conjI$)+

apply($erule Call-\tau red0r-param$)

apply($fastforce intro!$: $CallParams$)

apply($clarsimp split del: if-split split: if-split-asm simp add: is-vals-conv \tau reds0r-map-Val$)

apply $fastforce$

apply($rule conjI$)

apply $fastforce$

apply($rule if-intro$)

apply $fastforce$

apply($rule conjI$)

apply $fastforce$

apply($rule if-intro$)

apply($clarsimp split: if-split-asm$)

apply($clarsimp split: if-split-asm split del: if-split simp add: is-vals-conv$)

apply($fastforce intro$: $CallParams$)

done

lemma $sim-move10-Block$:

$sim-move10 P t ta1 e1 e1' e h (xs(V' := vo)) ta e' h' xs'$

$\implies sim-move10 P t ta1 (\{V:T=None; e1\}) (\{V:T=None; e1'\}) (\{V':T=vo; e\}) h xs ta (\{V':T=xs' V'; e'\}) h' (xs'(V' := xs V'))$

proof –

assume $sim-move10 P t ta1 e1 e1' e h (xs(V' := vo)) ta e' h' xs'$

moreover {

fix $e'' xs''$

assume $extTA2J0 P, P, t \vdash \langle e'', (h, xs'') \rangle -ta \rightarrow \langle e', (h', xs') \rangle$
hence $extTA2J0 P, P, t \vdash \langle e'', (h, xs''(V' := xs V', V' := xs'' V')) \rangle -ta \rightarrow \langle e', (h', xs') \rangle$ **by simp**
from *BlockRed[OF this, of T]*
have $extTA2J0 P, P, t \vdash \langle \{ V':T=xs'' V'; e'' \}, (h, xs''(V' := xs V')) \rangle -ta \rightarrow \langle \{ V':T=xs' V'; e' \}, (h', xs'(V' := xs V')) \rangle$
by simp }
ultimately show *?thesis*
by(*fastforce simp add: sim-move10-def final-iff split: if-split-asm*)
qed

lemma *sim-move10-reds:*

$\llbracket (h', a) \in allocate\ h\ (Class\text{-}type\ C); ta1 = \{\!\{ NewHeapElem\ a\ (Class\text{-}type\ C) \}\!\}; ta = \{\!\{ NewHeapElem\ a\ (Class\text{-}type\ C) \}\!\} \rrbracket$
 $\implies sim\text{-}move10\ P\ t\ ta1\ (new\ C)\ e1'\ (new\ C)\ h\ xs\ ta\ (addr\ a)\ h'\ xs$
 $allocate\ h\ (Class\text{-}type\ C) = \{\!\{ \}\!\} \implies sim\text{-}move10\ P\ t\ \varepsilon\ (new\ C)\ e1'\ (new\ C)\ h\ xs\ \varepsilon\ (THROW\ OutOfMemory)\ h\ xs$
 $\llbracket (h', a) \in allocate\ h\ (Array\text{-}type\ T\ (nat\ (sint\ i))); 0 \leq i; ta1 = \{\!\{ NewHeapElem\ a\ (Array\text{-}type\ T\ (nat\ (sint\ i))) \}\!\}; ta = \{\!\{ NewHeapElem\ a\ (Array\text{-}type\ T\ (nat\ (sint\ i))) \}\!\} \rrbracket$
 $\implies sim\text{-}move10\ P\ t\ ta1\ (newA\ T[Val\ (Intg\ i)])\ e1'\ (newA\ T[Val\ (Intg\ i)])\ h\ xs\ ta\ (addr\ a)\ h'\ xs$
 $i < s\ 0 \implies sim\text{-}move10\ P\ t\ \varepsilon\ (newA\ T[Val\ (Intg\ i)])\ e1'\ (newA\ T[Val\ (Intg\ i)])\ h\ xs\ \varepsilon\ (THROW\ NegativeArraySize)\ h\ xs$
 $\llbracket allocate\ h\ (Array\text{-}type\ T\ (nat\ (sint\ i))) = \{\!\{ \}\!\}; 0 \leq i \rrbracket$
 $\implies sim\text{-}move10\ P\ t\ \varepsilon\ (newA\ T[Val\ (Intg\ i)])\ e1'\ (newA\ T[Val\ (Intg\ i)])\ h\ xs\ \varepsilon\ (THROW\ OutOfMemory)\ h\ xs$
 $\llbracket typeof_h\ v = \lfloor U \rfloor; P \vdash U \leq T \rrbracket$
 $\implies sim\text{-}move10\ P\ t\ \varepsilon\ (Cast\ T\ (Val\ v))\ e1'\ (Cast\ T\ (Val\ v))\ h\ xs\ \varepsilon\ (Val\ v)\ h\ xs$
 $\llbracket typeof_h\ v = \lfloor U \rfloor; \neg P \vdash U \leq T \rrbracket$
 $\implies sim\text{-}move10\ P\ t\ \varepsilon\ (Cast\ T\ (Val\ v))\ e1'\ (Cast\ T\ (Val\ v))\ h\ xs\ \varepsilon\ (THROW\ ClassCast)\ h\ xs$
 $\llbracket typeof_h\ v = \lfloor U \rfloor; b \longleftrightarrow v \neq Null \wedge P \vdash U \leq T \rrbracket$
 $\implies sim\text{-}move10\ P\ t\ \varepsilon\ ((Val\ v)\ instanceof\ T)\ e1'\ ((Val\ v)\ instanceof\ T)\ h\ xs\ \varepsilon\ (Val\ (Bool\ b))\ h\ xs$
 $binop\ bop\ v1\ v2 = Some\ (Inl\ v) \implies sim\text{-}move10\ P\ t\ \varepsilon\ ((Val\ v1)\ \ll bop \gg\ (Val\ v2))\ e1'\ (Val\ v1\ \ll bop \gg\ Val\ v2)\ h\ xs\ \varepsilon\ (Val\ v)\ h\ xs$
 $binop\ bop\ v1\ v2 = Some\ (Inr\ a) \implies sim\text{-}move10\ P\ t\ \varepsilon\ ((Val\ v1)\ \ll bop \gg\ (Val\ v2))\ e1'\ (Val\ v1\ \ll bop \gg\ Val\ v2)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$
 $xs\ V = \lfloor v \rfloor \implies sim\text{-}move10\ P\ t\ \varepsilon\ (Var\ V')\ e1'\ (Var\ V)\ h\ xs\ \varepsilon\ (Val\ v)\ h\ xs$
 $sim\text{-}move10\ P\ t\ \varepsilon\ (V' := Val\ v)\ e1'\ (V := Val\ v)\ h\ xs\ \varepsilon\ unit\ h\ (xs(V \mapsto v))$
 $sim\text{-}move10\ P\ t\ \varepsilon\ (null\ \lfloor Val\ v \rfloor)\ e1'\ (null\ \lfloor Val\ v \rfloor)\ h\ xs\ \varepsilon\ (THROW\ NullPointer)\ h\ xs$
 $\llbracket typeof\text{-}addr\ h\ a = \lfloor Array\text{-}type\ T\ n \rfloor; i < s\ 0 \vee sint\ i \geq int\ n \rrbracket$
 $\implies sim\text{-}move10\ P\ t\ \varepsilon\ (addr\ a\ \lfloor Val\ (Intg\ i) \rfloor)\ e1'\ ((addr\ a)\ \lfloor Val\ (Intg\ i) \rfloor)\ h\ xs\ \varepsilon\ (THROW\ ArrayIndexOutOfBounds)\ h\ xs$
 $\llbracket typeof\text{-}addr\ h\ a = \lfloor Array\text{-}type\ T\ n \rfloor; 0 \leq i; sint\ i < int\ n; heap\text{-}read\ h\ a\ (ACell\ (nat\ (sint\ i)))\ v; ta1 = \{\!\{ ReadMem\ a\ (ACell\ (nat\ (sint\ i)))\ v \}\!\}; ta = \{\!\{ ReadMem\ a\ (ACell\ (nat\ (sint\ i)))\ v \}\!\} \rrbracket$
 $\implies sim\text{-}move10\ P\ t\ ta1\ (addr\ a\ \lfloor Val\ (Intg\ i) \rfloor)\ e1'\ ((addr\ a)\ \lfloor Val\ (Intg\ i) \rfloor)\ h\ xs\ ta\ (Val\ v)\ h\ xs$
 $sim\text{-}move10\ P\ t\ \varepsilon\ (null\ \lfloor Val\ v \rfloor := Val\ v')\ e1'\ (null\ \lfloor Val\ v \rfloor := Val\ v')\ h\ xs\ \varepsilon\ (THROW\ NullPointer)\ h\ xs$
 $\llbracket typeof\text{-}addr\ h\ a = \lfloor Array\text{-}type\ T\ n \rfloor; i < s\ 0 \vee sint\ i \geq int\ n \rrbracket$
 $\implies sim\text{-}move10\ P\ t\ \varepsilon\ (AAss\ (addr\ a)\ (Val\ (Intg\ i))\ (Val\ v))\ e1'\ (AAss\ (addr\ a)\ (Val\ (Intg\ i))\ (Val\ v))\ h\ xs\ \varepsilon\ (THROW\ ArrayIndexOutOfBounds)\ h\ xs$
 $\llbracket typeof\text{-}addr\ h\ a = \lfloor Array\text{-}type\ T\ n \rfloor; 0 \leq i; sint\ i < int\ n; typeof_h\ v = \lfloor U \rfloor; \neg (P \vdash U \leq T) \rrbracket$
 $\implies sim\text{-}move10\ P\ t\ \varepsilon\ (AAss\ (addr\ a)\ (Val\ (Intg\ i))\ (Val\ v))\ e1'\ (AAss\ (addr\ a)\ (Val\ (Intg\ i))\ (Val\ v))\ h\ xs\ \varepsilon\ (THROW\ ArrayStore)\ h\ xs$
 $\llbracket typeof\text{-}addr\ h\ a = \lfloor Array\text{-}type\ T\ n \rfloor; 0 \leq i; sint\ i < int\ n; typeof_h\ v = Some\ U; P \vdash U \leq T; \rrbracket$

$heap\text{-}write\ h\ a\ (ACell\ (nat\ (sint\ i)))\ v\ h'$;
 $ta1 = \{\!\!| WriteMem\ a\ (ACell\ (nat\ (sint\ i)))\ v \!\!\};\ ta = \{\!\!| WriteMem\ a\ (ACell\ (nat\ (sint\ i)))\ v \!\!\}$]
 $\implies sim\text{-}move10\ P\ t\ ta1\ (AAss\ (addr\ a)\ (Val\ (Intg\ i))\ (Val\ v))\ e1'\ (AAss\ (addr\ a)\ (Val\ (Intg\ i))\ (Val\ v))\ h\ xs\ ta\ unit\ h'\ xs$
 $typeof\text{-}addr\ h\ a = \lfloor Array\text{-}type\ T\ n \rfloor \implies sim\text{-}move10\ P\ t\ \varepsilon\ (addr\ a \cdot length)\ e1'\ (addr\ a \cdot length)\ h\ xs$
 $\varepsilon\ (Val\ (Intg\ (word\text{-}of\text{-}nat\ n)))\ h\ xs$
 $sim\text{-}move10\ P\ t\ \varepsilon\ (null \cdot length)\ e1'\ (null \cdot length)\ h\ xs\ \varepsilon\ (THROW\ NullPointer)\ h\ xs$
 $\llbracket heap\text{-}read\ h\ a\ (CField\ D\ F)\ v;\ ta1 = \{\!\!| ReadMem\ a\ (CField\ D\ F)\ v \!\!\};\ ta = \{\!\!| ReadMem\ a\ (CField\ D\ F)\ v \!\!\} \rrbracket$
 $\implies sim\text{-}move10\ P\ t\ ta1\ (addr\ a \cdot F\{D\})\ e1'\ (addr\ a \cdot F\{D\})\ h\ xs\ ta\ (Val\ v)\ h\ xs$
 $sim\text{-}move10\ P\ t\ \varepsilon\ (null \cdot F\{D\})\ e1'\ (null \cdot F\{D\})\ h\ xs\ \varepsilon\ (THROW\ NullPointer)\ h\ xs$
 $\llbracket heap\text{-}write\ h\ a\ (CField\ D\ F)\ v\ h';\ ta1 = \{\!\!| WriteMem\ a\ (CField\ D\ F)\ v \!\!\};\ ta = \{\!\!| WriteMem\ a\ (CField\ D\ F)\ v \!\!\} \rrbracket$
 $\implies sim\text{-}move10\ P\ t\ ta1\ (addr\ a \cdot F\{D\} := Val\ v)\ e1'\ (addr\ a \cdot F\{D\} := Val\ v)\ h\ xs\ ta\ unit\ h'\ xs$
 $sim\text{-}move10\ P\ t\ \varepsilon\ (null \cdot compareAndSwap(D \cdot F,\ Val\ v,\ Val\ v'))\ e1'\ (null \cdot compareAndSwap(D \cdot F,\ Val\ v,\ Val\ v'))\ h\ xs\ \varepsilon\ (THROW\ NullPointer)\ h\ xs$
 $\llbracket heap\text{-}read\ h\ a\ (CField\ D\ F)\ v'';\ heap\text{-}write\ h\ a\ (CField\ D\ F)\ v'\ h';\ v'' = v;$
 $ta1 = \{\!\!| ReadMem\ a\ (CField\ D\ F)\ v'',\ WriteMem\ a\ (CField\ D\ F)\ v' \!\!\};\ ta = \{\!\!| ReadMem\ a\ (CField\ D\ F)\ v'',\ WriteMem\ a\ (CField\ D\ F)\ v' \!\!\} \rrbracket$
 $\implies sim\text{-}move10\ P\ t\ ta1\ (addr\ a \cdot compareAndSwap(D \cdot F,\ Val\ v,\ Val\ v'))\ e1'\ (addr\ a \cdot compareAndSwap(D \cdot F,\ Val\ v,\ Val\ v'))\ h\ xs\ ta\ true\ h'\ xs$
 $\llbracket heap\text{-}read\ h\ a\ (CField\ D\ F)\ v'';\ v'' \neq v;$
 $ta1 = \{\!\!| ReadMem\ a\ (CField\ D\ F)\ v'' \!\!\};\ ta = \{\!\!| ReadMem\ a\ (CField\ D\ F)\ v'' \!\!\} \rrbracket$
 $\implies sim\text{-}move10\ P\ t\ ta1\ (addr\ a \cdot compareAndSwap(D \cdot F,\ Val\ v,\ Val\ v'))\ e1'\ (addr\ a \cdot compareAndSwap(D \cdot F,\ Val\ v,\ Val\ v'))\ h\ xs\ ta\ false\ h\ xs$

$sim\text{-}move10\ P\ t\ \varepsilon\ (null \cdot F\{D\} := Val\ v)\ e1'\ (null \cdot F\{D\} := Val\ v)\ h\ xs\ \varepsilon\ (THROW\ NullPointer)\ h\ xs$

$sim\text{-}move10\ P\ t\ \varepsilon\ (\{V':T=None;\ Val\ u\})\ e1'\ (\{V:T=vo;\ Val\ u\})\ h\ xs\ \varepsilon\ (Val\ u)\ h\ xs$

$sim\text{-}move10\ P\ t\ \varepsilon\ (\{V':T=[v];\ e\})\ (\{V':T=None;\ e\})\ (\{V:T=vo;\ e'\})\ h\ xs\ \varepsilon\ (\{V:T=vo;\ e'\})\ h\ xs$

$sim\text{-}move10\ P\ t\ \varepsilon\ (sync_{V'}(null)\ e0)\ e1'\ (sync(null)\ e1)\ h\ xs\ \varepsilon\ (THROW\ NullPointer)\ h\ xs$

$sim\text{-}move10\ P\ t\ \varepsilon\ (Val\ v;;e0)\ e1'\ (Val\ v;;e1)\ h\ xs\ \varepsilon\ e1\ h\ xs$

$sim\text{-}move10\ P\ t\ \varepsilon\ (if\ (true)\ e0\ else\ e0')\ e1'\ (if\ (true)\ e1\ else\ e2)\ h\ xs\ \varepsilon\ e1\ h\ xs$

$sim\text{-}move10\ P\ t\ \varepsilon\ (if\ (false)\ e0\ else\ e0')\ e1'\ (if\ (false)\ e1\ else\ e2)\ h\ xs\ \varepsilon\ e2\ h\ xs$

$sim\text{-}move10\ P\ t\ \varepsilon\ (throw\ null)\ e1'\ (throw\ null)\ h\ xs\ \varepsilon\ (THROW\ NullPointer)\ h\ xs$

$sim\text{-}move10\ P\ t\ \varepsilon\ (try\ (Val\ v)\ catch(C\ V')\ e0)\ e1'\ (try\ (Val\ v)\ catch(C\ V)\ e1)\ h\ xs\ \varepsilon\ (Val\ v)\ h\ xs$

$\llbracket typeof\text{-}addr\ h\ a = \lfloor Class\text{-}type\ D \rfloor;\ P \vdash D \leq^* C \rrbracket$

$\implies sim\text{-}move10\ P\ t\ \varepsilon\ (try\ (Throw\ a)\ catch(C\ V')\ e0)\ e1'\ (try\ (Throw\ a)\ catch(C\ V)\ e1)\ h\ xs\ \varepsilon\ (\{V:Class\ C=\lfloor Addr\ a \rfloor;\ e1\})\ h\ xs$

$sim\text{-}move10\ P\ t\ \varepsilon\ (newA\ T\ \lfloor Throw\ a \rfloor)\ e1'\ (newA\ T\ \lfloor Throw\ a \rfloor)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$

$sim\text{-}move10\ P\ t\ \varepsilon\ (Cast\ T\ (Throw\ a))\ e1'\ (Cast\ T\ (Throw\ a))\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$

$sim\text{-}move10\ P\ t\ \varepsilon\ ((Throw\ a)\ instanceof\ T)\ e1'\ ((Throw\ a)\ instanceof\ T)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$

$sim\text{-}move10\ P\ t\ \varepsilon\ ((Throw\ a)\ \ll bop \gg e0)\ e1'\ ((Throw\ a)\ \ll bop \gg e1)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$

$sim\text{-}move10\ P\ t\ \varepsilon\ (Val\ v\ \ll bop \gg (Throw\ a))\ e1'\ (Val\ v\ \ll bop \gg (Throw\ a))\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$

$sim\text{-}move10\ P\ t\ \varepsilon\ (V' := Throw\ a)\ e1'\ (V := Throw\ a)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$

$sim\text{-}move10\ P\ t\ \varepsilon\ (Throw\ a\ \lfloor e0 \rfloor)\ e1'\ (Throw\ a\ \lfloor e1 \rfloor)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$

$sim\text{-}move10\ P\ t\ \varepsilon\ (Val\ v\ \lfloor Throw\ a \rfloor)\ e1'\ (Val\ v\ \lfloor Throw\ a \rfloor)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$

$sim\text{-}move10\ P\ t\ \varepsilon\ (Throw\ a\ \lfloor e0 \rfloor := e0')\ e1'\ (Throw\ a\ \lfloor e1 \rfloor := e2)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$

$sim\text{-}move10\ P\ t\ \varepsilon\ (Val\ v\ \lfloor Throw\ a \rfloor := e0)\ e1'\ (Val\ v\ \lfloor Throw\ a \rfloor := e1)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$

$sim\text{-}move10\ P\ t\ \varepsilon\ (Val\ v\ \lfloor Val\ v \rfloor := Throw\ a)\ e1'\ (Val\ v\ \lfloor Val\ v \rfloor := Throw\ a)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$

$sim\text{-}move10\ P\ t\ \varepsilon\ (Throw\ a \cdot length)\ e1'\ (Throw\ a \cdot length)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$

$sim\text{-}move10\ P\ t\ \varepsilon\ (Throw\ a \cdot F\{D\})\ e1'\ (Throw\ a \cdot F\{D\})\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$

$sim\text{-}move10\ P\ t\ \varepsilon\ (Throw\ a \cdot F\{D\} := e0)\ e1'\ (Throw\ a \cdot F\{D\} := e1)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$

$sim-move10 P t \varepsilon (Val v \cdot F\{D\} := Throw a) e1' (Val v \cdot F\{D\} := Throw a) h xs \varepsilon (Throw a) h xs$
 $sim-move10 P t \varepsilon (CompareAndSwap (Throw a) D F e0 e0') e1' (Throw a \cdot compareAndSwap(D \cdot F, e1'', e1''')) h xs \varepsilon (Throw a) h xs$
 $sim-move10 P t \varepsilon (CompareAndSwap (Val v) D F (Throw a) e0') e1' (Val v \cdot compareAndSwap(D \cdot F, Throw a, e1'')) h xs \varepsilon (Throw a) h xs$
 $sim-move10 P t \varepsilon (CompareAndSwap (Val v) D F (Val v') (Throw a)) e1' (Val v \cdot compareAndSwap(D \cdot F, Val v', Throw a)) h xs \varepsilon (Throw a) h xs$
 $sim-move10 P t \varepsilon (Throw a \cdot M(es0)) e1' (Throw a \cdot M(es1)) h xs \varepsilon (Throw a) h xs$
 $sim-move10 P t \varepsilon (Val v \cdot M(map Val vs @ Throw a \# es0)) e1' (Val v \cdot M(map Val vs @ Throw a \# es1)) h xs \varepsilon (Throw a) h xs$
 $sim-move10 P t \varepsilon (\{V':T=None; Throw a\}) e1' (\{V:T=vo; Throw a\}) h xs \varepsilon (Throw a) h xs$
 $sim-move10 P t \varepsilon (sync_{V'}(Throw a) e0) e1' (sync(Throw a) e1) h xs \varepsilon (Throw a) h xs$
 $sim-move10 P t \varepsilon (Throw a;;e0) e1' (Throw a;;e1) h xs \varepsilon (Throw a) h xs$
 $sim-move10 P t \varepsilon (if (Throw a) e0 else e0') e1' (if (Throw a) e1 else e2) h xs \varepsilon (Throw a) h xs$
 $sim-move10 P t \varepsilon (throw (Throw a)) e1' (throw (Throw a)) h xs \varepsilon (Throw a) h xs$
apply(fastforce simp add: sim-move10-def no-calls-def no-call-def ta-bisim-def intro: red-reds.intros)+
done

lemma *sim-move10-CallNull*:

$sim-move10 P t \varepsilon (null \cdot M(map Val vs)) e1' (null \cdot M(map Val vs)) h xs \varepsilon (THROW NullPointer) h xs$

by(fastforce simp add: sim-move10-def map-eq-append-conv intro: RedCallNull)

lemma *sim-move10-SyncLocks*:

$\llbracket ta1 = \{Lock \rightarrow a, SyncLock a\}; ta = \{Lock \rightarrow a, SyncLock a\} \rrbracket$

$\implies sim-move10 P t ta1 (sync_{V'}(addr a) e0) e1' (sync(addr a) e1) h xs ta (insync(a) e1) h xs$

$\llbracket ta1 = \{Unlock \rightarrow a, SyncUnlock a\}; ta = \{Unlock \rightarrow a, SyncUnlock a\} \rrbracket$

$\implies sim-move10 P t ta1 (insync_{V'}(a') (Val v)) e1' (insync(a) (Val v)) h xs ta (Val v) h xs$

$\llbracket ta1 = \{Unlock \rightarrow a, SyncUnlock a\}; ta = \{Unlock \rightarrow a, SyncUnlock a\} \rrbracket$

$\implies sim-move10 P t ta1 (insync_{V'}(a') (Throw a'')) e1' (insync(a) (Throw a'')) h xs ta (Throw a'')$

$h xs$

by(fastforce simp add: sim-move10-def ta-bisim-def ta-upd-simps intro: red-reds.intros[simplified])+

lemma *sim-move10-TryFail*:

$\llbracket typeof-addr h a = [Class-type D]; \neg P \vdash D \preceq^* C \rrbracket$

$\implies sim-move10 P t \varepsilon (try (Throw a) catch(C V') e0) e1' (try (Throw a) catch(C V) e1) h xs \varepsilon (Throw a) h xs$

by(auto simp add: sim-move10-def intro!: RedTryFail)

lemmas *sim-move10-intros* =

sim-move10-expr sim-move10-reds sim-move10-CallNull sim-move10-TryFail sim-move10-Block sim-move10-Call

lemma *sim-move10-preserves-defass*:

assumes *wf: wf-J-prog P*

shows $\llbracket sim-move10 P t ta1 e1 e1' e h xs ta e' h' xs'; \mathcal{D} e [dom xs] \rrbracket \implies \mathcal{D} e' [dom xs']$

by(auto simp add: sim-move10-def split: if-split-asm dest!: $\tau red0r$ -preserves-defass[OF wf] $\tau red0t$ -preserves-defass[OF wf] *red-preserves-defass*[OF wf])

declare *sim-move10-intros*[intro]

lemma **assumes** *wf: wf-J-prog P*

shows *red-simulates-red1-aux*:

$\llbracket False, compP1 P, t \vdash 1 \langle e1, S \rangle - TA \rightarrow \langle e1', S' \rangle; bisim vs e2 e1 (lcl S); fv e2 \subseteq set vs; x \subseteq_m [vs \mapsto] lcl S; length vs + max-vars e1 \leq length (lcl S) \rrbracket$

$\mathcal{D} e2 \lfloor \text{dom } x \rfloor$
 $\implies \exists ta e2' x'. \text{sim-move10 } P t TA e1 e1' e2 (hp S) x ta e2' (hp S') x' \wedge \text{bisim } vs e2' e1' (lcl S')$
 $\wedge x' \subseteq_m [vs \mapsto] lcl S'$
(is $\llbracket -; -; -; -; - \rrbracket \implies ?\text{concl } e1 e1' e2 S x TA S' e1' vs$

and *reds-simulates-reds1-aux*:

$\llbracket \text{False}, \text{comp}P1 P, t \vdash 1 \langle es1, S \rangle [-TA \rightarrow] \langle es1', S' \rangle; \text{bisims } vs es2 es1 (lcl S); \text{fvs } es2 \subseteq \text{set } vs;$
 $x \subseteq_m [vs \mapsto] lcl S; \text{length } vs + \text{max-varss } es1 \leq \text{length } (lcl S);$
 $\mathcal{D} s es2 \lfloor \text{dom } x \rfloor$
 $\implies \exists ta es2' x'. \text{sim-moves10 } P t TA es1 es1' es2 (hp S) x ta es2' (hp S') x' \wedge \text{bisims } vs es2' es1'$
 $(lcl S') \wedge x' \subseteq_m [vs \mapsto] lcl S'$
(is $\llbracket -; -; -; -; - \rrbracket \implies ?\text{concls } es1 es1' es2 S x TA S' es1' vs$

proof(*induct arbitrary: vs e2 x and vs es2 x rule: red1-reds1.inducts*)

case (*Bin1OpRed1 e s ta e' s' bop e2 Vs E2 X*)

note $IH = \langle \bigwedge vs e2 x. \llbracket \text{bisim } vs e2 e (lcl s); \text{fv } e2 \subseteq \text{set } vs;$
 $x \subseteq_m [vs \mapsto] lcl s; \text{length } vs + \text{max-vars } e \leq \text{length } (lcl s); \mathcal{D} e2 \lfloor \text{dom } x \rfloor \rrbracket$
 $\implies ?\text{concl } e e' e2 s x ta s' e' vs$

from $\langle \text{False}, \text{comp}P1 P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \rangle$ **have** $\neg \text{is-val } e$ **by** *auto*

with $\langle \text{bisim } Vs E2 (e \langle \text{bop} \rangle e2) (lcl s) \rangle$ **obtain** $E E2'$

where $E2: E2 = E \langle \text{bop} \rangle E2' e2 = \text{comp}E1 Vs E2'$ **and** $\text{bisim } Vs E e (lcl s)$

and *sync*: $\neg \text{contains-insync } E2'$

by(*auto elim!*: *bisim-cases*)

moreover note $IH[\text{of } Vs E X] \langle \text{fv } E2 \subseteq \text{set } Vs \rangle \langle X \subseteq_m [Vs \mapsto] lcl s \rangle$

$\langle \text{length } Vs + \text{max-vars } (e \langle \text{bop} \rangle e2) \leq \text{length } (lcl s) \rangle \langle \mathcal{D} E2 \lfloor \text{dom } X \rfloor \rangle$

ultimately obtain $TA' e2' x'$ **where** *sim-move10* $P t ta e e' E (hp s) X TA' e2' (hp s') x'$

$\text{bisim } Vs e2' e' (lcl s') x' \subseteq_m [Vs \mapsto] lcl s'$ **by**(*auto*)

with $E2 \langle \text{fv } E2 \subseteq \text{set } Vs \rangle$ *sync* **show** $?case$ **by**(*cases is-val e2'*)(*auto intro: BinOpRed1*)

next

case (*Bin1OpRed2 e s ta e' s' v bop Vs E2 X*)

note $IH = \langle \bigwedge vs e2 x. \llbracket \text{bisim } vs e2 e (lcl s); \text{fv } e2 \subseteq \text{set } vs;$
 $x \subseteq_m [vs \mapsto] lcl s; \text{length } vs + \text{max-vars } e \leq \text{length } (lcl s); \mathcal{D} e2 \lfloor \text{dom } x \rfloor \rrbracket$
 $\implies ?\text{concl } e e' e2 s x ta s' e' vs$

from $\langle \text{bisim } Vs E2 (Val v \langle \text{bop} \rangle e) (lcl s) \rangle$ **obtain** E

where $E2: E2 = Val v \langle \text{bop} \rangle E$ **and** $\text{bisim } Vs E e (lcl s)$ **by**(*auto*)

moreover note $IH[\text{of } Vs E X] \langle \text{fv } E2 \subseteq \text{set } Vs \rangle \langle X \subseteq_m [Vs \mapsto] lcl s \rangle$

$\langle \text{length } Vs + \text{max-vars } (Val v \langle \text{bop} \rangle e) \leq \text{length } (lcl s) \rangle \langle \mathcal{D} E2 \lfloor \text{dom } X \rfloor \rangle E2$

ultimately show $?case$ **by**(*auto intro: BinOpRed2*)

next

case (*Red1Var s V v Vs E2 X*)

from $\langle \text{bisim } Vs E2 (Var V) (lcl s) \rangle \langle \text{fv } E2 \subseteq \text{set } Vs \rangle$

obtain V' **where** $E2 = Var V' V' = Vs ! V V = \text{index } Vs V'$ **by**(*clarify, simp*)

from $\langle E2 = Var V' \rangle \langle \mathcal{D} E2 \lfloor \text{dom } X \rfloor \rangle$

obtain v' **where** $X V' = \lfloor v' \rfloor$ **by**(*auto simp add: hyperset-defs*)

with $\langle X \subseteq_m [Vs \mapsto] lcl s \rangle$ **have** $[Vs \mapsto] lcl s V' = \lfloor v' \rfloor$ **by**(*rule map-le-SomeD*)

with $\langle \text{length } Vs + \text{max-vars } (Var V) \leq \text{length } (lcl s) \rangle$

have $lcl s ! (\text{index } Vs V') = v'$ **by**(*auto intro: map-upds-Some-eq-nth-index*)

with $\langle V = \text{index } Vs V' \rangle \langle lcl s ! V = v \rangle$ **have** $v = v'$ **by** *simp*

with $\langle X V' = \lfloor v' \rfloor \rangle \langle E2 = Var V' \rangle \langle X \subseteq_m [Vs \mapsto] lcl s \rangle$

show $?case$ **by**(*fastforce intro: RedVar*)

next

case (*LAss1Red e s ta e' s' V Vs E2 X*)

note $IH = \langle \bigwedge vs e2 x. \llbracket \text{bisim } vs e2 e (lcl s); \text{fv } e2 \subseteq \text{set } vs;$

$x \subseteq_m [vs \mapsto] lcl s; \text{length } vs + \text{max-vars } e \leq \text{length } (lcl s); \mathcal{D} e2 \lfloor \text{dom } x \rfloor \rrbracket$

$\implies ?\text{concl } e e' e2 s x ta s' e' vs$

from $\langle \text{bisim } Vs \ E2 \ (V := e) \ (lcl \ s) \rangle$ **obtain** $E \ V'$
where $E2: E2 = (V' := E) \ V = \text{index } Vs \ V'$ **and** $\text{bisim } Vs \ E \ e \ (lcl \ s)$ **by** *auto*
with $IH[\text{of } Vs \ E \ X] \ \langle \text{fv } E2 \subseteq \text{set } Vs \rangle \ \langle X \subseteq_m [Vs \ [\mapsto]] \ lcl \ s \rangle$
 $\langle \text{length } Vs + \text{max-vars } (V := e) \leq \text{length } (lcl \ s) \rangle \ \langle \mathcal{D} \ E2 \ [\text{dom } X] \rangle$
 $E2$ **show** $?case$ **by**(*auto intro: LAssRed*)

next
case (*Red1LAss V l v h Vs E2 X*)
from $\langle \text{bisim } Vs \ E2 \ (V := \text{Val } v) \ (lcl \ (h, \ l)) \rangle$ **obtain** V' **where** $E2 = (V' := \text{Val } v) \ V = \text{index } Vs \ V'$ **by**(*auto*)
moreover with $\langle \text{fv } E2 \subseteq \text{set } Vs \rangle \ \langle X \subseteq_m [Vs \ [\mapsto]] \ lcl \ (h, \ l) \rangle \ \langle \text{length } Vs + \text{max-vars } (V := \text{Val } v) \leq \text{length } (lcl \ (h, \ l)) \rangle$
have $X(V' \mapsto v) \subseteq_m [Vs \ [\mapsto]] \ l[\text{index } Vs \ V' := v]$ **by**(*auto intro: LAss-lem*)
ultimately show $?case$ **by**(*auto intro: RedLAss simp del: fun-upd-apply*)

next
case (*AAcc1Red1 a s ta a' s' i Vs E2 X*)
note $IH = \langle \bigwedge vs \ e2 \ x. \llbracket \text{bisim } vs \ e2 \ a \ (lcl \ s); \text{fv } e2 \subseteq \text{set } vs; \ x \subseteq_m [vs \ [\mapsto]] \ lcl \ s; \text{length } vs + \text{max-vars } a \leq \text{length } (lcl \ s); \mathcal{D} \ e2 \ [\text{dom } x] \rrbracket \implies ?concl \ a \ a' \ e2 \ s \ x \ ta \ s' \ a' \ vs \rangle$
from $\langle \text{False}, \text{comp}P1 \ P, t \vdash 1 \ \langle a, s \rangle \ -ta \rightarrow \langle a', s' \rangle \rangle$ **have** $\neg \text{is-val } a$ **by** *auto*
with $\langle \text{bisim } Vs \ E2 \ (a[i]) \ (lcl \ s) \rangle$ **obtain** $E \ E2'$
where $E2: E2 = E[E2'] \ i = \text{comp}E1 \ Vs \ E2'$ **and** $\text{bisim } Vs \ E \ a \ (lcl \ s)$
and $\text{sync}: \neg \text{contains-insync } E2'$ **by**(*fastforce*)
moreover note $IH[\text{of } Vs \ E \ X] \ \langle \text{fv } E2 \subseteq \text{set } Vs \rangle \ \langle X \subseteq_m [Vs \ [\mapsto]] \ lcl \ s \rangle$
 $\langle \text{length } Vs + \text{max-vars } (a[i]) \leq \text{length } (lcl \ s) \rangle \ \langle \mathcal{D} \ E2 \ [\text{dom } X] \rangle$
ultimately obtain $TA' \ e2' \ x'$ **where** $\text{sim-move}10 \ P \ t \ ta \ a \ a' \ E \ (hp \ s) \ X \ TA' \ e2' \ (hp \ s') \ x'$
 $\text{bisim } Vs \ e2' \ a' \ (lcl \ s') \ x' \subseteq_m [Vs \ [\mapsto]] \ lcl \ s'$ **by**(*auto*)
with $E2 \ \langle \text{fv } E2 \subseteq \text{set } Vs \rangle$ **sync show** $?case$
by(*cases is-val e2'*)(*auto intro: AAccRed1*)

next
case (*AAcc1Red2 i s ta i' s' a Vs E2 X*)
note $IH = \langle \bigwedge vs \ e2 \ x. \llbracket \text{bisim } vs \ e2 \ i \ (lcl \ s); \text{fv } e2 \subseteq \text{set } vs; \ x \subseteq_m [vs \ [\mapsto]] \ lcl \ s; \text{length } vs + \text{max-vars } i \leq \text{length } (lcl \ s); \mathcal{D} \ e2 \ [\text{dom } x] \rrbracket \implies ?concl \ i \ i' \ e2 \ s \ x \ ta \ s' \ i' \ vs \rangle$
from $\langle \text{bisim } Vs \ E2 \ (\text{Val } a[i]) \ (lcl \ s) \rangle$ **obtain** E
where $E2: E2 = \text{Val } a[E]$ **and** $\text{bisim } Vs \ E \ i \ (lcl \ s)$ **by**(*auto*)
moreover note $IH[\text{of } Vs \ E \ X] \ \langle \text{fv } E2 \subseteq \text{set } Vs \rangle \ \langle X \subseteq_m [Vs \ [\mapsto]] \ lcl \ s \rangle \ E2$
 $\langle \text{length } Vs + \text{max-vars } (\text{Val } a[i]) \leq \text{length } (lcl \ s) \rangle \ \langle \mathcal{D} \ E2 \ [\text{dom } X] \rangle$
ultimately show $?case$ **by**(*auto intro: AAccRed2*)

next
case *Red1AAcc* **thus** $?case$ **by**(*fastforce intro: RedAAcc simp del: fun-upd-apply*)

next
case (*AAss1Red1 a s ta a' s' i e Vs E2 X*)
note $IH = \langle \bigwedge vs \ e2 \ x. \llbracket \text{bisim } vs \ e2 \ a \ (lcl \ s); \text{fv } e2 \subseteq \text{set } vs; \ x \subseteq_m [vs \ [\mapsto]] \ lcl \ s; \text{length } vs + \text{max-vars } a \leq \text{length } (lcl \ s); \mathcal{D} \ e2 \ [\text{dom } x] \rrbracket \implies ?concl \ a \ a' \ e2 \ s \ x \ ta \ s' \ a' \ vs \rangle$
from $\langle \text{False}, \text{comp}P1 \ P, t \vdash 1 \ \langle a, s \rangle \ -ta \rightarrow \langle a', s' \rangle \rangle$ **have** $\neg \text{is-val } a$ **by** *auto*
with $\langle \text{bisim } Vs \ E2 \ (a[i] := e) \ (lcl \ s) \rangle$ **obtain** $E \ E2' \ E2''$
where $E2: E2 = E[E2'] := E2'' \ i = \text{comp}E1 \ Vs \ E2' \ e = \text{comp}E1 \ Vs \ E2''$ **and** $\text{bisim } Vs \ E \ a \ (lcl \ s)$
and $\text{sync}: \neg \text{contains-insync } E2' \ \neg \text{contains-insync } E2''$ **by**(*fastforce*)
moreover note $IH[\text{of } Vs \ E \ X] \ \langle \text{fv } E2 \subseteq \text{set } Vs \rangle \ \langle X \subseteq_m [Vs \ [\mapsto]] \ lcl \ s \rangle$
 $\langle \text{length } Vs + \text{max-vars } (a[i] := e) \leq \text{length } (lcl \ s) \rangle \ \langle \mathcal{D} \ E2 \ [\text{dom } X] \rangle$
ultimately obtain $TA' \ e2' \ x'$ **where** $IH': \text{sim-move}10 \ P \ t \ ta \ a \ a' \ E \ (hp \ s) \ X \ TA' \ e2' \ (hp \ s') \ x'$
 $\text{bisim } Vs \ e2' \ a' \ (lcl \ s') \ x' \subseteq_m [Vs \ [\mapsto]] \ lcl \ s'$ **by**(*auto*)
show $?case$

proof(*cases is-val e2'*)
case *True*
from $E2 \langle fv E2 \subseteq set Vs \rangle sync$ **have** *bisim Vs E2' i (lcl s')* *bisim Vs E2'' e (lcl s')* **by** *auto*
with $IH' E2 True sync$ **show** *?thesis*
by(*cases is-val E2'*)(*fastforce intro: AAssRed1*)+
next
case *False* **with** $IH' E2 sync$ **show** *?thesis* **by**(*fastforce intro: AAssRed1*)
qed
next
case (*AAss1Red2 i s ta i' s' a e Vs E2 X*)
note $IH = \langle \bigwedge vs e2 x. \llbracket bisim vs e2 i (lcl s); fv e2 \subseteq set vs; \rrbracket$
 $x \subseteq_m [vs [\mapsto] lcl s]; length vs + max-vars i \leq length (lcl s); \mathcal{D} e2 [dom x] \rrbracket$
 $\implies ?concl i i' e2 s x ta s' i' vs \rangle$
from $\langle False, compP1 P, t \vdash 1 \langle i, s \rangle -ta \rightarrow \langle i', s' \rangle \rangle$ **have** $\neg is-val i$ **by** *auto*
with $\langle bisim Vs E2 (Val a[i] := e) (lcl s) \rangle$ **obtain** $E E2'$
where $E2: E2 = Val a[E] := E2' e = compE1 Vs E2'$ **and** *bisim Vs E i (lcl s)*
and *sync: \neg contains-insync E2'* **by**(*fastforce*)
moreover note $IH[of Vs E X] \langle fv E2 \subseteq set Vs \rangle \langle X \subseteq_m [Vs [\mapsto] lcl s] \rangle$
 $\langle length Vs + max-vars (Val a[i] := e) \leq length (lcl s) \rangle \langle \mathcal{D} E2 [dom X] \rangle$
ultimately obtain $TA' e2' x'$ **where** *sim-move10 P t ta i i' E (hp s) X TA' e2' (hp s') x'*
bisim Vs e2' i' (lcl s') x' \subseteq_m [Vs \mapsto] lcl s'] **by**(*auto*)
with $E2 \langle fv E2 \subseteq set Vs \rangle sync$ **show** *?case*
by(*cases is-val e2'*)(*fastforce intro: AAssRed2*)+
next
case (*AAss1Red3 e s ta e' s' a i Vs E2 X*)
note $IH = \langle \bigwedge vs e2 x. \llbracket bisim vs e2 e (lcl s); fv e2 \subseteq set vs; \rrbracket$
 $x \subseteq_m [vs [\mapsto] lcl s]; length vs + max-vars e \leq length (lcl s); \mathcal{D} e2 [dom x] \rrbracket$
 $\implies ?concl e e' e2 s x ta s' e' vs \rangle$
from $\langle bisim Vs E2 (Val a[Val i] := e) (lcl s) \rangle$ **obtain** E
where $E2: E2 = Val a[Val i] := E$ **and** *bisim Vs E e (lcl s)* **by**(*fastforce*)
moreover note $IH[of Vs E X] \langle fv E2 \subseteq set Vs \rangle \langle X \subseteq_m [Vs [\mapsto] lcl s] \rangle E2$
 $\langle length Vs + max-vars (Val a[Val i] := e) \leq length (lcl s) \rangle \langle \mathcal{D} E2 [dom X] \rangle$
ultimately show *?case* **by**(*fastforce intro: AAssRed3*)
next
case *Red1AAssStore* **thus** *?case* **by**(*auto*)(*(rule exI conjI)+, auto*)
next
case *Red1AAss* **thus** *?case*
by(*fastforce simp del: fun-upd-apply*)
next
case (*FAss1Red1 e s ta e' s' F D e2 Vs E2 X*)
note $IH = \langle \bigwedge vs e2 x. \llbracket bisim vs e2 e (lcl s); fv e2 \subseteq set vs; \rrbracket$
 $x \subseteq_m [vs [\mapsto] lcl s]; length vs + max-vars e \leq length (lcl s); \mathcal{D} e2 [dom x] \rrbracket$
 $\implies ?concl e e' e2 s x ta s' e' vs \rangle$
from $\langle False, compP1 P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \rangle$ **have** $\neg is-val e$ **by** *auto*
with $\langle bisim Vs E2 (e \cdot F\{D\} := e2) (lcl s) \rangle$ **obtain** $E E2'$
where $E2: E2 = E \cdot F\{D\} := E2' e2 = compE1 Vs E2'$ **and** *bisim Vs E e (lcl s)*
and *sync: \neg contains-insync E2'* **by**(*fastforce*)
with $IH[of Vs E X] \langle fv E2 \subseteq set Vs \rangle \langle X \subseteq_m [Vs [\mapsto] lcl s] \rangle$
 $\langle length Vs + max-vars (e \cdot F\{D\} := e2) \leq length (lcl s) \rangle \langle \mathcal{D} E2 [dom X] \rangle$
obtain $TA' e2' x'$ **where** *sim-move10 P t ta e e' E (hp s) X TA' e2' (hp s') x'*
bisim Vs e2' e' (lcl s') x' \subseteq_m [Vs \mapsto] lcl s'] **by**(*fastforce*)
with $E2 \langle fv E2 \subseteq set Vs \rangle sync$ **show** *?case* **by**(*cases is-val e2'*)(*auto intro: FAssRed1*)
next
case (*FAss1Red2 e s ta e' s' v F D Vs E2 X*)

note $IH = \langle \bigwedge vs\ e2\ x. \llbracket \text{bisim}\ vs\ e2\ e\ (lcl\ s);\ fv\ e2 \subseteq \text{set}\ vs; \$
 $x \subseteq_m [vs \mapsto] lcl\ s];\ \text{length}\ vs + \text{max-vars}\ e \leq \text{length}\ (lcl\ s); \mathcal{D}\ e2 \llbracket \text{dom}\ x \rrbracket \rrbracket$
 $\implies ?\text{concl}\ e\ e'\ e2\ s\ x\ ta\ s'\ e'\ vs \rangle$

from $\langle \text{bisim}\ Vs\ E2\ (Val\ v \cdot F\ \{D\}; =e)\ (lcl\ s) \rangle$ **obtain** E
where $E2: E2 = (Val\ v \cdot F\ \{D\}; =E)$ **and** $\text{bisim}\ Vs\ E\ e\ (lcl\ s)$ **by**(*fastforce*)
with $IH[\text{of}\ Vs\ E\ X] \langle fv\ E2 \subseteq \text{set}\ Vs \rangle \langle X \subseteq_m [Vs \mapsto] lcl\ s \rangle$
 $\langle \text{length}\ Vs + \text{max-vars}\ (Val\ v \cdot F\ \{D\}; =e) \leq \text{length}\ (lcl\ s) \rangle \langle \mathcal{D}\ E2 \llbracket \text{dom}\ X \rrbracket \rangle$
 $E2$ **show** $?case$ **by**(*fastforce* *intro: FAssRed2*)

next
case (*CAS1Red1* $e\ s\ ta\ e'\ s'\ D\ F\ e2\ e3\ Vs\ E2\ X$)
note $IH = \langle \bigwedge vs\ e2\ x. \llbracket \text{bisim}\ vs\ e2\ e\ (lcl\ s);\ fv\ e2 \subseteq \text{set}\ vs; \$
 $x \subseteq_m [vs \mapsto] lcl\ s];\ \text{length}\ vs + \text{max-vars}\ e \leq \text{length}\ (lcl\ s); \mathcal{D}\ e2 \llbracket \text{dom}\ x \rrbracket \rrbracket$
 $\implies ?\text{concl}\ e\ e'\ e2\ s\ x\ ta\ s'\ e'\ vs \rangle$

from $\langle \text{False}, \text{comp}P1\ P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \rangle$ **have** $\neg\ is\text{-val}\ e$ **by** *auto*
with $\langle \text{bisim}\ Vs\ E2 - (lcl\ s) \rangle$ **obtain** $E\ E2'\ E2''$
where $E2: E2 = E \cdot \text{compareAndSwap}(D \cdot F, E2', E2'')$ $e2 = \text{comp}E1\ Vs\ E2'\ e3 = \text{comp}E1\ Vs\ E2''$ **and** $\text{bisim}\ Vs\ E\ e\ (lcl\ s)$
and $\text{sync}: \neg\ \text{contains-insync}\ E2' \neg\ \text{contains-insync}\ E2''$ **by**(*fastforce*)
moreover **note** $IH[\text{of}\ Vs\ E\ X] \langle fv\ E2 \subseteq \text{set}\ Vs \rangle \langle X \subseteq_m [Vs \mapsto] lcl\ s \rangle$
 $\langle \text{length}\ Vs + \text{max-vars} - \leq \text{length}\ (lcl\ s) \rangle \langle \mathcal{D}\ E2 \llbracket \text{dom}\ X \rrbracket \rangle$
ultimately **obtain** $TA'\ e2'\ x'$ **where** $IH': \text{sim-move}10\ P\ t\ ta\ e\ e'\ E\ (hp\ s)\ X\ TA'\ e2'\ (hp\ s')\ x'$
 $\text{bisim}\ Vs\ e2'\ e'\ (lcl\ s')\ x' \subseteq_m [Vs \mapsto] lcl\ s' \rangle$ **by**(*auto*)

show $?case$
proof(*cases is-val e2'*)
case *True*
from $E2 \langle fv\ E2 \subseteq \text{set}\ Vs \rangle \text{sync}$ **have** $\text{bisim}\ Vs\ E2'\ e2\ (lcl\ s')$ $\text{bisim}\ Vs\ E2''\ e3\ (lcl\ s')$ **by** *auto*
with $IH'\ E2\ \text{True}\ \text{sync}$ **show** $?thesis$ **by**(*cases is-val E2'*)(*fastforce*)+

next
case *False* **with** $IH'\ E2\ \text{sync}$ **show** $?thesis$ **by**(*fastforce*)

qed

next
case (*CAS1Red2* $e\ s\ ta\ e'\ s'\ v\ D\ F\ e3\ Vs\ E2\ X$)
note $IH = \langle \bigwedge vs\ e2\ x. \llbracket \text{bisim}\ vs\ e2\ e\ (lcl\ s);\ fv\ e2 \subseteq \text{set}\ vs; \$
 $x \subseteq_m [vs \mapsto] lcl\ s];\ \text{length}\ vs + \text{max-vars}\ e \leq \text{length}\ (lcl\ s); \mathcal{D}\ e2 \llbracket \text{dom}\ x \rrbracket \rrbracket$
 $\implies ?\text{concl}\ e\ e'\ e2\ s\ x\ ta\ s'\ e'\ vs \rangle$

from $\langle \text{False}, \text{comp}P1\ P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \rangle$ **have** $\neg\ is\text{-val}\ e$ **by** *auto*
with $\langle \text{bisim}\ Vs\ E2 - (lcl\ s) \rangle$ **obtain** $E\ E2'$
where $E2: E2 = (Val\ v \cdot \text{compareAndSwap}(D \cdot F, E, E2'))$ $e3 = \text{comp}E1\ Vs\ E2'$ **and** $\text{bisim}\ Vs\ E\ e\ (lcl\ s)$
and $\text{sync}: \neg\ \text{contains-insync}\ E2'$ **by**(*auto*)
moreover **note** $IH[\text{of}\ Vs\ E\ X] \langle fv\ E2 \subseteq \text{set}\ Vs \rangle \langle X \subseteq_m [Vs \mapsto] lcl\ s \rangle$
 $\langle \text{length}\ Vs + \text{max-vars} - \leq \text{length}\ (lcl\ s) \rangle \langle \mathcal{D}\ E2 \llbracket \text{dom}\ X \rrbracket \rangle$
ultimately **obtain** $TA'\ e2'\ x'$ **where** $\text{sim-move}10\ P\ t\ ta\ e\ e'\ E\ (hp\ s)\ X\ TA'\ e2'\ (hp\ s')\ x'$
 $\text{bisim}\ Vs\ e2'\ e'\ (lcl\ s')\ x' \subseteq_m [Vs \mapsto] lcl\ s' \rangle$ **by**(*auto*)
with $E2 \langle fv\ E2 \subseteq \text{set}\ Vs \rangle \text{sync}$ **show** $?case$
by(*cases is-val e2'*)(*fastforce*)+

next
case (*CAS1Red3* $e\ s\ ta\ e'\ s'\ v\ D\ F\ v'\ Vs\ E2\ X$)
note $IH = \langle \bigwedge vs\ e2\ x. \llbracket \text{bisim}\ vs\ e2\ e\ (lcl\ s);\ fv\ e2 \subseteq \text{set}\ vs; \$
 $x \subseteq_m [vs \mapsto] lcl\ s];\ \text{length}\ vs + \text{max-vars}\ e \leq \text{length}\ (lcl\ s); \mathcal{D}\ e2 \llbracket \text{dom}\ x \rrbracket \rrbracket$
 $\implies ?\text{concl}\ e\ e'\ e2\ s\ x\ ta\ s'\ e'\ vs \rangle$

from $\langle \text{bisim}\ Vs\ E2 - (lcl\ s) \rangle$ **obtain** E
where $E2: E2 = (Val\ v \cdot \text{compareAndSwap}(D \cdot F, Val\ v', E))$ **and** $\text{bisim}\ Vs\ E\ e\ (lcl\ s)$ **by**(*fastforce*)
moreover **note** $IH[\text{of}\ Vs\ E\ X] \langle fv\ E2 \subseteq \text{set}\ Vs \rangle \langle X \subseteq_m [Vs \mapsto] lcl\ s \rangle \langle E2$

$\langle \text{length } Vs + \text{max-vars} - \leq \text{length } (lcl\ s) \rangle \langle \mathcal{D}\ E2\ [dom\ X] \rangle$
ultimately show $?case\ \mathbf{by}(fastforce)$

next

case $(Call1Obj\ e\ s\ ta\ e'\ s'\ M\ es\ Vs\ E2\ X)$

note $IH = \langle \bigwedge vs\ e2\ x. \llbracket bisim\ vs\ e2\ e\ (lcl\ s);\ fv\ e2 \subseteq set\ vs;$
 $x \subseteq_m [vs\ [\mapsto]\ lcl\ s];\ \text{length } vs + \text{max-vars } e \leq \text{length } (lcl\ s);$
 $\mathcal{D}\ e2\ [dom\ x] \rrbracket \implies ?concl\ e\ e'\ e2\ s\ x\ ta\ s'\ e'\ vs \rangle$

from $\langle False, compP1\ P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \langle bisim\ Vs\ E2\ (e \cdot M(es))\ (lcl\ s) \rangle$

obtain $E\ es'$ **where** $E2: E2 = E \cdot M(es')$ $es = compEs1\ Vs\ es'$ **and** $bisim\ Vs\ E\ e\ (lcl\ s)$

and $sync: \neg\ \text{contains-insyncs } es'$ **by** $(auto\ elim!: bisim-cases\ simp\ add: compEs1-conv-map)$

with $IH[of\ Vs\ E\ X] \langle fv\ E2 \subseteq set\ Vs \rangle \langle X \subseteq_m [Vs\ [\mapsto]\ lcl\ s] \rangle$
 $\langle \text{length } Vs + \text{max-vars } (e \cdot M(es)) \leq \text{length } (lcl\ s) \rangle \langle \mathcal{D}\ E2\ [dom\ X] \rangle$

obtain $TA' e2' x'$ **where** $IH': sim-move10\ P\ t\ ta\ e\ e'\ E\ (hp\ s)\ X\ TA' e2' (hp\ s')\ x'$
 $bisim\ Vs\ e2'\ e'\ (lcl\ s')\ x' \subseteq_m [Vs\ [\mapsto]\ lcl\ s']$ **by** $(fastforce)$

with $E2 \langle fv\ E2 \subseteq set\ Vs \rangle \langle E2 = E \cdot M(es') \rangle$ **sync show** $?case$
by $(cases\ is-val\ e2') (auto\ intro: CallObj)$

next

case $(Call1Params\ es\ s\ ta\ es'\ s'\ v\ M\ Vs\ E2\ X)$

note $IH = \langle \bigwedge vs\ es2\ x. \llbracket bisims\ vs\ es2\ es\ (lcl\ s);\ fvs\ es2 \subseteq set\ vs;$
 $x \subseteq_m [vs\ [\mapsto]\ lcl\ s];\ \text{length } vs + \text{max-varss } es \leq \text{length } (lcl\ s); \mathcal{D}s\ es2\ [dom\ x] \rrbracket$
 $\implies ?concls\ es\ es'\ es2\ s\ x\ ta\ s'\ es'\ vs \rangle$

from $\langle bisim\ Vs\ E2\ (Val\ v \cdot M(es))\ (lcl\ s) \rangle$ **obtain** Es

where $E2 = Val\ v \cdot M(Es)$ $bisims\ Vs\ Es\ es\ (lcl\ s)$ **by** $(auto)$

with $IH[of\ Vs\ Es\ X] \langle fv\ E2 \subseteq set\ Vs \rangle \langle X \subseteq_m [Vs\ [\mapsto]\ lcl\ s] \rangle$
 $\langle \text{length } Vs + \text{max-vars } (Val\ v \cdot M(es)) \leq \text{length } (lcl\ s) \rangle \langle \mathcal{D}\ E2\ [dom\ X] \rangle$
 $\langle E2 = Val\ v \cdot M(Es) \rangle$ **show** $?case\ \mathbf{by}(fastforce\ intro: CallParams)$

next

case $(Red1CallExternal\ s\ a\ T\ M\ Ts\ Tr\ D\ vs\ ta\ va\ h'\ e'\ s'\ Vs\ E2\ X)$

from $\langle bisim\ Vs\ E2\ (addr\ a \cdot M(map\ Val\ vs))\ (lcl\ s) \rangle$ **have** $E2: E2 = addr\ a \cdot M(map\ Val\ vs)$ **by** $auto$

moreover from $\langle compP1\ P \vdash\ \text{class-type-of } T\ \text{sees } M: Ts \rightarrow Tr = \text{Native in } D \rangle$

have $P \vdash\ \text{class-type-of } T\ \text{sees } M: Ts \rightarrow Tr = \text{Native in } D$ **by** $simp$

moreover from $\langle compP1\ P, t \vdash \langle a \cdot M(vs), hp\ s \rangle -ta \rightarrow ext\ \langle va, h' \rangle$

have $P, t \vdash \langle a \cdot M(vs), hp\ s \rangle -ta \rightarrow ext\ \langle va, h' \rangle$ **by** $simp$

moreover from $wf\ \langle P, t \vdash \langle a \cdot M(vs), hp\ s \rangle -ta \rightarrow ext\ \langle va, h' \rangle$

have $ta\ \text{-bisim}01\ (extTA2J0\ P\ ta)\ (extTA2J1\ (compP1\ P)\ ta)$
by $(rule\ red-external-ta-bisim01)$

moreover note $\langle \text{typeof-addr } (hp\ s)\ a = [T] \rangle \langle e' = extRet2J1\ (addr\ a \cdot M(map\ Val\ vs))\ va \rangle \langle s' =$
 $(h',\ lcl\ s) \rangle$

moreover from $\langle \text{typeof-addr } (hp\ s)\ a = [T] \rangle \langle P, t \vdash \langle a \cdot M(vs), hp\ s \rangle -ta \rightarrow ext\ \langle va, h' \rangle$
 $\langle P \vdash\ \text{class-type-of } T\ \text{sees } M: Ts \rightarrow Tr = \text{Native in } D \rangle$

have $\tau\ \text{external-defs } D\ M \implies ta = \varepsilon \wedge h' = hp\ s$
by $(fastforce\ dest: \tau\ \text{external}'\ \text{-red-external-heap-unchanged } \tau\ \text{external}'\ \text{-red-external-TA-empty } simp$
 $add: \tau\ \text{external}'\ \text{-def } \tau\ \text{external-def})$

ultimately show $?case\ \mathbf{using} \langle X \subseteq_m [Vs\ [\mapsto]\ lcl\ s] \rangle$
by $(fastforce\ intro!: exI[where\ x=extTA2J0\ P\ ta])\ intro: RedCallExternal\ simp\ add: map-eq-append-conv$
 $sim-move10-def\ synthesized-call-def\ dest: sees-method-fun\ del: disjCI\ intro: disjI1\ disjI2)$

next

case $(Block1Red\ e\ h\ x\ ta\ e'\ h'\ x'\ V\ T\ Vs\ E2\ X)$

note $IH = \langle \bigwedge vs\ e2\ xa. \llbracket bisim\ vs\ e2\ e\ (lcl\ (h,\ x));\ fv\ e2 \subseteq set\ vs;\ xa \subseteq_m [vs\ [\mapsto]\ lcl\ (h,\ x)];$
 $\text{length } vs + \text{max-vars } e \leq \text{length } (lcl\ (h,\ x)); \mathcal{D}\ e2\ [dom\ xa] \rrbracket$
 $\implies ?concl\ e\ e'\ e2\ (h,\ x)\ xa\ ta\ (h',\ x')\ e'\ vs \rangle$

from $\langle False, compP1\ P, t \vdash 1 \langle e, (h, x) \rangle -ta \rightarrow \langle e', (h', x') \rangle \rangle$

have $\text{length } x = \text{length } x'$ **by** $(auto\ dest: red1-preserves-len)$

with $\langle \text{length } Vs + \text{max-vars } \{V:T=None; e\} \leq \text{length } (lcl\ (h,\ x)) \rangle$

```

have  $\text{length } Vs < \text{length } x'$  by simp
from  $\langle \text{bisim } Vs \ E2 \ \{V:T=None; e\} \ (lcl \ (h, x)) \rangle$ 
show ?case
proof(cases rule: bisim-cases(14)[consumes 1, case-names BlockNone BlockSome BlockSomeNone])
  case (BlockNone  $V' \ E$ )
    with  $\langle \text{fv } E2 \subseteq \text{set } Vs \rangle$  have  $\text{fv } E \subseteq \text{set } (Vs @ [V'])$  by auto
    with  $IH[\text{of } Vs @ [V'] \ E \ X(V' := None)] \ \text{BlockNone} \ \langle \text{fv } E2 \subseteq \text{set } Vs \rangle \ \langle X \subseteq_m [Vs \ [\mapsto]] \ lcl \ (h, x) \rangle$ 
       $\langle \text{length } Vs + \text{max-vars } \{V:T=None; e\} \leq \text{length } (lcl \ (h, x)) \rangle \ \langle \mathcal{D} \ E2 \ [\text{dom } X] \rangle$ 
    obtain  $TA' \ e2' \ X'$  where  $IH': \text{sim-move10 } P \ t \ ta \ e \ e' \ E \ h \ (X(V' := None)) \ TA' \ e2' \ h' \ X'$ 
       $\text{bisim } (Vs @ [V']) \ e2' \ e' \ x' \ X' \subseteq_m [Vs @ [V'] \ [\mapsto]] \ x'$ 
      by(fastforce simp del: fun-upd-apply)
    { assume  $V' \in \text{set } Vs$ 
      hence hidden  $(Vs @ [V']) \ (\text{index } Vs \ V')$  by(rule hidden-index)
      with  $\langle \text{bisim } (Vs @ [V']) \ E \ e \ (lcl \ (h, x)) \rangle$  have unmod  $e \ (\text{index } Vs \ V')$ 
        by(auto intro: hidden-bisim-unmod)
      moreover from  $\langle \text{length } Vs + \text{max-vars } \{V:T=None; e\} \leq \text{length } (lcl \ (h, x)) \rangle \ \langle V' \in \text{set } Vs \rangle$ 
        have  $\text{index } Vs \ V' < \text{length } x$  by(auto intro: index-less-aux)
      ultimately have  $x \ ! \ \text{index } Vs \ V' = x' \ ! \ \text{index } Vs \ V'$ 
        using red1-preserves-unmod[OF  $\langle \text{False, compP1 } P, t \vdash 1 \ \langle e, (h, x) \rangle \ -ta \rightarrow \langle e', (h', x') \rangle \rangle$ ]
        by(simp) }
    with  $\langle \text{length } Vs + \text{max-vars } \{V:T=None; e\} \leq \text{length } (lcl \ (h, x)) \rangle$ 
       $\langle X' \subseteq_m [Vs @ [V'] \ [\mapsto]] \ x' \rangle \ \langle \text{length } x = \text{length } x' \rangle \ \langle X \subseteq_m [Vs \ [\mapsto]] \ lcl \ (h, x) \rangle$ 
    have rel:  $X'(V' := X \ V') \subseteq_m [Vs \ [\mapsto]] \ x'$  by(auto intro: Block-lem)

  show ?thesis
  proof(cases  $X' \ V'$ )
    case None
      with BlockNone  $IH' \ \text{rel}$  show ?thesis by(fastforce intro: BlockRed)
    next
      case (Some  $v$ )
        with  $\langle X' \subseteq_m [Vs @ [V'] \ [\mapsto]] \ x' \rangle \ \langle \text{length } Vs < \text{length } x' \rangle$ 
        have  $x' \ ! \ \text{length } Vs = v$  by(auto dest: map-le-SomeD)
        with BlockNone  $IH' \ \text{rel} \ \text{Some}$  show ?thesis by(fastforce intro: BlockRed)
      qed
    next
      case BlockSome thus ?thesis by simp
    next
      case (BlockSomeNone  $V' \ E$ )
        with  $\langle \text{fv } E2 \subseteq \text{set } Vs \rangle$  have  $\text{fv } E \subseteq \text{set } (Vs @ [V'])$  by auto
        with  $IH[\text{of } Vs @ [V'] \ E \ X(V' \mapsto x \ ! \ \text{length } Vs)] \ \text{BlockSomeNone} \ \langle \text{fv } E2 \subseteq \text{set } Vs \rangle \ \langle X \subseteq_m [Vs \ [\mapsto]] \ lcl \ (h, x) \rangle$ 
           $\langle \text{length } Vs + \text{max-vars } \{V:T=None; e\} \leq \text{length } (lcl \ (h, x)) \rangle \ \langle \mathcal{D} \ E2 \ [\text{dom } X] \rangle$ 
        obtain  $TA' \ e2' \ X'$  where  $IH': \text{sim-move10 } P \ t \ ta \ e \ e' \ E \ h \ (X(V' \mapsto x \ ! \ \text{length } Vs)) \ TA' \ e2' \ h' \ X'$ 
           $\text{bisim } (Vs @ [V']) \ e2' \ e' \ x' \ X' \subseteq_m [Vs @ [V'] \ [\mapsto]] \ x'$ 
          by(fastforce simp del: fun-upd-apply)
        { assume  $V' \in \text{set } Vs$ 
          hence hidden  $(Vs @ [V']) \ (\text{index } Vs \ V')$  by(rule hidden-index)
          with  $\langle \text{bisim } (Vs @ [V']) \ E \ e \ (lcl \ (h, x)) \rangle$  have unmod  $e \ (\text{index } Vs \ V')$ 
            by(auto intro: hidden-bisim-unmod)
          moreover from  $\langle \text{length } Vs + \text{max-vars } \{V:T=None; e\} \leq \text{length } (lcl \ (h, x)) \rangle \ \langle V' \in \text{set } Vs \rangle$ 
            have  $\text{index } Vs \ V' < \text{length } x$  by(auto intro: index-less-aux)
          ultimately have  $x \ ! \ \text{index } Vs \ V' = x' \ ! \ \text{index } Vs \ V'$ 
            using red1-preserves-unmod[OF  $\langle \text{False, compP1 } P, t \vdash 1 \ \langle e, (h, x) \rangle \ -ta \rightarrow \langle e', (h', x') \rangle \rangle$ ]
            by(simp) }

```

with $\langle \text{length } Vs + \text{max-vars } \{V:T=None; e\} \leq \text{length } (\text{lcl } (h, x)) \rangle$
 $\langle X' \subseteq_m [Vs @ [V'] [\mapsto] x'] \rangle \langle \text{length } x = \text{length } x' \rangle \langle X \subseteq_m [Vs [\mapsto] \text{lcl } (h, x)] \rangle$
have $\text{rel}: X'(V' := X V') \subseteq_m [Vs [\mapsto] x']$ **by** (*auto intro: Block-lem*)
from $\langle \text{sim-move10 } P t \text{ ta } e e' E h (X(V' \mapsto x ! \text{length } Vs)) TA' e2' h' X' \rangle$
obtain v' **where** $X' V' = [v']$
by (*auto simp: sim-move10-def split: if-split-asm dest!: $\tau\text{red0t-lcl-incr } \tau\text{red0r-lcl-incr } \text{red-lcl-incr } \text{subsetD}$*)
with $\langle X' \subseteq_m [Vs @ [V'] [\mapsto] x'] \rangle \langle \text{length } Vs < \text{length } x' \rangle$
have $x' ! \text{length } Vs = v'$ **by** (*auto dest: map-le-SomeD*)
with *BlockSomeNone IH' rel* $\langle X' V' = [v'] \rangle$
show *?thesis* **by** (*fastforce intro: BlockRed*)
qed
next
case (*Block1Some V xs T v e h*)
from $\langle \text{bisim } vs e2 \{V:T=[v]; e\} (\text{lcl } (h, xs)) \rangle$ **obtain** $e' V'$ **where** $e2 = \{V':T=[v]; e'\}$
and $V = \text{length } vs \text{ bisim } (vs @ [V']) e' e (xs[\text{length } vs := v])$ **by** (*fastforce*)
moreover **have** *sim-move10 P t ε* $\{ \text{length } vs:T=[v]; e \} \{ \text{length } vs:T=None; e \} \{ V':T=[v]; e' \}$
 $x \varepsilon \{ V':T=[v]; e' \} h x$
by (*auto*)
moreover **from** $\langle \text{bisim } (vs @ [V']) e' e (xs[\text{length } vs := v]) \rangle$
 $\langle \text{length } vs + \text{max-vars } \{V:T=[v]; e\} \leq \text{length } (\text{lcl } (h, xs)) \rangle$
have *bisim vs* $\{V':T=[v]; e'\} \{ \text{length } vs:T=None; e \} (xs[\text{length } vs := v])$ **by** *auto*
moreover **from** $\langle x \subseteq_m [vs [\mapsto] \text{lcl } (h, xs)] \rangle \langle \text{length } vs + \text{max-vars } \{V:T=[v]; e\} \leq \text{length } (\text{lcl } (h, xs)) \rangle$
have $x \subseteq_m [vs [\mapsto] xs[\text{length } vs := v]]$ **by** *auto*
ultimately **show** *?case* **by** *auto*
next
case (*Lock1Synchronized V xs a e h Vs E2 X*)
note $\text{len} = \langle \text{length } Vs + \text{max-vars } (\text{sync}_V (\text{addr } a) e) \leq \text{length } (\text{lcl } (h, xs)) \rangle$
from $\langle \text{bisim } Vs E2 (\text{sync}_V (\text{addr } a) e) (\text{lcl } (h, xs)) \rangle$ **obtain** E
where $E2: E2 = \text{sync}(\text{addr } a) E e = \text{compE1 } (Vs@[\text{fresh-var } Vs]) E$
and *sync: \neg contains-insync E* **and** *[simp]: $V = \text{length } Vs$* **by** *auto*
moreover
have *extTA2J0 P,P,t* $\vdash \langle \text{sync}(\text{addr } a) E, (h, X) \rangle - \{ \text{Lock} \rightarrow a, \text{SyncLock } a \} \rightarrow \langle \text{insync}(a) E, (h, X) \rangle$
by (*rule LockSynchronized*)
moreover **from** $\langle \text{fv } E2 \subseteq \text{set } Vs \rangle$ *fresh-var-fresh[of Vs] sync len*
have *bisim Vs (insync(a) E)* *(insynclength Vs (a) e)* $(xs[\text{length } Vs := \text{Addr } a])$
unfolding $\langle e = \text{compE1 } (Vs@[\text{fresh-var } Vs]) E \rangle \langle E2 = \text{sync}(\text{addr } a) E \rangle$
by $-(\text{rule } \text{bisimInSynchronized}, \text{rule } \text{compE1-bisim}, \text{auto})$
moreover **have** *zip Vs* $(xs[\text{length } Vs := \text{Addr } a]) = (\text{zip } Vs xs)[\text{length } Vs := (\text{arbitrary}, \text{Addr } a)]$
by (*rule sym*) (*simp add: update-zip*)
hence *zip Vs* $(xs[\text{length } Vs := \text{Addr } a]) = \text{zip } Vs xs$ **by** *simp*
with $\langle X \subseteq_m [Vs [\mapsto] (\text{lcl } (h, xs))] \rangle$ **have** $X \subseteq_m [Vs [\mapsto] xs[\text{length } Vs := \text{Addr } a]]$
by (*auto simp add: map-le-def map-upds-def*)
ultimately **show** *?case* **by** (*fastforce intro: sim-move10-SyncLocks*)
next
case (*Synchronized1Red2 e s ta e' s' V a Vs E2 X*)
note $IH = \langle \bigwedge vs e2 x. \llbracket \text{bisim } vs e2 e (\text{lcl } s); \text{fv } e2 \subseteq \text{set } vs; x \subseteq_m [vs [\mapsto] \text{lcl } s]; \text{length } vs + \text{max-vars } e \leq \text{length } (\text{lcl } s); \mathcal{D} e2 [\text{dom } x] \rrbracket \implies ?\text{concl } e e' e2 s x \text{ ta } s' e' vs \rangle$
from $\langle \text{bisim } Vs E2 (\text{insync}_V (a) e) (\text{lcl } s) \rangle$ **obtain** E
where $E2: E2 = \text{insync}(a) E$ **and** *bisim: bisim (Vs@[fresh-var Vs]) E e (lcl s)*
and *xs a: lcl s ! length Vs = Addr a* **and** *[simp]: $V = \text{length } Vs$* **by** *auto*
with $\langle \text{fv } E2 \subseteq \text{set } Vs \rangle$ *fresh-var-fresh[of Vs]* **have** *fv: (fresh-var Vs) \notin fv E* **by** *auto*

from $\langle \text{length } Vs + \text{max-vars } (\text{insync}_V (a) e) \leq \text{length } (\text{lcl } s) \rangle$ **have** $\text{length } Vs < \text{length } (\text{lcl } s)$ **by** *simp*
{ **assume** $X (\text{fresh-var } Vs) \neq \text{None}$
then obtain v **where** $X (\text{fresh-var } Vs) = [v]$ **by** *auto*
with $\langle X \subseteq_m [Vs \mapsto] \text{lcl } s \rangle$ **have** $[Vs \mapsto] \text{lcl } s (\text{fresh-var } Vs) = [v]$
by(*auto simp add: map-le-def dest: bspec*)
hence $(\text{fresh-var } Vs) \in \text{set } Vs$
by(*auto simp add: map-upds-def set-zip dest!: map-of-SomeD*)
moreover have $(\text{fresh-var } Vs) \notin \text{set } Vs$ **by**(*rule fresh-var-fresh*)
ultimately have *False* **by** *contradiction* **}**
hence $X (\text{fresh-var } Vs) = \text{None}$ **by**(*cases X (fresh-var Vs), auto*)
hence $X (\text{fresh-var } Vs := \text{None}) = X$ **by**(*auto intro: ext*)
moreover from $\langle X \subseteq_m [Vs \mapsto] \text{lcl } s \rangle$
have $X (\text{fresh-var } Vs := \text{None}) \subseteq_m [Vs \mapsto] \text{lcl } s, (\text{fresh-var } Vs) \mapsto (\text{lcl } s) ! \text{length } Vs$ **by**(*simp*)
ultimately have $X \subseteq_m [Vs @ [\text{fresh-var } Vs] \mapsto] \text{lcl } s$
using $\langle \text{length } Vs < \text{length } (\text{lcl } s) \rangle$ **by**(*auto*)
moreover note $IH[\text{of } Vs @ [\text{fresh-var } Vs] E X] \text{bisim } E2 \langle \text{fv } E2 \subseteq \text{set } Vs \rangle \langle X \subseteq_m [Vs \mapsto] \text{lcl } s \rangle$
 $\langle \text{length } Vs + \text{max-vars } (\text{insync}_V (a) e) \leq \text{length } (\text{lcl } s) \rangle \langle \mathcal{D} E2 [\text{dom } X] \rangle$
ultimately obtain $TA' e2' x'$ **where** $IH': \text{sim-move10 } P t ta e e' E (hp s) X TA' e2' (hp s') x'$
 $\text{bisim } (Vs @ [\text{fresh-var } Vs]) e2' e' (\text{lcl } s') x' \subseteq_m [Vs @ [\text{fresh-var } Vs] \mapsto] \text{lcl } s'$ **by** *auto*
hence $\text{dom } x' \subseteq \text{dom } X \cup \text{fv } E$
by(*fastforce iff del: domIff simp add: sim-move10-def dest: red-dom-lcl τ red0r-dom-lcl [OF wf-prog-wwf-prog [OF wf]] τ red0t-dom-lcl [OF wf-prog-wwf-prog [OF wf]] τ red0r-fv-subset [OF wf-prog-wwf-prog [OF wf]] split: if-split-asm*)
with $\text{fv } \langle X (\text{fresh-var } Vs) = \text{None} \rangle$ **have** $(\text{fresh-var } Vs) \notin \text{dom } x'$ **by** *auto*
hence $x' (\text{fresh-var } Vs) = \text{None}$ **by** *auto*
moreover from $\langle \text{False}, \text{compP1 } P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \rangle$
have $\text{length } (\text{lcl } s) = \text{length } (\text{lcl } s')$ **by**(*auto dest: red1-preserves-len*)
moreover note $\langle x' \subseteq_m [Vs @ [\text{fresh-var } Vs] \mapsto] \text{lcl } s' \rangle \langle \text{length } Vs < \text{length } (\text{lcl } s) \rangle$
ultimately have $x' \subseteq_m [Vs \mapsto] \text{lcl } s'$ **by**(*auto simp add: map-le-def dest: bspec*)
moreover from *bisim fv* **have** *unmod e* $(\text{length } Vs)$ **by**(*auto intro: bisim-fv-unmod*)
with $\langle \text{False}, \text{compP1 } P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \rangle \langle \text{length } Vs < \text{length } (\text{lcl } s) \rangle$
have $\text{lcl } s ! \text{length } Vs = \text{lcl } s' ! \text{length } Vs$
by(*auto dest!: red1-preserves-unmod*)
with *xsa* **have** $\text{lcl } s' ! \text{length } Vs = \text{Addr } a$ **by** *simp*
ultimately show *?case* **using** $IH' E2$ **by**(*auto intro: SynchronizedRed2*)
next
case $(\text{Unlock1Synchronized } xs V a' a v h Vs E2 X)$
from $\langle \text{bisim } Vs E2 (\text{insync}_V (a) \text{Val } v) (\text{lcl } (h, xs)) \rangle$
have $E2: E2 = \text{insync}(a) \text{Val } v V = \text{length } Vs xs ! \text{length } Vs = \text{Addr } a$ **by** *auto*
moreover with $\langle xs ! V = \text{Addr } a' \rangle$ **have** $[simp]: a' = a$ **by** *simp*
have $\text{extTA2J0 } P, P, t \vdash \langle \text{insync}(a) (\text{Val } v), (h, X) \rangle -\{\text{Unlock} \rightarrow a, \text{SyncUnlock } a\} \rightarrow \langle \text{Val } v, (h, X) \rangle$
by(*rule UnlockSynchronized*)
ultimately show *?case* **using** $\langle X \subseteq_m [Vs \mapsto] \text{lcl } (h, xs) \rangle$ **by**(*fastforce intro: sim-move10-SyncLocks*)
next
case $(\text{Unlock1SynchronizedNull } xs V a v h Vs E2 X)$
from $\langle \text{bisim } Vs E2 (\text{insync}_V (a) \text{Val } v) (\text{lcl } (h, xs)) \rangle$
have $V = \text{length } Vs xs ! \text{length } Vs = \text{Addr } a$ **by**(*auto*)
with $\langle xs ! V = \text{Null} \rangle$ **have** *False* **by** *simp*
thus *?case ..*
next
case $(\text{Unlock1SynchronizedFail } xs V A' a' v h Vs E2 X)$
from $\langle \text{False} \rangle$ **show** *?case ..*
next

case (*Red1While* $b\ c\ s\ Vs\ E2\ X$)
from $\langle bisim\ Vs\ E2\ (while\ (b)\ c)\ (lcl\ s) \rangle$ **obtain** $B\ C$
where $E2: E2 = while\ (B)\ C\ b = compE1\ Vs\ B\ c = compE1\ Vs\ C$
and $sync: \neg\ contains-insync\ B\ \neg\ contains-insync\ C$ **by** *auto*
moreover **have** $extTA2J0\ P,P,t \vdash \langle while\ (B)\ C,\ (hp\ s,\ X) \rangle -\varepsilon \rightarrow \langle if\ (B)\ (C;;while\ (B)\ C)\ else\ unit,\ (hp\ s,\ X) \rangle$
by(*rule RedWhile*)
hence $sim-move10\ P\ t \in (while\ (compE1\ Vs\ B)\ (compE1\ Vs\ C))\ (if\ (compE1\ Vs\ B)\ (compE1\ Vs\ C;;while\ (compE1\ Vs\ B)\ (compE1\ Vs\ C))\ else\ unit)\ (while\ (B)\ C)\ (hp\ s)\ X \varepsilon (if\ (B)\ (C;;while\ (B)\ C)\ else\ unit)\ (hp\ s)\ X$
by(*auto simp add: sim-move10-def*)
moreover **from** $\langle fv\ E2 \subseteq set\ Vs \rangle\ E2\ sync$
have $bisim\ Vs\ (if\ (B)\ (C;;\ while\ (B)\ C)\ else\ unit)$
 $(if\ (compE1\ Vs\ B)\ (compE1\ Vs\ (C;;\ while(B)\ C))\ else\ (compE1\ Vs\ unit))\ (lcl\ s)$
by $-(rule\ bisimCond,\ auto)$
ultimately **show** $?case\ using\ \langle X \subseteq_m [Vs\ \mapsto]\ lcl\ s \rangle$
by(*simp*)(*rule exI, rule exI, rule exI, erule conjI, auto*)

next
case (*Red1TryCatch* $h\ a\ D\ C\ V\ x\ e2\ Vs\ E2\ X$)
from $\langle bisim\ Vs\ E2\ (try\ Throw\ a\ catch(C\ V)\ e2)\ (lcl\ (h,\ x)) \rangle$
obtain $E2'\ V'$ **where** $E2 = try\ Throw\ a\ catch(C\ V')\ E2'\ V = length\ Vs\ e2 = compE1\ (Vs\ @\ [V'])\ E2'$
and $sync: \neg\ contains-insync\ E2'$ **by**(*auto*)
with $\langle fv\ E2 \subseteq set\ Vs \rangle$ **have** $fv\ E2' \subseteq set\ (Vs\ @\ [V'])$ **by** *auto*
with $\langle e2 = compE1\ (Vs\ @\ [V'])\ E2' \rangle$ $sync$ **have** $bisim\ (Vs\ @\ [V'])\ E2'\ e2\ (x[V := Addr\ a])$
by(*auto intro!: compE1-bisim*)
with $\langle V = length\ Vs \rangle$ $\langle length\ Vs + max-vars\ (try\ Throw\ a\ catch(C\ V)\ e2) \leq length\ (lcl\ (h,\ x)) \rangle$
have $bisim\ Vs\ \{V':Class\ C=[Addr\ a];\ E2'\}\ \{length\ Vs:Class\ C=None;\ e2'\}\ (x[V := Addr\ a])$
by(*auto intro: bisimBlockSomeNone*)
moreover **from** $\langle length\ Vs + max-vars\ (try\ Throw\ a\ catch(C\ V)\ e2) \leq length\ (lcl\ (h,\ x)) \rangle$
have $[Vs\ \mapsto]\ x[length\ Vs := Addr\ a] = [Vs\ \mapsto]\ x$ **by** *simp*
with $\langle X \subseteq_m [Vs\ \mapsto]\ lcl\ (h,\ x) \rangle$ **have** $X \subseteq_m [Vs\ \mapsto]\ x[length\ Vs := Addr\ a]$ **by** *simp*
moreover **note** $\langle e2 = compE1\ (Vs\ @\ [V'])\ E2' \rangle\ \langle E2 = try\ Throw\ a\ catch(C\ V')\ E2' \rangle$
 $\langle typeof-addr\ h\ a = [Class-type\ D] \rangle\ \langle compP1\ P \vdash D \preceq^* C \rangle\ \langle V = length\ Vs \rangle$
ultimately **show** $?case$ **by**(*auto intro!: exI*)

next
case *Red1TryFail* **thus** $?case$ **by**(*auto intro!: exI sim-move10-TryFail*)

next
case (*List1Red1* $e\ s\ ta\ e'\ s'\ es\ Vs\ ES2\ X$)
note $IH = \langle \bigwedge vs\ e2\ x. \llbracket bisim\ vs\ e2\ e\ (lcl\ s); fv\ e2 \subseteq set\ vs; x \subseteq_m [vs\ \mapsto]\ lcl\ s; length\ vs + max-vars\ e \leq length\ (lcl\ s); \mathcal{D}\ e2\ [dom\ x] \rrbracket \implies \exists TA'\ e2'\ x'. sim-move10\ P\ t\ ta\ e\ e'\ e2\ (hp\ s)\ x\ TA'\ e2'\ (hp\ s')\ x' \wedge bisim\ vs\ e2'\ e'\ (lcl\ s') \wedge x' \subseteq_m [vs\ \mapsto]\ lcl\ s' \rangle$
from $\langle bisims\ Vs\ ES2\ (e\ \# es)\ (lcl\ s) \rangle\ \langle False, compP1\ P, t \vdash 1\ \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \rangle$
obtain $E\ ES$ **where** $ES2 = E\ \# ES\ \neg\ is-val\ E\ es = compEs1\ Vs\ ES\ bisim\ Vs\ E\ e\ (lcl\ s)$
and $sync: \neg\ contains-insyncs\ ES$ **by**(*auto elim!: bisims-cases simp add: compEs1-conv-map*)
with $IH[of\ Vs\ E\ X]\ \langle fvs\ ES2 \subseteq set\ Vs \rangle\ \langle X \subseteq_m [Vs\ \mapsto]\ lcl\ s \rangle$
 $\langle length\ Vs + max-varss\ (e\ \# es) \leq length\ (lcl\ s) \rangle\ \langle \mathcal{D}s\ ES2\ [dom\ X] \rangle$
obtain $TA'\ e2'\ x'$ **where** $IH': sim-move10\ P\ t\ ta\ e\ e'\ E\ (hp\ s)\ X\ TA'\ e2'\ (hp\ s')\ x'$
 $bisim\ Vs\ e2'\ e'\ (lcl\ s')\ x' \subseteq_m [Vs\ \mapsto]\ lcl\ s'$ **by** *fastforce*
show $?case$
proof(*cases is-val e2'*)
case *False*
with $IH'\ \langle ES2 = E\ \# ES \rangle\ \langle es = compEs1\ Vs\ ES \rangle\ sync$ **show** $?thesis$ **by**(*auto intro: sim-moves10-expr*)

```

next
  case True
    from ⟨fvs ES2 ⊆ set Vs⟩ ⟨ES2 = E # ES⟩ ⟨es = compEs1 Vs ES⟩ sync
    have bisims Vs ES es (lcl s') by(auto intro: compEs1-bisims)
    with IH' True ⟨ES2 = E # ES⟩ ⟨es = compEs1 Vs ES⟩ show ?thesis by(auto intro: sim-moves10-expr)
  qed
next
  case (List1Red2 es s ta es' s' v Vs ES2 X)
  note IH = ⟨∧vs es2 x. [[bisims vs es2 es (lcl s); fvs es2 ⊆ set vs;
    x ⊆m [vs [↦] lcl s]; length vs + max-varss es ≤ length (lcl s); Ds es2 [dom x]]
    ⇒ ∃ TA' es2' x'. sim-moves10 P t ta es es' es2 (hp s) x TA' es2' (hp s') x' ∧ bisims vs es2'
    es' (lcl s') ∧ x' ⊆m [vs [↦] lcl s']⟩
  from ⟨bisims Vs ES2 (Val v # es) (lcl s)⟩ obtain ES where ES2 = Val v # ES bisims Vs ES es
  (lcl s)
  by(auto elim!: bisims-cases)
  with IH[of Vs ES X] ⟨fvs ES2 ⊆ set Vs⟩ ⟨X ⊆m [Vs [↦] lcl s]⟩
  ⟨length Vs + max-varss (Val v # es) ≤ length (lcl s)⟩ ⟨Ds ES2 [dom X]⟩
  ⟨ES2 = Val v # ES⟩ show ?case by(fastforce intro: sim-moves10-expr)
next
  case Call1ThrowParams
  thus ?case by(fastforce intro: CallThrowParams elim!: bisim-cases simp add: bisims-map-Val-Throw2)
next
  case (Synchronized1Throw2 xs V a' a ad h Vs E2 X)
  from ⟨bisim Vs E2 (insyncV (a) Throw ad) (lcl (h, xs))⟩
  have xs ! length Vs = Addr a and V = length Vs by auto
  with ⟨xs ! V = Addr a'⟩ have [simp]: a' = a by simp
  have extTA2J0 P,P,t ⊢ ⟨insync(a) Throw ad, (h, X)⟩ -{Unlock→a, SyncUnlock a}→ ⟨Throw ad,
  (h, X)⟩
  by(rule SynchronizedThrow2)
  with ⟨X ⊆m [Vs [↦] lcl (h, xs)]⟩ ⟨bisim Vs E2 (insyncV (a) Throw ad) (lcl (h, xs))⟩
  show ?case by(auto intro: sim-move10-SyncLocks intro!: exI)
next
  case (Synchronized1Throw2Null xs V a a' h Vs E2 X)
  from ⟨bisim Vs E2 (insyncV (a) Throw a') (lcl (h, xs))⟩
  have V = length Vs xs ! length Vs = Addr a by(auto)
  with ⟨xs ! V = Null⟩ have False by simp
  thus ?case ..
next
  case (Synchronized1Throw2Fail xs V A' a' a h Vs E2 X)
  from ⟨False⟩ show ?case ..
next
  case InstanceOf1Red thus ?case by auto(blast)
next
  case Red1InstanceOf thus ?case by hypsubst-thin auto
next
  case InstanceOf1Throw thus ?case by auto
next
  case CAS1Throw thus ?case by fastforce
next
  case CAS1Throw2 thus ?case by fastforce
next
  case CAS1Throw3 thus ?case by fastforce
qed(simp-all del: fun-upd-apply, (fastforce intro: red-reds.intros simp del: fun-upd-apply simp add:
finfun-upd-apply)+)

```

lemma *bisim-call-Some-call1*:

$$\llbracket \text{bisim } Vs \ e \ e' \ xs; \text{ call } e = \lfloor aMvs \rfloor; \text{ length } Vs + \text{ max-vars } e' \leq \text{ length } xs \rrbracket \\ \implies \exists e'' \ xs'. \ \tau\text{red1}'r \ P \ t \ h \ (e', \ xs) \ (e'', \ xs') \wedge \text{call1 } e'' = \lfloor aMvs \rfloor \wedge \\ \text{bisim } Vs \ e \ e'' \ xs' \wedge \text{take } (\text{length } Vs) \ xs = \text{take } (\text{length } Vs) \ xs'$$

and *bisims-calls-Some-calls1*:

$$\llbracket \text{bisims } Vs \ es \ es' \ xs; \text{ calls } es = \lfloor aMvs \rfloor; \text{ length } Vs + \text{ max-varss } es' \leq \text{ length } xs \rrbracket \\ \implies \exists es'' \ xs'. \ \tau\text{reds1}'r \ P \ t \ h \ (es', \ xs) \ (es'', \ xs') \wedge \text{calls1 } es'' = \lfloor aMvs \rfloor \wedge \\ \text{bisims } Vs \ es \ es'' \ xs' \wedge \text{take } (\text{length } Vs) \ xs = \text{take } (\text{length } Vs) \ xs'$$

proof(*induct rule: bisim-bisims.inducts*)

case *bisimCallParams* **thus** ?*case*

by(*fastforce simp add: is-vals-conv split: if-split-asm*)

next

case *bisimBlockNone* **thus** ?*case* **by**(*fastforce intro: take-eq-take-le-eq*)

next

case (*bisimBlockSome* *Vs V e e' xs v T*)

from $\langle \text{length } Vs + \text{ max-vars } \{ \text{length } Vs: T = \lfloor v \rfloor; e' \} \leq \text{ length } xs \rangle$

have $\tau\text{red1}'r \ P \ t \ h \ (\{ \text{length } Vs: T = \lfloor v \rfloor; e' \}, \ xs) \ (\{ \text{length } Vs: T = \text{None}; e' \}, \ xs[\text{length } Vs := v])$

by(*auto intro!: \tau\text{red1}r-1step Block1Some*)

also from *bisimBlockSome* **obtain** $e'' \ xs'$

where $\tau\text{red1}'r \ P \ t \ h \ (e', \ xs[\text{length } Vs := v]) \ (e'', \ xs')$

and $\text{call1 } e'' = \lfloor aMvs \rfloor \text{ bisim } (Vs @ [V]) \ e \ e'' \ xs'$

and $\text{take } (\text{length } (Vs @ [V])) \ (xs[\text{length } Vs := v]) = \text{take } (\text{length } (Vs @ [V])) \ xs' \text{ by } \text{auto}$

hence $\tau\text{red1}'r \ P \ t \ h \ (\{ \text{length } Vs: T = \text{None}; e' \}, \ xs[\text{length } Vs := v]) \ (\{ \text{length } Vs: T = \text{None}; e'' \}, \ xs')$

by *auto*

also from $\langle \text{call1 } e'' = \lfloor aMvs \rfloor \rangle$ **have** $\text{call1 } \{ \text{length } Vs: T = \text{None}; e'' \} = \lfloor aMvs \rfloor$ **by** *simp*

moreover from $\langle \text{take } (\text{length } (Vs @ [V])) \ (xs[\text{length } Vs := v]) = \text{take } (\text{length } (Vs @ [V])) \ xs' \rangle$

have $\text{take } (\text{length } Vs) \ xs = \text{take } (\text{length } Vs) \ xs'$

by(*auto dest: take-eq-take-le-eq*[**where** $m = \text{length } Vs$] *simp add: take-update-cancel*)

moreover {

have $xs' ! \text{length } Vs = \text{take } (\text{length } (Vs @ [V])) \ xs' ! \text{length } Vs$ **by** *simp*

also note $\langle \text{take } (\text{length } (Vs @ [V])) \ (xs[\text{length } Vs := v]) = \text{take } (\text{length } (Vs @ [V])) \ xs' \rangle$ [*symmetric*]

also have $\text{take } (\text{length } (Vs @ [V])) \ (xs[\text{length } Vs := v]) ! \text{length } Vs = v$

using $\langle \text{length } Vs + \text{ max-vars } \{ \text{length } Vs: T = \lfloor v \rfloor; e' \} \leq \text{ length } xs \rangle$ **by** *simp*

finally have $\text{bisim } Vs \ \{ V: T = \lfloor v \rfloor; e \} \ \{ \text{length } Vs: T = \text{None}; e'' \} \ xs'$

using $\langle \text{bisim } (Vs @ [V]) \ e \ e'' \ xs' \rangle$ **by** *auto* }

ultimately show ?*case* **by** *blast*

next

case (*bisimBlockSomeNone* *Vs V e e' xs v T*)

then obtain $e'' \ xs'$ **where** $\tau\text{red1}'r \ P \ t \ h \ (e', \ xs) \ (e'', \ xs') \ \text{call1 } e'' = \lfloor aMvs \rfloor \ \text{bisim } (Vs @ [V]) \ e \ e'' \ xs'$

and $\text{take } (\text{length } (Vs @ [V])) \ xs = \text{take } (\text{length } (Vs @ [V])) \ xs' \text{ by } \text{auto}$

hence $\tau\text{red1}'r \ P \ t \ h \ (\{ \text{length } Vs: T = \text{None}; e' \}, \ xs) \ (\{ \text{length } Vs: T = \text{None}; e'' \}, \ xs') \text{ by } \text{auto}$

moreover from $\langle \text{call1 } e'' = \lfloor aMvs \rfloor \rangle$ **have** $\text{call1 } \{ \text{length } Vs: T = \text{None}; e'' \} = \lfloor aMvs \rfloor$ **by** *simp*

moreover from $\langle \text{take } (\text{length } (Vs @ [V])) \ xs = \text{take } (\text{length } (Vs @ [V])) \ xs' \rangle$

have $\text{take } (\text{length } Vs) \ xs = \text{take } (\text{length } Vs) \ xs' \text{ by } (\text{auto intro: take-eq-take-le-eq})$

moreover {

have $xs' ! \text{length } Vs = \text{take } (\text{length } (Vs @ [V])) \ xs' ! \text{length } Vs$ **by** *simp*

also note $\langle \text{take } (\text{length } (Vs @ [V])) \ xs = \text{take } (\text{length } (Vs @ [V])) \ xs' \rangle$ [*symmetric*]

also have $\text{take } (\text{length } (Vs @ [V])) \ xs ! \text{length } Vs = v$ **using** $\langle xs ! \text{length } Vs = v \rangle$ **by** *simp*

finally have $\text{bisim } Vs \ \{ V: T = \lfloor v \rfloor; e \} \ \{ \text{length } Vs: T = \text{None}; e'' \} \ xs'$

using $\langle \text{bisim } (Vs @ [V]) \ e \ e'' \ xs' \rangle$ **by** *auto* }

ultimately show ?*case* **by** *blast*

next

case (*bisimInSynchronized* $Vs\ e\ e'\ xs\ a$)
then obtain $e''\ xs'$ **where** $\tau red1\ 'r\ P\ t\ h\ (e',\ xs)\ (e'',\ xs')\ call1\ e'' = [aMvs]\ bisim\ (Vs\ @\ [fresh-var\ Vs])\ e\ e''\ xs'$
and $take\ (Suc\ (length\ Vs))\ xs = take\ (Suc\ (length\ Vs))\ xs'$ **by** *auto*
hence $\tau red1\ 'r\ P\ t\ h\ (insync_{length\ Vs}\ (a)\ e',\ xs)\ (insync_{length\ Vs}\ (a)\ e'',\ xs')$ **by** *auto*
moreover from $\langle call1\ e'' = [aMvs] \rangle$ **have** $call1\ (insync_{length\ Vs}\ (a)\ e'') = [aMvs]$ **by** *simp*
moreover from $\langle take\ (Suc\ (length\ Vs))\ xs = take\ (Suc\ (length\ Vs))\ xs' \rangle$
have $take\ (length\ Vs)\ xs = take\ (length\ Vs)\ xs'$ **by** (*auto intro: take-eq-take-le-eq*)
moreover {
have $xs'\ !\ length\ Vs = take\ (Suc\ (length\ Vs))\ xs'\ !\ length\ Vs$ **by** *simp*
also note $\langle take\ (Suc\ (length\ Vs))\ xs = take\ (Suc\ (length\ Vs))\ xs' \rangle$ [*symmetric*]
also have $take\ (Suc\ (length\ Vs))\ xs\ !\ length\ Vs = Addr\ a$
using $\langle xs\ !\ length\ Vs = Addr\ a \rangle$ **by** *simp*
finally have $bisim\ Vs\ (insync(a)\ e)\ (insync_{length\ Vs}\ (a)\ e'')\ xs'$
using $\langle bisim\ (Vs\ @\ [fresh-var\ Vs])\ e\ e''\ xs' \rangle$ **by** *auto* }
ultimately show *?case* **by** *blast*

next

case *bisimsCons1* **thus** *?case* **by** (*fastforce intro!: $\tau red1r\ -inj\ -\tau reds1r$*)

next

case *bisimsCons2* **thus** *?case* **by** (*fastforce intro!: $\tau reds1r\ -cons\ -\tau reds1r$*)

qed *fastforce+*

lemma *sim-move01-into-Red1*:

sim-move01 $P\ t\ ta\ e\ E'\ h\ xs\ ta'\ e2'\ h'\ xs'$

\implies *if* $\tau Move0\ P\ h\ (e,\ es1)$

then $\tau Red1\ 't\ P\ t\ h\ ((E',\ xs),\ exs2)\ ((e2',\ xs'),\ exs2) \wedge ta = \varepsilon \wedge h = h'$

else $\exists ex2'\ exs2'\ ta'. \tau Red1\ 'r\ P\ t\ h\ ((E',\ xs),\ exs2)\ (ex2',\ exs2') \wedge$
 $(call\ e = None \vee call1\ E' = None \vee ex2' = (E',\ xs) \wedge exs2' = exs2) \wedge$
 $False, P, t \vdash 1\ \langle ex2'/exs2', h \rangle -ta' \rightarrow \langle (e2',\ xs')/exs2, h' \rangle \wedge$
 $\neg \tau Move1\ P\ h\ (ex2',\ exs2') \wedge ta\ -bisim01\ ta\ ta'$

apply (*auto simp add: sim-move01-def intro: $\tau red1t\ -into\ -\tau Red1t\ \tau red1r\ -into\ -\tau Red1r\ red1Red\ split: if-split-asm$*)

apply (*fastforce intro: red1Red intro!: $\tau red1r\ -into\ -\tau Red1r$*)**+**

done

lemma *sim-move01-max-vars-decr*:

sim-move01 $P\ t\ ta\ e0\ e\ h\ xs\ ta'\ e'\ h'\ xs' \implies max-vars\ e' \leq max-vars\ e$

by (*fastforce simp add: sim-move01-def split: if-split-asm dest: $\tau red1t\ -max-vars\ red1\ -max-vars\ -decr\ \tau red1r\ -max-vars$*)

lemma *Red1-simulates-red0*:

assumes *wf: wf-J-prog* P

and *red*: $P, t \vdash 0\ \langle e1/es1, h \rangle -ta \rightarrow \langle e1'/es1', h' \rangle$

and *bisiml*: *bisim-list1* $(e1,\ es1)\ (ex2,\ exs2)$

shows $\exists ex2''\ exs2''. bisim-list1\ (e1',\ es1')\ (ex2'',\ exs2'') \wedge$

(*if* $\tau Move0\ P\ h\ (e1,\ es1)$

then $\tau Red1\ 't\ (compP1\ P)\ t\ h\ (ex2,\ exs2)\ (ex2'',\ exs2'') \wedge ta = \varepsilon \wedge h = h'$

else $\exists ex2'\ exs2'\ ta'. \tau Red1\ 'r\ (compP1\ P)\ t\ h\ (ex2,\ exs2)\ (ex2',\ exs2') \wedge$

$(call\ e1 = None \vee call1\ (fst\ ex2) = None \vee ex2' = ex2 \wedge exs2' = exs2) \wedge$

$False, compP1\ P, t \vdash 1\ \langle ex2'/exs2', h \rangle -ta' \rightarrow \langle ex2''/exs2'', h' \rangle \wedge$

$\neg \tau Move1\ P\ h\ (ex2',\ exs2') \wedge ta\ -bisim01\ ta\ ta'$)

(**is** $\exists ex2''\ exs2'' . - \wedge ?red\ ex2''\ exs2''$)

using *red*

```

proof(cases)
  case (red0Red XS')
  note [simp] = ⟨es1' = es1⟩
  and red = ⟨extTA2J0 P,P,t ⊢ ⟨e1,(h, Map.empty)⟩ -ta→ ⟨e1',(h', XS')⟩⟩
  and notsynth = ⟨∀ aMvs. call e1 = [aMvs] → synthesized-call P h aMvs⟩
from bisiml obtain E xs where ex2: ex2 = (E, xs)
  and bisim: bisim [] e1 E xs and fv: fv e1 = {}
  and length: max-vars E ≤ length xs and bsl: bisim-list es1 exs2
  and D: D e1 [{}] by(auto elim!: bisim-list1-elim)
from bisim-max-vars[OF bisim] length red1-simulates-red-aux[OF wf red bisim] fv notsynth
obtain ta' e2' xs' where sim: sim-move01 (compP1 P) t ta e1 E h xs ta' e2' h' xs'
  and bisim': bisim [] e1' e2' xs' and XS': XS' ⊆m Map.empty by auto
from sim-move01-into-Red1[OF sim, of es1 exs2]
have ?red (e2', xs') exs2 unfolding ex2 by auto
moreover {
  note bsl bisim' moreover
  from fv red-fv-subset[OF wf-prog-wwf-prog[OF wf] red]
  have fv e1' = {} by simp
  moreover from red D have D e1' [dom XS']
    by(auto dest: red-preserves-defass[OF wf] split: if-split-asm)
  with red-dom-lcl[OF red] ⟨fv e1 = {}⟩ have D e1' [{}]
    by(auto elim!: D-mono' simp add: hyperset-defs)
  moreover from sim have length xs = length xs' max-vars e2' ≤ max-vars E
    by(auto dest: sim-move01-preserves-len sim-move01-max-vars-decr)
  with length have length': max-vars e2' ≤ length xs' by(auto)
  ultimately have bisim-list1 (e1', es1) ((e2', xs'), exs2) by(rule bisim-list1I) }
  ultimately show ?thesis using ex2 by(simp split del: if-split)(rule exI conjI|assumption)+
next
  case (red0Call a M vs U Ts T pns body D)
  note [simp] = ⟨ta = ε⟩ ⟨h' = h⟩
  and es1' = ⟨es1' = e1 # es1⟩
  and e1' = ⟨e1' = blocks (this # pns) (Class D # Ts) (Addr a # vs) body⟩
  and call = ⟨call e1 = [(a, M, vs)]⟩
  and ha = ⟨typeof-addr h a = [U]⟩
  and sees = ⟨P ⊢ class-type-of U sees M: Ts→T = [(pns, body)] in D⟩
  and len = ⟨length vs = length pns⟩ ⟨length Ts = length pns⟩
from bisiml obtain E xs where ex2: ex2 = (E, xs)
  and bisim: bisim [] e1 E xs and fv: fv e1 = {}
  and length: max-vars E ≤ length xs and bsl: bisim-list es1 exs2
  and D: D e1 [{}] by(auto elim!: bisim-list1-elim)

from bisim-call-Some-call1[OF bisim call, of compP1 P t h] length
obtain e' xs' where red: τred1'r (compP1 P) t h (E, xs) (e', xs')
  and call1 e' = [(a, M, vs)] bisim [] e1 e' xs'
  and take 0 xs = take 0 xs' by auto

let ?e1' = blocks (this # pns) (Class D # Ts) (Addr a # vs) body
let ?e2' = blocks1 0 (Class D#Ts) (compE1 (this # pns) body)
let ?xs2' = Addr a # vs @ replicate (max-vars (compE1 (this # pns) body)) undefined-value
let ?exs2' = (e', xs') # exs2

have τRed1'r (compP1 P) t h ((E, xs), exs2) ((e', xs'), exs2)
  using red by(rule τred1r-into-τRed1r)
moreover {

```

```

note ⟨call1 e' = [(a, M, vs)]⟩
moreover note ha
moreover have compP1 P ⊢ class-type-of U sees M:Ts → T = map-option (λ(pns, body). compE1
(this # pns) body) [(pns, body)] in D
using sees unfolding compP1-def by(rule sees-method-compP)
hence sees': compP1 P ⊢ class-type-of U sees M:Ts → T = [compE1 (this # pns) body] in D by
simp
moreover from len have length vs = length Ts by simp
ultimately have False, compP1 P, t ⊢ 1 ⟨(e', xs')/exs2, h⟩ -ε → ⟨(?e2', ?xs2')/?exs2', h⟩ by(rule
red1Call)
moreover have τMove1 P h ((e', xs'), exs2) using ⟨call1 e' = [(a, M, vs)]⟩ ha sees
by(auto simp add: synthesized-call-def τexternal'-def dest: sees-method-fun dest!: τmove1-not-call1 [where
P=P and h=h])
ultimately have τRed1' (compP1 P) t h ((e', xs'), exs2) ((?e2', ?xs2'), ?exs2') by auto }
ultimately have τRed1't (compP1 P) t h ((E, xs), exs2) ((?e2', ?xs2'), ?exs2') by(rule rtran-
clp-into-tranclp1)
moreover
{ from red have length xs' = length xs by(rule τred1r-preserves-len)
moreover from red have max-vars e' ≤ max-vars E by(rule τred1r-max-vars)
ultimately have max-vars e' ≤ length xs' using length by simp }
with bsl ⟨bisim [] e1 e' xs'⟩ fv D have bisim-list (e1 # es1) ?exs2'
using ⟨call e1 = [(a, M, vs)]⟩ ⟨call1 e' = [(a, M, vs)]⟩ by(rule bisim-listCons)
hence bisim-list1 (e1', es1') ((?e2', ?xs2'), ?exs2')
unfolding e1' es1'
proof(rule bisim-list1I)
from wf-prog-wwf-prog[OF wf] sees
have wf-mdecl wf-J-mdecl P D (M, Ts, T, [(pns, body)]) by(rule sees-wf-mdecl)
hence fv': fv body ⊆ set pns ∪ {this} by(auto simp add: wf-mdecl-def)
moreover from ⟨P ⊢ class-type-of U sees M: Ts → T = [(pns, body)] in D⟩ have ¬ contains-insync
body
by(auto dest!: sees-wf-mdecl[OF wf] WT-expr-locks simp add: wf-mdecl-def contains-insync-conv)
ultimately have bisim ([this] @ pns) body (compE1 ([this] @ pns) body) ?xs2'
by -(rule compE1-bisim, auto)
with ⟨length vs = length pns⟩ ⟨length Ts = length pns⟩
have bisim ([ ] @ [this]) (blocks pns Ts vs body) (blocks1 (Suc 0) Ts (compE1 (this # pns) body))
?xs2'
by -(rule blocks-bisim, auto simp add: nth-append)
from bisimBlockSomeNone[OF this, of Addr a Class D]
show bisim [] ?e1' ?e2' ?xs2' by simp
from fv' len show fv ?e1' = {} by auto

from wf sees
have wf-mdecl wf-J-mdecl P D (M, Ts, T, [(pns, body)]) by(rule sees-wf-mdecl)
hence D body [set pns ∪ {this}] by(auto simp add: wf-mdecl-def)
with ⟨length vs = length pns⟩ ⟨length Ts = length pns⟩
have D (blocks pns Ts vs body) [dom [this ↦ Addr a]] by(auto)
thus D ?e1' [{} ] by auto

from len show max-vars ?e2' ≤ length ?xs2' by(auto simp add: blocks1-max-vars)
qed
moreover have τMove0 P h (e1, es1) using call ha sees
by(fastforce simp add: synthesized-call-def τexternal'-def dest: sees-method-fun τmove0-callD [where
P=P and h=h])
ultimately show ?thesis using ex2 ⟨call e1 = [(a, M, vs)]⟩

```

```

  by(simp del:  $\tau$ Move1.simps)(rule exI conjI|assumption)+
next
case (red0Return e)
note es1 =  $\langle es1 = e \# es1' \rangle$  and e1' =  $\langle e1' = inline-call\ e1\ e \rangle$ 
  and [simp] =  $\langle ta = \varepsilon \rangle \langle h' = h \rangle$ 
  and fin =  $\langle final\ e1 \rangle$ 
from bisiml es1 obtain E' xs' E xs exs' aMvs where ex2:  $ex2 = (E', xs')$  and exs2:  $exs2 = (E,$ 
 $xs) \# exs'$ 
  and bisim':  $bisim \ []\ e1\ E'\ xs'$ 
  and bisim:  $bisim \ []\ e\ E\ xs$ 
  and fv:  $fv\ e = \{\}$ 
  and length:  $max-vars\ E \leq length\ xs$ 
  and bisiml:  $bisim-list\ es1'\ exs'$ 
  and D:  $\mathcal{D}\ e\ [\{\}]\$ 
  and call:  $call\ e = [aMvs]$ 
  and call1:  $call1\ E = [aMvs]$ 
  by(fastforce elim: bisim-list1-elim)
let ?e2' = inline-call E' E

from  $\langle final\ e1 \rangle$  bisim' have final E' by(auto)
hence red':  $False, compP1\ P, t \vdash 1 \langle ex2/exs2, h \rangle -\varepsilon \rightarrow \langle (\ ?e2', xs)/exs', h \rangle$ 
  unfolding ex2 exs2 by(rule red1Return)
moreover have  $\tau Move0\ P\ h\ (e1, es1) = \tau Move1\ P\ h\ ((E', xs'), exs2)$ 
  using  $\langle final\ e1 \rangle \langle final\ E' \rangle$  by auto
moreover {
  note bisiml
  moreover
  from bisim' fin bisim
  have  $bisim \ []\ (inline-call\ e1\ e)\ (inline-call\ E'\ E)\ xs$ 
    using call by(rule bisim-inline-call)(simp add: fv)
  moreover from fv-inline-call[of e1 e] fv fin
  have  $fv\ (inline-call\ e1\ e) = \{\}$  by auto
  moreover from fin have  $\mathcal{D}\ (inline-call\ e1\ e)\ [\{\}]\$ 
    using call D by(rule defass-inline-call)
  moreover have  $max-vars\ ?e2' \leq max-vars\ E + max-vars\ E'$  by(rule inline-call-max-vars1)
  with  $\langle final\ E' \rangle$  length have  $max-vars\ ?e2' \leq length\ xs$  by(auto elim!: final.cases)
  ultimately have  $bisim-list1\ (inline-call\ e1\ e, es1')\ ((?e2', xs), exs')$ 
    by(rule bisim-list1I) }
ultimately show ?thesis using e1'  $\langle final\ e1 \rangle \langle final\ E' \rangle$  ex2
  apply(simp del:  $\tau$ Move0.simps  $\tau$ Move1.simps)
  apply(rule exI conjI impI|assumption)+
  apply(rule tranclp.r-into-trancl, simp)
  apply blast
done
qed

```

lemma *sim-move10-into-red0*:

```

  assumes wwf:  $wwf-J-prog\ P$ 
  and sim:  $sim-move10\ P\ t\ ta\ e2\ e2'\ e\ h\ Map.empty\ ta'\ e'\ h'\ x'$ 
  and fv:  $fv\ e = \{\}$ 
  shows if  $\tau move1\ P\ h\ e2$ 
    then  $(\tau Red0t\ P\ t\ h\ (e, es)\ (e', es) \vee countInitBlock\ e2' < countInitBlock\ e2 \wedge e' = e \wedge x' =$ 
 $Map.empty) \wedge ta = \varepsilon \wedge h = h'$ 
    else  $\exists e''\ ta'. \tau Red0r\ P\ t\ h\ (e, es)\ (e'', es) \wedge$ 

```

$(call1\ e2 = None \vee call\ e = None \vee e'' = e) \wedge$
 $P, t \vdash_0 \langle e''/es, h \rangle -ta' \rightarrow \langle e'/es, h' \rangle \wedge$
 $\neg \tau Move0\ P\ h\ (e'', es) \wedge ta\text{-bisim}01\ ta'\ (extTA2J1\ (compP1\ P)\ ta)$

proof(cases $\tau move1\ P\ h\ e2$)

case *True*

with *sim* **have** $\neg\ final\ e2$

and *red*: $\tau red0t\ (extTA2J0\ P)\ P\ t\ h\ (e, Map.empty)\ (e', x') \vee$
 $countInitBlock\ e2' < countInitBlock\ e2 \wedge e' = e \wedge x' = Map.empty$

and [*simp*]: $h' = h\ ta = \varepsilon\ ta' = \varepsilon$ **by**(*simp-all add: sim-move10-def*)

from *red* **have** $\tau Red0t\ P\ t\ h\ (e, es)\ (e', es) \vee$
 $countInitBlock\ e2' < countInitBlock\ e2 \wedge e' = e \wedge x' = Map.empty$

proof

assume *red*: $\tau red0t\ (extTA2J0\ P)\ P\ t\ h\ (e, Map.empty)\ (e', x')$

from $\tau red0t\text{-fv-subset}[OF\ wwf\ red]\ \tau red0t\text{-dom-lcl}[OF\ wwf\ red]\ fv$

have $dom\ x' \subseteq \{\}$ **by**(*auto split: if-split-asm*)

hence $x' = Map.empty$ **by** *auto*

with *red* **have** $\tau red0t\ (extTA2J0\ P)\ P\ t\ h\ (e, Map.empty)\ (e', Map.empty)$ **by** *simp*

with *wwf* **have** $\tau Red0t\ P\ t\ h\ (e, es)\ (e', es)$

using *fv* **by**(*rule\ \tau red0t-into-\tau Red0t*)

thus *?thesis ..*

qed *simp*

with *True* **show** *?thesis* **by** *simp*

next

case *False*

with *sim* **obtain** $e''\ xs''$ **where** $\neg\ final\ e2$

and *red*: $\tau red0r\ (extTA2J0\ P)\ P\ t\ h\ (e, Map.empty)\ (e'', xs'')$

and *red*: $extTA2J0\ P, P, t \vdash \langle e'', (h, xs'') \rangle -ta' \rightarrow \langle e', (h', x') \rangle$

and *call*: $call1\ e2 = None \vee call\ e = None \vee e'' = e$

and $\neg\ \tau move0\ P\ h\ e''\ ta\text{-bisim}01\ ta'\ (extTA2J1\ (compP1\ P)\ ta)\ no\text{-call}\ P\ h\ e''$

by(*auto simp add: sim-move10-def split: if-split-asm*)

from $\tau red0r\text{-fv-subset}[OF\ wwf\ \tau red]\ \tau red0r\text{-dom-lcl}[OF\ wwf\ \tau red]\ fv$

have $dom\ xs'' \subseteq \{\}$ **by**(*auto*)

hence $xs'' = Map.empty$ **by**(*auto*)

with *red* **have** $\tau red0r\ (extTA2J0\ P)\ P\ t\ h\ (e, Map.empty)\ (e'', Map.empty)$ **by** *simp*

with *wwf* **have** $\tau Red0r\ P\ t\ h\ (e, es)\ (e'', es)$

using *fv* **by**(*rule\ \tau red0r-into-\tau Red0r*)

moreover **from** *red* $\langle xs'' = Map.empty \rangle$

have $extTA2J0\ P, P, t \vdash \langle e'', (h, Map.empty) \rangle -ta' \rightarrow \langle e', (h', x') \rangle$ **by** *simp*

from $red0Red[OF\ this]\ \langle no\text{-call}\ P\ h\ e'' \rangle$

have $P, t \vdash_0 \langle e''/es, h \rangle -ta' \rightarrow \langle e'/es, h' \rangle$ **by**(*simp add: no-call-def*)

moreover **from** $\langle \neg\ \tau move0\ P\ h\ e'' \rangle\ red$

have $\neg\ \tau Move0\ P\ h\ (e'', es)$ **by** *auto*

ultimately **show** *?thesis* **using** *False* $\langle ta\text{-bisim}01\ ta'\ (extTA2J1\ (compP1\ P)\ ta) \rangle\ call$

by(*simp del: \tau Move0.simps*) *blast*

qed

lemma *red0-simulates-Red1*:

assumes *wf*: *wf-J-prog* *P*

and *red*: $False, compP1\ P, t \vdash_1 \langle ex2/exs2, h \rangle -ta \rightarrow \langle ex2'/exs2', h' \rangle$

and *bisiml*: *bisim-list1* $(e, es)\ (ex2, exs2)$

shows $\exists e'\ es'.\ bisim\text{-list}1\ (e', es')\ (ex2', exs2') \wedge$
 $(if\ \tau Move1\ P\ h\ (ex2, exs2)$
 $then\ (\tau Red0t\ P\ t\ h\ (e, es)\ (e', es') \vee countInitBlock\ (fst\ ex2') < countInitBlock\ (fst$
 $ex2) \wedge exs2' = exs2 \wedge e' = e \wedge es' = es) \wedge$


```

    ta = ε ∧ h = h'
  else ∃ e'' es'' ta'. τRed0r P t h (e, es) (e'', es'') ∧
    (call1 (fst ex2) = None ∨ call e = None ∨ e'' = e ∧ es'' = es) ∧
    P, t ⊢ 0 ⟨e''/es'', h⟩ -ta'→ ⟨e'/es', h'⟩ ∧
    ¬ τMove0 P h (e'', es'') ∧ ta-bisim01 ta' ta)
(is ∃ e' es' . - ∧ ?red e' es')
using red
proof(cases)
  case (red1Red E xs TA E' xs')
  note red = ⟨False, compP1 P, t ⊢ 1 ⟨E, (h, xs)⟩ -TA→ ⟨E', (h', xs')⟩⟩
  and ex2 = ⟨ex2 = (E, xs)⟩
  and ex2' = ⟨ex2' = (E', xs')⟩
  and [simp] = ⟨ta = extTA2J1 (compP1 P) TA⟩ ⟨exs2' = exs2⟩
  from bisiml ex2 have bisim: bisim [] e E xs and fv: fv e = {}
  and length: max-vars E ≤ length xs and bsl: bisim-list es exs2
  and D: D e [{}] by(auto elim: bisim-list1-elim)
  from red-simulates-red1-aux[OF wf red, simplified, OF bisim, of Map.empty] fv length D
  obtain TA' e2' x' where red': sim-move10 P t TA E E' e h Map.empty TA' e2' h' x'
  and bisim'': bisim [] e2' E' xs' and lcl': x' ⊆m Map.empty by auto
  from red have ¬ final E by auto
  with sim-move10-into-red0[OF wf-prog-wwf-prog[OF wf] red', of es] fv ex2 ex2'
  have red'': ?red e2' es by fastforce
  moreover {
    note bsl bisim''
    moreover from red' fv have fv e2' = {}
    by(fastforce simp add: sim-move10-def split: if-split-asm dest: τred0r-fv-subset[OF wf-prog-wwf-prog[OF wf]]
    τred0t-fv-subset[OF wf-prog-wwf-prog[OF wf]] red-fv-subset[OF wf-prog-wwf-prog[OF wf]])
    moreover from red' have dom x' ⊆ dom (Map.empty) ∪ fv e
    unfolding sim-move10-def
    apply(auto split: if-split-asm del: subsetI dest: τred0r-dom-lcl[OF wf-prog-wwf-prog[OF wf]]
    τred0t-dom-lcl[OF wf-prog-wwf-prog[OF wf]])
    apply(frule-tac [1-2] τred0r-fv-subset[OF wf-prog-wwf-prog[OF wf]])
    apply(auto dest!: τred0r-dom-lcl[OF wf-prog-wwf-prog[OF wf]] red-dom-lcl del: subsetI, blast+)
    done
  with fv have dom x' ⊆ {} by(auto)
  hence x' = Map.empty by(auto)
  with D red' have D e2' [{}].
  by(auto dest!: sim-move10-preserves-defass[OF wf] split: if-split-asm)
  moreover from red have length xs' = length xs by(auto dest: red1-preserves-len)
  with red1-max-vars[OF red] length
  have max-vars E' ≤ length xs' by simp
  ultimately have bisim-list1 (e2', es) ((E', xs'), exs2)
  by(rule bisim-list1I) }
  ultimately show ?thesis using ex2'
  by(clarsimp split: if-split-asm)(rule exI conjI|assumption|simp)+
next
  case (red1Call E a M vs U Ts T body D xs)
  note [simp] = ⟨ex2 = (E, xs)⟩ ⟨h' = h⟩ ⟨ta = ε⟩
  and ex2' = ⟨ex2' = (blocks1 0 (Class D#Ts) body, Addr a # vs @ replicate (max-vars body)
  undefined-value)⟩
  and exs' = ⟨exs2' = (E, xs) # exs2⟩
  and call = ⟨call1 E = [(a, M, vs)]⟩ and ha = ⟨typeof-addr h a = [U]⟩
  and sees = ⟨compP1 P ⊢ class-type-of U sees M: Ts→T = [body] in D⟩
  and len = ⟨length vs = length Ts⟩

```

```

let ?e2' = blocks1 0 (Class D#Ts) body
let ?xs2' = Addr a # vs @ replicate (max-vars body) undefined-value
from bisim1 have bisim: bisim [] e E xs and fv: fv e = {}
  and length: max-vars E ≤ length xs and bsl: bisim-list es exs2
  and D: D e [{}] by(auto elim: bisim-list1-elim)

from bisim ⟨call1 E = [(a, M, vs)]⟩
have call e = [(a, M, vs)] by(rule bisim-call1-Some-call)
moreover note ha
moreover from ⟨compP1 P ⊢ class-type-of U sees M: Ts→T = [body] in D⟩
obtain pns Jbody where sees': P ⊢ class-type-of U sees M: Ts→T = [(pns, Jbody)] in D
  and body: body = compE1 (this # pns) Jbody
  by(auto dest: sees-method-compPD)
let ?e' = blocks (this # pns) (Class D # Ts) (Addr a # vs) Jbody
note sees' moreover from wf sees' have length Ts = length pns
  by(auto dest!: sees-wf-mdecl simp add: wf-mdecl-def)
with len have length vs = length pns length Ts = length pns by simp-all
ultimately have red': P,t ⊢ 0 ⟨e/es, h⟩ -ε→ ⟨?e'/e#es, h⟩ by(rule red0Call)
moreover from ⟨call e = [(a, M, vs)]⟩ ha sees'
have τMove0 P h (e, es)
  by(fastforce simp add: synthesized-call-def dest: τmove0-callD[where P=P and h=h] sees-method-fun)
ultimately have τRed0t P t h (e, es) (?e', e#es) by auto
moreover
from bsl bisim fv D length ⟨call e = [(a, M, vs)]⟩ ⟨call1 E = [(a, M, vs)]⟩
have bisim-list (e # es) ((E, xs) # exs2) by(rule bisim-list.intros)
hence bisim-list1 (?e', e # es) (ex2', (E, xs) # exs2) unfolding ex2'
proof(rule bisim-list1I)
  from wf-prog-wf-prog[OF wf] sees'
  have wf-mdecl wf-J-mdecl P D (M, Ts, T, [(pns, Jbody)]) by(rule sees-wf-mdecl)
  hence fv Jbody ⊆ set pns ∪ {this} by(auto simp add: wf-mdecl-def)
  moreover from sees' have ¬ contains-insync Jbody
    by(auto dest!: sees-wf-mdecl[OF wf] WT-expr-locks simp add: wf-mdecl-def contains-insync-conv)
  ultimately have bisim ([] @ this # pns) Jbody (compE1 ([] @ this # pns) Jbody) ?xs2'
    by -(rule compE1-bisim, auto)
  with ⟨length vs = length Ts⟩ ⟨length Ts = length pns⟩ body
  have bisim [] ?e' (blocks1 (length ([] :: vname list)) (Class D # Ts) body) ?xs2'
    by -(rule blocks-bisim, auto simp add: nth-append nth-Cons')
  thus bisim [] ?e' ?e2' ?xs2' by simp
  from ⟨length vs = length Ts⟩ ⟨length Ts = length pns⟩ ⟨fv Jbody ⊆ set pns ∪ {this}⟩
  show fv ?e' = {} by auto
  from wf sees'
  have wf-mdecl wf-J-mdecl P D (M, Ts, T, [(pns, Jbody)]) by(rule sees-wf-mdecl)
  hence D Jbody [set pns ∪ {this}] by(auto simp add: wf-mdecl-def)
  with ⟨length vs = length Ts⟩ ⟨length Ts = length pns⟩
  have D (blocks pns Ts vs Jbody) [dom [this ↦ Addr a]] by(auto)
  thus D ?e' [{}] by simp
  from len show max-vars ?e2' ≤ length ?xs2' by(simp add: blocks1-max-vars)
qed
moreover have τMove1 P h (ex2, exs2) using ha ⟨call1 E = [(a, M, vs)]⟩ sees'
  by(auto simp add: synthesized-call-def τexternal'-def dest!: τmove1-not-call1[where P=P and
h=h] dest: sees-method-fun)
  ultimately show ?thesis using exs'
    by(simp del: τMove1.simps τMove0.simps)(rule exI conjI rtranclp.rtrancl-refl|assumption)+
next

```

```

case (red1Return  $E' x' E x$ )
note [simp] =  $\langle h' = h \rangle \langle ta = \varepsilon \rangle$ 
  and  $ex2 = \langle ex2 = (E', x') \rangle$  and  $exs2 = \langle exs2 = (E, x) \# exs2' \rangle$ 
  and  $ex2' = \langle ex2' = (inline-call E' E, x) \rangle$ 
  and  $fin = \langle final E' \rangle$ 
from bisiml  $ex2 exs2$  obtain  $e' es' aMvs$  where  $es: es = e' \# es'$ 
  and  $bsl: bisim-list es' exs2'$ 
  and  $bisim: bisim [] e E' x'$ 
  and  $bisim': bisim [] e' E x$ 
  and  $fv: fv e' = \{\}$ 
  and  $length: max-vars E \leq length x$ 
  and  $D: \mathcal{D} e' [\{\}]$ 
  and  $call: call e' = [aMvs]$ 
  and  $call1: call1 E = [aMvs]$ 
  by(fastforce elim!: bisim-list1-elim)

from  $\langle final E' \rangle bisim$  have  $fin': final e$  by(auto)
hence  $P, t \vdash 0 \langle e/e' \# es', h \rangle -\varepsilon \rightarrow \langle inline-call e e'/es', h \rangle$  by(rule red0Return)
moreover from bisim  $fin' bisim' call$ 
have  $bisim [] (inline-call e e') (inline-call E' E) x$ 
  by(rule bisim-inline-call)(simp add: fv)
with bsl have bisim-list1 (inline-call e e',  $es'$ ) ( $ex2', exs2'$ ) unfolding  $ex2'$ 
proof(rule bisim-list1I)
  from  $fin' fv-inline-call[of e e'] fv$  show  $fv (inline-call e e') = \{\}$  by auto
  from  $fin'$  show  $\mathcal{D} (inline-call e e') [\{\}]$  using call D by(rule defass-inline-call)

  from call1-imp-call[OF call1]
  have  $max-vars (inline-call E' E) \leq max-vars E + max-vars E'$ 
    by(rule inline-call-max-vars)
  with fin length show  $max-vars (inline-call E' E) \leq length x$  by(auto elim!: final.cases)
qed
moreover have  $\tau Move1 P h (ex2, exs2) = \tau Move0 P h (e, es)$  using  $ex2 call1 call fin fin'$  by(auto)
ultimately show ?thesis using es
  by(simp del:  $\tau Move1.simps \tau Move0.simps$ ) blast
qed

end

sublocale J0-J1-heap-base < red0-Red1': FWdelay-bisimulation-base
  final-expr0
  mred0 P
  final-expr1
  mred1' (compP1 P)
  convert-RA
   $\lambda t. bisim-red0-Red1$ 
  bisim-wait01
   $\tau MOVE0 P$ 
   $\tau MOVE1 (compP1 P)$ 
for P
  .

```

context J0-J1-heap-base **begin**

lemma delay-bisimulation-red0-Red1:

assumes *wf*: *wf-J-prog P*
shows *delay-bisimulation-measure* (*mred0 P t*) (*mred1' (compP1 P) t*) *bisim-red0-Red1* (*ta-bisim*
($\lambda t. \text{bisim-red0-Red1}$)) ($\tau\text{MOVE0 } P$) ($\tau\text{MOVE1 } (\text{compP1 } P)$) ($\lambda es'. \text{False}$) ($\lambda((e', xs'), exs'), h'$)
($((e, xs), exs), h$). *countInitBlock* $e' < \text{countInitBlock } e$)
(**is** *delay-bisimulation-measure* - - - - - $?\mu1$ $?\mu2$)
proof(*unfold-locales*)
fix *s1 s2 s1'*
assume *bisim-red0-Red1 s1 s2 red0-mthr.silent-move P t s1 s1'*
moreover obtain *ex1 exs1 h1* **where** *s1*: $s1 = ((ex1, exs1), h1)$ **by**(*cases s1, auto*)
moreover obtain *ex1' exs1' h1'* **where** *s1'*: $s1' = ((ex1', exs1'), h1')$ **by**(*cases s1', auto*)
moreover obtain *ex2 exs2 h2* **where** *s2*: $s2 = ((ex2, exs2), h2)$ **by**(*cases s2, auto*)
ultimately have *bisim*: *bisim-list1* (*ex1, exs1*) (*ex2, exs2*)
and *heap*: $h1 = h2$
and *red*: $P, t \vdash 0 \langle ex1/exs1, h1 \rangle -\varepsilon \rightarrow \langle ex1'/exs1', h1' \rangle$
and τ : $\tau\text{Move0 } P \ h1 \ (ex1, exs1)$
by(*auto simp add: bisim-red0-Red1-def red0-mthr.silent-move-iff*)
from *Red1-simulates-red0*[*OF wf red bisim*] τ
obtain *ex2'' exs2''* **where** *bisim'*: *bisim-list1* (*ex1', exs1'*) (*ex2'', exs2''*)
and *red'*: $\tau\text{Red1}'t \ (\text{compP1 } P) \ t \ h1 \ (ex2, exs2) \ (ex2'', exs2'')$
and [*simp*]: $h1' = h1$ **by** *auto*
from $\tau\text{Red1}'t\text{-into-Red1}'\text{-}\tau\text{mthr-silent-movet}$ [*OF red'*] *bisim' heap*
have $\exists s2'. \text{Red1-mthr.silent-movet } \text{False} \ (\text{compP1 } P) \ t \ s2 \ s2' \wedge \text{bisim-red0-Red1 } s1' \ s2'$
unfolding *s2 s1'* **by**(*auto simp add: bisim-red0-Red1-def*)
thus *bisim-red0-Red1 s1' s2* $\wedge ?\mu1^{\wedge++} s1' s1 \vee (\exists s2'. \text{Red1-mthr.silent-movet } \text{False} \ (\text{compP1 } P)$
 $t \ s2 \ s2' \wedge \text{bisim-red0-Red1 } s1' \ s2')$..
next
fix *s1 s2 s2'*
assume *bisim-red0-Red1 s1 s2 Red1-mthr.silent-move False (compP1 P) t s2 s2'*
moreover obtain *ex1 exs1 h1* **where** *s1*: $s1 = ((ex1, exs1), h1)$ **by**(*cases s1, auto*)
moreover obtain *ex2 exs2 h2* **where** *s2*: $s2 = ((ex2, exs2), h2)$ **by**(*cases s2, auto*)
moreover obtain *ex2' exs2' h2'* **where** *s2'*: $s2' = ((ex2', exs2'), h2')$ **by**(*cases s2', auto*)
ultimately have *bisim*: *bisim-list1* (*ex1, exs1*) (*ex2, exs2*)
and *heap*: $h1 = h2$
and *red*: $\text{False, compP1 } P, t \vdash 1 \langle ex2/exs2, h2 \rangle -\varepsilon \rightarrow \langle ex2'/exs2', h2' \rangle$
and τ : $\tau\text{Move1 } P \ h2 \ (ex2, exs2)$
by(*fastforce simp add: bisim-red0-Red1-def Red1-mthr.silent-move-iff*)+
from *red0-simulates-Red1*[*OF wf red bisim*] τ
obtain *e' es'* **where** *bisim'*: *bisim-list1* (*e', es'*) (*ex2', exs2'*)
and *red'*: $\tau\text{Red0}t \ P \ t \ h2 \ (ex1, exs1) \ (e', es') \vee$
 $\text{countInitBlock } (\text{fst } ex2') < \text{countInitBlock } (\text{fst } ex2) \wedge exs2' = exs2 \wedge e' = ex1 \wedge es' =$
 $exs1$
and [*simp*]: $h2' = h2$ **by** *auto*
from *red'*
show *bisim-red0-Red1 s1 s2'* $\wedge ?\mu2^{\wedge++} s2' s2 \vee (\exists s1'. \text{red0-mthr.silent-movet } P \ t \ s1 \ s1' \wedge$
 $\text{bisim-red0-Red1 } s1' \ s2')$
(**is** *?refl* \vee *?step*)
proof
assume $\tau\text{Red0}t \ P \ t \ h2 \ (ex1, exs1) \ (e', es')$
from $\tau\text{Red0}t\text{-into-red0-}\tau\text{mthr-silent-movet}$ [*OF this*] *bisim' heap*
have *?step* **unfolding** *s1 s2'* **by**(*auto simp add: bisim-red0-Red1-def*)
thus *?thesis* ..
next
assume $\text{countInitBlock } (\text{fst } ex2') < \text{countInitBlock } (\text{fst } ex2) \wedge exs2' = exs2 \wedge e' = ex1 \wedge es' =$
 $exs1$

hence *?refl* using **bisim' heap unfolding** $s1\ s2'\ s2$
 by (*auto simp add: bisim-red0-Red1-def split-beta*)
 thus *?thesis ..*
qed
next
fix $s1\ s2\ ta1\ s1'$
assume *bisim-red0-Red1* $s1\ s2$ **and** *mred0* $P\ t\ s1\ ta1\ s1'$ **and** $\tau: \neg \tau\text{MOVE0}\ P\ s1\ ta1\ s1'$
moreover obtain $ex1\ exs1\ h1$ **where** $s1: s1 = ((ex1, exs1), h1)$ **by**(*cases s1, auto*)
moreover obtain $ex1'\ exs1'\ h1'$ **where** $s1': s1' = ((ex1', exs1'), h1')$ **by**(*cases s1', auto*)
moreover obtain $ex2\ exs2\ h2$ **where** $s2: s2 = ((ex2, exs2), h2)$ **by**(*cases s2, auto*)
ultimately have *heap*: $h2 = h1$
and *bisim*: *bisim-list1* $(ex1, exs1)\ (ex2, exs2)$
and *red*: $P, t \vdash 0\ \langle ex1/exs1, h1 \rangle -ta1 \rightarrow \langle ex1'/exs1', h1' \rangle$
by(*auto simp add: bisim-red0-Red1-def*)
from τ **have** $\neg \tau\text{Move0}\ P\ h1\ (ex1, exs1)$ **unfolding** $s1$
using *red* **by**(*auto elim!: red0.cases dest: red- τ -taD*[**where** $extTA=extTA2J0\ P, OF\ extTA2J0-\epsilon$])
with *Red1-simulates-red0*[*OF wf red bisim*]
obtain $ex2''\ exs2''\ ex2'\ exs2'\ ta'$
where *bisim'*: *bisim-list1* $(ex1', exs1')\ (ex2'', exs2'')$
and *red'*: $\tau\text{Red1}'r\ (compP1\ P)\ t\ h1\ (ex2, exs2)\ (ex2', exs2')$
and *red''*: $False, compP1\ P, t \vdash 1\ \langle ex2'/exs2', h1 \rangle -ta' \rightarrow \langle ex2''/exs2'', h1' \rangle$
and τ' : $\neg \tau\text{Move1}\ P\ h1\ (ex2', exs2')$
and *ta*: *ta-bisim01* $ta1\ ta'$ **by** *fastforce*
from *red''* **have** *mred1'* $(compP1\ P)\ t\ ((ex2', exs2'), h1)\ ta'\ ((ex2'', exs2''), h1)$ **by** *auto*
moreover from τ' **have** $\neg \tau\text{MOVE1}\ (compP1\ P)\ ((ex2', exs2'), h1)\ ta'\ ((ex2'', exs2''), h1)$ **by**
simp
moreover from *red'* **have** *Red1-mthr.silent-moves* $False\ (compP1\ P)\ t\ s2\ ((ex2', exs2'), h1)$
unfolding $s2$ *heap* **by**(*rule $\tau\text{Red1}'r$ -into-Red1'- τ mthr-silent-moves*)
moreover from *bisim'* **have** *bisim-red0-Red1* $((ex1', exs1'), h1')\ ((ex2'', exs2''), h1')$
by(*auto simp add: bisim-red0-Red1-def*)
ultimately
show $\exists s2'\ s2''\ ta2. Red1-mthr.silent-moves\ False\ (compP1\ P)\ t\ s2\ s2' \wedge mred1'\ (compP1\ P)\ t\ s2'$
 $ta2\ s2'' \wedge$
 $\neg \tau\text{MOVE1}\ (compP1\ P)\ s2'\ ta2\ s2'' \wedge bisim-red0-Red1\ s1'\ s2'' \wedge ta-bisim01\ ta1\ ta2$
using *ta unfolding s1'* **by** *blast*
next
fix $s1\ s2\ ta2\ s2'$
assume *bisim-red0-Red1* $s1\ s2$ **and** *mred1'* $(compP1\ P)\ t\ s2\ ta2\ s2'$ **and** $\tau: \neg \tau\text{MOVE1}\ (compP1\ P)\ s2\ ta2\ s2'$
moreover obtain $ex1\ exs1\ h1$ **where** $s1: s1 = ((ex1, exs1), h1)$ **by**(*cases s1, auto*)
moreover obtain $ex2\ exs2\ h2$ **where** $s2: s2 = ((ex2, exs2), h2)$ **by**(*cases s2, auto*)
moreover obtain $ex2'\ exs2'\ h2'$ **where** $s2': s2' = ((ex2', exs2'), h2')$ **by**(*cases s2', auto*)
ultimately have *heap*: $h2 = h1$
and *bisim*: *bisim-list1* $(ex1, exs1)\ (ex2, exs2)$
and *red*: $False, compP1\ P, t \vdash 1\ \langle ex2/exs2, h1 \rangle -ta2 \rightarrow \langle ex2'/exs2', h2' \rangle$
by(*auto simp add: bisim-red0-Red1-def*)
from τ *heap* **have** $\neg \tau\text{Move1}\ P\ h2\ (ex2, exs2)$ **unfolding** $s2$
using *red* **by**(*fastforce elim!: Red1.cases dest: red1- τ -taD*)
with *red0-simulates-Red1*[*OF wf red bisim*]
obtain $e'\ es'\ e''\ es''\ ta'$
where *bisim'*: *bisim-list1* $(e', es')\ (ex2', exs2')$
and *red'*: $\tau\text{Red0}r\ P\ t\ h1\ (ex1, exs1)\ (e'', es'')$
and *red''*: $P, t \vdash 0\ \langle e''/es'', h1 \rangle -ta' \rightarrow \langle e'/es', h2' \rangle$
and τ' : $\neg \tau\text{Move0}\ P\ h1\ (e'', es'')$

and ta : ta - $bisim01$ ta' $ta2$ **using** $heap$ **by** $fastforce$
from red'' **have** $mred0$ P t $((e'', es''), h1)$ ta' $((e', es'), h2')$ **by** $auto$
moreover from red' **have** $red0$ - $mthr$. $silent$ - $moves$ P t $((ex1, exs1), h1)$ $((e'', es''), h1)$
by($rule$ τ $Red0r$ - $into$ - $red0$ - τ $mthr$ - $silent$ - $moves$)
moreover from τ' **have** $\neg \tau$ $MOVE0$ P $((e'', es''), h1)$ ta' $((e', es'), h2')$ **by** $simp$
moreover from $bisim'$ **have** $bisim$ - $red0$ - $Red1$ $((e', es'), h2')$ $((ex2', exs2'), h2')$
by($auto$ $simp$ add : $bisim$ - $red0$ - $Red1$ - def)
ultimately
show $\exists s1' s1'' ta1$. $red0$ - $mthr$. $silent$ - $moves$ P t $s1$ $s1' \wedge$
 $mred0$ P t $s1' ta1 s1'' \wedge \neg \tau$ $MOVE0$ P $s1' ta1 s1'' \wedge$
 $bisim$ - $red0$ - $Red1$ $s1'' s2' \wedge ta$ - $bisim01$ $ta1 ta2$
using ta **unfolding** $s1$ $s2'$ **by** $blast$
next
show wfP $?mu1$ **by** $auto$
next
have wf ($measure$ $countInitBlock$) $..$
hence wf (inv - $image$ ($measure$ $countInitBlock$) $(\lambda(((e', xs'), exs'), h'). e')$) $..$
also have inv - $image$ ($measure$ $countInitBlock$) $(\lambda(((e', xs'), exs'), h'). e') = \{(s2', s2). ?mu2 s2' s2\}$
by($simp$ add : inv - $image$ - def $split$ - $beta$)
finally show wfP $?mu2$ **by**($simp$ add : wfp - def)
qed

lemma $delay$ - $bisimulation$ - $diverge$ - $red0$ - $Red1$:

assumes wf - J - $prog$ P

shows $delay$ - $bisimulation$ - $diverge$ ($mred0$ P t) ($mred1'$ ($compP1$ P) t) $bisim$ - $red0$ - $Red1$ (ta - $bisim$ (λt . $bisim$ - $red0$ - $Red1$)) (τ $MOVE0$ P) (τ $MOVE1$ ($compP1$ P))

proof –

interpret $delay$ - $bisimulation$ - $measure$

$mred0$ P t $mred1'$ ($compP1$ P) t $bisim$ - $red0$ - $Red1$ ta - $bisim$ (λt . $bisim$ - $red0$ - $Red1$) τ $MOVE0$ P τ $MOVE1$ ($compP1$ P)

$\lambda es es'$. $False$ $\lambda(((e', xs'), exs'), h') (((e, xs), exs), h)$. $countInitBlock$ $e' < countInitBlock$ e

using $assms$ **by**($rule$ $delay$ - $bisimulation$ - $red0$ - $Red1$)

show $?thesis$ **by** $unfold$ - $locales$

qed

lemma $red0$ - $Red1'$ - FW $weak$ - $bisim$:

assumes wf : wf - J - $prog$ P

shows FW $delay$ - $bisimulation$ - $diverge$ $final$ - $expr0$ ($mred0$ P) $final$ - $expr1$ ($mred1'$ ($compP1$ P))
 $(\lambda t$. $bisim$ - $red0$ - $Red1$) $bisim$ - $wait01$ (τ $MOVE0$ P) (τ $MOVE1$ ($compP1$ P))

proof –

interpret $delay$ - $bisimulation$ - $diverge$

$mred0$ P t

$mred1'$ ($compP1$ P) t

$bisim$ - $red0$ - $Red1$

ta - $bisim$ (λt . $bisim$ - $red0$ - $Red1$) τ $MOVE0$ P τ $MOVE1$ ($compP1$ P)

for t

using wf **by**($rule$ $delay$ - $bisimulation$ - $diverge$ - $red0$ - $Red1$)

show $?thesis$

proof

fix t **and** $s1$ **and** $s2$ $:: ((\text{'addr expr1} \times \text{'addr locals1}) \times (\text{'addr expr1} \times \text{'addr locals1}) \text{list}) \times \text{'heap}$

assume $bisim$ - $red0$ - $Red1$ $s1$ $s2$ $(\lambda(x1, m)$. $final$ - $expr0$ $x1$) $s1$

moreover hence $(\lambda(x2, m)$. $final$ - $expr1$ $x2$) $s2$

by($cases$ $s1$)($cases$ $s2$, $auto$ $simp$ add : $bisim$ - $red0$ - $Red1$ - def $final$ - iff $elim$!: $bisim$ - $list1$ - $elim$ $elim$: $bisim$ - $list$. $cases$)

ultimately show $\exists s2'. \text{Red1-mthr.silent-moves False (compP1 P) t s2 s2}' \wedge \text{bisim-red0-Red1 s1 s2}' \wedge (\lambda(x2, m). \text{final-expr1 } x2) s2'$

by *blast*

next

fix $t\ s1$ **and** $s2 :: (('addr\ expr1 \times 'addr\ locals1) \times ('addr\ expr1 \times 'addr\ locals1)\ list) \times 'heap$

assume $\text{bisim-red0-Red1 s1 s2 } (\lambda(x2, m). \text{final-expr1 } x2) s2$

moreover hence $(\lambda(x1, m). \text{final-expr0 } x1) s1$

by(*cases s1*)(*cases s2, auto simp add: bisim-red0-Red1-def final-iff elim!: bisim-list1-elim elim: bisim-list.cases*)

ultimately show $\exists s1'. \text{red0-mthr.silent-moves P t s1 s1}' \wedge \text{bisim-red0-Red1 s1}' s2 \wedge (\lambda(x1, m). \text{final-expr0 } x1) s1'$

by *blast*

next

fix $t' x\ m1\ x'\ m2\ t\ x1\ x2\ x1'\ ta1\ x1''\ m1'\ x2'\ ta2\ x2''\ m2'$

assume $b: \text{bisim-red0-Red1 } (x, m1) (x', m2)$

and $bo: \text{bisim-red0-Red1 } (x1, m1) (x2, m2)$

and $\tau red1: \text{red0-mthr.silent-moves P t } (x1, m1) (x1', m1)$

and $red1: \text{mred0 P t } (x1', m1) ta1 (x1'', m1')$

and $\tau 1: \neg \tau \text{MOVE0 P } (x1', m1) ta1 (x1'', m1')$

and $\tau red2: \text{Red1-mthr.silent-moves False (compP1 P) t } (x2, m2) (x2', m2)$

and $red2: \text{mred1}' (\text{compP1 P}) t (x2', m2) ta2 (x2'', m2')$

and $bo': \text{bisim-red0-Red1 } (x1'', m1') (x2'', m2')$

and $tb: \text{ta-bisim } (\lambda t. \text{bisim-red0-Red1}) ta1 ta2$

from b **have** $m1 = m2$ **by**(*auto simp add: bisim-red0-Red1-def split-beta*)

moreover from bo' **have** $m1' = m2'$ **by**(*auto simp add: bisim-red0-Red1-def split-beta*)

ultimately show $\text{bisim-red0-Red1 } (x, m1') (x', m2')$ **using** b

by(*auto simp add: bisim-red0-Red1-def split-beta*)

next

fix $t\ x1\ m1\ x2\ m2\ x1'\ ta1\ x1''\ m1'\ x2'\ ta2\ x2''\ m2'\ w$

assume $\text{bisim-red0-Red1 } (x1, m1) (x2, m2)$

and $\text{red0-mthr.silent-moves P t } (x1, m1) (x1', m1)$

and $red0: \text{mred0 P t } (x1', m1) ta1 (x1'', m1')$

and $\neg \tau \text{MOVE0 P } (x1', m1) ta1 (x1'', m1')$

and $\text{Red1-mthr.silent-moves False (compP1 P) t } (x2, m2) (x2', m2)$

and $red1: \text{mred1}' (\text{compP1 P}) t (x2', m2) ta2 (x2'', m2')$

and $\neg \tau \text{MOVE1 (compP1 P) } (x2', m2) ta2 (x2'', m2')$

and $\text{bisim-red0-Red1 } (x1'', m1') (x2'', m2')$

and $\text{ta-bisim01 } ta1 ta2$

and $\text{Suspend: Suspend } w \in \text{set } \{\{ta1\}\}_w \text{ Suspend } w \in \text{set } \{\{ta2\}\}_w$

from $red0\ red1\ \text{Suspend}$ **show** $\text{bisim-wait01 } x1''\ x2''$

by(*cases x2'*)(*cases x2'', auto dest: Red-Suspend-is-call Red1-Suspend-is-call simp add: split-beta bisim-wait01-def is-call-def*)

next

fix $t\ x1\ m1\ x2\ m2\ ta1\ x1'\ m1'$

assume $\text{bisim-red0-Red1 } (x1, m1) (x2, m2)$

and $\text{bisim-wait01 } x1\ x2$

and $\text{mred0 P t } (x1, m1) ta1 (x1', m1')$

and $\text{wakeup: Notified} \in \text{set } \{\{ta1\}\}_w \vee \text{WokenUp} \in \text{set } \{\{ta1\}\}_w$

moreover obtain $e0\ es0$ **where** $[\text{simp}]: x1 = (e0, es0)$ **by**(*cases x1*)

moreover obtain $e0'\ es0'$ **where** $[\text{simp}]: x1' = (e0', es0')$ **by**(*cases x1'*)

moreover obtain $e1\ xs1\ exs1$ **where** $[\text{simp}]: x2 = ((e1, xs1), exs1)$ **by**(*cases x2*) *auto*

ultimately have $\text{bisim: bisim-list1 } (e0, es0) ((e1, xs1), exs1)$

and $m1: m2 = m1$

and $\text{call: call } e0 \neq \text{None call1 } e1 \neq \text{None}$

```

    and red:  $P, t \vdash 0 \langle e0/es0, m1 \rangle -ta1 \rightarrow \langle e0'/es0', m1' \rangle$ 
    by(auto simp add: bisim-wait01-def bisim-red0-Red1-def)
  from red wakeup have  $\neg \tau Move0 P m1 (e0, es0)$ 
    by(auto elim!: red0.cases dest: red- $\tau$ -taD[where extTA=extTA2J0 P, simplified])
  with Red1-simulates-red0[OF wf red bisim] call m1
  show  $\exists ta2 x2' m2'. mred1' (compP1 P) t (x2, m2) ta2 (x2', m2') \wedge bisim-red0-Red1 (x1', m1')$ 
 $(x2', m2') \wedge ta-bisim01 ta1 ta2$ 
    by(auto simp add: bisim-red0-Red1-def)
  next
  fix t x1 m1 x2 m2 ta2 x2' m2'
  assume bisim-red0-Red1 (x1, m1) (x2, m2)
    and bisim-wait01 x1 x2
    and mred1' (compP1 P) t (x2, m2) ta2 (x2', m2')
    and wakeup:  $Notified \in set \{ta2\}_w \vee WokenUp \in set \{ta2\}_w$ 
  moreover obtain e0 es0 where [simp]:  $x1 = (e0, es0)$  by(cases x1)
  moreover obtain e1 xs1 exs1 where [simp]:  $x2 = ((e1, xs1), exs1)$  by(cases x2) auto
  moreover obtain e1' xs1' exs1' where [simp]:  $x2' = ((e1', xs1'), exs1')$  by(cases x2') auto
  ultimately have bisim: bisim-list1 (e0, es0) ((e1, xs1), exs1)
    and m1:  $m2 = m1$ 
    and call:  $call e0 \neq None$  call1 e1  $\neq None$ 
    and red:  $False, compP1 P, t \vdash 1 \langle (e1, xs1)/exs1, m1 \rangle -ta2 \rightarrow \langle (e1', xs1')/exs1', m2' \rangle$ 
    by(auto simp add: bisim-wait01-def bisim-red0-Red1-def)
  from red wakeup have  $\neg \tau Move1 P m1 ((e1, xs1), exs1)$ 
    by(auto elim!: Red1.cases dest: red1- $\tau$ -taD)
  with red0-simulates-Red1[OF wf red bisim] m1 call
  show  $\exists ta1 x1' m1'. mred0 P t (x1, m1) ta1 (x1', m1') \wedge bisim-red0-Red1 (x1', m1') (x2', m2')$ 
 $\wedge ta-bisim01 ta1 ta2$ 
    by(auto simp add: bisim-red0-Red1-def)
  next
  show  $(\exists x. final-expr0 x) \longleftrightarrow (\exists x. final-expr1 x)$ 
    by(auto simp add: split-paired-Ex final-iff)
  qed
qed

```

lemma bisim-J0-J1-start:

assumes wf: wf-J-prog P

and start: wf-start-state P C M vs

shows red0-Red1'.mbisim (J0-start-state P C M vs) (J1-start-state (compP1 P) C M vs)

proof –

from start obtain Ts T pns body D

where sees: $P \vdash C$ sees $M: Ts \rightarrow T = [(pns, body)]$ in D

and conf: $P, start-heap \vdash vs [:\leq] Ts$

by cases auto

from conf have vs: $length vs = length Ts$ by(rule list-all2-lengthD)

from sees-wf-mdecl[OF wf-prog-wwf-prog[OF wf] sees]

have ftbody: $fv body \subseteq \{this\} \cup set pns$ and len: $length pns = length Ts$

by(auto simp add: wf-mdecl-def)

with vs have fv: $fv (blocks pns Ts vs body) \subseteq \{this\}$ by auto

have wfCM: wf-J-mdecl P D (M, Ts, T, pns, body)

using sees-wf-mdecl[OF wf sees] by(auto simp add: wf-mdecl-def)

then obtain T' where wtbody: $P, [this \# pns \vdash \rightarrow] Class D \# Ts \vdash body :: T'$ by auto

hence elbody: $expr-locks body = (\lambda l. 0)$ by(rule WT-expr-locks)

hence cisbody: $\neg contains-insync body$ by(auto simp add: contains-insync-conv)

from wfCM len vs have dabody: $D (blocks pns Ts vs body) [\{this\}]$ by auto


```

from sees have sees1: compP1 P ⊢ C sees M:Ts→T = [compE1 (this # pns) body] in D
  by(auto dest: sees-method-compP[where f=(λC M Ts T (pns, body). compE1 (this # pns) body)])

let ?e = blocks1 0 (Class C#Ts) (compE1 (this # pns) body)
let ?xs = Null # vs @ replicate (max-vars body) undefined-value
from ftbody cisbody len vs
have bisim [] (blocks (this # pns) (Class D # Ts) (Null # vs) body) (blocks1 (length ([] :: vname
list)) (Class D # Ts) (compE1 (this # pns) body)) ?xs
  by-(rule blocks-bisim,(fastforce simp add: nth-Cons' nth-append)+)
with fv dabody len vs elbody sees sees1
show ?thesis unfolding start-state-def
  by(auto intro!: red0-Red1'.mbisimI split: if-split-asm)(auto simp add: bisim-red0-Red1-def blocks1-max-vars
intro!: bisim-list.intros bisim-listII wset-thread-okI)
qed

end

end

```

7.25 Preservation of well-formedness from source code to intermediate language

```

theory JJ1WellForm imports
  ../J/JWellForm
  J1WellForm
  Compiler1
begin

```

The compiler preserves well-formedness. Is less trivial than it may appear. We start with two simple properties: preservation of well-typedness

```

lemma assumes wf: wf-prog wfmd P
shows compE1-pres-wt: [ P,[Vs[↦]Ts] ⊢ e :: U; size Ts = size Vs ] ⇒ compP f P,Ts ⊢1 compE1
Vs e :: U
and compEs1-pres-wt: [ P,[Vs[↦]Ts] ⊢ es [::] Us; size Ts = size Vs ] ⇒ compP f P,Ts ⊢1 compEs1
Vs es [::] Us
proof(induct Vs e and Vs es arbitrary: Ts U and Ts Us rule: compE1-compEs1-induct)
  case Var thus ?case by(fastforce simp:map-upds-apply-eq-Some split:if-split-asm)
next
  case LAss thus ?case by(fastforce simp:map-upds-apply-eq-Some split:if-split-asm)
next
  case Call thus ?case
    by(fastforce dest: sees-method-compP[where f = f])
next
  case Block thus ?case by(fastforce simp:nth-append)
next
  case (Synchronized Vs V exp1 exp2 Ts U)
    note IH1 = ⟨∧ Ts U. [P,[Vs [↦] Ts] ⊢ exp1 :: U;
length Ts = length Vs] ⇒ compP f P,Ts ⊢1 compE1 Vs exp1 :: U⟩
    note IH2 = ⟨∧ Ts U. [P,[Vs @ [fresh-var Vs] [↦] Ts] ⊢ exp2 :: U; length Ts = length (Vs @
[fresh-var Vs])]
⇒ compP f P,Ts ⊢1 compE1 (Vs @ [fresh-var Vs]) exp2 :: U⟩
    note length = ⟨length Ts = length Vs⟩
    from ⟨P,[Vs [↦] Ts] ⊢ syncV (exp1) exp2 :: U⟩

```

```

obtain  $U1$  where  $wt1: P, [Vs \mapsto Ts] \vdash exp1 :: U1$ 
  and  $wt2: P, [Vs \mapsto Ts] \vdash exp2 :: U$ 
  and  $U1: is-refT\ U1\ U1 \neq NT$ 
  by(auto)
from  $IH1$ [of  $Ts\ U1$ ]  $wt1$  length
have  $wt1': compP\ f\ P, Ts \vdash 1\ compE1\ Vs\ exp1 :: U1$  by simp
from  $length\ fresh-var-fresh$ [of  $Vs$ ] have  $[Vs \mapsto Ts] \subseteq_m [Vs @ [fresh-var\ Vs] \mapsto Ts @ [Class\ Object]]$ 
  by(auto simp add: map-le-def fun-upd-def)
with  $wt2$  have  $P, [Vs @ [fresh-var\ Vs] \mapsto Ts @ [Class\ Object]] \vdash exp2 :: U$ 
  by(rule wt-env-mono)
with  $length\ IH2$ [of  $Ts @ [Class\ Object]\ U$ ]
have  $compP\ f\ P, Ts @ [Class\ Object] \vdash 1\ compE1\ (Vs @ [fresh-var\ Vs])\ exp2 :: U$  by simp
with  $wt1'\ U1$  show  $?case$  by(auto)
next
case (TryCatch  $Vs\ exp1\ C\ V\ exp2$ )
  note  $IH1 = \langle \bigwedge Ts\ U. \llbracket P, [Vs \mapsto Ts] \vdash exp1 :: U; length\ Ts = length\ Vs \rrbracket \implies compP\ f\ P, Ts \vdash 1\ compE1\ Vs\ exp1 :: U \rangle$ 
  note  $IH2 = \langle \bigwedge Ts\ U. \llbracket P, [(Vs @ [V]) \mapsto Ts] \vdash exp2 :: U; length\ Ts = length\ (Vs @ [V]) \rrbracket \implies compP\ f\ P, Ts \vdash 1\ compE1\ (Vs @ [V])\ exp2 :: U \rangle$ 
  note  $length = \langle length\ Ts = length\ Vs \rangle$ 
  with  $\langle P, [Vs \mapsto Ts] \vdash try\ exp1\ catch(C\ V)\ exp2 :: U \rangle$ 
  have  $wt1: P, [Vs \mapsto Ts] \vdash exp1 :: U$  and  $wt2: P, [(Vs @ [V]) \mapsto (Ts @ [Class\ C])] \vdash exp2 :: U$ 
    and  $C: P \vdash C \preceq^* Throwable$  by(auto simp add: nth-append)
  from  $wf\ C$  have  $is-class\ P\ C$  by(rule is-class-sub-Throwable)
  with  $IH1$ [OF  $wt1\ length$ ]  $IH2$ [OF  $wt2$ ]  $length$  show  $?case$  by(auto)
qed(fastforce)+

```

and the correct block numbering:

The main complication is preservation of definite assignment \mathcal{D} .

```

lemma fixes  $e :: 'addr\ expr$  and  $es :: 'addr\ expr\ list$ 
  shows  $A\ compE1\ None[simp]: \mathcal{A}\ e = None \implies \mathcal{A}\ (compE1\ Vs\ e) = None$ 
  and  $As\ compEs1\ None: \mathcal{A}s\ es = None \implies \mathcal{A}s\ (compEs1\ Vs\ es) = None$ 
apply(induct  $Vs\ e$  and  $Vs\ es$  rule: compE1-compEs1-induct)
apply(auto simp: hyperset-defs)
done

```

```

lemma fixes  $e :: 'addr\ expr$  and  $es :: 'addr\ expr\ list$ 
  shows  $A\ compE1: \llbracket \mathcal{A}\ e = \lfloor A \rfloor; fv\ e \subseteq set\ Vs \rrbracket \implies \mathcal{A}\ (compE1\ Vs\ e) = \lfloor index\ Vs\ 'A \rfloor$ 
  and  $As\ compEs1: \llbracket \mathcal{A}s\ es = \lfloor A \rfloor; fvs\ es \subseteq set\ Vs \rrbracket \implies \mathcal{A}s\ (compEs1\ Vs\ es) = \lfloor index\ Vs\ 'A \rfloor$ 
proof(induct  $Vs\ e$  and  $Vs\ es$  arbitrary: A and A rule: compE1-compEs1-induct)
  case (Block  $Vs\ V'\ T\ vo\ e$ )
  hence  $fv\ e \subseteq set\ (Vs @ [V'])$  by fastforce
  moreover obtain  $B$  where  $\mathcal{A}\ e = \lfloor B \rfloor$ 
    using  $Block.prems$  by(simp add: hyperset-defs)
  moreover from calculation have  $B \subseteq set\ (Vs @ [V'])$  by(auto dest!: A-fv)
  ultimately show  $?case$  using  $Block$ 
    by(auto simp add: hyperset-defs image-index)
next
case (Synchronized  $Vs\ V\ exp1\ exp2\ A$ )
  have  $IH1: \bigwedge A. \llbracket \mathcal{A}\ exp1 = \lfloor A \rfloor; fv\ exp1 \subseteq set\ Vs \rrbracket \implies \mathcal{A}\ (compE1\ Vs\ exp1) = \lfloor index\ Vs\ 'A \rfloor$  by fact
  have  $IH2: \bigwedge A. \llbracket \mathcal{A}\ exp2 = \lfloor A \rfloor; fv\ exp2 \subseteq set\ (Vs @ [fresh-var\ Vs]) \rrbracket \implies \mathcal{A}\ (compE1\ (Vs @ [fresh-var\ Vs])\ exp2) = \lfloor index\ (Vs @ [fresh-var\ Vs])\ 'A \rfloor$  by fact
  from  $\langle fv\ (sync_V\ (exp1)\ exp2) \subseteq set\ Vs \rangle$ 

```

```

have fv1: fv exp1 ⊆ set Vs
  and fv2: fv exp2 ⊆ set Vs by auto
from ⟨A (sync_V (exp1) exp2) = [A]⟩ have A: A exp1 ⊔ A exp2 = [A] by (simp)
then obtain A1 A2 where A1: A exp1 = [A1] and A2: A exp2 = [A2] by (auto simp add:
hyperset-defs)
from A2 fv2 have A2 ⊆ set Vs by (auto dest: A-fv del: subsetI)
with fresh-var-fresh[of Vs] have (fresh-var Vs) ∉ A2 by (auto)
from fv2 have fv exp2 ⊆ set (Vs @ [fresh-var Vs]) by auto
with IH2[OF A2] have A (compE1 (Vs @ [fresh-var Vs]) exp2) = [index (Vs @ [fresh-var Vs]) ‘
A2] by (auto)
with IH1[OF A1 fv1] A[symmetric] ⟨A2 ⊆ set Vs⟩ ⟨(fresh-var Vs) ∉ A2⟩ A1 A2 show ?case
  by (auto simp add: image-index)
next
case (InSynchronized Vs V a exp A)
have IH: ∧A. [A exp = [A]; fv exp ⊆ set (Vs @ [fresh-var Vs])] ⇒ A (compE1 (Vs @ [fresh-var
Vs]) exp) = [index (Vs @ [fresh-var Vs]) ‘ A] by fact
from ⟨A (insync_V (a) exp) = [A]⟩ have A: A exp = [A] by simp
from ⟨fv (insync_V (a) exp) ⊆ set Vs⟩ have fv: fv exp ⊆ set Vs by simp
from A fv have A ⊆ set Vs by (auto dest: A-fv del: subsetI)
with fresh-var-fresh[of Vs] have (fresh-var Vs) ∉ A by (auto)
from fv IH[OF A] have A (compE1 (Vs @ [fresh-var Vs]) exp) = [index (Vs @ [fresh-var Vs]) ‘
A] by simp
with ⟨A ⊆ set Vs⟩ ⟨(fresh-var Vs) ∉ A⟩ show ?case by (simp add: image-index)
next
case (TryCatch Vs e1 C V' e2)
hence fve2: fv e2 ⊆ set (Vs@[V']) by auto
show ?case
proof (cases A e1)
  assume A1: A e1 = None
  then obtain A2 where A2: A e2 = [A2] using TryCatch
    by (simp add: hyperset-defs)
  hence A2 ⊆ set (Vs@[V']) using TryCatch.prem A-fv[OF A2] by simp blast
  thus ?thesis using TryCatch fve2 A1 A2
    by (auto simp add: hyperset-defs image-index)
next
fix A1 assume A1: A e1 = [A1]
show ?thesis
proof (cases A e2)
  assume A2: A e2 = None
  then show ?case using TryCatch A1 by (simp add: hyperset-defs)
next
fix A2 assume A2: A e2 = [A2]
have A1 ⊆ set Vs using TryCatch.prem A-fv[OF A1] by simp blast
moreover
have A2 ⊆ set (Vs@[V']) using TryCatch.prem A-fv[OF A2] by simp blast
ultimately show ?thesis using TryCatch A1 A2
  by (fastforce simp add: hyperset-defs image-index
    Diff-subset-conv inj-on-image-Int[OF inj-on-index])
qed
qed
next
case (Cond Vs e e1 e2)
{ assume A e = None ∨ A e1 = None ∨ A e2 = None
  hence ?case using Cond by (auto simp add: hyperset-defs image-Un)
}

```

```

}
moreover
{ fix  $A A1 A2$ 
  assume  $\mathcal{A} e = [A]$  and  $A1: \mathcal{A} e1 = [A1]$  and  $A2: \mathcal{A} e2 = [A2]$ 
  moreover
  have  $A1 \subseteq \text{set } Vs$  using  $\text{Cond.prem}s A\text{-fv}[OF A1]$  by  $\text{simp blast}$ 
  moreover
  have  $A2 \subseteq \text{set } Vs$  using  $\text{Cond.prem}s A\text{-fv}[OF A2]$  by  $\text{simp blast}$ 
  ultimately have  $?case$  using  $\text{Cond}$ 
    by( $\text{auto simp add: hyperset-defs image-Un}$ 
       $\text{inj-on-image-Int}[OF \text{inj-on-index}]$ )
}
ultimately show  $?case$  by  $\text{fastforce}$ 
qed ( $\text{auto simp add: hyperset-defs}$ )

lemma fixes  $e :: ('a, 'b, 'addr) \text{exp}$  and  $es :: ('a, 'b, 'addr) \text{exp list}$ 
  shows  $D\text{-None}$  [iff]:  $\mathcal{D} e \text{ None}$ 
  and  $Ds\text{-None}$  [iff]:  $\mathcal{D}s es \text{ None}$ 
by( $\text{induct } e$  and  $es$   $\text{rule: } \mathcal{D}.\text{induct } \mathcal{D}s.\text{induct}$ )( $\text{simp-all}$ )

declare  $\text{Un-ac}$  [ $\text{simp}$ ]

lemma fixes  $e :: 'addr \text{expr}$  and  $es :: 'addr \text{expr list}$ 
  shows  $D\text{-index-compE1}$ :  $\llbracket A \subseteq \text{set } Vs; \text{fv } e \subseteq \text{set } Vs \rrbracket \implies \mathcal{D} e [A] \implies \mathcal{D} (\text{compE1 } Vs e) [\text{index } Vs 'A]$ 
  and  $Ds\text{-index-compEs1}$ :  $\llbracket A \subseteq \text{set } Vs; \text{fvs } es \subseteq \text{set } Vs \rrbracket \implies \mathcal{D}s es [A] \implies \mathcal{D}s (\text{compEs1 } Vs es) [\text{index } Vs 'A]$ 
proof( $\text{induct } e$  and  $es$   $\text{arbitrary: } A Vs$  and  $A Vs$   $\text{rule: } \mathcal{D}.\text{induct } \mathcal{D}s.\text{induct}$ )
  case ( $\text{BinOp } e1 \text{ bop } e2$ )
  hence  $IH1: \mathcal{D} (\text{compE1 } Vs e1) [\text{index } Vs 'A]$  by  $\text{simp}$ 
  show  $?case$ 
  proof ( $\text{cases } \mathcal{A} e1$ )
    case  $\text{None}$  thus  $?thesis$  using  $\text{BinOp}$  by  $\text{simp}$ 
  next
    case ( $\text{Some } A1$ )
    have  $\text{indexA1}: \mathcal{A} (\text{compE1 } Vs e1) = [\text{index } Vs 'A1]$ 
      using  $A\text{-compE1}[OF \text{Some}] \text{BinOp.prem}s$  by  $\text{auto}$ 
    have  $A \cup A1 \subseteq \text{set } Vs$  using  $\text{BinOp.prem}s A\text{-fv}[OF \text{Some}]$  by  $\text{auto}$ 
    hence  $\mathcal{D} (\text{compE1 } Vs e2) [\text{index } Vs '(A1 \cup A)]$  using  $\text{BinOp Some}$  by( $\text{auto}$ )
    hence  $\mathcal{D} (\text{compE1 } Vs e2) [\text{index } Vs 'A1 \cup \text{index } Vs 'A]$ 
      by( $\text{simp add: image-Un}$ )
    thus  $?thesis$  using  $IH1 \text{indexA1}$  by  $\text{auto}$ 
  qed
next
  case ( $\text{AAcc } a \text{ i } A Vs$ )
  have  $IH1: \bigwedge A Vs. \llbracket A \subseteq \text{set } Vs; \text{fv } a \subseteq \text{set } Vs; \mathcal{D} a [A] \rrbracket \implies \mathcal{D} (\text{compE1 } Vs a) [\text{index } Vs 'A]$  by  $\text{fact}$ 
  have  $IH2: \bigwedge A Vs. \llbracket A \subseteq \text{set } Vs; \text{fv } i \subseteq \text{set } Vs; \mathcal{D} i [A] \rrbracket \implies \mathcal{D} (\text{compE1 } Vs i) [\text{index } Vs 'A]$  by  $\text{fact}$ 
  from  $\langle \mathcal{D} (a[i]) [A] \rangle$  have  $D1: \mathcal{D} a [A]$  and  $D2: \mathcal{D} i ([A] \sqcup \mathcal{A} a)$  by  $\text{auto}$ 
  from  $\langle \text{fv } (a[i]) \subseteq \text{set } Vs \rangle$  have  $\text{fv1}: \text{fv } a \subseteq \text{set } Vs$  and  $\text{fv2}: \text{fv } i \subseteq \text{set } Vs$  by  $\text{auto}$ 
  show  $?case$ 
  proof( $\text{cases } \mathcal{A} a$ )
    case  $\text{None}$  thus  $?thesis$  using  $\text{AAcc}$  by  $\text{simp}$ 

```

next
 case (*Some A1*)
 with *fv1* have $\mathcal{A} (\text{compE1 } Vs \ a) = \lfloor \text{index } Vs \ ' \ A1 \rfloor$ **by**-(*rule A-compE1*)
 moreover from *A-fv*[*OF Some*] *fv1* $\langle A \subseteq \text{set } Vs \rangle$ have $A1 \cup A \subseteq \text{set } Vs$ **by** *auto*
 from *IH2*[*OF this fv2*] *Some D2* have $\mathcal{D} (\text{compE1 } Vs \ i) \lfloor \text{index } Vs \ ' \ A1 \cup \text{index } Vs \ ' \ A \rfloor$
by(*simp add: image-Un*)
 ultimately show *?thesis* **using** *IH1*[*OF* $\langle A \subseteq \text{set } Vs \rangle$ *fv1 D1*] **by**(*simp*)
qed
next
 case (*AAss a i e A Vs*)
 have *IH1*: $\bigwedge A \ Vs. \llbracket A \subseteq \text{set } Vs; \text{fv } a \subseteq \text{set } Vs; \mathcal{D} \ a \ \lfloor A \rfloor \rrbracket \implies \mathcal{D} (\text{compE1 } Vs \ a) \lfloor \text{index } Vs \ ' \ A \rfloor$ **by**
fact
 have *IH2*: $\bigwedge A \ Vs. \llbracket A \subseteq \text{set } Vs; \text{fv } i \subseteq \text{set } Vs; \mathcal{D} \ i \ \lfloor A \rfloor \rrbracket \implies \mathcal{D} (\text{compE1 } Vs \ i) \lfloor \text{index } Vs \ ' \ A \rfloor$ **by**
fact
 have *IH3*: $\bigwedge A \ Vs. \llbracket A \subseteq \text{set } Vs; \text{fv } e \subseteq \text{set } Vs; \mathcal{D} \ e \ \lfloor A \rfloor \rrbracket \implies \mathcal{D} (\text{compE1 } Vs \ e) \lfloor \text{index } Vs \ ' \ A \rfloor$ **by**
fact
 from $\langle \mathcal{D} (a \lfloor i \rfloor := e) \ \lfloor A \rfloor \rangle$ have *D1*: $\mathcal{D} \ a \ \lfloor A \rfloor$ and *D2*: $\mathcal{D} \ i \ (\lfloor A \rfloor \sqcup \mathcal{A} \ a)$
 and *D3*: $\mathcal{D} \ e \ (\lfloor A \rfloor \sqcup \mathcal{A} \ a \sqcup \mathcal{A} \ i)$ **by** *auto*
 from $\langle \text{fv } (a \lfloor i \rfloor := e) \subseteq \text{set } Vs \rangle$
 have *fv1*: $\text{fv } a \subseteq \text{set } Vs$ and *fv2*: $\text{fv } i \subseteq \text{set } Vs$ and *fv3*: $\text{fv } e \subseteq \text{set } Vs$ **by** *auto*
show *?case*
proof(*cases A a*)
 case *None* **thus** *?thesis* **using** *AAss* **by** *simp*
next
 case (*Some A1*)
 with *fv1* have *A1*: $\mathcal{A} (\text{compE1 } Vs \ a) = \lfloor \text{index } Vs \ ' \ A1 \rfloor$ **by**-(*rule A-compE1*)
 from *A-fv*[*OF Some*] *fv1* $\langle A \subseteq \text{set } Vs \rangle$ have $A1 \cup A \subseteq \text{set } Vs$ **by** *auto*
 from *IH2*[*OF this fv2*] *Some D2* have *D2'*: $\mathcal{D} (\text{compE1 } Vs \ i) \lfloor \text{index } Vs \ ' \ A1 \cup \text{index } Vs \ ' \ A \rfloor$
by(*simp add: image-Un*)
show *?thesis*
proof(*cases A i*)
 case *None* **thus** *?thesis* **using** *AAss D2'* *Some A1* **by** *simp*
next
 case (*Some A2*)
 with *fv2* have *A2*: $\mathcal{A} (\text{compE1 } Vs \ i) = \lfloor \text{index } Vs \ ' \ A2 \rfloor$ **by**-(*rule A-compE1*)
 moreover from *A-fv*[*OF Some*] *fv2* $\langle A1 \cup A \subseteq \text{set } Vs \rangle$ have $A1 \cup A \cup A2 \subseteq \text{set } Vs$ **by** *auto*
 from *IH3*[*OF this fv3*] *Some* $\langle \mathcal{A} \ a = \lfloor A1 \rfloor \rangle$ *D3*
 have $\mathcal{D} (\text{compE1 } Vs \ e) \lfloor \text{index } Vs \ ' \ A1 \cup \text{index } Vs \ ' \ A \cup \text{index } Vs \ ' \ A2 \rfloor$
by(*simp add: image-Un Un-commute Un-assoc*)
 ultimately show *?thesis* **using** *IH1*[*OF* $\langle A \subseteq \text{set } Vs \rangle$ *fv1 D1*] *D2'* *A1 A2* **by**(*simp*)
qed
qed
next
 case (*FAss e1 F D e2*)
 hence *IH1*: $\mathcal{D} (\text{compE1 } Vs \ e1) \lfloor \text{index } Vs \ ' \ A \rfloor$ **by** *simp*
show *?case*
proof (*cases A e1*)
 case *None* **thus** *?thesis* **using** *FAss* **by** *simp*
next
 case (*Some A1*)
 have *indexA1*: $\mathcal{A} (\text{compE1 } Vs \ e1) = \lfloor \text{index } Vs \ ' \ A1 \rfloor$
using *A-compE1*[*OF Some*] *FAss.prem*s **by** *auto*
 have $A \cup A1 \subseteq \text{set } Vs$ **using** *FAss.prem*s *A-fv*[*OF Some*] **by** *auto*
 hence $\mathcal{D} (\text{compE1 } Vs \ e2) \lfloor \text{index } Vs \ ' \ (A \cup A1) \rfloor$ **using** *FAss Some* **by** *auto*

hence $\mathcal{D} (\text{compE1 } Vs \ e2) \ [index\ Vs \ ' \ A \cup \ index\ Vs \ ' \ A1]$
 by(*simp add: image-Un*)
 thus *?thesis using IH1 indexA1 by auto*
 qed
 next
 case (*CompareAndSwap e1 D F e2 e3 A Vs*)
 have *IH1: $\bigwedge A \ Vs. [A \subseteq \text{set } Vs; \text{fv } e1 \subseteq \text{set } Vs; \mathcal{D} \ e1 \ [A]] \implies \mathcal{D} (\text{compE1 } Vs \ e1) \ [index\ Vs \ ' \ A]$*
 by *fact*
 have *IH2: $\bigwedge A \ Vs. [A \subseteq \text{set } Vs; \text{fv } e2 \subseteq \text{set } Vs; \mathcal{D} \ e2 \ [A]] \implies \mathcal{D} (\text{compE1 } Vs \ e2) \ [index\ Vs \ ' \ A]$*
 by *fact*
 have *IH3: $\bigwedge A \ Vs. [A \subseteq \text{set } Vs; \text{fv } e3 \subseteq \text{set } Vs; \mathcal{D} \ e3 \ [A]] \implies \mathcal{D} (\text{compE1 } Vs \ e3) \ [index\ Vs \ ' \ A]$*
 by *fact*
 from $\langle \mathcal{D} (e1.\text{compareAndSwap}(D.F, e2, e3)) \ [A] \rangle$ have *D1: $\mathcal{D} \ e1 \ [A]$ and *D2: $\mathcal{D} \ e2 \ ([A] \sqcup \mathcal{A} \ e1)$*
 and *D3: $\mathcal{D} \ e3 \ ([A] \sqcup \mathcal{A} \ e1 \sqcup \mathcal{A} \ e2)$ by auto*
 from $\langle \text{fv} (e1.\text{compareAndSwap}(D.F, e2, e3)) \subseteq \text{set } Vs \rangle$
 have *fv1: $\text{fv } e1 \subseteq \text{set } Vs$ and *fv2: $\text{fv } e2 \subseteq \text{set } Vs$ and *fv3: $\text{fv } e3 \subseteq \text{set } Vs$ by auto*
 show *?case*
 proof(*cases $\mathcal{A} \ e1$*)
 case *None thus ?thesis using CompareAndSwap by simp*
 next
 case (*Some A1*)
 with *fv1 have A1: $\mathcal{A} (\text{compE1 } Vs \ e1) = [index\ Vs \ ' \ A1]$ by-(*rule A-compE1*)*
 from *A-fv[OF Some] fv1 $\langle A \subseteq \text{set } Vs \rangle$ have $A1 \cup A \subseteq \text{set } Vs$ by *auto**
 from *IH2[OF this fv2] Some D2 have D2': $\mathcal{D} (\text{compE1 } Vs \ e2) \ [index\ Vs \ ' \ A1 \cup \ index\ Vs \ ' \ A]$*
 by(*simp add: image-Un*)
 show *?thesis*
 proof(*cases $\mathcal{A} \ e2$*)
 case *None thus ?thesis using CompareAndSwap D2' Some A1 by simp*
 next
 case (*Some A2*)
 with *fv2 have A2: $\mathcal{A} (\text{compE1 } Vs \ e2) = [index\ Vs \ ' \ A2]$ by-(*rule A-compE1*)*
 moreover from *A-fv[OF Some] fv2 $\langle A1 \cup A \subseteq \text{set } Vs \rangle$ have $A1 \cup A \cup A2 \subseteq \text{set } Vs$ by *auto**
 from *IH3[OF this fv3] Some $\langle \mathcal{A} \ e1 = [A1] \rangle$ D3*
 have $\mathcal{D} (\text{compE1 } Vs \ e3) \ [index\ Vs \ ' \ A1 \cup \ index\ Vs \ ' \ A \cup \ index\ Vs \ ' \ A2]$
 by(*simp add: image-Un Un-commute Un-assoc*)
 ultimately show *?thesis using IH1[OF $\langle A \subseteq \text{set } Vs \rangle$ fv1 D1] D2' A1 A2 by(simp)*
 qed
 qed
 next
 case (*Call e1 M es*)
 hence *IH1: $\mathcal{D} (\text{compE1 } Vs \ e1) \ [index\ Vs \ ' \ A]$ by *simp**
 show *?case*
 proof (*cases $\mathcal{A} \ e1$*)
 case *None thus ?thesis using Call by simp*
 next
 case (*Some A1*)
 have *indexA1: $\mathcal{A} (\text{compE1 } Vs \ e1) = [index\ Vs \ ' \ A1]$*
 using *A-compE1[OF Some] Call.premis by auto*
 have $A \cup A1 \subseteq \text{set } Vs$ using *Call.premis A-fv[OF Some] by auto*
 hence $\mathcal{D}_s (\text{compEs1 } Vs \ es) \ [index\ Vs \ ' \ (A \cup A1)]$ using *Call Some by auto*
 hence $\mathcal{D}_s (\text{compEs1 } Vs \ es) \ [index\ Vs \ ' \ A \cup \ index\ Vs \ ' \ A1]$
 by(*simp add: image-Un*)
 thus *?thesis using IH1 indexA1 by auto****

qed
next
case (*Synchronized V exp1 exp2 A Vs*)
have *IH1*: $\bigwedge A \text{ Vs}. \llbracket A \subseteq \text{set } Vs; \text{fv } \text{exp1} \subseteq \text{set } Vs; \mathcal{D} \text{ exp1 } [A] \rrbracket \implies \mathcal{D} (\text{compE1 } Vs \text{ exp1}) [index \text{ Vs } ' A]$ by *fact*
have *IH2*: $\bigwedge A \text{ Vs}. \llbracket A \subseteq \text{set } Vs; \text{fv } \text{exp2} \subseteq \text{set } Vs; \mathcal{D} \text{ exp2 } [A] \rrbracket \implies \mathcal{D} (\text{compE1 } Vs \text{ exp2}) [index \text{ Vs } ' A]$ by *fact*
from $\langle \mathcal{D} (\text{sync}_V (\text{exp1}) \text{ exp2}) [A] \rangle$ have *D1*: $\mathcal{D} \text{ exp1 } [A]$ and *D2*: $\mathcal{D} \text{ exp2 } ([A] \sqcup \mathcal{A} \text{ exp1})$ by *auto*
from $\langle \text{fv } (\text{sync}_V (\text{exp1}) \text{ exp2}) \subseteq \text{set } Vs \rangle$ have *fv1*: $\text{fv } \text{exp1} \subseteq \text{set } Vs$ and *fv2*: $\text{fv } \text{exp2} \subseteq \text{set } Vs$ by *auto*
show ?*case*
proof(*cases A exp1*)
case *None* thus ?*thesis* using *Synchronized* by(*simp*)
next
case (*Some A1*)
with *fv1* have *A1*: $\mathcal{A} (\text{compE1 } Vs \text{ exp1}) = [index \text{ Vs } ' A1]$ by-(*rule A-compE1*)
from *A-fv[OF Some]* *fv1* $\langle A \subseteq \text{set } Vs \rangle$ have $A \cup A1 \subseteq \text{set } Vs$ by *auto*
hence $A \cup A1 \subseteq \text{set } (Vs @ [fresh\text{-var } Vs])$ by *simp*
from *IH2[OF this]* *fv2* *Some D2*
have *D2'*: $\mathcal{D} (\text{compE1 } (Vs @ [fresh\text{-var } Vs]) \text{ exp2}) [index (Vs @ [fresh\text{-var } Vs]) ' (A \cup A1)]$
by(*simp*)
moreover from *fresh-var-fresh[of Vs]* $\langle A \cup A1 \subseteq \text{set } Vs \rangle$
have $(fresh\text{-var } Vs) \notin A \cup A1$ by *auto*
with $\langle A \cup A1 \subseteq \text{set } Vs \rangle$
have $index (Vs @ [fresh\text{-var } Vs]) ' (A \cup A1) = index \text{ Vs } ' A \cup index \text{ Vs } ' A1$
by(*simp add: image-index image-Un*)
ultimately show ?*thesis* using *IH1[OF A A1]* *D2'* *A1* by(*simp*)
qed
next
case (*InSynchronized V a e A Vs*)
have *IH*: $\bigwedge A \text{ Vs}. \llbracket A \subseteq \text{set } Vs; \text{fv } e \subseteq \text{set } Vs; \mathcal{D} e [A] \rrbracket \implies \mathcal{D} (\text{compE1 } Vs e) [index \text{ Vs } ' A]$ by *fact*
from *IH*[*of A Vs @ [fresh-var Vs]*] $\langle A \subseteq \text{set } Vs \rangle$ $\langle \text{fv } (\text{insync}_V (a) e) \subseteq \text{set } Vs \rangle$ $\langle \mathcal{D} (\text{insync}_V (a) e) [A] \rangle$
have $\mathcal{D} (\text{compE1 } (Vs @ [fresh\text{-var } Vs]) e) [index (Vs @ [fresh\text{-var } Vs]) ' A]$ by(*auto*)
moreover from *fresh-var-fresh[of Vs]* $\langle A \subseteq \text{set } Vs \rangle$ have $(fresh\text{-var } Vs) \notin A$ by *auto*
with $\langle A \subseteq \text{set } Vs \rangle$ have $index (Vs @ [fresh\text{-var } Vs]) ' A = index \text{ Vs } ' A$ by(*simp add: image-index*)
ultimately show ?*case* by(*simp*)
next
case (*TryCatch e1 C V e2*)
have $\llbracket A \cup \{V\} \subseteq \text{set } (Vs@[V]); \text{fv } e2 \subseteq \text{set } (Vs@[V]); \mathcal{D} e2 [A \cup \{V\}] \rrbracket \implies$
 $\mathcal{D} (\text{compE1 } (Vs@[V]) e2) [index (Vs@[V]) ' (A \cup \{V\})]$ by *fact*
hence $\mathcal{D} (\text{compE1 } (Vs@[V]) e2) [index (Vs@[V]) ' (A \cup \{V\})]$
using *TryCatch.prem*s by(*simp add: Diff-subset-conv*)
moreover have $index (Vs@[V]) ' A \subseteq index \text{ Vs } ' A \cup \{size \text{ Vs}\}$
using *TryCatch.prem*s by(*auto simp add: image-index split:if-split-asm*)
ultimately show ?*case* using *TryCatch* by(*auto simp: hyperset-defs elim!: D-mono'*)
next
case (*Seq e1 e2*)
hence *IH1*: $\mathcal{D} (\text{compE1 } Vs e1) [index \text{ Vs } ' A]$ by *simp*
show ?*case*
proof(*cases A e1*)
case *None* thus ?*thesis* using *Seq* by *simp*

```

next
  case (Some A1)
  have indexA1:  $\mathcal{A}$  (compE1 Vs e1) = [index Vs ' A1]
    using A-compE1[OF Some] Seq.premis by auto
  have  $A \cup A1 \subseteq \text{set } Vs$  using Seq.premis A-fv[OF Some] by auto
  hence  $\mathcal{D}$  (compE1 Vs e2) [index Vs ' (A  $\cup$  A1)] using Seq Some by auto
  hence  $\mathcal{D}$  (compE1 Vs e2) [index Vs ' A  $\cup$  index Vs ' A1]
    by(simp add: image-Un)
  thus ?thesis using IH1 indexA1 by auto
qed
next
case (Cond e e1 e2)
hence IH1:  $\mathcal{D}$  (compE1 Vs e) [index Vs ' A] by simp
show ?case
proof (cases  $\mathcal{A}$  e)
  case None thus ?thesis using Cond by simp
next
  case (Some B)
  have indexB:  $\mathcal{A}$  (compE1 Vs e) = [index Vs ' B]
    using A-compE1[OF Some] Cond.premis by auto
  have  $A \cup B \subseteq \text{set } Vs$  using Cond.premis A-fv[OF Some] by auto
  hence  $\mathcal{D}$  (compE1 Vs e1) [index Vs ' (A  $\cup$  B)]
    and  $\mathcal{D}$  (compE1 Vs e2) [index Vs ' (A  $\cup$  B)]
    using Cond Some by auto
  hence  $\mathcal{D}$  (compE1 Vs e1) [index Vs ' A  $\cup$  index Vs ' B]
    and  $\mathcal{D}$  (compE1 Vs e2) [index Vs ' A  $\cup$  index Vs ' B]
    by(simp add: image-Un)+
  thus ?thesis using IH1 indexB by auto
qed
next
case (While e1 e2)
hence IH1:  $\mathcal{D}$  (compE1 Vs e1) [index Vs ' A] by simp
show ?case
proof (cases  $\mathcal{A}$  e1)
  case None thus ?thesis using While by simp
next
  case (Some A1)
  have indexA1:  $\mathcal{A}$  (compE1 Vs e1) = [index Vs ' A1]
    using A-compE1[OF Some] While.premis by auto
  have  $A \cup A1 \subseteq \text{set } Vs$  using While.premis A-fv[OF Some] by auto
  hence  $\mathcal{D}$  (compE1 Vs e2) [index Vs ' (A  $\cup$  A1)] using While Some by auto
  hence  $\mathcal{D}$  (compE1 Vs e2) [index Vs ' A  $\cup$  index Vs ' A1]
    by(simp add: image-Un)
  thus ?thesis using IH1 indexA1 by auto
qed
next
case (Block V T vo e A Vs)
have IH:  $\bigwedge A$  Vs. [ $A \subseteq \text{set } Vs$ ;  $fv\ e \subseteq \text{set } Vs$ ;  $\mathcal{D}\ e\ [A]$ ]  $\implies \mathcal{D}$  (compE1 Vs e) [index Vs ' A] by
fact
from  $\langle fv\ \{V:T=vo; e\} \subseteq \text{set } Vs \rangle$  have fv:  $fv\ e \subseteq \text{set } (Vs\ @\ [V])$  by auto
show ?case
proof (cases vo)
  case None
  with  $\langle \mathcal{D}\ \{V:T=vo; e\}\ [A] \rangle$  have D:  $\mathcal{D}\ e\ [A - \{V\}]$  by(auto)

```



```

from ⟨ $A \subseteq \text{set } Vs$ ⟩ have  $A - \{V\} \subseteq \text{set } (Vs @ [V])$  by auto
from  $IH[OF \text{ this } fv D]$  have  $\mathcal{D} (\text{compE1 } (Vs @ [V]) e) \llbracket \text{index } (Vs @ [V]) \text{ ' } (A - \{V\}) \rrbracket$  .
moreover from ⟨ $A \subseteq \text{set } Vs$ ⟩ have  $\text{size: size } Vs \notin \text{index } Vs \text{ ' } A$  by(auto simp add: image-def)
hence  $\llbracket \text{index } Vs \text{ ' } (A - \{V\}) \rrbracket \subseteq \llbracket \text{index } Vs \text{ ' } A \rrbracket$  by(auto simp add: hyperset-defs)
ultimately have  $\mathcal{D} (\text{compE1 } (Vs @ [V]) e) \llbracket \text{index } Vs \text{ ' } A \rrbracket$  using ⟨ $A - \{V\} \subseteq \text{set } (Vs @ [V])$ ⟩
  by(simp add: image-index)(erule D-mono', auto)
thus ?thesis using None size by(simp)
next
  case (Some v)
  with ⟨ $\mathcal{D} \{V:T=vo; e\} \llbracket A \rrbracket$ ⟩ have  $D: \mathcal{D} e \llbracket \text{insert } V A \rrbracket$  by(auto)
  from ⟨ $A \subseteq \text{set } Vs$ ⟩ have  $\text{insert } V A \subseteq \text{set } (Vs @ [V])$  by auto
  from  $IH[OF \text{ this } fv D]$  have  $\mathcal{D} (\text{compE1 } (Vs @ [V]) e) \llbracket \text{index } (Vs @ [V]) \text{ ' } \text{insert } V A \rrbracket$  by simp
  moreover from ⟨ $A \subseteq \text{set } Vs$ ⟩ have  $\text{index } (Vs @ [V]) \text{ ' } \text{insert } V A \subseteq \text{insert } (\text{length } Vs) (\text{index } Vs \text{ ' } A)$ 
    by(auto simp add: image-index)
  ultimately show ?thesis using Some by(auto elim!: D-mono' simp add: hyperset-defs)
  qed
next
  case (Cons-exp e1 es)
  hence  $IH1: \mathcal{D} (\text{compE1 } Vs e1) \llbracket \text{index } Vs \text{ ' } A \rrbracket$  by simp
  show ?case
  proof (cases A e1)
    case None thus ?thesis using Cons-exp by simp
  next
    case (Some A1)
    have  $\text{index}A1: A (\text{compE1 } Vs e1) = \llbracket \text{index } Vs \text{ ' } A1 \rrbracket$ 
      using  $A\text{-compE1}[OF \text{ Some}] \text{ Cons-exp.prem1}$  by auto
    have  $A \cup A1 \subseteq \text{set } Vs$  using  $\text{Cons-exp.prem1 } A\text{-fv}[OF \text{ Some}]$  by auto
    hence  $\mathcal{D}_s (\text{compEs1 } Vs es) \llbracket \text{index } Vs \text{ ' } (A \cup A1) \rrbracket$  using  $\text{Cons-exp } \text{Some}$  by auto
    hence  $\mathcal{D}_s (\text{compEs1 } Vs es) \llbracket \text{index } Vs \text{ ' } A \cup \text{index } Vs \text{ ' } A1 \rrbracket$ 
      by(simp add: image-Un)
    thus ?thesis using  $IH1 \text{ index}A1$  by auto
  qed
qed (simp-all add: hyperset-defs)

declare  $Un\text{-ac}$  [simp del]

lemma index-image-set: distinct xs  $\implies$  index xs ' set xs = {.. $\text{size } xs$ }
by(induct xs rule: rev-induct) (auto simp add: image-index)

lemma D-compE1:
   $\llbracket \mathcal{D} e \llbracket \text{set } Vs \rrbracket; fv e \subseteq \text{set } Vs; \text{distinct } Vs \rrbracket \implies \mathcal{D} (\text{compE1 } Vs e) \llbracket \{.. $\text{length } Vs$ \} \rrbracket$ 
by(fastforce dest!: D-index-compE1[OF subset-refl] simp add: index-image-set)

lemma D-compE1':
  assumes  $\mathcal{D} e \llbracket \text{set } (V \# Vs) \rrbracket$  and  $fv e \subseteq \text{set } (V \# Vs)$  and  $\text{distinct } (V \# Vs)$ 
  shows  $\mathcal{D} (\text{compE1 } (V \# Vs) e) \llbracket \{.. $\text{length } Vs$ \} \rrbracket$ 
proof –
  have  $\{.. $\text{size } Vs$ \} = \{.. $\text{size } (V \# Vs)$ \}$  by auto
  thus ?thesis using assms by (simp only:)(rule D-compE1)
qed

lemma compP1-pres-wf: wf-J-prog P  $\implies$  wf-J1-prog (compP1 P)
apply simp

```

```

apply(rule wf-prog-compPI)
  prefer 2 apply assumption
apply(case-tac m)
apply(simp add:wf-mdecl-def)
apply(clarify)
apply(frule WT-fv)
apply(fastforce intro!: compE1-pres-wt D-compE1' B syncvars-compE1)
done

```

```

end
theory Compiler imports Compiler1 Compiler2 begin

```

```

definition J2JVM :: 'addr J-prog  $\Rightarrow$  'addr jvm-prog
where [code del]: J2JVM  $\equiv$  compP2  $\circ$  compP1

```

```

lemma J2JVM-code [code]:
  J2JVM = compP ( $\lambda C M Ts T (pns, body). compMb2 (compE1 (this\#pns) body)$ )
by(simp add: J2JVM-def compP2-def o-def compP-compP split-def)

```

```

end

```

7.26 Correctness of both stages

```

theory Correctness

```

```

imports

```

```

  J0Bisim
  J1Deadlock
  ../Framework/FWBisimDeadlock
  Correctness2
  Correctness1Threaded
  Correctness1
  JJ1WellForm
  Compiler

```

```

begin

```

```

locale J-JVM-heap-conf-base =
  J0-J1-heap-base
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
+
  J1-JVM-heap-conf-base
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  hconf compP1 P
for addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype
and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool

```

```

and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool
and P :: 'addr J-prog
begin

```

definition *bisimJ2JVM* ::

```

((('addr,'thread-id,'addr expr × 'addr locals,'heap,'addr) state,
 ('addr,'thread-id,'addr option × 'addr frame list,'heap,'addr) state) bisim

```

where *bisimJ2JVM* = red-red0.mbisim ◦_B red0-Red1'.mbisim ◦_B mbisim-Red1'-Red1 ◦_B Red1-execd.mbisim

definition *tlsimJ2JVM* ::

```

('thread-id × ('addr, 'thread-id, 'heap) J-thread-action,
 'thread-id × ('addr, 'thread-id, 'heap) jvm-thread-action) bisim

```

where *tlsimJ2JVM* = red-red0.mta-bisim ◦_B red0-Red1'.mta-bisim ◦_B (=) ◦_B Red1-execd.mta-bisim

end

lemma *compP2-has-method [simp]: compP2 P ⊢ C has M ⟷ P ⊢ C has M*
by(*auto simp add: compP2-def compP-has-method*)

locale *J-JVM-conf-read* =

```

J1-JVM-conf-read
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  hconf compP1 P

```

for *addr2thread-id* :: ('addr :: addr) ⇒ 'thread-id

and *thread-id2addr* :: 'thread-id ⇒ 'addr

and *spurious-wakeups* :: bool

and *empty-heap* :: 'heap

and *allocate* :: 'heap ⇒ htype ⇒ ('heap × 'addr) set

and *typeof-addr* :: 'heap ⇒ 'addr → htype

and *heap-read* :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool

and *heap-write* :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool

and *hconf* :: 'heap ⇒ bool

and *P* :: 'addr J-prog

begin

sublocale *J-JVM-heap-conf-base* **by**(*unfold-locales*)

theorem *bisimJ2JVM-weak-bisim:*

assumes *wf: wf-J-prog P*

shows *delay-bisimulation-diverge-final (mredT P) (execd-mthr.redT (J2JVM P)) bisimJ2JVM tl-
simJ2JVM*

(red-mthr.mτmove P) (execd-mthr.mτmove (J2JVM P)) red-mthr.mfinal exec-mthr.mfinal

unfolding *bisimJ2JVM-def tlsimJ2JVM-def J2JVM-def o-apply*

apply(*rule delay-bisimulation-diverge-final-compose*)

apply(*rule FWdelay-bisimulation-diverge.mthr-delay-bisimulation-diverge-final*)

apply(*rule red-red0-FWbisim[OF wf-prog-wwf-prog[OF wf]]*)

apply(*rule delay-bisimulation-diverge-final-compose*)

apply(*rule FWdelay-bisimulation-diverge.mthr-delay-bisimulation-diverge-final*)

apply(*rule red0-Red1'-FWweak-bisim[OF wf]*)

apply(*rule delay-bisimulation-diverge-final-compose*)

apply(*rule delay-bisimulation-diverge-final.intro*)

```

apply(rule bisimulation-into-delay.delay-bisimulation)
apply(rule Red1'-Red1-bisim-into-weak[OF compP1-pres-wf[OF wf]])
apply(rule bisimulation-final.delay-bisimulation-final-base)
apply(rule Red1'-Red1-bisimulation-final[OF compP1-pres-wf[OF wf]])
apply(rule FWdelay-bisimulation-diverge.mthr-delay-bisimulation-diverge-final)
apply(rule Red1-exec1-FWwbisim[OF compP1-pres-wf[OF wf]])
done

```

```

lemma bisimJ2JVM-start:
  assumes wf: wf-J-prog P
  and start: wf-start-state P C M vs
  shows bisimJ2JVM (J-start-state P C M vs) (JVM-start-state (J2JVM P) C M vs)
using assms
unfolding bisimJ2JVM-def J2JVM-def o-def
apply(intro bisim-composeI)
  apply(erule (1) bisim-J-J0-start[OF wf-prog-wwf-prog])
  apply(erule (1) bisim-J0-J1-start)
  apply(erule bisim-J1-J1-start[OF compP1-pres-wf])
  apply simp
apply(erule bisim-J1-JVM-start[OF compP1-pres-wf])
apply simp
done

```

end

```

fun exception :: 'addr expr × 'addr locals ⇒ 'addr option × 'addr frame list
where exception (Throw a, xs) = ([a], [])
| exception - = (None, [])

```

```

definition mexception ::
  ('addr, 'thread-id, 'addr expr × 'addr locals, 'heap, 'addr) state ⇒
  ('addr, 'thread-id, 'addr option × 'addr frame list, 'heap, 'addr) state
where
  ∧ ln. mexception s ≡
  (locks s, (λt. case thr s t of [(e, ln)] ⇒ [(exception e, ln)] | None ⇒ None, shr s), wset s, interrupts
  s)

```

```

declare compP1-def [simp del]

```

```

context J-JVM-heap-conf-base begin

```

```

lemma bisimJ2JVM-mfinal-mexception:
  assumes bisim: bisimJ2JVM s s'
  and fin: exec-mthr.mfinal s'
  and fin': red-mthr.mfinal s
  and tsNotEmpty: thr s t ≠ None
  shows s' = mexception s
proof –
  obtain ls ts m ws is where s: s = (ls, (ts, m), ws, is) by(cases s) fastforce
  from bisim obtain s0 s1 where bisimJ0: red-red0.mbisim s s0
  and bisim01: red0-Red1'.mbisim s0 s1
  and bisim1JVM: Red1-execd.mbisim s1 s'
  unfolding bisimJ2JVM-def by(fastforce simp add: mbisim-Red1'-Red1-def)

```

```

from bisimJ0 s have [simp]: locks s0 = ls wset s0 = ws interrupts s0 = is
  and tbisimJ0:  $\bigwedge t. \text{red-red0.tbisim } (ws\ t = \text{None})\ t\ (ts\ t)\ m\ (thr\ s0\ t)\ (shr\ s0)$ 
  by(auto simp add: red-red0.mbisim-def)
from bisim01 have [simp]: locks s1 = ls wset s1 = ws interrupts s1 = is
  and tbisim01:  $\bigwedge t. \text{red0-Red1'.tbisim } (ws\ t = \text{None})\ t\ (thr\ s0\ t)\ (shr\ s0)\ (thr\ s1\ t)\ (shr\ s1)$ 
  by(auto simp add: red0-Red1'.mbisim-def)
from bisim1JVM have locks s' = ls wset s' = ws interrupts s' = is
  and tbisim1JVM:  $\bigwedge t. \text{Red1-execd.tbisim } (ws\ t = \text{None})\ t\ (thr\ s1\ t)\ (shr\ s1)\ (thr\ s'\ t)\ (shr\ s')$ 
  by(auto simp add: Red1-execd.mbisim-def)
then obtain ts' m' where s': s' = (ls, (ts', m'), ws, is) by(cases s') fastforce
{ fix t e x ln
  assume tst: ts t = [(e, x), ln]
  from tbisimJ0[of t] tst obtain e' xs' where ts0t: thr s0 t = [(e', xs'), ln]
  and bisimtJ0: bisim-red-red0 ((e, x), m) ((e', xs'), shr s0)
  by(auto simp add: red-red0.tbisim-def)
  from tbisim01[of t] ts0t obtain e'' xs'' xs''
  where ts1t: thr s1 t = [((e'', xs''), xs''), ln]
  and bisimt01: bisim-red0-Red1 ((e', xs'), shr s0) (((e'', xs''), xs''), shr s1)
  by(auto simp add: red0-Red1'.tbisim-def)
  from tbisim1JVM[of t] ts1t s' obtain xcp frs
  where ts't: ts' t = [(xcp, frs), ln] and [simp]: m' = shr s1
  and bisimt1JVM: bisim1-list1 t m' (e'', xs'') xs'' xcp frs
  by(fastforce simp add: Red1-execd.tbisim-def)

  from fin ts't s s' have [simp]: frs = [] by(auto dest: exec-mthr.mfinalD)
  from bisimt1JVM have [simp]: xs'' = [] by(auto elim: bisim1-list1.cases)
  from bisimt01 have [simp]: xs' = []
  by(auto simp add: bisim-red0-Red1-def elim!: bisim-list1E elim: bisim-list.cases)
  from tst fin' s have fine: final e by(auto dest: red-mthr.mfinalD)
  hence exception (e, x) = (xcp, frs)
  proof(cases)
    fix v
    assume [simp]: e = Val v
    from bisimtJ0 have e' = Val v by(auto elim!: bisim-red-red0.cases)
    with bisimt01 have e'' = Val v by(auto simp add: bisim-red0-Red1-def elim: bisim-list1E)
    with bisimt1JVM have xcp = None by(auto elim: bisim1-list1.cases)
    thus ?thesis by simp
  next
    fix a
    assume [simp]: e = Throw a
    from bisimtJ0 have e' = Throw a by(auto elim!: bisim-red-red0.cases)
    with bisimt01 have e'' = Throw a by(auto simp add: bisim-red0-Red1-def elim: bisim-list1E)
    with bisimt1JVM have xcp = [a] by(auto elim: bisim1-list1.cases)
    thus ?thesis by simp
  qed
  moreover from bisimtJ0 have shr s0 = m by(auto elim: bisim-red-red0.cases)
  moreover from bisimt01 have shr s1 = shr s0 by(auto simp add: bisim-red0-Red1-def)
  ultimately have ts' t = [exception (e, x), ln] m' = m using ts't by simp-all }
moreover {
  fix t
  assume ts t = None
  with red-red0.mbisim-thrNone-eq[OF bisimJ0, of t] s have thr s0 t = None by simp
  with bisim01 have thr s1 t = None by(auto simp add: red0-Red1'.mbisim-thrNone-eq)
  with bisim1JVM s' have ts' t = None by(simp add: Red1-execd.mbisim-thrNone-eq) }

```

ultimately show *?thesis* using $s\ s'\ tsNotEmpty$ by(*auto simp add: mexception-def fun-eq-iff*)
qed

end

context *J-JVM-conf-read* begin

theorem *J2JVM-correct1*:

fixes $C\ M\ vs$

defines $s: s \equiv J\text{-start-state}\ P\ C\ M\ vs$

and *comps*: $cs \equiv JVM\text{-start-state}\ (J2JVM\ P)\ C\ M\ vs$

assumes *wf*: $wf\text{-J-prog}\ P$

and *wf-start*: $wf\text{-start-state}\ P\ C\ M\ vs$

and *red*: $red\text{-mthr.mthr.}\tauRuns\ P\ s\ \xi$

obtains ξ'

where $execd\text{-mthr.mthr.}\tauRuns\ (J2JVM\ P)\ cs\ \xi'\ tllist\text{-all2}\ tlsimJ2JVM\ (rel\text{-option}\ bisimJ2JVM)\ \xi\ \xi'$

and $\bigwedge s'. \llbracket tfinite\ \xi; terminal\ \xi = \lfloor s' \rfloor; red\text{-mthr.mfinal}\ s' \rrbracket$

$\implies tfinite\ \xi' \wedge terminal\ \xi' = \lfloor mexception\ s' \rfloor$

and $\bigwedge s'. \llbracket tfinite\ \xi; terminal\ \xi = \lfloor s' \rfloor; red\text{-mthr.deadlock}\ P\ s' \rrbracket$

$\implies \exists cs'. tfinite\ \xi' \wedge terminal\ \xi' = \lfloor cs' \rfloor \wedge execd\text{-mthr.deadlock}\ (J2JVM\ P)\ cs' \wedge bisimJ2JVM\ s'\ cs'$

and $\llbracket tfinite\ \xi; terminal\ \xi = None \rrbracket \implies tfinite\ \xi' \wedge terminal\ \xi' = None$

and $\neg tfinite\ \xi \implies \neg tfinite\ \xi'$

proof –

from *wf wf-start* have *bisim*: $bisimJ2JVM\ s\ cs$ **unfolding** *s comps* by(*rule bisimJ2JVM-start*)

note *divfin* = $delay\text{-bisimulation-diverge-final.delay-bisimulation-diverge}[OF\ bisimJ2JVM\text{-weak-bisim}[OF\ wf]]$

note *divfin2* = $delay\text{-bisimulation-diverge-final.delay-bisimulation-final-base}[OF\ bisimJ2JVM\text{-weak-bisim}[OF\ wf]]$

from $delay\text{-bisimulation-diverge.simulation-}\tauRuns1[OF\ divfin, OF\ bisim\ red]$ **obtain** ξ'

where *exec*: $execd\text{-mthr.mthr.}\tauRuns\ (J2JVM\ P)\ cs\ \xi'$

and *tlsim*: $tllist\text{-all2}\ tlsimJ2JVM\ (rel\text{-option}\ bisimJ2JVM)\ \xi\ \xi'$ **by** *blast*

moreover {

fix s'

assume *fin*: $tfinite\ \xi$ and s' : $terminal\ \xi = \lfloor s' \rfloor$ and *final*: $red\text{-mthr.mfinal}\ s'$

from $delay\text{-bisimulation-final-base.}\tauRuns\text{-terminate-final}1[OF\ divfin2, OF\ red\ exec\ tlsim\ fin\ s'\ final]$

obtain cs' where *fin'*: $tfinite\ \xi'$ and cs' : $terminal\ \xi' = \lfloor cs' \rfloor$

and *final'*: $exec\text{-mthr.mfinal}\ cs'$ **by** *blast*

from $tlsim\ fin\ s'\ cs'$ **have** *bisim'*: $bisimJ2JVM\ s'\ cs'$ **by**(*auto dest: tllist-all2-tfinite1-terminalD*)

from $red\text{-mthr.mthr.}\tauRuns\text{-into-}\tau rtrancl3p[OF\ red\ fin\ s']$

have *thr* s' *start-tid* $\neq None$ **unfolding** *s*

by(*rule red-mthr.}\tau rtrancl3p-redT-thread-not-disappear*)(*simp add: start-state-def*)

with *bisim'* *final* *final'* **have** [*simp*]: $cs' = mexception\ s'$

by(*intro bisimJ2JVM-mfinal-mexception disjI1*)

with *fin'* cs' **have** $tfinite\ \xi' \wedge terminal\ \xi' = \lfloor mexception\ s' \rfloor$ **by** *simp* }

moreover {

fix s'

assume *fin*: $tfinite\ \xi$ and s' : $terminal\ \xi = \lfloor s' \rfloor$ and *dead*: $red\text{-mthr.deadlock}\ P\ s'$

from $tlsim\ fin\ s'$

obtain cs' where $tfinite\ \xi'$ and cs' : $terminal\ \xi' = \lfloor cs' \rfloor$

and *bisim'*: $bisimJ2JVM\ s'\ cs'$

by(cases terminal ξ')(fastforce dest: tllist-all2-tfinite1-terminalD tllist-all2-tfiniteD)+
from *bisim'* **obtain** $s0' s1' S1'$ **where** *bisim0*: red-red0.mbisim $s' s0'$
and *bisim01*: red0-Red1'.mbisim $s0' s1'$
and *bisim11*: mbisim-Red1'-Red1 $s1' S1'$
and *bisim12*: Red1-execd.mbisim $S1' cs'$
unfolding *bisimJ2JVM-def* **by** *auto*

note *b0* = red-red0-FWbisim[OF wf-prog-wuf-prog[OF wf]]
note *b01* = red0-Red1'-FWweak-bisim[OF wf]
note *b01mthr* = FWdelay-bisimulation-diverge.mbisim-delay-bisimulation[OF *b01*]
note *b11* = Red1'-Red1-bisim-into-weak[OF compP1-pres-wf[OF wf]]
note *b11delay* = bisimulation-into-delay.delay-bisimulation[OF *b11*]
note *b12* = Red1-exec1-FWwbisim[OF compP1-pres-wf[OF wf]]
note *b12mthr* = FWdelay-bisimulation-diverge.mbisim-delay-bisimulation[OF *b12*]

from FWdelay-bisimulation-diverge.deadlock1-imp- τ s-deadlock2[OF *b0*, OF *bisim0* dead, of convert-RA]

obtain $s0''$ **where** red0-mthr.mthr.silent-moves $P s0' s0''$
and *bisim0'*: red-red0.mbisim $s' s0''$
and *dead0*: red0-mthr.deadlock $P s0''$ **by** *auto*

from delay-bisimulation-diverge.simulation-silents1[OF *b01mthr*, OF *bisim01* \langle red0-mthr.mthr.silent-moves $P s0' s0''$ \rangle]

obtain $s1''$ **where** Red1-mthr.mthr.silent-moves *False* (compP1 P) $s1' s1''$
and red0-Red1'.mbisim $s0'' s1''$ **by** *auto*

from FWdelay-bisimulation-diverge.deadlock1-imp- τ s-deadlock2[OF *b01*, OF \langle red0-Red1'.mbisim $s0'' s1''$ \rangle *dead0*, of convert-RA]

obtain $s1'''$ **where** Red1-mthr.mthr.silent-moves *False* (compP1 P) $s1'' s1'''$
and *dead1*: Red1-mthr.deadlock *False* (compP1 P) $s1'''$
and *bisim01'*: red0-Red1'.mbisim $s0'' s1'''$ **by** *auto*

from \langle Red1-mthr.mthr.silent-moves *False* (compP1 P) $s1' s1''$ \rangle \langle Red1-mthr.mthr.silent-moves *False* (compP1 P) $s1'' s1'''$ \rangle

have Red1-mthr.mthr.silent-moves *False* (compP1 P) $s1' s1'''$ **by**(rule rtranclp-trans)

from delay-bisimulation-diverge.simulation-silents1[OF *b11delay*, OF *bisim11* *this*]

obtain $S1''$ **where** Red1-mthr.mthr.silent-moves *True* (compP1 P) $S1' S1''$
and *bisim11'*: mbisim-Red1'-Red1 $s1''' S1''$ **by** *auto*

from *bisim11'* **have** $s1''' = S1''$ **by**(simp add: mbisim-Red1'-Red1-def)

with *dead1* **have** *dead1'*: Red1-mthr.deadlock *True* (compP1 P) $S1''$
by(simp add: Red1-Red1'-deadlock-inv)

from delay-bisimulation-diverge.simulation-silents1[OF *b12mthr*, OF *bisim12* \langle Red1-mthr.mthr.silent-moves *True* (compP1 P) $S1' S1''$ \rangle]

obtain cs'' **where** execd-mthr.mthr.silent-moves (compP2 (compP1 P)) $cs' cs''$
and Red1-execd.mbisim $S1'' cs''$ **by** *auto*

from FWdelay-bisimulation-diverge.deadlock1-imp- τ s-deadlock2[OF *b12* \langle Red1-execd.mbisim $S1'' cs''$ \rangle *dead1'*, of convert-RA]

obtain cs''' **where** execd-mthr.mthr.silent-moves (compP2 (compP1 P)) $cs'' cs'''$
and *bisim12'*: Red1-execd.mbisim $S1'' cs'''$
and *dead'*: execd-mthr.deadlock (compP2 (compP1 P)) cs''' **by** *auto*

from \langle execd-mthr.mthr.silent-moves (compP2 (compP1 P)) $cs' cs''$ \rangle \langle execd-mthr.mthr.silent-moves (compP2 (compP1 P)) $cs'' cs'''$ \rangle

have execd-mthr.mthr.silent-moves (compP2 (compP1 P)) $cs' cs'''$ **by**(rule rtranclp-trans)

hence $cs''' = cs'$ **using** execd-mthr.mthr. τ Runs-terminal-stuck[OF *exec* \langle tfinite ξ' \rangle \langle terminal $\xi' =$

$\lfloor cs' \rfloor \rangle$
by(cases rule: converse-rtranclpE)(fastforce simp add: J2JVM-def)+
with dead' **have** execd-mthr.deadlock (J2JVM P) cs' **by**(simp add: J2JVM-def)
hence $\exists cs'. \text{tfinite } \xi' \wedge \text{terminal } \xi' = \lfloor cs' \rfloor \wedge \text{execd-mthr.deadlock} (J2JVM P) cs' \wedge \text{bisim.J2JVM } s' cs'$
using $\langle \text{tfinite } \xi' \rangle \langle \text{terminal } \xi' = \lfloor cs' \rfloor \rangle \text{bisim}' \text{by blast}$ }
moreover {
assume tfinite ξ **and** terminal $\xi = \text{None}$
hence tfinite $\xi' \wedge \text{terminal } \xi' = \text{None}$ **using** tsim tllist-all2-tfiniteD[OF tsim]
by(cases terminal ξ')(auto dest: tllist-all2-tfinite1-terminalD) }
moreover {
assume $\neg \text{tfinite } \xi$
hence $\neg \text{tfinite } \xi'$ **using** tsim **by**(blast dest: tllist-all2-tfiniteD) }
ultimately show thesis by(rule that)
qed

theorem J2JVM-correct2:

fixes C M vs

defines s: $s \equiv J\text{-start-state } P C M \text{ vs}$

and comps: $cs \equiv JVM\text{-start-state} (J2JVM P) C M \text{ vs}$

assumes wf: wf-J-prog P

and wf-start: wf-start-state P C M vs

and exec: execd-mthr.mthr. τ Runs (J2JVM P) cs ξ'

obtains ξ

where red-mthr.mthr. τ Runs P s ξ tllist-all2 tsimJ2JVM (rel-option bisimJ2JVM) $\xi \xi'$

and $\bigwedge cs'. \llbracket \text{tfinite } \xi'; \text{terminal } \xi' = \lfloor cs' \rfloor; \text{exec-mthr.mfinal } cs' \rrbracket$

$\implies \exists s'. \text{tfinite } \xi \wedge \text{terminal } \xi = \lfloor s' \rfloor \wedge cs' = \text{mexception } s' \wedge \text{bisim.J2JVM } s' cs'$

and $\bigwedge cs'. \llbracket \text{tfinite } \xi'; \text{terminal } \xi' = \lfloor cs' \rfloor; \text{execd-mthr.deadlock} (J2JVM P) cs' \rrbracket$

$\implies \exists s'. \text{tfinite } \xi \wedge \text{terminal } \xi = \lfloor s' \rfloor \wedge \text{red-mthr.deadlock } P s' \wedge \text{bisim.J2JVM } s' cs'$

and $\llbracket \text{tfinite } \xi'; \text{terminal } \xi' = \text{None} \rrbracket \implies \text{tfinite } \xi \wedge \text{terminal } \xi = \text{None}$

and $\neg \text{tfinite } \xi' \implies \neg \text{tfinite } \xi$

proof –

from wf wf-start **have** bisim: bisimJ2JVM s cs **unfolding** s comps **by**(rule bisimJ2JVM-start)

note divfin = delay-bisimulation-diverge-final.delay-bisimulation-diverge[OF bisimJ2JVM-weak-bisim[OF wf]]

note divfin2 = delay-bisimulation-diverge-final.delay-bisimulation-final-base[OF bisimJ2JVM-weak-bisim[OF wf]]

from delay-bisimulation-diverge.simulation- τ Runs2[OF divfin, OF bisim exec] **obtain** ξ

where red: red-mthr.mthr. τ Runs P s ξ

and tsim: tllist-all2 tsimJ2JVM (rel-option bisimJ2JVM) $\xi \xi'$ **by** blast

moreover {

fix cs'

assume fin: tfinite ξ' **and** cs': terminal $\xi' = \lfloor cs' \rfloor$ **and** final: exec-mthr.mfinal cs'

from delay-bisimulation-final-base. τ Runs-terminate-final2[OF divfin2, OF red exec tsim fin cs' final]

obtain s' **where** fin': tfinite ξ **and** s': terminal $\xi = \lfloor s' \rfloor$

and final': red-mthr.mfinal s' **by** blast

from tsim fin s' cs' **have** bisim': bisimJ2JVM s' cs' **by**(auto dest: tllist-all2-tfinite2-terminalD)

from red-mthr.mthr. τ Runs-into- τ rtrancl3p[OF red fin' s']

have thr s' start-tid $\neq \text{None}$ **unfolding** s

by(rule red-mthr. τ rtrancl3p-redT-thread-not-disappear)(simp add: start-state-def)

with bisim' final final' **have** [simp]: $cs' = \text{mexception } s'$


```

  by(intro bisimJ2JVM-mfinal-mexception)
  with fin' s' bisim' have  $\exists s'. \text{tfinite } \xi \wedge \text{terminal } \xi = \lfloor s' \rfloor \wedge \text{cs}' = \text{mexception } s' \wedge \text{bisimJ2JVM}$ 
  s' cs' by simp }
  moreover {
    fix cs'
    assume fin: tfinite  $\xi'$  and cs': terminal  $\xi' = \lfloor \text{cs}' \rfloor$  and dead': execd-mthr.deadlock (J2JVM P) cs'
    from tlsim fin cs'
    obtain s' where tfinite  $\xi$  and s': terminal  $\xi = \lfloor s' \rfloor$ 
      and bisim': bisimJ2JVM s' cs'
      by(cases terminal  $\xi$ )(fastforce dest: tllist-all2-tfinite2-terminalD tllist-all2-tfiniteD)+
    from bisim' obtain s0' s1' S1' where bisim0: red-red0.mbisim s' s0'
      and bisim01: red0-Red1'.mbisim s0' s1'
      and bisim11: mbisim-Red1'-Red1 s1' S1'
      and bisim12: Red1-execd.mbisim S1' cs'
    unfolding bisimJ2JVM-def by auto

    note b0 = red-red0-FWbisim[OF wf-prog-wuf-prog[OF wf]]
    note b0mthr = FWdelay-bisimulation-diverge.mbisim-delay-bisimulation[OF b0]
    note b01 = red0-Red1'-FWweak-bisim[OF wf]
    note b01mthr = FWdelay-bisimulation-diverge.mbisim-delay-bisimulation[OF b01]
    note b11 = Red1'-Red1-bisim-into-weak[OF compP1-pres-wf[OF wf]]
    note b11delay = bisimulation-into-delay.delay-bisimulation[OF b11]
    note b12 = Red1-exec1-FWbisim[OF compP1-pres-wf[OF wf]]

    from FWdelay-bisimulation-diverge.deadlock2-imp- $\tau$ s-deadlock1[OF b12 bisim12, of convert-RA]
  dead'
    obtain S1'' where Red1-mthr.mthr.silent-moves True (compP1 P) S1' S1''
      and bisim12': Red1-execd.mbisim S1'' cs'
      and dead': Red1-mthr.deadlock True (compP1 P) S1'' by(auto simp add: J2JVM-def)
    from delay-bisimulation-diverge.simulation-silents2[OF b11delay, OF bisim11  $\langle$ Red1-mthr.mthr.silent-moves
  True (compP1 P) S1' S1'' $\rangle$ ]
    obtain s1'' where Red1-mthr.mthr.silent-moves False (compP1 P) s1' s1''
      and bisim11': mbisim-Red1'-Red1 s1'' S1'' by blast
    from bisim11' have s1'' = S1'' by(simp add: mbisim-Red1'-Red1-def)
    with dead' have dead1: Red1-mthr.deadlock False (compP1 P) s1''
      by(simp add: Red1-Red1'-deadlock-inv)
    from delay-bisimulation-diverge.simulation-silents2[OF b01mthr, OF bisim01  $\langle$ Red1-mthr.mthr.silent-moves
  False (compP1 P) s1' s1'' $\rangle$ ]
    obtain s0'' where red0-mthr.mthr.silent-moves P s0' s0''
      and bisim01': red0-Red1'.mbisim s0'' s1'' by auto
    from FWdelay-bisimulation-diverge.deadlock2-imp- $\tau$ s-deadlock1[OF b01 bisim01' dead1, of con-
  vert-RA]
    obtain s0''' where red0-mthr.mthr.silent-moves P s0'' s0'''
      and bisim01'': red0-Red1'.mbisim s0''' s1''
      and dead0: red0-mthr.deadlock P s0''' by auto
    from  $\langle$ red0-mthr.mthr.silent-moves P s0' s0'' $\rangle$   $\langle$ red0-mthr.mthr.silent-moves P s0'' s0''' $\rangle$ 
    have red0-mthr.mthr.silent-moves P s0' s0''' by(rule rtranclp-trans)
    from delay-bisimulation-diverge.simulation-silents2[OF b0mthr, OF bisim0 this]
    obtain s'' where red-mthr.mthr.silent-moves P s' s''
      and red-red0.mbisim s'' s0''' by blast
    from FWdelay-bisimulation-diverge.deadlock2-imp- $\tau$ s-deadlock1[OF b0  $\langle$ red-red0.mbisim s'' s0''' $\rangle$ ,
  dead0, of convert-RA]
    obtain s''' where red-mthr.mthr.silent-moves P s'' s'''
      and red-red0.mbisim s''' s0'''

```

```

    and dead: red-mthr.deadlock P s''' by blast
  from ⟨red-mthr.mthr.silent-moves P s' s''⟩ ⟨red-mthr.mthr.silent-moves P s'' s'''⟩
  have red-mthr.mthr.silent-moves P s' s''' by(rule rtranclp-trans)
  hence s''' = s' using red-mthr.mthr.τRuns-terminal-stuck[OF red ⟨tfinite ξ⟩ ⟨terminal ξ = [s']⟩]
    by(cases rule: converse-rtranclpE) fastforce+
  with dead have red-mthr.deadlock P s' by(simp)
  hence ∃ s'. tfinite ξ ∧ terminal ξ = [s'] ∧ red-mthr.deadlock P s' ∧ bisim.J2JVM s' cs'
    using ⟨tfinite ξ⟩ ⟨terminal ξ = [s']⟩ bisim' by blast }
  moreover {
    assume tfinite ξ' and terminal ξ' = None
    hence tfinite ξ ∧ terminal ξ = None using tlsim tlist-all2-tfiniteD[OF tlsim]
      by(cases terminal ξ)(auto dest: tlist-all2-tfinite2-terminalD) }
  moreover {
    assume ¬ tfinite ξ'
    hence ¬ tfinite ξ using tlsim by(blast dest: tlist-all2-tfiniteD) }
  ultimately show thesis by(rule that)
qed

end

declare compP1-def [simp]

theorem wt-J2JVM: wf-J-prog P ⇒ wf-jvm-prog (J2JVM P)
unfolding J2JVM-def o-def
by(rule wt-compP2)(rule compP1-pres-wf)

end
theory Preprocessor
imports
  PCompiler
  ../J/Annotate
  ../J/JWellForm
begin

primrec annotate-Mb ::
  'addr J-prog ⇒ cname ⇒ mname ⇒ ty list ⇒ ty ⇒ (vname list × 'addr expr) ⇒ (vname list ×
'addr expr)
where annotate-Mb P C M Ts T (pns, e) = (pns, annotate P [this # pns [↦] Class C # Ts] e)
declare annotate-Mb.simps [simp del]

primrec annotate-Mb-code ::
  'addr J-prog ⇒ cname ⇒ mname ⇒ ty list ⇒ ty ⇒ (vname list × 'addr expr) ⇒ (vname list ×
'addr expr)
where annotate-Mb-code P C M Ts T (pns, e) = (pns, annotate-code P [this # pns [↦] Class C #
Ts] e)
declare annotate-Mb-code.simps [simp del]

definition annotate-prog :: 'addr J-prog ⇒ 'addr J-prog
where annotate-prog P = compP (annotate-Mb P) P

definition annotate-prog-code :: 'addr J-prog ⇒ 'addr J-prog
where annotate-prog-code P = compP (annotate-Mb-code P) P

lemma fixes is-lub

```

```

shows WT-compP:  $is-lub, P, E \vdash e :: T \implies is-lub, compP f P, E \vdash e :: T$ 
and WTs-compP:  $is-lub, P, E \vdash es [::] Ts \implies is-lub, compP f P, E \vdash es [::] Ts$ 
proof(induct rule: WT-WTs.inducts)
  case (WTCall E e U C M Ts T meth D es Ts')
  from  $\langle P \vdash C \text{ sees } M: Ts \rightarrow T = \text{meth in } D \rangle$ 
  have  $compP f P \vdash C \text{ sees } M: Ts \rightarrow T = \text{map-option } (f D M Ts T) \text{ meth in } D$ 
    by(auto dest: sees-method-compP[where  $f=f$ ])
  with WTCall show ?case by(auto)
qed(auto simp del: fun-upd-apply)

lemma fixes is-lub
  shows Anno-compP:  $is-lub, P, E \vdash e \rightsquigarrow e' \implies is-lub, compP f P, E \vdash e \rightsquigarrow e'$ 
  and Annos-compP:  $is-lub, P, E \vdash es [\rightsquigarrow] es' \implies is-lub, compP f P, E \vdash es [\rightsquigarrow] es'$ 
apply(induct rule: Anno-Annos.inducts)
apply(auto intro: Anno-Annos.intros simp del: fun-upd-apply dest: WT-compP simp add: compC-def)
done

lemma annotate-prog-code-eq-annotate-prog:
  assumes wf: wf-J-prog (annotate-prog-code P)
  shows annotate-prog-code P = annotate-prog P
proof –
  let ?wf-md =  $\lambda - (-, -, -, body). \text{set } (block-types \text{ body}) \subseteq types P$ 
  from wf have wf-prog ?wf-md (annotate-prog-code P)
    unfolding annotate-prog-code-def
    by(rule wf-prog-lift)(auto dest!: WT-block-types-is-type[OF wf[unfolded annotate-prog-code-def]])
  simp add: wf-J-mdecl-def)
  hence wf': wf-prog ?wf-md P
    unfolding annotate-prog-code-def [abs-def]
  proof(rule wf-prog-compPD)
    fix C M Ts T m
    assume  $compP (annotate-Mb-code P) P \vdash C \text{ sees } M: Ts \rightarrow T = \lfloor annotate-Mb-code P C M Ts T m \rfloor$ 
      in C
    and  $wf-mdecl ?wf-md (compP (annotate-Mb-code P) P) C (M, Ts, T, \lfloor annotate-Mb-code P C M Ts T m \rfloor)$ 
    moreover obtain pns body where  $m = (pns, body)$  by(cases m)
    ultimately show  $wf-mdecl ?wf-md P C (M, Ts, T, \lfloor m \rfloor)$ 
      by(fastforce simp add: annotate-Mb-code-def annotate-code-def wf-mdecl-def THE-default-def
the-equality Anno-code-def split: if-split-asm dest: Anno-block-types)
  qed

{ fix C D fs ms M Ts T pns body
  assume  $(C, D, fs, ms) \in set (classes P)$ 
  and  $(M, Ts, T, \lfloor (pns, body) \rfloor) \in set ms$ 
  from  $\langle (C, D, fs, ms) \in set (classes P) \rangle$  have class P C =  $\lfloor (D, fs, ms) \rfloor$  using wf'
    by(cases P)(auto simp add: wf-prog-def dest: map-of-SomeI)
  with wf' have  $P \vdash C \text{ sees } M: Ts \rightarrow T = \lfloor (pns, body) \rfloor$  in C
    using  $\langle (M, Ts, T, \lfloor (pns, body) \rfloor) \in set ms \rangle$  by(rule mdecl-visible)

  from sees-method-compP[OF this, where  $f=annotate-Mb-code P$ ]
  have sees':  $annotate-prog-code P \vdash C \text{ sees } M: Ts \rightarrow T = \lfloor (pns, annotate-code P [this \mapsto Class C,$ 
pns  $\lfloor \mapsto \rfloor Ts \rfloor body) \rfloor$  in C
    unfolding annotate-prog-code-def annotate-Mb-code-def by(auto)
  with wf
  have  $wf-mdecl wf-J-mdecl (annotate-prog-code P) C (M, Ts, T, \lfloor (pns, annotate-code P [this \mapsto$ 
```

```

Class C, pns [↦] Ts] body)])
  by(rule sees-wf-mdecl)
  hence set Ts ⊆ types P by(auto simp add: wf-mdecl-def annotate-prog-code-def)
  moreover from sees have is-class P C by(rule sees-method-is-class)
  moreover from wf' sees have wf-mdecl ?wf-md P C (M, Ts, T, [(pns, body)]) by(rule sees-wf-mdecl)
  hence set (block-types body) ⊆ types P by(simp add: wf-mdecl-def)
  ultimately have ran [this ↦ Class C, pns [↦] Ts] ∪ set (block-types body) ⊆ types P
    by(auto simp add: ran-def wf-mdecl-def map-upds-def split: if-split-asm dest!: map-of-SomeD
set-zip-rightD)
  hence annotate-code P [this ↦ Class C, pns [↦] Ts] body = annotate P [this ↦ Class C, pns [↦]
Ts] body
    unfolding annotate-code-def annotate-def
    by -(rule arg-cong[where f=THE-default body], auto intro!: ext intro: Anno-code-into-Anno[OF
wf'] Anno-into-Anno-code[OF wf'] )
  thus ?thesis unfolding annotate-prog-code-def annotate-prog-def
    by(cases P)(auto simp add: compC-def compM-def annotate-Mb-def annotate-Mb-code-def map-option-case)
qed

end
theory Compiler-Main
imports
  J0
  Correctness
  Preprocessor
begin

end

```

Chapter 8

Memory Models

```
theory MM
imports
  ../Common/Heap
begin

type-synonym addr = nat
type-synonym thread-id = addr

abbreviation (input)
  addr2thread-id :: addr  $\Rightarrow$  thread-id
where addr2thread-id  $\equiv \lambda x. x$ 

abbreviation (input)
  thread-id2addr :: thread-id  $\Rightarrow$  addr
where thread-id2addr  $\equiv \lambda x. x$ 

instantiation nat :: addr begin
definition hash-addr  $\equiv$  int
definition monitor-funfun-to-list  $\equiv$  (funfun-to-list :: nat  $\Rightarrow$  nat  $\Rightarrow$  nat list)
instance
by(intro-classes)(simp add: monitor-funfun-to-list-nat-def)
end

definition new-Addr :: (addr  $\rightarrow$  'b)  $\Rightarrow$  addr option
where new-Addr h  $\equiv$  if  $\exists a. h a = \text{None}$  then Some(LEAST a. h a = None) else None

lemma new-Addr-SomeD:
  new-Addr h = Some a  $\implies$  h a = None
by(auto simp add:new-Addr-def split:if-splits intro: LeastI)

lemma new-Addr-SomeI:
  finite (dom h)  $\implies \exists a. \text{new-Addr } h = \text{Some } a$ 
by(simp add: new-Addr-def) (metis finite-map-freshness infinite-UNIV-nat)

8.0.1 Code generation

definition gen-new-Addr :: (addr  $\rightarrow$  'b)  $\Rightarrow$  addr  $\Rightarrow$  addr option
where gen-new-Addr h n  $\equiv$  if  $\exists a. a \geq n \wedge h a = \text{None}$  then Some(LEAST a. a  $\geq$  n  $\wedge$  h a = None)
else None
```

```

lemma new-Addr-code-code [code]:
  new-Addr h = gen-new-Addr h 0
by(simp add: new-Addr-def gen-new-Addr-def)

```

```

lemma gen-new-Addr-code [code]:
  gen-new-Addr h n = (if h n = None then Some n else gen-new-Addr h (Suc n))
apply(simp add: gen-new-Addr-def)
apply(rule impI)
apply(rule conjI)
apply safe[1]
  apply(auto intro: Least-equality)[2]
apply(rule arg-cong[where f=Least])
apply(rule ext)
apply auto[1]
apply(case-tac n = ab)
  apply simp
  apply simp
apply clarify
apply(subgoal-tac a = n)
  apply simp
  apply(rule Least-equality)
  apply auto[2]
apply(rule ccontr)
apply(erule-tac x=a in allE)
apply simp
done

end

```

8.1 Sequential consistency

```

theory SC
imports
  ../Common/Conform
  MM
begin

```

8.1.1 Objects and Arrays

```

type-synonym
  fields = vname × cname → addr val — field name, defining class, value

```

```

type-synonym
  cells = addr val list

```

```

datatype heapobj
  = Obj cname fields
  — class instance with class name and fields

```

```

| Arr ty fields cells
  — element type, fields (from object), and list of each cell's content

```

```

lemma rec-heapobj [simp]: rec-heapobj = case-heapobj

```

by(*auto intro!*: *ext split*: *heapobj.split*)

primrec *obj-ty* :: *heapobj* \Rightarrow *htype*

where

obj-ty (*Obj C f*) = *Class-type C*
 | *obj-ty* (*Arr T fs cs*) = *Array-type T (length cs)*

fun *is-Arr* :: *heapobj* \Rightarrow *bool* **where**

is-Arr (*Obj C fs*) = *False*
 | *is-Arr* (*Arr T f el*) = *True*

lemma *is-Arr-conv*:

is-Arr arrobj = $(\exists T f el. arrobj = Arr T f el)$
by(*cases arrobj*, *auto*)

lemma *is-ArrE*:

$\llbracket is-Arr arrobj; \bigwedge T f el. arrobj = Arr T f el \implies thesis \rrbracket \implies thesis$
 $\llbracket \neg is-Arr arrobj; \bigwedge C fs. arrobj = Obj C fs \implies thesis \rrbracket \implies thesis$
by(*cases arrobj*, *auto*)⁺

definition *init-fields* :: (*'field-name* \times (*ty* \times *fmod*)) *list* \Rightarrow *'field-name* \rightarrow *addr val*
where *init-fields* \equiv *map-of* \circ *map* ($\lambda(FD, (T, fm)). (FD, default-val T)$)

primrec

— a new, blank object with default values in all fields:

blank :: *'m prog* \Rightarrow *htype* \Rightarrow *heapobj*

where

blank P (Class-type C) = *Obj C (init-fields (fields P C))*
 | *blank P (Array-type T n)* = *Arr T (init-fields (fields P Object)) (replicate n (default-val T))*

lemma *obj-ty-blank* [*iff*]:

obj-ty (blank P hT) = *hT*
by(*cases hT*)(*simp-all*)

8.1.2 Heap

type-synonym *heap* = *addr* \rightarrow *heapobj*

translations

(*type*) *heap* \leq (*type*) *nat* \Rightarrow *heapobj option*

abbreviation *sc-empty* :: *heap*

where *sc-empty* \equiv *Map.empty*

fun *the-obj* :: *heapobj* \Rightarrow *cname* \times *fields* **where**

the-obj (*Obj C fs*) = (*C*, *fs*)

fun *the-arr* :: *heapobj* \Rightarrow *ty* \times *fields* \times *cells* **where**

the-arr (*Arr T f el*) = (*T*, *f*, *el*)

abbreviation

cname-of :: *heap* \Rightarrow *addr* \Rightarrow *cname* **where**
cname-of hp a == *fst (the-obj (the (hp a)))*

definition $sc\text{-allocate} :: 'm\ prog \Rightarrow heap \Rightarrow htype \Rightarrow (heap \times addr)\ set$

where

$$sc\text{-allocate } P\ h\ hT = \\ (case\ new\text{-Addr } h\ of\ None \Rightarrow \{\} \\ | Some\ a \Rightarrow \{(h(a \mapsto blank\ P\ hT),\ a)\})$$

definition $sc\text{-typeof-addr} :: heap \Rightarrow addr \Rightarrow htype\ option$

where $sc\text{-typeof-addr } h\ a = map\text{-option } obj\text{-ty } (h\ a)$

inductive $sc\text{-heap-read} :: heap \Rightarrow addr \Rightarrow addr\text{-loc} \Rightarrow addr\ val \Rightarrow bool$

for $h :: heap$ **and** $a :: addr$

where

$$Obj: \llbracket h\ a = [Obj\ C\ fs]; fs\ (F,\ D) = [v] \rrbracket \Longrightarrow sc\text{-heap-read } h\ a\ (CField\ D\ F)\ v \\ | Arr: \llbracket h\ a = [Arr\ T\ f\ el]; n < length\ el \rrbracket \Longrightarrow sc\text{-heap-read } h\ a\ (ACell\ n)\ (el\ !\ n) \\ | ArrObj: \llbracket h\ a = [Arr\ T\ f\ el]; f\ (F,\ Object) = [v] \rrbracket \Longrightarrow sc\text{-heap-read } h\ a\ (CField\ Object\ F)\ v$$

hide-fact (open) $Obj\ Arr\ ArrObj$

inductive-cases $sc\text{-heap-read-cases } [elim!]:$

$$sc\text{-heap-read } h\ a\ (CField\ C\ F)\ v \\ sc\text{-heap-read } h\ a\ (ACell\ n)\ v$$

inductive $sc\text{-heap-write} :: heap \Rightarrow addr \Rightarrow addr\text{-loc} \Rightarrow addr\ val \Rightarrow heap \Rightarrow bool$

for $h :: heap$ **and** $a :: addr$

where

$$Obj: \llbracket h\ a = [Obj\ C\ fs]; h' = h(a \mapsto Obj\ C\ (fs((F,\ D) \mapsto v))) \rrbracket \Longrightarrow sc\text{-heap-write } h\ a\ (CField\ D\ F)\ v\ h' \\ | Arr: \llbracket h\ a = [Arr\ T\ f\ el]; h' = h(a \mapsto Arr\ T\ f\ (el[n := v])) \rrbracket \Longrightarrow sc\text{-heap-write } h\ a\ (ACell\ n)\ v\ h' \\ | ArrObj: \llbracket h\ a = [Arr\ T\ f\ el]; h' = h(a \mapsto Arr\ T\ (f((F,\ Object) \mapsto v))\ el) \rrbracket \Longrightarrow sc\text{-heap-write } h\ a\ (CField\ Object\ F)\ v\ h'$$

hide-fact (open) $Obj\ Arr\ ArrObj$

inductive-cases $sc\text{-heap-write-cases } [elim!]:$

$$sc\text{-heap-write } h\ a\ (CField\ C\ F)\ v\ h' \\ sc\text{-heap-write } h\ a\ (ACell\ n)\ v\ h'$$

consts $sc\text{-spurious-wakeups} :: bool$

interpretation $sc:$

$$heap\text{-base} \\ addr2thread\text{-id} \\ thread\text{-id}2addr \\ sc\text{-spurious-wakeups} \\ sc\text{-empty} \\ sc\text{-allocate } P \\ sc\text{-typeof-addr} \\ sc\text{-heap-read} \\ sc\text{-heap-write}$$

for P .

Translate notation from $heap\text{-base}$

abbreviation $sc\text{-preallocated} :: 'm\ prog \Rightarrow heap \Rightarrow bool$

where $sc\text{-preallocated} == sc.preallocated\ TYPE('m)$

abbreviation $sc\text{-}start\text{-}tid :: 'md\ prog \Rightarrow thread\text{-}id$
where $sc\text{-}start\text{-}tid \equiv sc.start\text{-}tid\ TYPE('md)$

abbreviation $sc\text{-}start\text{-}heap\text{-}ok :: 'm\ prog \Rightarrow bool$
where $sc\text{-}start\text{-}heap\text{-}ok \equiv sc.start\text{-}heap\text{-}ok\ TYPE('m)$

abbreviation $sc\text{-}start\text{-}heap :: 'm\ prog \Rightarrow heap$
where $sc\text{-}start\text{-}heap \equiv sc.start\text{-}heap\ TYPE('m)$

abbreviation $sc\text{-}start\text{-}state ::$
 $(cname \Rightarrow mname \Rightarrow ty\ list \Rightarrow ty \Rightarrow 'm \Rightarrow addr\ val\ list \Rightarrow 'x)$
 $\Rightarrow 'm\ prog \Rightarrow cname \Rightarrow mname \Rightarrow addr\ val\ list \Rightarrow (addr, thread\text{-}id, 'x, heap, addr)\ state$
where
 $sc\text{-}start\text{-}state\ f\ P \equiv sc.start\text{-}state\ TYPE('m)\ P\ f\ P$

abbreviation $sc\text{-}wf\text{-}start\text{-}state :: 'm\ prog \Rightarrow cname \Rightarrow mname \Rightarrow addr\ val\ list \Rightarrow bool$
where $sc\text{-}wf\text{-}start\text{-}state\ P \equiv sc.wf\text{-}start\text{-}state\ TYPE('m)\ P\ P$

notation $sc.conf\ (\langle -, - \vdash_{sc} - : \leq \rightarrow [51, 51, 51, 51] 50)$
notation $sc.conf\ (\langle -, - \vdash_{sc} - [:\leq] \rightarrow [51, 51, 51, 51] 50)$
notation $sc.hext\ (\langle - \triangleleft_{sc} \rightarrow [51, 51] 50)$

lemma $sc\text{-}start\text{-}heap\text{-}ok: sc\text{-}start\text{-}heap\text{-}ok\ P$
apply($simp\ add: sc.start\text{-}heap\text{-}ok\text{-}def\ sc.start\text{-}heap\text{-}data\text{-}def\ initialization\text{-}list\text{-}def\ sc.create\text{-}initial\text{-}object\text{-}simps$
 $sc.allocate\text{-}def\ sys.xcpts\text{-}list\text{-}def\ case\text{-}option\text{-}conv\text{-}if\ new\text{-}Addr\text{-}SomeI\ del: blank.simps\ split\ del: option.split$
 $if\text{-}split$)
done

lemma $sc\text{-}wf\text{-}start\text{-}state\text{-}iff:$
 $sc\text{-}wf\text{-}start\text{-}state\ P\ C\ M\ vs \longleftrightarrow (\exists\ Ts\ T\ meth\ D. P \vdash C\ sees\ M: Ts \rightarrow T = [meth]\ in\ D \wedge P, sc\text{-}start\text{-}heap$
 $P \vdash_{sc}\ vs\ [:\leq]\ Ts)$
by($simp\ add: sc.wf\text{-}start\text{-}state.simps\ sc\text{-}start\text{-}heap\text{-}ok$)

lemma $sc\text{-}heap:$
 $heap\ addr2thread\text{-}id\ thread\text{-}id2addr\ (sc.allocate\ P)\ sc\text{-}typeof\text{-}addr\ sc\text{-}heap\text{-}write\ P$

proof
fix $h'\ a\ h\ hT$
assume $(h', a) \in sc.allocate\ P\ h\ hT$
thus $sc\text{-}typeof\text{-}addr\ h'\ a = [hT]$
by($auto\ simp\ add: sc.allocate\text{-}def\ sc\text{-}typeof\text{-}addr\text{-}def\ dest: new\text{-}Addr\text{-}SomeD\ split: if\text{-}split\text{-}asm$)
next
fix $h'\ h\ hT\ a$
assume $(h', a) \in sc.allocate\ P\ h\ hT$
from $this[symmetric]$ **show** $h \triangleleft_{sc} h'$
by($fastforce\ simp\ add: sc.allocate\text{-}def\ sc\text{-}typeof\text{-}addr\text{-}def\ sc.hext\text{-}def\ dest: new\text{-}Addr\text{-}SomeD\ intro!:$
 $map\text{-}leI$)
next
fix $h\ a\ al\ v\ h'$
assume $sc\text{-}heap\text{-}write\ h\ a\ al\ v\ h'$
thus $h \triangleleft_{sc} h'$
by($cases\ al$)($auto\ intro!:$ $sc.hextI\ simp\ add: sc\text{-}typeof\text{-}addr\text{-}def$)
qed $simp$

interpretation *sc*:

heap
addr2thread-id
thread-id2addr
sc-spurious-wakeups
sc-empty
sc-allocate P
sc-typeof-addr
sc-heap-read
sc-heap-write
for *P* **by**(*rule sc-heap*)

lemma *sc-heap-new*:

$h\ a = \text{None} \implies h \sqsubseteq_{sc} h(a \mapsto \text{arobj})$

by(*rule sc.heapI*)(*auto simp add: sc-typeof-addr-def dest!: new-Addr-SomeD*)

lemma *sc-heap-upd-obj*: $h\ a = \text{Some } (\text{Obj } C\ fs) \implies h \sqsubseteq_{sc} h(a \mapsto (\text{Obj } C\ fs'))$

by(*rule sc.heapI*)(*auto simp: fun-upd-apply sc-typeof-addr-def*)

lemma *sc-heap-upd-arr*: $\llbracket h\ a = \text{Some } (\text{Arr } T\ f\ e); \text{length } e = \text{length } e' \rrbracket \implies h \sqsubseteq_{sc} h(a \mapsto (\text{Arr } T\ f'\ e'))$

by(*rule sc.heapI*)(*auto simp: fun-upd-apply sc-typeof-addr-def*)

8.1.3 Conformance

definition *sc-fconf* :: $'m\ \text{prog} \Rightarrow \text{cname} \Rightarrow \text{heap} \Rightarrow \text{fields} \Rightarrow \text{bool}$ ($\langle -, - \rangle \vdash_{sc} - \checkmark$) [51,51,51,51] 50)
where $P, C, h \vdash_{sc} fs \checkmark = (\forall F\ D\ T\ fm. P \vdash C\ \text{has } F:T\ (fm)\ \text{in } D \longrightarrow (\exists v. fs(F,D) = \text{Some } v \wedge P, h \vdash_{sc} v : \leq T))$

primrec *sc-oconf* :: $'m\ \text{prog} \Rightarrow \text{heap} \Rightarrow \text{heapobj} \Rightarrow \text{bool}$ ($\langle -, - \rangle \vdash_{sc} - \checkmark$) [51,51,51] 50)

where

$P, h \vdash_{sc} \text{Obj } C\ fs \checkmark \longleftrightarrow \text{is-class } P\ C \wedge P, C, h \vdash_{sc} fs \checkmark$

$| P, h \vdash_{sc} \text{Arr } T\ fs\ el \checkmark \longleftrightarrow \text{is-type } P\ (T[]) \wedge P, \text{Object}, h \vdash_{sc} fs \checkmark \wedge (\forall v \in \text{set } el. P, h \vdash_{sc} v : \leq T)$

definition *sc-hconf* :: $'m\ \text{prog} \Rightarrow \text{heap} \Rightarrow \text{bool}$ ($\langle - \rangle \vdash_{sc} - \checkmark$) [51,51] 50)

where $P \vdash_{sc} h \checkmark \longleftrightarrow (\forall a\ \text{obj}. h\ a = \text{Some } \text{obj} \longrightarrow P, h \vdash_{sc} \text{obj} \checkmark)$

interpretation *sc*: *heap-conf-base*

addr2thread-id
thread-id2addr
sc-spurious-wakeups
sc-empty
sc-allocate P
sc-typeof-addr
sc-heap-read
sc-heap-write
sc-hconf P
P

for *P* .

declare *sc.typeof-addr-thread-id2-addr-addr2thread-id* [*simp del*]

lemma *sc-conf-upd-obj*: $h\ a = \text{Some}(\text{Obj } C\ fs) \implies (P, h(a \mapsto (\text{Obj } C\ fs'))) \vdash_{sc} x : \leq T = (P, h \vdash_{sc} x : \leq T)$

apply (*unfold sc.conf-def*)
apply (*rule val.induct*)
apply (*auto simp:fun-upd-apply*)
apply (*auto simp add: sc-typeof-addr-def split: if-split-asm*)
done

lemma *sc-conf-upd-arr*: $h a = \text{Some}(\text{Arr } T f \text{ el}) \implies (P, h(a \mapsto (\text{Arr } T f' \text{ el}')) \vdash_{sc} x : \leq T') = (P, h \vdash_{sc} x : \leq T')$

apply(*unfold sc.conf-def*)
apply (*rule val.induct*)
apply (*auto simp:fun-upd-apply*)
apply(*auto simp add: sc-typeof-addr-def split: if-split-asm*)
done

lemma *sc-oconf-hext*: $P, h \vdash_{sc} \text{obj } \checkmark \implies h \trianglelefteq_{sc} h' \implies P, h' \vdash_{sc} \text{obj } \checkmark$
by(*cases obj*)(*fastforce elim: sc.conf-hext simp add: sc-fconf-def*)+

lemma *sc-oconf-init-fields*:

assumes $P \vdash C \text{ has-fields FDTs}$
shows $P, h \vdash_{sc} (\text{Obj } C (\text{init-fields FDTs})) \checkmark$
using *assms has-fields-is-class[OF assms]*
by(*auto simp add: has-field-def init-fields-def sc-fconf-def split-def o-def map-of-map[simplified split-def,*
where $f = \lambda p. \text{default-val } (fst p)$ *dest: has-fields-fun*)

lemma *sc-oconf-init*:

is-htype $P hT \implies P, h \vdash_{sc} \text{blank } P hT \checkmark$
by(*cases hT*)(*auto simp add: sc-fconf-def has-field-def init-fields-def split-def o-def map-of-map[simplified split-def,*
where $f = \lambda p. \text{default-val } (fst p)$ *dest: has-fields-fun*)

lemma *sc-oconf-fupd* [*intro?*]:

$\llbracket P \vdash C \text{ has } F:T (fm) \text{ in } D; P, h \vdash_{sc} v : \leq T; P, h \vdash_{sc} (\text{Obj } C fs) \checkmark \rrbracket$
 $\implies P, h \vdash_{sc} (\text{Obj } C (fs((F,D) \mapsto v))) \checkmark$
unfolding *has-field-def*
by(*auto simp add: sc-fconf-def has-field-def dest: has-fields-fun*)

lemma *sc-oconf-fupd-arr* [*intro?*]:

$\llbracket P, h \vdash_{sc} v : \leq T; P, h \vdash_{sc} (\text{Arr } T f \text{ el}) \checkmark \rrbracket$
 $\implies P, h \vdash_{sc} (\text{Arr } T f (\text{el}[i := v])) \checkmark$
by(*auto dest: subsetD[OF set-update-subset-insert]*)

lemma *sc-oconf-fupd-arr-fields*:

$\llbracket P \vdash \text{Object has } F:T (fm) \text{ in Object}; P, h \vdash_{sc} v : \leq T; P, h \vdash_{sc} (\text{Arr } T' f \text{ el}) \checkmark \rrbracket$
 $\implies P, h \vdash_{sc} (\text{Arr } T' (f((F, \text{Object}) \mapsto v)) \text{ el}) \checkmark$
by(*auto dest: has-fields-fun simp add: sc-fconf-def has-field-def*)

lemma *sc-oconf-new*: $\llbracket P, h \vdash_{sc} \text{obj } \checkmark; h a = \text{None} \rrbracket \implies P, h(a \mapsto \text{arrobj}) \vdash_{sc} \text{obj } \checkmark$
by(*erule sc-oconf-hext*)(*rule sc-hext-new*)

lemmas *sc-oconf-upd-obj = sc-oconf-hext* [*OF - sc-hext-upd-obj*]

lemma *sc-oconf-upd-arr*:

assumes $P, h \vdash_{sc} \text{obj } \checkmark$
and $ha: h a = \lfloor \text{Arr } T f \text{ el} \rfloor$
shows $P, h(a \mapsto \text{Arr } T f' \text{ el}') \vdash_{sc} \text{obj } \checkmark$

using *assms*

by(*cases obj*)(*auto simp add: sc-conf-upd-arr*[**where** $h=h$, $OF\ ha$] *sc-fconf-def*)

lemma *sc-hconfD*: $\llbracket P \vdash_{sc} h \checkmark; h\ a = \text{Some } obj \rrbracket \implies P, h \vdash_{sc} obj \checkmark$

unfolding *sc-hconf-def* **by** *blast*

lemmas *sc-preallocated-new* = *sc.preallocated-hext*[$OF - sc-hext-new$]

lemmas *sc-preallocated-upd-obj* = *sc.preallocated-hext* [$OF - sc-hext-upd-obj$]

lemmas *sc-preallocated-upd-arr* = *sc.preallocated-hext* [$OF - sc-hext-upd-arr$]

lemma *sc-hconf-new*: $\llbracket P \vdash_{sc} h \checkmark; h\ a = \text{None}; P, h \vdash_{sc} obj \checkmark \rrbracket \implies P \vdash_{sc} h(a \mapsto obj) \checkmark$

unfolding *sc-hconf-def*

by(*auto intro: sc-oconf-new*)

lemma *sc-hconf-upd-obj*: $\llbracket P \vdash_{sc} h \checkmark; h\ a = \text{Some } (Obj\ C\ fs); P, h \vdash_{sc} (Obj\ C\ fs') \checkmark \rrbracket \implies P \vdash_{sc} h(a \mapsto (Obj\ C\ fs')) \checkmark$

unfolding *sc-hconf-def*

by(*auto intro: sc-oconf-upd-obj simp del: sc-oconf.simps*)

lemma *sc-hconf-upd-arr*: $\llbracket P \vdash_{sc} h \checkmark; h\ a = \text{Some } (Arr\ T\ f\ el); P, h \vdash_{sc} (Arr\ T\ f'\ el') \checkmark \rrbracket \implies P \vdash_{sc} h(a \mapsto (Arr\ T\ f'\ el')) \checkmark$

unfolding *sc-hconf-def*

by(*auto intro: sc-oconf-upd-arr simp del: sc-oconf.simps*)

lemma *sc-heap-conf*:

heap-conf addr2thread-id thread-id2addr sc-empty (sc-allocate P) sc-typeof-addr sc-heap-write (sc-hconf P) P

proof

show $P \vdash_{sc} sc\ empty \checkmark$ **by**(*simp add: sc-hconf-def*)

next

fix $h\ a\ hT$

assume *sc-typeof-addr* $h\ a = [hT]$ $P \vdash_{sc} h \checkmark$

thus *is-htype* $P\ hT$

by(*auto simp add: sc-typeof-addr-def sc-oconf-def dest!: sc-hconfD split: heapobj.split-asm*)

next

fix $h\ h'\ hT\ a$

assume $P \vdash_{sc} h \checkmark$ $(h', a) \in sc\ allocate\ P\ h\ hT$ *is-htype* $P\ hT$

thus $P \vdash_{sc} h' \checkmark$

by(*auto simp add: sc-allocate-def dest!: new-Addr-SomeD intro: sc-hconf-new sc-oconf-init split: if-split-asm*)

next

fix $h\ a\ al\ T\ v\ h'$

assume $P \vdash_{sc} h \checkmark$

and *sc.addr-loc-type* $P\ h\ a\ al\ T$

and $P, h \vdash_{sc} v \leq T$

and *sc-heap-write* $h\ a\ al\ v\ h'$

thus $P \vdash_{sc} h' \checkmark$

by(*cases al*)(*fastforce elim!: sc.addr-loc-type.cases simp add: sc-typeof-addr-def intro: sc-hconf-upd-obj sc-oconf-fupd sc-hconfD sc-hconf-upd-arr sc-oconf-fupd-arr sc-oconf-fupd-arr-fields*)**+**

qed

interpretation *sc: heap-conf*

addr2thread-id

thread-id2addr

sc-spurious-wakeups
sc-empty
sc-allocate P
sc-typeof-addr
sc-heap-read
sc-heap-write
sc-hconf P
P

for *P*

by(*rule sc-heap-conf*)

lemma *sc-heap-progress*:

heap-progress addr2thread-id thread-id2addr sc-empty (sc-allocate P) sc-typeof-addr sc-heap-read sc-heap-write (sc-hconf P) P

proof

fix *h a al T*

assume *hconf: P ⊢_{sc} h √*

and *alt: sc.addr-loc-type P h a al T*

from *alt* **obtain** *arobj* **where** *arobj: h a = [arobj]*

by(*auto elim! sc.addr-loc-type.cases simp add: sc.typeof-addr-def*)

from *alt* **show** $\exists v. sc\text{-heap-read } h \ a \ al \ v \wedge P, h \vdash_{sc} v : \leq T$

proof(*cases*)

case (*addr-loc-type-field U F fm D*)

note [*simp*] = $\langle al = CField \ D \ F \rangle$

show *?thesis*

proof(*cases arobj*)

case (*Obj C' fs*)

with $\langle sc\text{-typeof-addr } h \ a = [U] \rangle \ arobj$

have [*simp*]: *C' = class-type-of U* **by**(*auto simp add: sc.typeof-addr-def*)

from *hconf arobj Obj* **have** *P, h ⊢_{sc} Obj (class-type-of U) fs √* **by**(*auto dest: sc-hconfD*)

with $\langle P \vdash \text{class-type-of } U \text{ has } F:T \ (fm) \ \text{in } D \rangle$ **obtain** *v*

where *fs (F, D) = [v] P, h ⊢_{sc} v : ≤ T* **by**(*fastforce simp add: sc-fconf-def*)

thus *?thesis* **using** *Obj arobj* **by**(*auto intro: sc-heap-read.intros*)

next

case (*Arr T' f el*)

with $\langle sc\text{-typeof-addr } h \ a = [U] \rangle \ arobj$

have [*simp*]: *U = Array-type T' (length el)* **by**(*auto simp add: sc.typeof-addr-def*)

from *hconf arobj Arr* **have** *P, h ⊢_{sc} Arr T' f el √* **by**(*auto dest: sc-hconfD*)

from $\langle P \vdash \text{class-type-of } U \text{ has } F:T \ (fm) \ \text{in } D \rangle$ **have** [*simp*]: *D = Object*

by(*auto dest: has-field-decl-above*)

with $\langle P, h \vdash_{sc} Arr \ T' \ f \ el \ \sqrt{\rangle} \ \langle P \vdash \text{class-type-of } U \text{ has } F:T \ (fm) \ \text{in } D \rangle$

obtain *v* **where** *f (F, Object) = [v] P, h ⊢_{sc} v : ≤ T*

by(*fastforce simp add: sc-fconf-def*)

thus *?thesis* **using** *Arr arobj* **by**(*auto intro: sc-heap-read.intros*)

qed

next

case (*addr-loc-type-cell n' n*)

with *arobj* **obtain** *f el*

where [*simp*]: *arobj = Arr T f el*

by(*cases arobj*)(*auto simp add: sc.typeof-addr-def*)

from *addr-loc-type-cell arobj*

have [*simp*]: *al = ACell n n < length el* **by**(*auto simp add: sc.typeof-addr-def*)

from *hconf arobj* **have** *P, h ⊢_{sc} Arr T f el √* **by**(*auto dest: sc-hconfD*)

hence *P, h ⊢_{sc} el ! n : ≤ T* **by**(*fastforce*)

```

    thus ?thesis using arobj by(fastforce intro: sc-heap-read.intros)
  qed
next
  fix h a al T v
  assume alt: sc.addr-loc-type P h a al T
  from alt obtain arobj where arobj: h a = [arobj]
  by(auto elim!: sc.addr-loc-type.cases simp add: sc.typeof-addr-def)
  thus  $\exists h'$ . sc-heap-write h a al v h' using alt
  by(cases arobj)(fastforce intro: sc-heap-write.intros elim!: sc.addr-loc-type.cases simp add: sc.typeof-addr-def
  dest: has-field-decl-above)+
  qed

```

interpretation *sc: heap-progress*

```

  addr2thread-id
  thread-id2addr
  sc-spurious-wakeups
  sc-empty
  sc-allocate P
  sc.typeof-addr
  sc-heap-read
  sc-heap-write
  sc-hconf P
  P

```

for *P*

by(rule *sc-heap-progress*)

lemma *sc-heap-conf-read*:

*heap-conf-read addr2thread-id thread-id2addr sc-empty (sc-allocate P) sc.typeof-addr sc-heap-read
sc-heap-write (sc-hconf P) P*

proof

```

  fix h a al v T
  assume read: sc-heap-read h a al v
  and alt: sc.addr-loc-type P h a al T
  and hconf:  $P \vdash_{sc} h \checkmark$ 
  thus  $P, h \vdash_{sc} v \leq T$ 

```

by(auto elim!: sc-heap-read.cases sc.addr-loc-type.cases simp add: sc.typeof-addr-def)(fastforce dest!:
sc-hconfD simp add: sc-fconf-def)+

qed

interpretation *sc: heap-conf-read*

```

  addr2thread-id
  thread-id2addr
  sc-spurious-wakeups
  sc-empty
  sc-allocate P
  sc.typeof-addr
  sc-heap-read
  sc-heap-write
  sc-hconf P
  P

```

for *P*

by(rule *sc-heap-conf-read*)

abbreviation *sc-deterministic-heap-ops* :: 'm prog \Rightarrow bool

where *sc-deterministic-heap-ops* \equiv *sc.deterministic-heap-ops* *TYPE*('m)

lemma *sc-deterministic-heap-ops*: \neg *sc-spurious-wakeups* \implies *sc-deterministic-heap-ops* *P*

by(rule *sc.deterministic-heap-opsI*)(auto elim: *sc-heap-read.cases sc-heap-write.cases simp add: sc-allocate-def*)

8.1.4 Code generation

code-pred

(modes: $i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow \text{bool}$, $i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}$)
sc-heap-read .

code-pred

(modes: $i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow \text{bool}$, $i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}$)
sc-heap-write .

lemma *eval-sc-heap-read-i-i-i-o*:

Predicate.eval (*sc-heap-read-i-i-i-o* *h ad al*) = *sc-heap-read* *h ad al*

by(auto elim: *sc-heap-read-i-i-i-oE* intro: *sc-heap-read-i-i-i-oI* intro!: *ext*)

lemma *eval-sc-heap-write-i-i-i-i-o*:

Predicate.eval (*sc-heap-write-i-i-i-i-o* *h ad al v*) = *sc-heap-write* *h ad al v*

by(auto elim: *sc-heap-write-i-i-i-i-oE* intro: *sc-heap-write-i-i-i-i-oI* intro!: *ext*)

end

theory *SC-Interp*

imports

SC
../*Compiler/Correctness*
../*J/Deadlocked*
../*BV/JVMDeadlocked*

begin

Do not interpret these locales, it just takes too long to generate all definitions and theorems.

lemma *sc-J-typesafe*:

J-typesafe *addr2thread-id thread-id2addr sc-empty* (*sc-allocate* *P*) *sc-typeof-addr sc-heap-read sc-heap-write*
(*sc-hconf* *P*) *P*

by *unfold-locales*

lemma *sc-JVM-typesafe*:

JVM-typesafe *addr2thread-id thread-id2addr sc-empty* (*sc-allocate* *P*) *sc-typeof-addr sc-heap-read*
sc-heap-write (*sc-hconf* *P*) *P*

by *unfold-locales*

lemma *compP2-compP1-convs*:

is-type (*compP2* (*compP1* *P*)) = *is-type* *P*
is-class (*compP2* (*compP1* *P*)) = *is-class* *P*
sc.addr-loc-type (*compP2* (*compP1* *P*)) = *sc.addr-loc-type* *P*
sc.conf (*compP2* (*compP1* *P*)) = *sc.conf* *P*

by(*simp-all add: compP2-def heap-base.compP-conf heap-base.compP-addr-loc-type fun-eq-iff split: addr-loc.splits*)

lemma *sc-J-JVM-conf-read*:

J-JVM-conf-read *addr2thread-id thread-id2addr sc-empty* (*sc-allocate* *P*) *sc-typeof-addr sc-heap-read*

```

sc-heap-write (sc-hconf P) P
apply(rule J-JVM-conf-read.intro)
apply(rule J1-JVM-conf-read.intro)
apply(rule JVM-conf-read.intro)
prefer 2
apply(rule JVM-heap-conf.intro)
apply(rule JVM-heap-conf-base'.intro)
apply(unfold compP2-def compP1-def compP-heap compP-heap-conf compP-heap-conf-read)
apply unfold-locales
done

end

```

8.2 Sequential consistency with efficient data structures

```

theory SC-Collections
imports
  ../Common/Conform

  ../Basic/JT-ICF
  MM
begin

```

```

hide-const (open) new-Addr
hide-fact (open) new-Addr-SomeD new-Addr-SomeI

```

8.2.1 Objects and Arrays

```

type-synonym fields = (char, (cname, addr val) lm) tm
type-synonym array-cells = (nat, addr val) rbt
type-synonym array-fields = (vname, addr val) lm

```

```

datatype heapobj
  = Obj cname fields — class instance with class name and fields
  | Arr ty nat array-fields array-cells — element type, size, fields and cell contents

```

```

lemma rec-heapobj [simp]: rec-heapobj = case-heapobj
by(auto intro!: ext split: heapobj.split)

```

```

primrec obj-ty :: heapobj ⇒ htype
where
  obj-ty (Obj c f) = Class-type c
  | obj-ty (Arr t si f e) = Array-type t si

```

```

fun is-Arr :: heapobj ⇒ bool where
  is-Arr (Obj C fs) = False
  | is-Arr (Arr T f si el) = True

```

```

lemma is-Arr-conv:
  is-Arr arrobj = ( $\exists T si f el. arrobj = Arr T si f el$ )
by(cases arrobj, auto)

```

```

lemma is-ArrE:
   $\llbracket is-Arr arrobj; \bigwedge T si f el. arrobj = Arr T si f el \implies thesis \rrbracket \implies thesis$ 

```


$\llbracket \neg \text{is-Arr } \text{arrobj}; \bigwedge C \text{ fs. } \text{arrobj} = \text{Obj } C \text{ fs} \implies \text{thesis} \rrbracket \implies \text{thesis}$
by(cases arrobj, auto)+

definition *init-fields* :: ((vname × cname) × ty) list ⇒ fields

where

init-fields FDTs ≡
 foldr (λ((F, D), T) fields.
 let F' = String.explode F
 in tm-update F' (lm-update D (default-val T)
 (case tm-lookup F' fields of None ⇒ lm-empty () | Some lm ⇒ lm))
 fields)
 FDTs (tm-empty ())

definition *init-fields-array* :: (vname × ty) list ⇒ array-fields

where

init-fields-array ≡ lm.to-map ∘ map (λ(F, T). (F, default-val T))

definition *init-cells* :: ty ⇒ nat ⇒ array-cells

where *init-cells* T n = foldl (λcells i. rm-update i (default-val T) cells) (rm-empty ()) [0..<n]

primrec — a new, blank object with default values in all fields:

blank :: 'm prog ⇒ htype ⇒ heapobj

where

blank P (Class-type C) = Obj C (*init-fields* (map (λ(FD, (T, fm)). (FD, T)) (TypeRel.fields P C)))
 | *blank* P (Array-type T n) =
 Arr T n (*init-fields-array* (map (λ((F, D), (T, fm)). (F, T)) (TypeRel.fields P Object))) (*init-cells*
 T n)

lemma *obj-ty-blank* [iff]: *obj-ty* (*blank* P hT) = hT

by(cases hT) simp-all

8.2.2 Heap

type-synonym *heap* = (addr, heapobj) rbt

translations

(*type*) *heap* <= (*type*) (nat, heapobj) rbt

abbreviation *sc-empty* :: heap

where *sc-empty* ≡ rm-empty ()

fun *the-obj* :: heapobj ⇒ cname × fields **where**

the-obj (Obj C fs) = (C, fs)

fun *the-arr* :: heapobj ⇒ ty × nat × array-fields × array-cells **where**

the-arr (Arr T si f el) = (T, si, f, el)

abbreviation

cname-of :: heap ⇒ addr ⇒ cname **where**

cname-of hp a == fst (*the-obj* (*the* (rm-lookup a hp)))

definition *new-Addr* :: heap ⇒ addr option

where *new-Addr* h = Some (case rm-max h (λ-. True) of None ⇒ 0 | Some (a, -) ⇒ a + 1)

definition $sc\text{-allocate} :: 'm\ prog \Rightarrow heap \Rightarrow htype \Rightarrow (heap \times addr)\ set$

where

$$sc\text{-allocate } P\ h\ hT = \\ (case\ new\text{-Addr } h\ of\ None \Rightarrow \{\} \\ | Some\ a \Rightarrow \{(rm\text{-update } a\ (blank\ P\ hT)\ h,\ a)\})$$

definition $sc\text{-typeof-addr} :: heap \Rightarrow addr \Rightarrow htype\ option$

where $sc\text{-typeof-addr } h\ a = map\text{-option } obj\text{-ty } (rm\text{-lookup } a\ h)$

inductive $sc\text{-heap-read} :: heap \Rightarrow addr \Rightarrow addr\text{-loc} \Rightarrow addr\ val \Rightarrow bool$

for $h :: heap$ **and** $a :: addr$

where

$$Obj: \llbracket rm\text{-lookup } a\ h = \lfloor Obj\ C\ fs \rfloor; tm\text{-lookup } (String.\text{explode } F)\ fs = \lfloor fs' \rfloor; lm\text{-lookup } D\ fs' = \lfloor v \rfloor \rrbracket \\ \Longrightarrow sc\text{-heap-read } h\ a\ (CField\ D\ F)\ v \\ | Arr: \llbracket rm\text{-lookup } a\ h = \lfloor Arr\ T\ si\ f\ el \rfloor; n < si \rrbracket \Longrightarrow sc\text{-heap-read } h\ a\ (ACell\ n)\ (the\ (rm\text{-lookup } n \\ el)) \\ | ArrObj: \llbracket rm\text{-lookup } a\ h = \lfloor Arr\ T\ si\ f\ el \rfloor; lm\text{-lookup } F\ f = \lfloor v \rfloor \rrbracket \Longrightarrow sc\text{-heap-read } h\ a\ (CField\ Object \\ F)\ v$$

hide-fact (open) $Obj\ Arr\ ArrObj$

inductive-cases $sc\text{-heap-read-cases}$ $[elim!]$:

$$sc\text{-heap-read } h\ a\ (CField\ C\ F)\ v \\ sc\text{-heap-read } h\ a\ (ACell\ n)\ v$$

inductive $sc\text{-heap-write} :: heap \Rightarrow addr \Rightarrow addr\text{-loc} \Rightarrow addr\ val \Rightarrow heap \Rightarrow bool$

for $h :: heap$ **and** $a :: addr$

where

$$Obj: \\ \llbracket rm\text{-lookup } a\ h = \lfloor Obj\ C\ fs \rfloor; F' = String.\text{explode } F; \\ h' = rm\text{-update } a\ (Obj\ C\ (tm\text{-update } F'\ (lm\text{-update } D\ v\ (case\ tm\text{-lookup } (String.\text{explode } F)\ fs)\ of \\ None \Rightarrow lm\text{-empty } ()\ | Some\ fs' \Rightarrow fs^{\wedge}))\ fs)\ h \rrbracket \\ \Longrightarrow sc\text{-heap-write } h\ a\ (CField\ D\ F)\ v\ h' \\ | Arr: \\ \llbracket rm\text{-lookup } a\ h = \lfloor Arr\ T\ si\ f\ el \rfloor; h' = rm\text{-update } a\ (Arr\ T\ si\ f\ (rm\text{-update } n\ v\ el))\ h \rrbracket \\ \Longrightarrow sc\text{-heap-write } h\ a\ (ACell\ n)\ v\ h' \\ | ArrObj: \\ \llbracket rm\text{-lookup } a\ h = \lfloor Arr\ T\ si\ f\ el \rfloor; h' = rm\text{-update } a\ (Arr\ T\ si\ (lm\text{-update } F\ v\ f)\ el)\ h \rrbracket \\ \Longrightarrow sc\text{-heap-write } h\ a\ (CField\ Object\ F)\ v\ h'$$

hide-fact (open) $Obj\ Arr\ ArrObj$

inductive-cases $sc\text{-heap-write-cases}$ $[elim!]$:

$$sc\text{-heap-write } h\ a\ (CField\ C\ F)\ v\ h' \\ sc\text{-heap-write } h\ a\ (ACell\ n)\ v\ h'$$

consts $sc\text{-spurious-wakeups} :: bool$

lemma $new\text{-Addr-SomeD}$: $new\text{-Addr } h = \lfloor a \rfloor \Longrightarrow rm\text{-lookup } a\ h = None$

apply($simp\ add: new\text{-Addr-def}$)

apply($drule\ rm.\text{max-None}[OF\ rm.\text{invar}]$)

apply($simp\ add: rm.\text{lookup-correct rel-of-def}$)

```

apply(clarsimp simp add: rm.lookup-correct)
apply(frule rm.max-Some[OF rm.invar])
apply(clarsimp simp add: rel-of-def)
apply(hypsubst-thin)
apply(rule ccontr)
apply(clarsimp)
apply(drule-tac k'=Suc a in rm.max-Some(2)[OF rm.invar])
apply(auto simp add: rel-of-def)
done

```

interpretation *sc*:

```

heap-base
  addr2thread-id
  thread-id2addr
  sc-spurious-wakeups
  sc-empty
  sc-allocate P
  sc-typeof-addr
  sc-heap-read
  sc-heap-write
for P .

```

Translate notation from *heap-base*

abbreviation *sc-preallocated* :: '*m prog* ⇒ *heap* ⇒ *bool*
where *sc-preallocated* == *sc.preallocated TYPE('m)*

abbreviation *sc-start-tid* :: '*md prog* ⇒ *thread-id*
where *sc-start-tid* ≡ *sc.start-tid TYPE('md)*

abbreviation *sc-start-heap-ok* :: '*m prog* ⇒ *bool*
where *sc-start-heap-ok* ≡ *sc.start-heap-ok TYPE('m)*

abbreviation *sc-start-heap* :: '*m prog* ⇒ *heap*
where *sc-start-heap* ≡ *sc.start-heap TYPE('m)*

abbreviation *sc-start-state* ::
(*cname* ⇒ *mname* ⇒ *ty list* ⇒ *ty* ⇒ '*m* ⇒ *addr val list* ⇒ '*x*)
⇒ '*m prog* ⇒ *cname* ⇒ *mname* ⇒ *addr val list* ⇒ (*addr, thread-id, 'x, heap, addr*) *state*
where
sc-start-state f P ≡ *sc.start-state TYPE('m) P f P*

abbreviation *sc-wf-start-state* :: '*m prog* ⇒ *cname* ⇒ *mname* ⇒ *addr val list* ⇒ *bool*
where *sc-wf-start-state P* ≡ *sc.wf-start-state TYPE('m) P P*

notation *sc.conf* (⟨*-*, *⊢sc - :≤* → [51,51,51,51] 50)
notation *sc.conf*s (⟨*-*, *⊢sc - [:≤]* → [51,51,51,51] 50)
notation *sc.hext* (⟨*-* *⊑sc* → [51,51] 50)

lemma *new-Addr-SomeI*: ∃ *a*. *new-Addr h* = *Some a*
by(*simp add: new-Addr-def*)

lemma *sc-start-heap-ok*: *sc-start-heap-ok P*
by(*simp add: sc.start-heap-ok-def sc.start-heap-data-def initialization-list-def sc.create-initial-object-simps*
sc-allocate-def case-option-conv-if new-Addr-SomeI sys-xcpts-list-def del: blank.simps split del: option.split)

if-split)

lemma *sc-wf-start-state-iff*:

sc-wf-start-state $P C M vs \longleftrightarrow (\exists Ts T meth D. P \vdash C \text{ sees } M: Ts \rightarrow T = [meth] \text{ in } D \wedge P, sc\text{-start-heap}$
 $P \vdash_{sc} vs [:\leq] Ts)$

by(*simp add: sc-wf-start-state.simps sc-start-heap-ok*)

lemma *sc-heap*:

heap addr2thread-id thread-id2addr (sc-allocate P) sc-typeof-addr sc-heap-write P

proof

fix $h' a h hT$

assume $(h', a) \in sc\text{-allocate } P h hT$

thus $sc\text{-typeof-addr } h' a = [hT]$

by(*auto simp add: sc-allocate-def sc-typeof-addr-def rm.lookup-correct rm.update-correct dest: new-Addr-SomeD split: if-split-asm*)

next

fix $h h' hT a$

assume $(h', a) \in sc\text{-allocate } P h hT$

from *this[symmetric]* **show** $h \trianglelefteq_{sc} h'$

by(*fastforce simp add: sc-allocate-def sc-typeof-addr-def sc.heap-def rm.lookup-correct rm.update-correct intro!: map-leI dest: new-Addr-SomeD*)

next

fix $h a al v h'$

assume $sc\text{-heap-write } h a al v h'$

thus $h \trianglelefteq_{sc} h'$

by(*cases al*)(*auto intro!: sc.heapI simp add: sc-typeof-addr-def rm.lookup-correct rm.update-correct*)

qed *simp*

interpretation *sc*:

heap

addr2thread-id

thread-id2addr

sc-spurious-wakeups

sc-empty

sc-allocate P

sc-typeof-addr

sc-heap-read

sc-heap-write

P

for P **by**(*rule sc-heap*)

declare *sc.typeof-addr-thread-id2-addr-addr2thread-id* [*simp del*]

lemma *sc-heap-new*:

rm-lookup a h = None $\implies h \trianglelefteq_{sc} rm\text{-update } a \text{ arobj } h$

by(*rule sc.heapI*)(*auto simp add: sc-typeof-addr-def rm.lookup-correct rm.update-correct dest!: new-Addr-SomeD*)

lemma *sc-heap-upd-obj*: $rm\text{-lookup } a h = \text{Some } (Obj C fs) \implies h \trianglelefteq_{sc} rm\text{-update } a (Obj C fs') h$

by(*rule sc.heapI*)(*auto simp: fun-upd-apply sc-typeof-addr-def rm.lookup-correct rm.update-correct*)

lemma *sc-heap-upd-arr*: $\llbracket rm\text{-lookup } a h = \text{Some } (Arr T si f e) \rrbracket \implies h \trianglelefteq_{sc} rm\text{-update } a (Arr T si f' e') h$

by(*rule sc.heapI*)(*auto simp: fun-upd-apply sc-typeof-addr-def rm.lookup-correct rm.update-correct*)

8.2.3 Conformance

definition $sc\text{-}oconf :: 'm\ prog \Rightarrow heap \Rightarrow heapobj \Rightarrow bool$ ($\langle -, - \vdash_{sc} - \sqrt{\rangle}$ [51,51,51] 50)

where

$$\begin{aligned}
 P, h \vdash_{sc} obj \sqrt{} &\equiv \\
 (\text{case } obj \text{ of} & \\
 \quad Obj\ C\ fs \Rightarrow & \\
 \quad \quad is\text{-}class\ P\ C \wedge & \\
 \quad \quad (\forall F\ D\ T\ fm. P \vdash C \text{ has } F:T\ (fm) \text{ in } D \longrightarrow & \\
 \quad \quad \quad (\exists fs' v. tm\text{-}\alpha\ fs\ (String.explode\ F) = Some\ fs' \wedge lm\text{-}\alpha\ fs'\ D = Some\ v \wedge P, h \vdash_{sc} v : \leq T)) & \\
 \quad | Arr\ T\ si\ f\ el \Rightarrow & \\
 \quad \quad is\text{-}type\ P\ (T[]) \wedge (\forall n. n < si \longrightarrow (\exists v. rm\text{-}\alpha\ el\ n = Some\ v \wedge P, h \vdash_{sc} v : \leq T)) \wedge & \\
 \quad \quad (\forall F\ T\ fm. P \vdash Object \text{ has } F:T\ (fm) \text{ in } Object \longrightarrow (\exists v. lm\text{-}lookup\ F\ f = Some\ v \wedge P, h \vdash_{sc} v : \leq & \\
 T))) &
 \end{aligned}$$

definition $sc\text{-}hconf :: 'm\ prog \Rightarrow heap \Rightarrow bool$ ($\langle - \vdash_{sc} - \sqrt{\rangle}$ [51,51] 50)

where $P \vdash_{sc} h \sqrt{} \longleftrightarrow (\forall a\ obj. rm\text{-}\alpha\ h\ a = Some\ obj \longrightarrow P, h \vdash_{sc} obj \sqrt{})$

interpretation sc :

$heap\text{-}conf\text{-}base$
 $addr2thread\text{-}id$
 $thread\text{-}id2addr$
 $sc\text{-}spurious\text{-}wakeups$
 $sc\text{-}empty$
 $sc\text{-}allocate\ P$
 $sc\text{-}typeof\text{-}addr$
 $sc\text{-}heap\text{-}read$
 $sc\text{-}heap\text{-}write$
 $sc\text{-}hconf\ P$
 P
for P

lemma $sc\text{-}conf\text{-}upd\text{-}obj$: $rm\text{-}lookup\ a\ h = Some(Obj\ C\ fs) \Longrightarrow (P, rm\text{-}update\ a\ (Obj\ C\ fs')\ h \vdash_{sc} x : \leq T) = (P, h \vdash_{sc} x : \leq T)$

apply ($unfold\ sc.conf\text{-}def$)

apply ($rule\ val.induct$)

apply ($auto\ simp:fun\text{-}upd\text{-}apply$)

apply ($auto\ simp\ add: sc\text{-}typeof\text{-}addr\text{-}def\ rm.lookup\text{-}correct\ rm.update\text{-}correct\ split: if\text{-}split\text{-}asm$)

done

lemma $sc\text{-}conf\text{-}upd\text{-}arr$:

$rm\text{-}lookup\ a\ h = Some(Arr\ T\ si\ f\ el) \Longrightarrow (P, rm\text{-}update\ a\ (Arr\ T\ si\ f'\ el')\ h \vdash_{sc} x : \leq T') = (P, h \vdash_{sc} x : \leq T')$

apply ($unfold\ sc.conf\text{-}def$)

apply ($rule\ val.induct$)

apply ($auto\ simp:fun\text{-}upd\text{-}apply$)

apply ($auto\ simp\ add: sc\text{-}typeof\text{-}addr\text{-}def\ rm.lookup\text{-}correct\ rm.update\text{-}correct\ split: if\text{-}split\text{-}asm$)

done

lemma $sc\text{-}oconf\text{-}hext$: $P, h \vdash_{sc} obj \sqrt{} \Longrightarrow h \sqsubseteq_{sc} h' \Longrightarrow P, h' \vdash_{sc} obj \sqrt{}$

unfolding $sc\text{-}oconf\text{-}def$

by ($fastforce\ split: heapobj.split\ elim: sc.conf\text{-}hext$)

lemma *map-of-fields-init-fields*:

assumes *map-of FDTs* $(F, D) = \llbracket (T, fm) \rrbracket$
shows $\exists fs' v. tm-\alpha (init-fields (map (\lambda(FD, (T, fm)). (FD, T)) FDTs)) (String.explode F) = \llbracket fs' \rrbracket$
 $\wedge lm-\alpha fs' D = \llbracket v \rrbracket \wedge sc.conf P h v T$
using *assms*
by(*induct FDTs*)(*auto simp add: tm.lookup-correct tm.update-correct lm.update-correct init-fields-def String.explode-inject*)

lemma *sc-oconf-init-fields*:

assumes $P \vdash C$ *has-fields FDTs*
shows $P, h \vdash_{sc} (Obj C (init-fields (map (\lambda(FD, (T, fm)). (FD, T)) FDTs))) \checkmark$
using *assms has-fields-is-class[OF assms] map-of-fields-init-fields[of FDTs]*
by(*fastforce simp add: has-field-def sc-oconf-def dest: has-fields-fun*)

lemma *sc-oconf-init-arr*:

assumes *type: is-type P* $(T \llbracket \cdot \rrbracket)$
shows $P, h \vdash_{sc} Arr T n (init-fields-array (map (\lambda((F, D), (T, fm)). (F, T)) (TypeRel.fields P Object))) (init-cells T n) \checkmark$
proof –
 { **fix** n'
 assume $n' < n$
 { **fix** *rm* **and** $k :: nat$
 assume $\forall i < k. \exists v. rm-\alpha rm i = \llbracket v \rrbracket \wedge sc.conf P h v T$
 with $\langle n' < n \rangle$ **have** $\exists v. rm-\alpha (foldl (\lambda cells i. rm-update i (default-val T) cells) rm [k..<n]) n'$
 $= \llbracket v \rrbracket \wedge sc.conf P h v T$
 by(*induct m≡n-k arbitrary: n k rm*)(*auto simp add: rm.update-correct upt-conv-Cons type*)
 }
 from *this*[*of 0 rm-empty ()*]
 have $\exists v. rm-\alpha (foldl (\lambda cells i. rm-update i (default-val T) cells) (rm-empty ()) [0..<n]) n' = \llbracket v \rrbracket$
 $\wedge sc.conf P h v T$ **by** *simp*
 }
moreover
 { **fix** $F T fm$
 assume $P \vdash Object$ *has F:T (fm) in Object*
 then obtain *FDTs* **where** *has: P ⊢ Object has-fields FDTs*
 and *FDTs: map-of FDTs (F, Object) = ⌊(T, fm)⌋*
 by(*auto simp add: has-field-def*)
 from *has* **have** *snd 'fst ' set FDTs ⊆ {Object}* **by**(*rule Object-has-fields-Object*)
 with *FDTs* **have** *map-of (map ((λ(F, T). (F, default-val T)) ∘ (λ((F, D), T, fm). (F, T))) FDTs)*
 $F = \llbracket default-val T \rrbracket$
 by(*induct FDTs*) *auto*
 with *has FDTs*
 have $\exists v. lm-lookup F (init-fields-array (map (\lambda((F, D), T, fm). (F, T)) (TypeRel.fields P Object)))$
 $= \llbracket v \rrbracket \wedge$
 $sc.conf P h v T$
 by(*auto simp add: init-fields-array-def lm-correct has-field-def*)
 }
ultimately show *?thesis* **using** *type* **by**(*auto simp add: sc-oconf-def init-cells-def*)
qed

lemma *sc-oconf-fupd* [*intro?*]:

$\llbracket P \vdash C$ *has F:T (fm) in D*; $P, h \vdash_{sc} v : \leq T$; $P, h \vdash_{sc} (Obj C fs) \checkmark$;
 $fs' = (case tm-lookup (String.explode F) fs of None \Rightarrow lm-empty () \mid Some fs' \Rightarrow fs') \rrbracket$
 $\Longrightarrow P, h \vdash_{sc} (Obj C (tm-update (String.explode F) (lm-update D v fs') fs)) \checkmark$

unfolding *sc-oconf-def has-field-def*
apply(*auto dest: has-fields-fun simp add: lm.update-correct tm.update-correct tm.lookup-correct String.explode-inject*)
apply(*drule (1) has-fields-fun, fastforce*)
apply(*drule (1) has-fields-fun, fastforce*)
done

lemma *sc-oconf-fupd-arr [intro?]*:
 $\llbracket P, h \vdash_{sc} v : \leq T; P, h \vdash_{sc} (Arr\ T\ si\ f\ el) \checkmark \rrbracket$
 $\implies P, h \vdash_{sc} (Arr\ T\ si\ f\ (rm\ update\ i\ v\ el)) \checkmark$

unfolding *sc-oconf-def*
by(*auto simp add: rm.update-correct*)

lemma *sc-oconf-fupd-arr-fields*:
 $\llbracket P \vdash Object\ has\ F:T\ (fm)\ in\ Object; P, h \vdash_{sc} v : \leq T; P, h \vdash_{sc} (Arr\ T'\ si\ f\ el) \checkmark \rrbracket$
 $\implies P, h \vdash_{sc} (Arr\ T'\ si\ (lm\ update\ F\ v\ f)\ el) \checkmark$

unfolding *sc-oconf-def* **by**(*auto dest: has-field-fun simp add: lm-correct*)

lemma *sc-oconf-new*: $\llbracket P, h \vdash_{sc} obj \checkmark; rm\ lookup\ a\ h = None \rrbracket \implies P, rm\ update\ a\ arrobj\ h \vdash_{sc} obj \checkmark$
by(*erule sc-oconf-heat*)(*rule sc-heat-new*)

lemmas *sc-oconf-upd-obj = sc-oconf-heat [OF - sc-heat-upd-obj]*

lemma *sc-oconf-upd-arr*:
assumes $P, h \vdash_{sc} obj \checkmark$
and $ha: rm\ lookup\ a\ h = \lfloor Arr\ T\ si\ f\ el \rfloor$
shows $P, rm\ update\ a\ (Arr\ T\ si\ f'\ el')\ h \vdash_{sc} obj \checkmark$

using *assms*

by(*fastforce simp add: sc-oconf-def sc-conf-upd-arr[OF ha] split: heapobj.split*)

lemma *sc-oconf-blank: is-htype P hT* $\implies P, h \vdash_{sc} blank\ P\ hT \checkmark$

apply(*cases hT*)

apply(*fastforce dest: map-of-fields-init-fields simp add: has-field-def sc-oconf-def*)

by(*auto intro: sc-oconf-init-arr*)

lemma *sc-hconfD*: $\llbracket P \vdash_{sc} h \checkmark; rm\ lookup\ a\ h = Some\ obj \rrbracket \implies P, h \vdash_{sc} obj \checkmark$

unfolding *sc-hconf-def* **by**(*auto simp add: rm.lookup-correct*)

lemmas *sc-preallocated-new = sc.preallocated-heat[OF - sc-heat-new]*

lemmas *sc-preallocated-upd-obj = sc.preallocated-heat [OF - sc-heat-upd-obj]*

lemmas *sc-preallocated-upd-arr = sc.preallocated-heat [OF - sc-heat-upd-arr]*

lemma *sc-hconf-new*: $\llbracket P \vdash_{sc} h \checkmark; rm\ lookup\ a\ h = None; P, h \vdash_{sc} obj \checkmark \rrbracket \implies P \vdash_{sc} rm\ update\ a\ obj\ h \checkmark$

unfolding *sc-hconf-def*

by(*auto intro: sc-oconf-new simp add: rm.lookup-correct rm.update-correct*)

lemma *sc-hconf-upd-obj*: $\llbracket P \vdash_{sc} h \checkmark; rm\ lookup\ a\ h = Some\ (Obj\ C\ fs); P, h \vdash_{sc} (Obj\ C\ fs') \checkmark \rrbracket$
 $\implies P \vdash_{sc} rm\ update\ a\ (Obj\ C\ fs')\ h \checkmark$

unfolding *sc-hconf-def*

by(*auto intro: sc-oconf-upd-obj simp add: rm.lookup-correct rm.update-correct*)

lemma *sc-hconf-upd-arr*: $\llbracket P \vdash_{sc} h \checkmark; rm\ lookup\ a\ h = Some(Arr\ T\ si\ f\ el); P, h \vdash_{sc} (Arr\ T\ si\ f'\ el') \checkmark \rrbracket \implies P \vdash_{sc} rm\ update\ a\ (Arr\ T\ si\ f'\ el')\ h \checkmark$

unfolding *sc-hconf-def*

by(*auto intro: sc-oconf-upd-arr simp add: rm.lookup-correct rm.update-correct*)

lemma *sc-heap-conf*:

heap-conf addr2thread-id thread-id2addr sc-empty (sc-allocate P) sc-typeof-addr sc-heap-write (sc-hconf P) P

proof

show $P \vdash_{sc} sc\text{-empty} \checkmark$ **by**(*simp add: sc-hconf-def rm.empty-correct*)

next

fix $h a hT$

assume *sc-typeof-addr* $h a = [hT]$ $P \vdash_{sc} h \checkmark$

thus *is-htype* $P hT$

by(*auto simp add: sc-typeof-addr-def sc-oconf-def dest!: sc-hconfD split: heapobj.split-asm*)

next

fix $h' hT h a$

assume $P \vdash_{sc} h \checkmark (h', a) \in sc\text{-allocate } P h hT$ *is-htype* $P hT$

thus $P \vdash_{sc} h' \checkmark$

by(*auto simp add: sc-allocate-def dest!: new-Addr-SomeD intro: sc-hconf-new sc-oconf-blank split: if-split-asm*)

next

fix $h a al T v h'$

assume $P \vdash_{sc} h \checkmark$

and *sc.addr-loc-type* $P h a al T$

and $P, h \vdash_{sc} v \leq T$

and *sc-heap-write* $h a al v h'$

thus $P \vdash_{sc} h' \checkmark$

by(*cases al*)(*fastforce elim!: sc.addr-loc-type.cases simp add: sc-typeof-addr-def intro: sc-hconf-upd-obj sc-oconf-fupd sc-hconfD sc-hconf-upd-arr sc-oconf-fupd-arr sc-oconf-fupd-arr-fields*)+

qed

interpretation *sc*:

heap-conf

addr2thread-id

thread-id2addr

sc-spurious-wakeups

sc-empty

sc-allocate P

sc-typeof-addr

sc-heap-read

sc-heap-write

sc-hconf P

P

for P

by(*rule sc-heap-conf*)

lemma *sc-heap-progress*:

heap-progress addr2thread-id thread-id2addr sc-empty (sc-allocate P) sc-typeof-addr sc-heap-read sc-heap-write (sc-hconf P) P

proof

fix $h a al T$

assume *hconf*: $P \vdash_{sc} h \checkmark$

and *alt*: *sc.addr-loc-type* $P h a al T$

from *alt* **obtain** *arobj* **where** *arobj*: *rm.lookup* $a h = [arobj]$

by(*auto elim!: sc.addr-loc-type.cases simp add: sc-typeof-addr-def*)


```

from alt show  $\exists v. \text{sc-heap-read } h \ a \ al \ v \wedge P, h \vdash_{sc} v : \leq T$ 
proof(cases)
  case (addr-loc-type-field  $U \ F \ fm \ D$ )
    note [simp] =  $\langle al = CField \ D \ F \rangle$ 
    show ?thesis
    proof(cases arrobj)
      case (Obj  $C' \ fs$ )
        with  $\langle \text{sc-typeof-addr } h \ a = \lfloor U \rfloor \rangle$  arrobj
        have [simp]:  $C' = \text{class-type-of } U$  by(auto simp add: sc-typeof-addr-def)
        from hconf arrobj Obj have  $P, h \vdash_{sc} Obj \ (\text{class-type-of } U) \ fs \ \surd$  by(auto dest: sc-hconfD)
        with  $\langle P \vdash \text{class-type-of } U \text{ has } F:T \ (fm) \ \text{in } D \rangle$  obtain  $fs' \ v$ 
        where  $tm\text{-lookup } (String.\text{explode } F) \ fs = \lfloor fs' \rfloor \ \text{lm-lookup } D \ fs' = \lfloor v \rfloor \ P, h \vdash_{sc} v : \leq T$ 
        by(fastforce simp add: sc-oconf-def tm.lookup-correct lm.lookup-correct)
        thus ?thesis using Obj arrobj by(auto intro: sc-heap-read.intros)
      next
        case (Arr  $T' \ si \ f \ el$ )
          with  $\langle \text{sc-typeof-addr } h \ a = \lfloor U \rfloor \rangle$  arrobj
          have [simp]:  $U = \text{Array-type } T' \ si$  by(auto simp add: sc-typeof-addr-def)
          from hconf arrobj Arr have  $P, h \vdash_{sc} Arr \ T' \ si \ f \ el \ \surd$  by(auto dest: sc-hconfD)
          from  $\langle P \vdash \text{class-type-of } U \text{ has } F:T \ (fm) \ \text{in } D \rangle$  have [simp]:  $D = \text{Object}$ 
            by(auto dest: has-field-decl-above)
          with  $\langle P, h \vdash_{sc} Arr \ T' \ si \ f \ el \ \surd \rangle \ \langle P \vdash \text{class-type-of } U \text{ has } F:T \ (fm) \ \text{in } D \rangle$ 
          obtain  $v$  where  $lm\text{-lookup } F \ f = \lfloor v \rfloor \ P, h \vdash_{sc} v : \leq T$ 
            by(fastforce simp add: sc-oconf-def)
          thus ?thesis using Arr arrobj by(auto intro: sc-heap-read.intros)
        qed
      next
        case (addr-loc-type-cell  $n' \ n$ )
          with arrobj obtain  $si \ f \ el$ 
            where [simp]:  $arrobj = Arr \ T \ si \ f \ el$ 
            by(cases arrobj)(auto simp add: sc-typeof-addr-def)
          from addr-loc-type-cell arrobj
          have [simp]:  $al = ACell \ n$  and  $n: n < si$  by(auto simp add: sc-typeof-addr-def)
          from hconf arrobj have  $P, h \vdash_{sc} Arr \ T \ si \ f \ el \ \surd$  by(auto dest: sc-hconfD)
          with  $n$  obtain  $v$  where  $rm\text{-lookup } n \ el = \lfloor v \rfloor \ P, h \vdash_{sc} v : \leq T$ 
            by(fastforce simp add: sc-oconf-def rm.lookup-correct)
          thus ?thesis using arrobj  $n$  by(fastforce intro: sc-heap-read.intros)
        qed
      next
        fix  $h \ a \ al \ T \ v$ 
        assume alt:  $\text{sc.addr-loc-type } P \ h \ a \ al \ T$ 
        from alt obtain arrobj where arrobj:  $rm\text{-lookup } a \ h = \lfloor arrobj \rfloor$ 
          by(auto elim!: sc.addr-loc-type.cases simp add: sc-typeof-addr-def)
        thus  $\exists h'. \text{sc-heap-write } h \ a \ al \ v \ h'$  using alt
          by(cases arrobj)(fastforce intro: sc-heap-write.intros elim!: sc.addr-loc-type.cases simp add: sc-typeof-addr-def
            dest: has-field-decl-above)+
        qed

```

interpretation sc:

```

heap-progress
addr2thread-id
thread-id2addr
sc-spurious-wakeups
sc-empty

```

```

  sc-allocate P
  sc-typeof-addr
  sc-heap-read
  sc-heap-write
  sc-hconf P
  P
for P
by(rule sc-heap-progress)

```

lemma *sc-heap-conf-read*:

```

  heap-conf-read addr2thread-id thread-id2addr sc-empty (sc-allocate P) sc-typeof-addr sc-heap-read
  sc-heap-write (sc-hconf P) P

```

proof

```

  fix h a al v T
  assume read: sc-heap-read h a al v
  and alt: sc.addr-loc-type P h a al T
  and hconf: P ⊢sc h √
  thus P, h ⊢sc v :≤ T
  apply(auto elim!: sc-heap-read.cases sc.addr-loc-type.cases simp add: sc-typeof-addr-def)
  apply(fastforce dest!: sc-hconfD simp add: sc-oconf-def tm.lookup-correct lm.lookup-correct rm.lookup-correct)+
  done

```

qed

interpretation *sc*:

```

  heap-conf-read
  addr2thread-id
  thread-id2addr
  sc-spurious-wakeups
  sc-empty
  sc-allocate P
  sc-typeof-addr
  sc-heap-read
  sc-heap-write
  sc-hconf P
  P

```

for *P*

by(rule sc-heap-conf-read)

abbreviation *sc-deterministic-heap-ops* :: 'm prog ⇒ bool

where *sc-deterministic-heap-ops* ≡ *sc.deterministic-heap-ops* TYPE('m)

lemma *sc-deterministic-heap-ops*: ¬ *sc-spurious-wakeups* ⇒ *sc-deterministic-heap-ops* *P*

by(rule *sc.deterministic-heap-opsI*)(auto elim: sc-heap-read.cases sc-heap-write.cases simp add: sc-allocate-def)

8.2.4 Code generation

code-pred

```

  (modes: i ⇒ i ⇒ i ⇒ i ⇒ bool, i ⇒ i ⇒ i ⇒ o ⇒ bool)
  sc-heap-read .

```

code-pred

```

  (modes: i ⇒ i ⇒ i ⇒ i ⇒ i ⇒ bool, i ⇒ i ⇒ i ⇒ i ⇒ o ⇒ bool)
  sc-heap-write .

```

lemma *eval-sc-heap-read-i-i-i-o*:

Predicate.eval (sc-heap-read-i-i-i-o h ad al) = sc-heap-read h ad al
by(*auto elim: sc-heap-read-i-i-i-oE intro: sc-heap-read-i-i-i-oI intro!: ext*)

lemma *eval-sc-heap-write-i-i-i-i-o*:

Predicate.eval (sc-heap-write-i-i-i-i-o h ad al v) = sc-heap-write h ad al v
by(*auto elim: sc-heap-write-i-i-i-i-oE intro: sc-heap-write-i-i-i-i-oI intro!: ext*)

end

8.3 Orders as predicates

theory *Orders*

imports

Main

begin

8.3.1 Preliminaries

transfer *refl-on* et al. from *HOL.Relation* to predicates

abbreviation *refl-onP* :: *'a set* \Rightarrow (*'a* \Rightarrow *'a* \Rightarrow *bool*) \Rightarrow *bool*
where *refl-onP* *A r* \equiv *refl-on* *A* {(*x, y*). *r x y*}

abbreviation *reflP* :: (*'a* \Rightarrow *'a* \Rightarrow *bool*) \Rightarrow *bool*
where *reflP* == *refl-onP UNIV*

abbreviation *symP* :: (*'a* \Rightarrow *'a* \Rightarrow *bool*) \Rightarrow *bool*
where *symP* *r* \equiv *sym* {(*x, y*). *r x y*}

abbreviation *total-onP* :: *'a set* \Rightarrow (*'a* \Rightarrow *'a* \Rightarrow *bool*) \Rightarrow *bool*
where *total-onP* *A r* \equiv *total-on* *A* {(*x, y*). *r x y*}

abbreviation *irreflP* :: (*'a* \Rightarrow *'a* \Rightarrow *bool*) \Rightarrow *bool*
where *irreflP* *r* == *irrefl* {(*x, y*). *r x y*}

definition *irreflclp* :: (*'a* \Rightarrow *'a* \Rightarrow *bool*) \Rightarrow *'a* \Rightarrow *'a* \Rightarrow *bool* ($\langle \cdot \neq \cdot \rangle$ [1000] 1000)
where *r[≠]* *a b* = (*r a b* \wedge *a* \neq *b*)

definition *porder-on* :: *'a set* \Rightarrow (*'a* \Rightarrow *'a* \Rightarrow *bool*) \Rightarrow *bool*
where *porder-on* *A r* \iff *refl-onP* *A r* \wedge *transp* *r* \wedge *antisymp* *r*

definition *torder-on* :: *'a set* \Rightarrow (*'a* \Rightarrow *'a* \Rightarrow *bool*) \Rightarrow *bool*
where *torder-on* *A r* \iff *porder-on* *A r* \wedge *total-onP* *A r*

definition *order-consistent* :: (*'a* \Rightarrow *'a* \Rightarrow *bool*) \Rightarrow (*'a* \Rightarrow *'a* \Rightarrow *bool*) \Rightarrow *bool*
where *order-consistent* *r s* \iff (\forall *a a'*. *r a a'* \wedge *s a' a* \longrightarrow *a = a'*)

definition *restrictP* :: (*'a* \Rightarrow *'a* \Rightarrow *bool*) \Rightarrow *'a set* \Rightarrow *'a* \Rightarrow *'a* \Rightarrow *bool* (**infixl** $\langle | \cdot \rangle$ 110)
where (*r* | *A*) *a b* \iff *r a b* \wedge *a* \in *A* \wedge *b* \in *A*

definition *inv-imageP* :: (*'a* \Rightarrow *'a* \Rightarrow *bool*) \Rightarrow (*'b* \Rightarrow *'a*) \Rightarrow *'b* \Rightarrow *'b* \Rightarrow *bool*
where [*iff*]: *inv-imageP* *r f a b* \iff *r (f a) (f b)*

lemma *refl-onPI*: $(\bigwedge a a'. r a a' \implies a \in A \wedge a' \in A) \implies (\bigwedge a. a : A \implies r a a) \implies \text{refl-onP } A r$
by(rule *refl-onI*)(*auto*)

lemma *refl-onPD*: $\text{refl-onP } A r \implies a : A \implies r a a$
by(*drule* (1) *refl-onD*)(*simp*)

lemma *refl-onPD1*: $\text{refl-onP } A r \implies r a b \implies a : A$
by(*erule* *refl-onD1*)(*simp*)

lemma *refl-onPD2*: $\text{refl-onP } A r \implies r a b \implies b : A$
by(*erule* *refl-onD2*)(*simp*)

lemma *refl-onP-Int*: $\text{refl-onP } A r \implies \text{refl-onP } B s \implies \text{refl-onP } (A \cap B) (\lambda a a'. r a a' \wedge s a a')$
by(*drule* (1) *refl-on-Int*)(*simp* *add*: *split-def inf-fun-def inf-set-def*)

lemma *refl-onP-Un*: $\text{refl-onP } A r \implies \text{refl-onP } B s \implies \text{refl-onP } (A \cup B) (\lambda a a'. r a a' \vee s a a')$
by(*drule* (1) *refl-on-Un*)(*simp* *add*: *split-def sup-fun-def sup-set-def*)

lemma *refl-onP-empty*[*simp*]: $\text{refl-onP } \{ \} (\lambda a a'. \text{False})$
unfolding *split-def* **by** *simp*

lemma *refl-onP-tranclp*:

assumes *refl-onP* $A r$

shows $\text{refl-onP } A r^{\hat{++}}$

proof(rule *refl-onPI*)

fix $a a'$

assume $r^{\hat{++}} a a'$

thus $a \in A \wedge a' \in A$

by(*induct*)(*blast* *intro*: *refl-onPD1*[*OF* *assms*] *refl-onPD2*[*OF* *assms*])+

next

fix a

assume $a \in A$

from *refl-onPD*[*OF* *assms* *this*] **show** $r^{\hat{++}} a a ..$

qed

lemma *irreflPI*: $(\bigwedge a. \neg r a a) \implies \text{irreflP } r$
unfolding *irrefl-def* **by** *blast*

lemma *irreflPE*:

assumes *irreflP* r

obtains $\forall a. \neg r a a$

using *assms* **unfolding** *irrefl-def* **by** *blast*

lemma *irreflPD*: $\llbracket \text{irreflP } r; r a a \rrbracket \implies \text{False}$
unfolding *irrefl-def* **by** *blast*

lemma *irreflclpD*:

$r^{\neq} a b \implies r a b \wedge a \neq b$

by(*simp* *add*: *irreflclp-def*)

lemma *irreflclpI* [*intro!*]:

$\llbracket r a b; a \neq b \rrbracket \implies r^{\neq} a b$

by(*simp* *add*: *irreflclp-def*)

lemma *irreflclpE* [*elim!*]:

assumes $r \neq a \ b$

obtains $r \ a \ b \ a \neq b$

using *assms* **by** (*simp add: irreflclp-def*)

lemma *transPI*: $(\bigwedge x \ y \ z. \llbracket r \ x \ y; r \ y \ z \rrbracket \Longrightarrow r \ x \ z) \Longrightarrow \text{transp } r$

by (*fact transpI*)

lemma *transPD*: $\llbracket \text{transp } r; r \ x \ y; r \ y \ z \rrbracket \Longrightarrow r \ x \ z$

by (*fact transpD*)

lemma *transP-tranclp*: $\text{transp } r \hat{++}$

by (*fact trans-trancl [to-pred]*)

lemma *antisymPI*: $(\bigwedge x \ y. \llbracket r \ x \ y; r \ y \ x \rrbracket \Longrightarrow x = y) \Longrightarrow \text{antisymp } r$

by (*fact antisymPI*)

lemma *antisymPD*: $\llbracket \text{antisymp } r; r \ a \ b; r \ b \ a \rrbracket \Longrightarrow a = b$

by (*fact antisymPD*)

lemma *antisym-subset*:

$\llbracket \text{antisymp } r; \bigwedge a \ a'. s \ a \ a' \Longrightarrow r \ a \ a' \rrbracket \Longrightarrow \text{antisymp } s$

by (*blast intro: antisymp-less-eq [of s r]*)

lemma *symPI*: $(\bigwedge x \ y. r \ x \ y \Longrightarrow r \ y \ x) \Longrightarrow \text{symP } r$

by (*rule symI*)(*blast*)

lemma *symPD*: $\llbracket \text{symP } r; r \ x \ y \rrbracket \Longrightarrow r \ y \ x$

by (*blast dest: symD*)

8.3.2 Easy properties

lemma *porder-onI*:

$\llbracket \text{refl-onP } A \ r; \text{antisymp } r; \text{transp } r \rrbracket \Longrightarrow \text{porder-on } A \ r$

unfolding *porder-on-def* **by** *blast*

lemma *porder-onE*:

assumes *porder-on* $A \ r$

obtains *refl-onP* $A \ r$ *antisymp* r *transp* r

using *assms* **unfolding** *porder-on-def* **by** *blast*

lemma *torder-onI*:

$\llbracket \text{porder-on } A \ r; \text{total-onP } A \ r \rrbracket \Longrightarrow \text{torder-on } A \ r$

unfolding *torder-on-def* **by** *blast*

lemma *torder-onE*:

assumes *torder-on* $A \ r$

obtains *porder-on* $A \ r$ *total-onP* $A \ r$

using *assms* **unfolding** *torder-on-def* **by** *blast*

lemma *total-onI*:

$(\bigwedge x \ y. \llbracket x \in A; y \in A \rrbracket \Longrightarrow (x, y) \in r \vee x = y \vee (y, x) \in r) \Longrightarrow \text{total-on } A \ r$

unfolding *total-on-def* **by** *blast*

lemma total-onPI:

$(\bigwedge x y. \llbracket x \in A; y \in A \rrbracket \implies r x y \vee x = y \vee r y x) \implies \text{total-onP } A r$
by(rule total-onI) simp

lemma total-onD:

$\llbracket \text{total-on } A r; x \in A; y \in A \rrbracket \implies (x, y) \in r \vee x = y \vee (y, x) \in r$
unfolding total-on-def by blast

lemma total-onPD:

$\llbracket \text{total-onP } A r; x \in A; y \in A \rrbracket \implies r x y \vee x = y \vee r y x$
by(drule (2) total-onD) blast

8.3.3 Order consistency

lemma order-consistentI:

$(\bigwedge a a'. \llbracket r a a'; s a' a \rrbracket \implies a = a') \implies \text{order-consistent } r s$
unfolding order-consistent-def by blast

lemma order-consistentD:

$\llbracket \text{order-consistent } r s; r a a'; s a' a \rrbracket \implies a = a'$
unfolding order-consistent-def by blast

lemma order-consistent-subset:

$\llbracket \text{order-consistent } r s; \bigwedge a a'. r' a a' \implies r a a'; \bigwedge a a'. s' a a' \implies s a a' \rrbracket \implies \text{order-consistent } r' s'$
by(blast intro: order-consistentI order-consistentD)

lemma order-consistent-sym:

$\text{order-consistent } r s \implies \text{order-consistent } s r$
by(blast intro: order-consistentI dest: order-consistentD)

lemma antisym-order-consistent-self:

$\text{antisym } r \implies \text{order-consistent } r r$
by(rule order-consistentI)(erule antisymPD, simp-all)

lemma total-on-refl-on-consistent-into:

assumes $r: \text{total-onP } A r \text{ refl-onP } A r$
and $\text{consist: order-consistent } r s$
and $x: x \in A$ **and** $y: y \in A$ **and** $s: s x y$
shows $r x y$
proof(cases $x = y$)
 case True
 with $r x y$ **show** ?thesis **by**(blast intro: refl-onPD)
next
 case False
 with $r x y$ **have** $r x y \vee r y x$ **by**(blast dest: total-onD)
 thus ?thesis
 proof
 assume $r y x$
 with $s \text{ consist}$ **have** $x = y$ **by**(blast dest: order-consistentD)
 with False **show** ?thesis **by**(contradiction)
 qed
qed

lemma porder-torder-tranclpE [consumes 5, case-names base step]:

```

assumes  $r$ : porder-on  $A$   $r$ 
and  $s$ : torder-on  $B$   $s$ 
and consist: order-consistent  $r$   $s$ 
and B-subset-A:  $B \subseteq A$ 
and tranci:  $(\lambda a b. r a b \vee s a b)^{++} x y$ 
obtains  $r x y$ 
  |  $u v$  where  $r x u s u v r v y$ 
proof(atomize-elim)
from  $r$  have refl-onP  $A$   $r$  transp  $r$  by(blast elim: porder-onE)+
from  $s$  have refl-onP  $B$   $s$  total-onP  $B$   $s$  transp  $s$ 
  by(blast elim: torder-onE porder-onE)+

from tranci show  $r x y \vee (\exists u v. r x u \wedge s u v \wedge r v y)$ 
proof(induct)
  case (base  $y$ )
  thus ?case
  proof
    assume  $s x y$ 
    with  $s$  have  $x \in B y \in B$ 
      by(blast elim: torder-onE porder-onE dest: refl-onPD1 refl-onPD2)+
    with B-subset-A have  $x \in A y \in A$  by blast+
    with refl-onPD[OF  $\langle$ refl-onP  $A$   $r$  $\rangle$ , of  $x$ ] refl-onPD[OF  $\langle$ refl-onP  $A$   $r$  $\rangle$ , of  $y$ ]  $\langle$  $s x y$  $\rangle$ 
    show ?thesis by(iprover)
  next
    assume  $r x y$ 
    thus ?thesis ..
  qed
next
  case (step  $y z$ )
  note  $IH = \langle r x y \vee (\exists u v. r x u \wedge s u v \wedge r v y) \rangle$ 
  from  $\langle r y z \vee s y z \rangle$  show ?case
  proof
    assume  $s y z$ 
    with  $\langle$ refl-onP  $B$   $s$  $\rangle$  have  $y \in B z \in B$ 
      by(blast dest: refl-onPD2 refl-onPD1)+
    from  $IH$  show ?thesis
  proof
    assume  $r x y$ 
    moreover from  $\langle z \in B \rangle$  B-subset-A  $r$  have  $r z z$ 
      by(blast elim: porder-onE dest: refl-onPD)
    ultimately show ?thesis using  $\langle s y z \rangle$  by blast
  next
    assume  $\exists u v. r x u \wedge s u v \wedge r v y$ 
    then obtain  $u v$  where  $r x u s u v r v y$  by blast
    from  $\langle$ refl-onP  $B$   $s$  $\rangle$   $\langle$  $s u v$  $\rangle$  have  $v \in B$  by(rule refl-onPD2)
    with  $\langle$ total-onP  $B$   $s$  $\rangle$   $\langle$ refl-onP  $B$   $s$  $\rangle$  order-consistent-sym[OF consist]
    have  $s v y$  using  $\langle y \in B \rangle$   $\langle r v y \rangle$ 
      by(rule total-on-refl-on-consistent-into)
    with  $\langle$ transp  $s$  $\rangle$  have  $s v z$  using  $\langle s y z \rangle$  by(rule transPD)
    with  $\langle$ transp  $s$  $\rangle$   $\langle s u v \rangle$  have  $s u z$  by(rule transPD)
    moreover from  $\langle z \in B \rangle$  B-subset-A have  $z \in A$  ..
    with  $\langle$ refl-onP  $A$   $r$  $\rangle$  have  $r z z$  by(rule refl-onPD)
    ultimately show ?thesis using  $\langle r x u \rangle$  by blast
  qed

```

```

next
  assume  $r y z$ 
  with  $IH$  show  $?thesis$ 
    by( $blast$  intro:  $transPD[OF \langle transp r \rangle]$ )
qed
qed
qed

lemma torder-on-porder-on-consistent-tranclp-antisym:
  assumes  $r$ : porder-on  $A r$ 
  and  $s$ : torder-on  $B s$ 
  and consist: order-consistent  $r s$ 
  and B-subset-A:  $B \subseteq A$ 
  shows antisymp  $(\lambda x y. r x y \vee s x y)^{++}$ 
proof(rule antisymPI)
  fix  $x y$ 
  let  $?rs = \lambda x y. r x y \vee s x y$ 
  assume  $?rs^{++} x y ?rs^{++} y x$ 
  from  $r$  have antisymp  $r$  transp  $r$  by( $blast$  elim: porder-onE)
  from  $s$  have total-onP  $B s$  refl-onP  $B s$  transp  $s$  antisymp  $s$ 
    by( $blast$  elim: torder-onE porder-onE)

  from  $r s$  consist B-subset-A  $\langle ?rs^{++} x y \rangle$ 
  show  $x = y$ 
proof(cases rule: porder-torder-tranclpE)
  case base
  from  $r s$  consist B-subset-A  $\langle ?rs^{++} y x \rangle$ 
  show  $?thesis$ 
proof(cases rule: porder-torder-tranclpE)
  case base
  with  $\langle antisymp r \rangle \langle r x y \rangle$  show  $?thesis$  by(rule antisymPD)
next
  case (step  $u v$ )
  from  $\langle r v x \rangle \langle r x y \rangle \langle r y u \rangle$  have  $r v u$  by( $blast$  intro:  $transPD[OF \langle transp r \rangle]$ )
  with consist have  $v = u$  using  $\langle s u v \rangle$  by(rule order-consistentD)
  with  $\langle r y u \rangle \langle r v x \rangle$  have  $r y x$  by( $blast$  intro:  $transPD[OF \langle transp r \rangle]$ )
  with  $\langle r x y \rangle$  show  $?thesis$  by(rule antisymPD[OF \langle antisymp r \rangle])
qed
next
  case (step  $u v$ )
  from  $r s$  consist B-subset-A  $\langle ?rs^{++} y x \rangle$ 
  show  $?thesis$ 
proof(cases rule: porder-torder-tranclpE)
  case base
  from  $\langle r v y \rangle \langle r y x \rangle \langle r x u \rangle$  have  $r v u$  by( $blast$  intro:  $transPD[OF \langle transp r \rangle]$ )
  with order-consistent-sym $[OF$  consist] $\langle s u v \rangle$ 
  have  $u = v$  by(rule order-consistentD)
  with  $\langle r v y \rangle \langle r x u \rangle$  have  $r x y$  by( $blast$  intro:  $transPD[OF \langle transp r \rangle]$ )
  thus  $?thesis$  using  $\langle r y x \rangle$  by(rule antisymPD[OF \langle antisymp r \rangle])
next
  case (step  $u' v'$ )
  note  $r$ -into- $s = total-on-refl-on-consistent-into[OF \langle total-onP B s \rangle \langle refl-onP B s \rangle order-consistent-sym[OF$ 
consist]]
  from  $\langle refl-onP B s \rangle \langle s u v \rangle \langle s u' v' \rangle$ 

```


have $u \in B \ v \in B \ u' \in B \ v' \in B$ **by**(*blast dest: refl-onPD1 refl-onPD2*)
from $\langle r \ v' \ x \rangle \langle r \ x \ u \rangle$ **have** $r \ v' \ u$ **by**(*rule transPD[OF $\langle \text{transp } r \rangle$]*)
with $\langle v' \in B \rangle \langle u \in B \rangle$ **have** $s \ v' \ u$ **by**(*rule r-into-s*)
also note $\langle s \ u \ v \rangle$
also (*transPD[OF $\langle \text{transp } s \rangle$]*)
from $\langle r \ v \ y \rangle \langle r \ y \ u' \rangle$ **have** $r \ v \ u'$ **by**(*rule transPD[OF $\langle \text{transp } r \rangle$]*)
with $\langle v \in B \rangle \langle u' \in B \rangle$ **have** $s \ v \ u'$ **by**(*rule r-into-s*)
finally (*transPD[OF $\langle \text{transp } s \rangle$]*)
have $v' = u'$ **using** $\langle s \ u' \ v' \rangle$ **by**(*rule antisymPD[OF $\langle \text{antisymp } s \rangle$]*)
moreover with $\langle s \ v \ u' \rangle \langle s \ v' \ u \rangle$ **have** $s \ v \ u$ **by**(*blast intro: transPD[OF $\langle \text{transp } s \rangle$]*)
with $\langle s \ u \ v \rangle$ **have** $u = v$ **by**(*rule antisymPD[OF $\langle \text{antisymp } s \rangle$]*)
ultimately have $r \ x \ y \ r \ y \ x$ **using** $\langle r \ x \ u \rangle \langle r \ v \ y \rangle \langle r \ y \ u' \rangle \langle r \ v' \ x \rangle$
by(*blast intro: transPD[OF $\langle \text{transp } r \rangle$]*)
thus *?thesis* **by**(*rule antisymPD[OF $\langle \text{antisymp } r \rangle$]*)
qed
qed
qed

lemma *porder-on-torder-on-tranclp-porder-onI*:

assumes r : *porder-on* $A \ r$
and s : *torder-on* $B \ s$
and *consist*: *order-consistent* $r \ s$
and *subset*: $B \subseteq A$
shows *porder-on* $A \ (\lambda a \ b. \ r \ a \ b \ \vee \ s \ a \ b)^{++}$
proof(*rule porder-onI*)
let $?rs = \lambda a \ b. \ r \ a \ b \ \vee \ s \ a \ b$
from r **have** *refl-onP* $A \ r$ **by**(*rule porder-onE*)
moreover from s **have** *refl-onP* $B \ s$ **by**(*blast elim: torder-onE porder-onE*)
ultimately have *refl-onP* $(A \cup B) \ ?rs$ **by**(*rule refl-onP-Un*)
also from *subset* **have** $A \cup B = A$ **by** *blast*
finally show *refl-onP* $A \ ?rs^{++}$ **by**(*rule refl-onP-tranclp*)

show *transp* $?rs^{++}$ **by**(*rule transP-tranclp*)

from $r \ s$ *consist subset* **show** *antisymp* $?rs^{++}$
by (*rule torder-on-porder-on-consistent-tranclp-antisym*)
qed

lemma *porder-on-sub-torder-on-tranclp-porder-onI*:

assumes r : *porder-on* $A \ r$
and s : *torder-on* $B \ s$
and *consist*: *order-consistent* $r \ s$
and t : $\bigwedge x \ y. \ t \ x \ y \implies s \ x \ y$
and *subset*: $B \subseteq A$
shows *porder-on* $A \ (\lambda x \ y. \ r \ x \ y \ \vee \ t \ x \ y)^{++}$
proof(*rule porder-onI*)
let $?rt = \lambda x \ y. \ r \ x \ y \ \vee \ t \ x \ y$
let $?rs = \lambda x \ y. \ r \ x \ y \ \vee \ s \ x \ y$
from $r \ s$ *consist subset* **have** *antisymp* $?rs^{++}$
by(*rule torder-on-porder-on-consistent-tranclp-antisym*)
thus *antisymp* $?rt^{++}$
proof(*rule antisym-subset*)
fix $x \ y$
assume $?rt^{++} \ x \ y$ **thus** $?rs^{++} \ x \ y$

by(*induct*)(*blast intro: tranclp.r-into-trancl t tranclp.trancl-into-trancl t*)
qed

from s **have** *refl-onP B s* **by**(*blast elim: porder-onE torder-onE*)
{ **fix** $x y$
assume $t x y$
hence $s x y$ **by**(*rule t*)
hence $x \in B y \in B$
by(*blast dest: refl-onPD1[OF <refl-onP B s>] refl-onPD2[OF <refl-onP B s>]*)
with *subset* **have** $x \in A y \in A$ **by** *blast+* **}**
note $t\text{-reflD} = \text{this}$

from r **have** *refl-onP A r* **by**(*rule porder-onE*)
show *refl-onP A ?rt⁺⁺*
proof(*rule refl-onPI*)
fix $a a'$
assume $?rt⁺⁺ a a'$
thus $a \in A \wedge a' \in A$
by(*induct*)(*auto dest: refl-onPD1[OF <refl-onP A r>] refl-onPD2[OF <refl-onP A r>] t-reflD*)
next
fix a
assume $a \in A$
with $\langle \text{refl-onP } A \ r \rangle$ **have** $r \ a \ a$ **by**(*rule refl-onPD*)
thus $?rt⁺⁺ a a$ **by**(*blast intro: tranclp.r-into-trancl*)
qed

show *transp ?rt⁺⁺* **by**(*rule transP-tranclp*)
qed

8.3.4 Order restrictions

lemma *restrictPI* [*intro!*, *simp*]:
 $\llbracket r \ a \ b; a \in A; b \in A \rrbracket \implies (r \mid' A) \ a \ b$
unfolding *restrictP-def* **by** *simp*

lemma *restrictPE* [*elim!*]:
assumes $(r \mid' A) \ a \ b$
obtains $r \ a \ b \ a \in A \ b \in A$
using *assms* **unfolding** *restrictP-def* **by** *simp*

lemma *restrictP-empty* [*simp*]: $R \mid' \{\} = (\lambda _ \cdot \text{False})$
by(*simp add: restrictP-def[abs-def]*)

lemma *refl-on-restrictPI*:
 $\text{refl-onP } A \ r \implies \text{refl-onP } (A \cap B) \ (r \mid' B)$
by(*rule refl-onPI*)(*blast dest: refl-onPD1 refl-onPD2 refl-onPD*)**+**

lemma *refl-on-restrictPI'*:
 $\llbracket \text{refl-onP } A \ r; B = A \cap C \rrbracket \implies \text{refl-onP } B \ (r \mid' C)$
by(*simp add: refl-on-restrictPI*)

lemma *antisym-restrictPI*:
 $\text{antisym } r \implies \text{antisym } (r \mid' A)$
by(*rule antisymPI*)(*blast dest: antisymPD*)

lemma *trans-restrictPI*:

$\text{transp } r \Longrightarrow \text{transp } (r \mid' A)$

by(*rule transPI*)(*blast dest: transPD*)

lemma *porder-on-restrictPI*:

$\text{porder-on } A \ r \Longrightarrow \text{porder-on } (A \cap B) \ (r \mid' B)$

by(*blast elim: porder-onE intro: refl-on-restrictPI antisym-restrictPI trans-restrictPI porder-onI*)

lemma *porder-on-restrictPI'*:

$\llbracket \text{porder-on } A \ r; B = A \cap C \rrbracket \Longrightarrow \text{porder-on } B \ (r \mid' C)$

by(*simp add: porder-on-restrictPI*)

lemma *total-on-restrictPI*:

$\text{total-onP } A \ r \Longrightarrow \text{total-onP } (A \cap B) \ (r \mid' B)$

by(*blast intro: total-onPI dest: total-onPD*)

lemma *total-on-restrictPI'*:

$\llbracket \text{total-onP } A \ r; B = A \cap C \rrbracket \Longrightarrow \text{total-onP } B \ (r \mid' C)$

by(*simp add: total-on-restrictPI*)

lemma *torder-on-restrictPI*:

$\text{torder-on } A \ r \Longrightarrow \text{torder-on } (A \cap B) \ (r \mid' B)$

by(*blast elim: torder-onE intro: torder-onI porder-on-restrictPI total-on-restrictPI*)

lemma *torder-on-restrictPI'*:

$\llbracket \text{torder-on } A \ r; B = A \cap C \rrbracket \Longrightarrow \text{torder-on } B \ (r \mid' C)$

by(*simp add: torder-on-restrictPI*)

lemma *restrictP-commute*:

fixes $r :: 'a \Rightarrow 'a \Rightarrow \text{bool}$

shows $r \mid' A \mid' B = r \mid' B \mid' A$

by(*blast intro!: ext*)

lemma *restrictP-subsume1*:

fixes $r :: 'a \Rightarrow 'a \Rightarrow \text{bool}$

assumes $A \subseteq B$

shows $r \mid' A \mid' B = r \mid' A$

using *assms* **by**(*blast intro!: ext*)

lemma *restrictP-subsume2*:

fixes $r :: 'a \Rightarrow 'a \Rightarrow \text{bool}$

assumes $B \subseteq A$

shows $r \mid' A \mid' B = r \mid' B$

using *assms* **by**(*blast intro!: ext*)

lemma *restrictP-idem* [*simp*]:

fixes $r :: 'a \Rightarrow 'a \Rightarrow \text{bool}$

shows $r \mid' A \mid' A = r \mid' A$

by(*simp add: restrictP-subsume1*)

8.3.5 Maximal elements w.r.t. a total order

definition *max-torder* :: $('a \Rightarrow 'a \Rightarrow \text{bool}) \Rightarrow 'a \Rightarrow 'a \Rightarrow 'a$

where $\text{max-torder } r \ a \ b = (\text{if } \text{Domainp } r \ a \ \wedge \ \text{Domainp } r \ b \ \text{then } \text{if } r \ a \ b \ \text{then } b \ \text{else } a \ \text{else if } a = b \ \text{then } a \ \text{else } \text{SOME } a. \neg \text{Domainp } r \ a)$

lemma *refl-on-DomainD*: $\text{refl-on } A \ r \Longrightarrow A = \text{Domain } r$
by(*auto simp add: Domain-unfold dest: refl-onD refl-onD1*)

lemma *refl-onP-DomainPD*: $\text{refl-onP } A \ r \Longrightarrow A = \{a. \text{Domainp } r \ a\}$
by(*drule refl-on-DomainD*) *auto*

lemma *semilattice-max-torder*:
assumes *tot: torder-on A r*
shows *semilattice (max-torder r)*

proof –

from *tot* **have** *as: antisymp r*
and *to: total-onP A r*
and *trans: transp r*
and *refl: refl-onP A r*

by(*auto elim: torder-onE porder-onE*)

from *refl* **have** $\{a. \text{Domainp } r \ a\} = A$ **by** (*rule refl-onP-DomainPD[symmetric]*)

from *this [symmetric]* **have** *domain: $\bigwedge a. \text{Domainp } r \ a \longleftrightarrow a \in A$* **by** *simp*

show *?thesis*

proof

fix *x y z*

show $\text{max-torder } r \ (\text{max-torder } r \ x \ y) \ z = \text{max-torder } r \ x \ (\text{max-torder } r \ y \ z)$

proof (*cases $x \neq y \wedge x \neq z \wedge y \neq z$*)

case *True*

have $*$: $\bigwedge a \ b. a \neq b \Longrightarrow \text{max-torder } r \ a \ b = (\text{if } \text{Domainp } r \ a \ \wedge \ \text{Domainp } r \ b \ \text{then } \text{if } r \ a \ b \ \text{then } b \ \text{else } a \ \text{else } \text{SOME } a. \neg \text{Domainp } r \ a)$

by (*auto simp add: max-torder-def*)

with *True* **show** *?thesis*

apply (*simp only: max-torder-def domain*)

apply (*auto split!: if-splits*)

apply (*blast dest: total-onPD [OF to] transPD [OF trans] antisymPD [OF as] refl-onPD1 [OF refl] refl-onPD2 [OF refl] someI [where $P = \lambda a. a \notin A$]*)+

done

next

have *max-torder-idem: $\bigwedge a. \text{max-torder } r \ a \ a = a$* **by** (*simp add: max-torder-def*)

case *False* **then show** *?thesis*

apply (*auto simp add: max-torder-idem*)

apply (*auto simp add: max-torder-def domain dest: someI [where $P = \lambda a. a \notin A$]*)

done

qed

next

fix *x y*

show $\text{max-torder } r \ x \ y = \text{max-torder } r \ y \ x$

by (*auto simp add: max-torder-def domain dest: total-onPD [OF to] antisymPD [OF as]*)

next

fix *x*

show $\text{max-torder } r \ x \ x = x$

by (*simp add: max-torder-def*)

qed

qed

lemma *max-torder-ge-conv-disj*:

assumes *tot*: *torder-on* A r **and** x : $x \in A$ **and** y : $y \in A$
shows $r z$ (*max-torder* r x y) $\longleftrightarrow r z x \vee r z y$
proof –
from *tot* **have** *as*: *antisymp* r
and *to*: *total-onP* A r
and *trans*: *transp* r
and *refl*: *refl-onP* A r
by(*auto elim*: *torder-onE porder-onE*)
from *refl* **have** $\{a. \text{Domainp } r a\} = A$ **by** (*rule refl-onP-DomainPD*[*symmetric*])
from *this* [*symmetric*] **have** *domain*: $\bigwedge a. \text{Domainp } r a \longleftrightarrow a \in A$ **by** *simp*
show *?thesis* **using** x y
by(*simp add*: *max-torder-def domain*)(*blast dest*: *total-onPD*[*OF to*] *transPD*[*OF trans*])
qed

definition *Max-torder* :: $('a \Rightarrow 'a \Rightarrow \text{bool}) \Rightarrow 'a \text{ set} \Rightarrow 'a$
where

Max-torder $r = \text{semilattice-set.F } (\text{max-torder } r)$

context

fixes A r

assumes *tot*: *torder-on* A r

begin

lemma *semilattice-set*:

semilattice-set (*max-torder* r)

by (*rule semilattice-set.intro*, *rule semilattice-max-torder*) (*fact tot*)

lemma *domain*:

Domainp r $a \longleftrightarrow a \in A$

proof –

from *tot* **have** $\{a. \text{Domainp } r a\} = A$

by (*auto elim*: *torder-onE porder-onE dest*: *refl-onP-DomainPD* [*symmetric*])

from *this* [*symmetric*] **show** *?thesis* **by** *simp*

qed

lemma *Max-torder-in-Domain*:

assumes B : *finite* B $B \neq \{\}$ $B \subseteq A$

shows *Max-torder* r $B \in A$

proof –

interpret *Max-torder*: *semilattice-set max-torder* r

rewrites

semilattice-set.F (*max-torder* r) = *Max-torder* r

by (*fact semilattice-set Max-torder-def* [*symmetric*])+

show *?thesis* **using** B

by (*induct rule*: *finite-ne-induct*) (*simp-all add*: *max-torder-def domain*)

qed

lemma *Max-torder-in-set*:

assumes B : *finite* B $B \neq \{\}$ $B \subseteq A$

shows *Max-torder* r $B \in B$

proof –

interpret *Max-torder*: *semilattice-set max-torder* r

rewrites

semilattice-set.F (*max-torder* r) = *Max-torder* r

```

  by (fact semilattice-set Max-torder-def [symmetric])+
  show ?thesis using B
  by (induct rule: finite-ne-induct) (auto simp add: max-torder-def domain)
qed

```

lemma *Max-torder-above-iff*:

```

  assumes B: finite B B ≠ {} B ⊆ A
  shows r x (Max-torder r B) ↔ (∃ a ∈ B. r x a)

```

proof –

```

  interpret Max-torder: semilattice-set max-torder r
  rewrites

```

```

  semilattice-set.F (max-torder r) = Max-torder r

```

```

  by (fact semilattice-set Max-torder-def [symmetric])+
  from B show ?thesis

```

```

  by (induct rule: finite-ne-induct) (simp-all add: max-torder-ge-conv-disj [OF tot] Max-torder-in-Domain)

```

qed

end

lemma *Max-torder-above*:

```

  assumes tot: torder-on A r
  and finite B a ∈ B B ⊆ A
  shows r a (Max-torder r B)

```

proof –

```

  from tot have refl: refl-onP A r by (auto elim: torder-onE porder-onE)

```

```

  with ⟨a ∈ B⟩ ⟨B ⊆ A⟩ have r a a by (blast dest: refl-onPD [OF refl])

```

```

  from ⟨a ∈ B⟩ have B ≠ {} by auto

```

```

  from Max-torder-above-iff [OF tot ⟨finite B⟩ this ⟨B ⊆ A⟩, of a] ⟨r a a⟩ ⟨a ∈ B⟩

```

```

  show ?thesis by blast

```

qed

lemma *inv-imageP-id [simp]*: $inv\text{-image}P\ R\ id = R$

by (*simp add: fun-eq-iff*)

lemma *inv-into-id [simp]*: $a \in A \implies inv\text{-into}\ A\ id\ a = a$

by (*metis f-inv-into-f id-apply image-id*)

end

8.4 Axiomatic specification of the JMM

theory *JMM-Spec*

imports

Orders

../Common/Observable-Events

Coinductive.Coinductive-List

begin

8.4.1 Definitions

type-synonym *JMM-action* = *nat*

type-synonym (*'addr, 'thread-id*) *execution* = (*'thread-id* × (*'addr, 'thread-id*) *obs-event action*) *llist*

definition *actions* :: (*'addr, 'thread-id*) *execution* ⇒ *JMM-action set*

where $actions\ E = \{n.\ enat\ n < \ellength\ E\}$

definition $action-tid :: ('addr, 'thread-id)\ execution \Rightarrow JMM-action \Rightarrow 'thread-id$
where $action-tid\ E\ a = fst\ (\lnth\ E\ a)$

definition $action-obs :: ('addr, 'thread-id)\ execution \Rightarrow JMM-action \Rightarrow ('addr, 'thread-id)\ obs-event\ action$
where $action-obs\ E\ a = snd\ (\lnth\ E\ a)$

definition $tactions :: ('addr, 'thread-id)\ execution \Rightarrow 'thread-id \Rightarrow JMM-action\ set$
where $tactions\ E\ t = \{a.\ a \in actions\ E \wedge action-tid\ E\ a = t\}$

inductive $is-new-action :: ('addr, 'thread-id)\ obs-event\ action \Rightarrow bool$
where
 $NewHeapElem: is-new-action\ (NormalAction\ (NewHeapElem\ a\ hT))$

inductive $is-write-action :: ('addr, 'thread-id)\ obs-event\ action \Rightarrow bool$
where
 $NewHeapElem: is-write-action\ (NormalAction\ (NewHeapElem\ ad\ hT))$
 $| WriteMem: is-write-action\ (NormalAction\ (WriteMem\ ad\ al\ v))$

Initialisation actions are synchronisation actions iff they initialize volatile fields – cf. JMM mailing list, message no. 62 (5 Nov. 2006). However, intuitively correct programs might not be correctly synchronized:

```

x = 0
-----
r1 = x | r2 = x

```

Here, if x is not volatile, the initial write can belong to at most one thread. Hence, it is happens-before to either $r1 = x$ or $r2 = x$, but not both. In any sequentially consistent execution, both reads must read from the initialisation action $x = 0$, but it is not happens-before ordered to one of them.

Moreover, if only volatile initialisations synchronize-with all thread-start actions, this breaks the proof of the DRF guarantee since it assumes that the happens-before relation $\leq_{hb} a$ for an action a in a topologically sorted action sequence depends only on the actions before it. Counter example: (y is volatile)

$[(t1, start), (t1, init\ x), (t2, start), (t1, init\ y), \dots]$

Here, $(t1, init\ x) \leq_{hb} (t2, start)$ via: $(t1, init\ x) \leq_{po} (t1, init\ y) \leq_{sw} (t2, start)$, but in $[(t1, start), (t1, init\ x), (t2, start)]$, not $(t1, init\ x) \leq_{hb} (t2, start)$.

Sevcik speculated that one might add an initialisation thread which performs all initialisation actions. All normal threads' start action would then synchronize on the final action of the initialisation thread. However, this contradicts the memory chain condition in the final field extension to the JMM (threads must read addresses of objects that they have not created themselves before they can access the fields of the object at that address) – not modelled here.

Instead, we leave every initialisation action in the thread it belongs to, but order it explicitly before the thread's start action and add synchronizes-with edges from *all* initialisation actions to *all* thread start actions.

inductive $saction :: 'm\ prog \Rightarrow ('addr, 'thread-id)\ obs-event\ action \Rightarrow bool$
for $P :: 'm\ prog$
where

$NewHeapElem: saction\ P\ (NormalAction\ (NewHeapElem\ a\ hT))$

```

| Read: is-volatile P al  $\implies$  saction P (NormalAction (ReadMem a al v))
| Write: is-volatile P al  $\implies$  saction P (NormalAction (WriteMem a al v))
| ThreadStart: saction P (NormalAction (ThreadStart t))
| ThreadJoin: saction P (NormalAction (ThreadJoin t))
| SyncLock: saction P (NormalAction (SyncLock a))
| SyncUnlock: saction P (NormalAction (SyncUnlock a))
| ObsInterrupt: saction P (NormalAction (ObsInterrupt t))
| ObsInterrupted: saction P (NormalAction (ObsInterrupted t))
| InitialThreadAction: saction P InitialThreadAction
| ThreadFinishAction: saction P ThreadFinishAction

```

definition *sactions* :: 'm prog \Rightarrow ('addr, 'thread-id) execution \Rightarrow JMM-action set
where *sactions* P E = {a. a \in actions E \wedge saction P (action-obs E a)}

inductive-set *write-actions* :: ('addr, 'thread-id) execution \Rightarrow JMM-action set
for E :: ('addr, 'thread-id) execution
where

*write-actions*I: $\llbracket a \in \text{actions } E; \text{is-write-action (action-obs } E \ a) \rrbracket \implies a \in \text{write-actions } E$

NewObj and *NewArr* actions only initialize those fields and array cells that are in fact in the object or array. Hence, they are not a write for - reads from addresses for which no object/array is created during the whole execution - reads from fields/cells that are not part of the object/array at the specified address.

primrec *addr-locs* :: 'm prog \Rightarrow htype \Rightarrow addr-loc set
where

```

addr-locs P (Class-type C) = {CField D F | D F.  $\exists$  fm T. P  $\vdash$  C has F:T (fm) in D}
| addr-locs P (Array-type T n) = ({ACell n' | n'. n' < n}  $\cup$  {CField Object F | F.  $\exists$  fm T. P  $\vdash$  Object has F:T (fm) in Object})

```

action-loc-aux would naturally be an inductive set, but *inductive_set* does not allow to pattern match on parameters. Hence, specify it using function and derive the setup manually.

fun *action-loc-aux* :: 'm prog \Rightarrow ('addr, 'thread-id) obs-event action \Rightarrow ('addr \times addr-loc) set
where

```

action-loc-aux P (NormalAction (NewHeapElem ad (Class-type C))) =
  {(ad, CField D F) | D F T fm. P  $\vdash$  C has F:T (fm) in D}
| action-loc-aux P (NormalAction (NewHeapElem ad (Array-type T n'))) =
  {(ad, ACell n) | n. n < n'}  $\cup$  {(ad, CField D F) | D F T fm. P  $\vdash$  Object has F:T (fm) in D}
| action-loc-aux P (NormalAction (WriteMem ad al v)) = {(ad, al)}
| action-loc-aux P (NormalAction (ReadMem ad al v)) = {(ad, al)}
| action-loc-aux - - = {}

```

lemma *action-loc-aux-intros* [*intro?*]:

```

P  $\vdash$  class-type-of hT has F:T (fm) in D  $\implies$  (ad, CField D F)  $\in$  action-loc-aux P (NormalAction (NewHeapElem ad hT))
n < n'  $\implies$  (ad, ACell n)  $\in$  action-loc-aux P (NormalAction (NewHeapElem ad (Array-type T n')))
(ad, al)  $\in$  action-loc-aux P (NormalAction (WriteMem ad al v))
(ad, al)  $\in$  action-loc-aux P (NormalAction (ReadMem ad al v))

```

by(cases hT) auto

lemma *action-loc-aux-cases* [*elim?*, cases set: *action-loc-aux*]:

```

assumes adal  $\in$  action-loc-aux P obs
obtains (NewHeapElem) hT F T fm D ad where obs = NormalAction (NewHeapElem ad hT) adal
= (ad, CField D F) P  $\vdash$  class-type-of hT has F:T (fm) in D

```


| (*NewArr*) $n\ n'\ ad\ T$ **where** $obs = NormalAction\ (NewHeapElem\ ad\ (Array\text{-}type\ T\ n'))$ $adal = (ad, ACell\ n)\ n < n'$
 | (*WriteMem*) $ad\ al\ v$ **where** $obs = NormalAction\ (WriteMem\ ad\ al\ v)$ $adal = (ad, al)$
 | (*ReadMem*) $ad\ al\ v$ **where** $obs = NormalAction\ (ReadMem\ ad\ al\ v)$ $adal = (ad, al)$
using *assms* **by**(*cases* (P, obs) *rule: action-loc-aux.cases*) *fastforce*+

lemma *action-loc-aux-simps* [*simp*]:

$(ad', al') \in action\text{-}loc\text{-}aux\ P\ (NormalAction\ (NewHeapElem\ ad\ hT)) \longleftrightarrow$
 $(\exists D\ F\ T\ fm.\ ad = ad' \wedge al' = CField\ D\ F \wedge P \vdash class\text{-}type\text{-}of\ hT\ has\ F:T\ (fm)\ in\ D) \vee$
 $(\exists n\ T\ n'.\ ad = ad' \wedge al' = ACell\ n \wedge hT = Array\text{-}type\ T\ n' \wedge n < n')$
 $(ad', al') \in action\text{-}loc\text{-}aux\ P\ (NormalAction\ (WriteMem\ ad\ al\ v)) \longleftrightarrow ad = ad' \wedge al = al'$
 $(ad', al') \in action\text{-}loc\text{-}aux\ P\ (NormalAction\ (ReadMem\ ad\ al\ v)) \longleftrightarrow ad = ad' \wedge al = al'$
 $(ad', al') \notin action\text{-}loc\text{-}aux\ P\ InitialThreadAction$
 $(ad', al') \notin action\text{-}loc\text{-}aux\ P\ ThreadFinishAction$
 $(ad', al') \notin action\text{-}loc\text{-}aux\ P\ (NormalAction\ (ExternalCall\ a\ m\ vs\ v))$
 $(ad', al') \notin action\text{-}loc\text{-}aux\ P\ (NormalAction\ (ThreadStart\ t))$
 $(ad', al') \notin action\text{-}loc\text{-}aux\ P\ (NormalAction\ (ThreadJoin\ t))$
 $(ad', al') \notin action\text{-}loc\text{-}aux\ P\ (NormalAction\ (SyncLock\ a))$
 $(ad', al') \notin action\text{-}loc\text{-}aux\ P\ (NormalAction\ (SyncUnlock\ a))$
 $(ad', al') \notin action\text{-}loc\text{-}aux\ P\ (NormalAction\ (ObsInterrupt\ t))$
 $(ad', al') \notin action\text{-}loc\text{-}aux\ P\ (NormalAction\ (ObsInterrupted\ t))$
by(*cases* hT) *auto*

declare *action-loc-aux.simps* [*simp del*]

abbreviation *action-loc* :: $'m\ prog \Rightarrow ('addr, 'thread\text{-}id)\ execution \Rightarrow JMM\text{-}action \Rightarrow ('addr \times addr\text{-}loc)\ set$
where *action-loc* $P\ E\ a \equiv action\text{-}loc\text{-}aux\ P\ (action\text{-}obs\ E\ a)$

inductive-set *read-actions* :: $('addr, 'thread\text{-}id)\ execution \Rightarrow JMM\text{-}action\ set$

for E :: $('addr, 'thread\text{-}id)\ execution$

where

ReadMem: $\llbracket a \in actions\ E; action\text{-}obs\ E\ a = NormalAction\ (ReadMem\ ad\ al\ v) \rrbracket \Longrightarrow a \in read\text{-}actions\ E$

fun *addr-loc-default* :: $'m\ prog \Rightarrow htype \Rightarrow addr\text{-}loc \Rightarrow 'addr\ val$

where

addr-loc-default $P\ (Class\text{-}type\ C)\ (CField\ D\ F) = default\text{-}val\ (fst\ (the\ (map\text{-}of\ (fields\ P\ C)\ (F, D))))$
 | *addr-loc-default* $P\ (Array\text{-}type\ T\ n)\ (ACell\ n') = default\text{-}val\ T$
 | *addr-loc-default-Array-CField*:
addr-loc-default $P\ (Array\text{-}type\ T\ n)\ (CField\ D\ F) = default\text{-}val\ (fst\ (the\ (map\text{-}of\ (fields\ P\ Object)\ (F, Object))))$
 | *addr-loc-default* $P\ - = undefined$

definition *new-actions-for* :: $'m\ prog \Rightarrow ('addr, 'thread\text{-}id)\ execution \Rightarrow ('addr \times addr\text{-}loc) \Rightarrow JMM\text{-}action\ set$

where

new-actions-for $P\ E\ adal =$
 $\{a.\ a \in actions\ E \wedge adal \in action\text{-}loc\ P\ E\ a \wedge is\text{-}new\text{-}action\ (action\text{-}obs\ E\ a)\}$

inductive-set *external-actions* :: $('addr, 'thread\text{-}id)\ execution \Rightarrow JMM\text{-}action\ set$

for E :: $('addr, 'thread\text{-}id)\ execution$

where

$\llbracket a \in actions\ E; action\text{-}obs\ E\ a = NormalAction\ (ExternalCall\ ad\ M\ vs\ v) \rrbracket$

$\implies a \in \text{external-actions } E$

fun *value-written-aux* :: 'm prog \Rightarrow ('addr, 'thread-id) obs-event action \Rightarrow addr-loc \Rightarrow 'addr val
where

value-written-aux P (NormalAction (NewHeapElem ad' hT)) al = addr-loc-default P hT al
| *value-written-aux-WriteMem*':
value-written-aux P (NormalAction (WriteMem ad al' v)) al = (if al = al' then v else undefined)
| *value-written-aux-undefined*:
value-written-aux P - al = undefined

primrec *value-written* :: 'm prog \Rightarrow ('addr, 'thread-id) execution \Rightarrow JMM-action \Rightarrow ('addr \times addr-loc)
 \Rightarrow 'addr val

where *value-written* P E a (ad, al) = *value-written-aux* P (action-obs E a) al

definition *value-read* :: ('addr, 'thread-id) execution \Rightarrow JMM-action \Rightarrow 'addr val

where

value-read E a =
(case action-obs E a of
NormalAction obs \Rightarrow
(case obs of
ReadMem ad al v \Rightarrow v
| - \Rightarrow undefined)
| - \Rightarrow undefined)

definition *action-order* :: ('addr, 'thread-id) execution \Rightarrow JMM-action \Rightarrow JMM-action \Rightarrow bool (\langle -
 $\leq a \rightarrow [51,0,50]$ 50)

where

$E \vdash a \leq a' \iff$
 $a \in \text{actions } E \wedge a' \in \text{actions } E \wedge$
(if is-new-action (action-obs E a)
then is-new-action (action-obs E a') $\longrightarrow a \leq a'$
else \neg is-new-action (action-obs E a') $\wedge a \leq a'$)

definition *program-order* :: ('addr, 'thread-id) execution \Rightarrow JMM-action \Rightarrow JMM-action \Rightarrow bool (\langle -
 $\leq po \rightarrow [51,0,50]$ 50)

where

$E \vdash a \leq po a' \iff E \vdash a \leq a' \wedge \text{action-tid } E a = \text{action-tid } E a'$

inductive *synchronizes-with* ::

'm prog
 \Rightarrow ('thread-id \times ('addr, 'thread-id) obs-event action) \Rightarrow ('thread-id \times ('addr, 'thread-id) obs-event
action) \Rightarrow bool

(\langle - \vdash - \rightsquigarrow_{sw} $\rightarrow [51, 51, 51]$ 50)

for P :: 'm prog

where

ThreadStart: $P \vdash (t, \text{NormalAction } (\text{ThreadStart } t')) \rightsquigarrow_{sw} (t', \text{InitialThreadAction})$
| *ThreadFinish*: $P \vdash (t, \text{ThreadFinishAction}) \rightsquigarrow_{sw} (t', \text{NormalAction } (\text{ThreadJoin } t))$
| *UnlockLock*: $P \vdash (t, \text{NormalAction } (\text{SyncUnlock } a)) \rightsquigarrow_{sw} (t', \text{NormalAction } (\text{SyncLock } a))$
| — Only volatile writes synchronize with volatile reads. We could check volatility of al here, but this
is checked by *sactions* in *sync-with* anyway.

Volatile: $P \vdash (t, \text{NormalAction } (\text{WriteMem } a al v)) \rightsquigarrow_{sw} (t', \text{NormalAction } (\text{ReadMem } a al v'))$

| *VolatileNew*:

$al \in \text{addr-locs } P$ hT

$\implies P \vdash (t, \text{NormalAction } (\text{NewHeapElem } a hT)) \rightsquigarrow_{sw} (t', \text{NormalAction } (\text{ReadMem } a al v))$

| *NewHeapElem*: $P \vdash (t, \text{NormalAction } (\text{NewHeapElem } a \ hT)) \rightsquigarrow_{sw} (t', \text{InitialThreadAction})$
 | *Interrupt*: $P \vdash (t, \text{NormalAction } (\text{ObsInterrupt } t')) \rightsquigarrow_{sw} (t'', \text{NormalAction } (\text{ObsInterrupted } t'))$

definition *sync-order* ::

'*m prog* \Rightarrow ('*addr*, '*thread-id*) *execution* \Rightarrow *JMM-action* \Rightarrow *JMM-action* \Rightarrow *bool*
 ($\langle _, - \vdash - \leq_{so} \rightarrow [51, 0, 0, 50] \ 50$)

where

$P, E \vdash a \leq_{so} a' \iff a \in \text{sactions } P \ E \wedge a' \in \text{sactions } P \ E \wedge E \vdash a \leq a'$

definition *sync-with* ::

'*m prog* \Rightarrow ('*addr*, '*thread-id*) *execution* \Rightarrow *JMM-action* \Rightarrow *JMM-action* \Rightarrow *bool*
 ($\langle _, - \vdash - \leq_{sw} \rightarrow [51, 0, 0, 50] \ 50$)

where

$P, E \vdash a \leq_{sw} a' \iff$

$P, E \vdash a \leq_{so} a' \wedge P \vdash (\text{action-tid } E \ a, \text{action-obs } E \ a) \rightsquigarrow_{sw} (\text{action-tid } E \ a', \text{action-obs } E \ a')$

definition *po-sw* :: '*m prog* \Rightarrow ('*addr*, '*thread-id*) *execution* \Rightarrow *JMM-action* \Rightarrow *JMM-action* \Rightarrow *bool*

where *po-sw* $P \ E \ a \ a' \iff E \vdash a \leq_{po} a' \vee P, E \vdash a \leq_{sw} a'$

abbreviation *happens-before* ::

'*m prog* \Rightarrow ('*addr*, '*thread-id*) *execution* \Rightarrow *JMM-action* \Rightarrow *JMM-action* \Rightarrow *bool*
 ($\langle _, - \vdash - \leq_{hb} \rightarrow [51, 0, 0, 50] \ 50$)

where *happens-before* $P \ E \equiv (\text{po-sw } P \ E) \hat{+} \hat{+}$

type-synonym *write-seen* = *JMM-action* \Rightarrow *JMM-action*

definition *is-write-seen* :: '*m prog* \Rightarrow ('*addr*, '*thread-id*) *execution* \Rightarrow *write-seen* \Rightarrow *bool* **where**

is-write-seen $P \ E \ ws \iff$

$(\forall a \in \text{read-actions } E. \forall ad \ al \ v. \text{action-obs } E \ a = \text{NormalAction } (\text{ReadMem } ad \ al \ v) \longrightarrow$

$ws \ a \in \text{write-actions } E \wedge (ad, al) \in \text{action-loc } P \ E \ (ws \ a) \wedge$

$\text{value-written } P \ E \ (ws \ a) \ (ad, al) = v \wedge \neg P, E \vdash a \leq_{hb} ws \ a \wedge$

$(\text{is-volatile } P \ al \longrightarrow \neg P, E \vdash a \leq_{so} ws \ a) \wedge$

$(\forall w' \in \text{write-actions } E. (ad, al) \in \text{action-loc } P \ E \ w' \longrightarrow$

$(P, E \vdash ws \ a \leq_{hb} w' \wedge P, E \vdash w' \leq_{hb} a \vee \text{is-volatile } P \ al \wedge P, E \vdash ws \ a \leq_{so} w' \wedge P, E \vdash w'$

$\leq_{so} a) \longrightarrow$

$w' = ws \ a))$

definition *thread-start-actions-ok* :: ('*addr*, '*thread-id*) *execution* \Rightarrow *bool*

where

thread-start-actions-ok $E \iff$

$(\forall a \in \text{actions } E. \neg \text{is-new-action } (\text{action-obs } E \ a) \longrightarrow$

$(\exists i. i \leq a \wedge \text{action-obs } E \ i = \text{InitialThreadAction} \wedge \text{action-tid } E \ i = \text{action-tid } E \ a))$

primrec *wf-exec* :: '*m prog* \Rightarrow ('*addr*, '*thread-id*) *execution* \times *write-seen* \Rightarrow *bool* ($\langle _ \vdash - \sqrt{\ } \rightarrow [51, 50] \ 51$)

where $P \vdash (E, ws) \sqrt{\ } \iff \text{is-write-seen } P \ E \ ws \wedge \text{thread-start-actions-ok } E$

inductive *most-recent-write-for* :: '*m prog* \Rightarrow ('*addr*, '*thread-id*) *execution* \Rightarrow *JMM-action* \Rightarrow *JMM-action* \Rightarrow *bool*

($\langle _, - \vdash - \rightsquigarrow_{mrw} \rightarrow [50, 0, 51] \ 51$)

for P :: '*m prog* **and** E :: ('*addr*, '*thread-id*) *execution* **and** ra :: *JMM-action* **and** wa :: *JMM-action*

where

$\llbracket ra \in \text{read-actions } E; adal \in \text{action-loc } P \ E \ ra; E \vdash wa \leq a \ ra;$

$wa \in \text{write-actions } E; adal \in \text{action-loc } P \ E \ wa;$

$$\begin{aligned}
& \bigwedge wa'. \llbracket wa' \in \text{write-actions } E; \text{adal} \in \text{action-loc } P \ E \ wa' \rrbracket \\
& \implies E \vdash wa' \leq a \ wa \vee E \vdash ra \leq a \ wa' \rrbracket \\
& \implies P, E \vdash ra \rightsquigarrow_{mrw} wa
\end{aligned}$$

primrec *sequentially-consistent* :: 'm prog \Rightarrow (('addr, 'thread-id) execution \times write-seen) \Rightarrow bool
where

$$\text{sequentially-consistent } P \ (E, \text{ws}) \iff (\forall r \in \text{read-actions } E. P, E \vdash r \rightsquigarrow_{mrw} \text{ws } r)$$

8.4.2 Actions

inductive-cases *is-new-action-cases* [elim!]:

is-new-action (NormalAction (ExternalCall a M vs v))
is-new-action (NormalAction (ReadMem a al v))
is-new-action (NormalAction (WriteMem a al v))
is-new-action (NormalAction (NewHeapElem a hT))
is-new-action (NormalAction (ThreadStart t))
is-new-action (NormalAction (ThreadJoin t))
is-new-action (NormalAction (SyncLock a))
is-new-action (NormalAction (SyncUnlock a))
is-new-action (NormalAction (ObsInterrupt t))
is-new-action (NormalAction (ObsInterrupted t))
is-new-action InitialThreadAction
is-new-action ThreadFinishAction

inductive-simps *is-new-action-simps* [simp]:

is-new-action (NormalAction (NewHeapElem a hT))
is-new-action (NormalAction (ExternalCall a M vs v))
is-new-action (NormalAction (ReadMem a al v))
is-new-action (NormalAction (WriteMem a al v))
is-new-action (NormalAction (ThreadStart t))
is-new-action (NormalAction (ThreadJoin t))
is-new-action (NormalAction (SyncLock a))
is-new-action (NormalAction (SyncUnlock a))
is-new-action (NormalAction (ObsInterrupt t))
is-new-action (NormalAction (ObsInterrupted t))
is-new-action InitialThreadAction
is-new-action ThreadFinishAction

lemmas *is-new-action-iff* = *is-new-action.simps*

inductive-simps *is-write-action-simps* [simp]:

is-write-action InitialThreadAction
is-write-action ThreadFinishAction
is-write-action (NormalAction (ExternalCall a m vs v))
is-write-action (NormalAction (ReadMem a al v))
is-write-action (NormalAction (WriteMem a al v))
is-write-action (NormalAction (NewHeapElem a hT))
is-write-action (NormalAction (ThreadStart t))
is-write-action (NormalAction (ThreadJoin t))
is-write-action (NormalAction (SyncLock a))
is-write-action (NormalAction (SyncUnlock a))
is-write-action (NormalAction (ObsInterrupt t))
is-write-action (NormalAction (ObsInterrupted t))

declare *saction.intros* [intro!]

inductive-cases *saction-cases* [elim!]:

saction *P* (*NormalAction* (*ExternalCall* *a M vs v*))
saction *P* (*NormalAction* (*ReadMem* *a al v*))
saction *P* (*NormalAction* (*WriteMem* *a al v*))
saction *P* (*NormalAction* (*NewHeapElem* *a hT*))
saction *P* (*NormalAction* (*ThreadStart* *t*))
saction *P* (*NormalAction* (*ThreadJoin* *t*))
saction *P* (*NormalAction* (*SyncLock* *a*))
saction *P* (*NormalAction* (*SyncUnlock* *a*))
saction *P* (*NormalAction* (*ObsInterrupt* *t*))
saction *P* (*NormalAction* (*ObsInterrupted* *t*))
saction *P* *InitialThreadAction*
saction *P* *ThreadFinishAction*

inductive-simps *saction-simps* [simp]:

saction *P* (*NormalAction* (*ExternalCall* *a M vs v*))
saction *P* (*NormalAction* (*ReadMem* *a al v*))
saction *P* (*NormalAction* (*WriteMem* *a al v*))
saction *P* (*NormalAction* (*NewHeapElem* *a hT*))
saction *P* (*NormalAction* (*ThreadStart* *t*))
saction *P* (*NormalAction* (*ThreadJoin* *t*))
saction *P* (*NormalAction* (*SyncLock* *a*))
saction *P* (*NormalAction* (*SyncUnlock* *a*))
saction *P* (*NormalAction* (*ObsInterrupt* *t*))
saction *P* (*NormalAction* (*ObsInterrupted* *t*))
saction *P* *InitialThreadAction*
saction *P* *ThreadFinishAction*

lemma *new-action-saction* [simp, intro]: *is-new-action a* \implies *saction P a*
by(blast elim: *is-new-action.cases*)

lemmas *saction-iff* = *saction.simps*

lemma *actionsD*: *a* \in *actions E* \implies *enat a* < *llength E*

unfolding *actions-def* **by** *blast*

lemma *actionsE*:

assumes *a* \in *actions E*

obtains *enat a* < *llength E*

using *assms* **unfolding** *actions-def* **by** *blast*

lemma *actions-lappend*:

llength xs = *enat n* \implies *actions (lappend xs ys)* = *actions xs* \cup ((+) *n*) ‘ *actions ys*

unfolding *actions-def*

apply *safe*

apply(*erule contrapos-np*)

apply(*rule-tac x=x - n in image-eqI*)

apply *simp-all*

apply(*case-tac* [!] *llength ys*)

apply *simp-all*

done

lemma *tactionsE*:

assumes $a \in \text{tactions } E \ t$

obtains *obs* **where** $a \in \text{actions } E \ \text{action-tid } E \ a = t \ \text{action-obs } E \ a = \text{obs}$

using *assms*

by(*cases lnth E a*)(*auto simp add: tactions-def action-tid-def action-obs-def*)

lemma *sactionsI*:

$\llbracket a \in \text{actions } E; \text{saction } P \ (\text{action-obs } E \ a) \rrbracket \implies a \in \text{sactions } P \ E$

unfolding *sactions-def* **by** *blast*

lemma *sactionsE*:

assumes $a \in \text{sactions } P \ E$

obtains $a \in \text{actions } E \ \text{saction } P \ (\text{action-obs } E \ a)$

using *assms* **unfolding** *sactions-def* **by** *blast*

lemma *sactions-actions* [*simp*]:

$a \in \text{sactions } P \ E \implies a \in \text{actions } E$

by(*rule sactionsE*)

lemma *value-written-aux-WriteMem* [*simp*]:

value-written-aux $P \ (\text{NormalAction} \ (\text{WriteMem} \ ad \ al \ v)) \ al = v$

by *simp*

declare *value-written-aux-undefined* [*simp del*]

declare *value-written-aux-WriteMem'* [*simp del*]

inductive-simps *is-write-action-iff*:

is-write-action a

inductive-simps *write-actions-iff*:

$a \in \text{write-actions } E$

lemma *write-actions-actions* [*simp*]:

$a \in \text{write-actions } E \implies a \in \text{actions } E$

by(*rule write-actions.induct*)

inductive-simps *read-actions-iff*:

$a \in \text{read-actions } E$

lemma *read-actions-actions* [*simp*]:

$a \in \text{read-actions } E \implies a \in \text{actions } E$

by(*rule read-actions.induct*)

lemma *read-action-action-locE*:

assumes $r \in \text{read-actions } E$

obtains $ad \ al$ **where** $(ad, al) \in \text{action-loc } P \ E \ r$

using *assms* **by** *cases auto*

lemma *read-actions-not-write-actions*:

$\llbracket a \in \text{read-actions } E; a \in \text{write-actions } E \rrbracket \implies \text{False}$

by(*auto elim!: read-actions.cases write-actions.cases*)

lemma *read-actions-Int-write-actions* [*simp*]:

$\text{read-actions } E \cap \text{write-actions } E = \{\}$

$write\text{-}actions\ E \cap read\text{-}actions\ E = \{\}$
by(blast dest: read-actions-not-write-actions)+

lemma *action-loc-addr-fun*:

$\llbracket (ad, al) \in action\text{-}loc\ P\ E\ a; (ad', al') \in action\text{-}loc\ P\ E\ a \rrbracket \implies ad = ad'$
by(auto elim!: action-loc-aux-cases)

lemma *value-written-cong* [cong]:

$\llbracket P = P'; a = a'; action\text{-}obs\ E\ a' = action\text{-}obs\ E'\ a' \rrbracket$
 $\implies value\text{-}written\ P\ E\ a = value\text{-}written\ P'\ E'\ a'$
by(rule ext)(auto split: action.splits)

declare *value-written.simps* [simp del]

lemma *new-actionsI*:

$\llbracket a \in actions\ E; adal \in action\text{-}loc\ P\ E\ a; is\text{-}new\text{-}action\ (action\text{-}obs\ E\ a) \rrbracket$
 $\implies a \in new\text{-}actions\text{-}for\ P\ E\ adal$
unfolding *new-actions-for-def* **by** blast

lemma *new-actionsE*:

assumes $a \in new\text{-}actions\text{-}for\ P\ E\ adal$
obtains $a \in actions\ E\ adal \in action\text{-}loc\ P\ E\ a\ is\text{-}new\text{-}action\ (action\text{-}obs\ E\ a)$
using *assms* **unfolding** *new-actions-for-def* **by** blast

lemma *action-loc-read-action-singleton*:

$\llbracket r \in read\text{-}actions\ E; adal \in action\text{-}loc\ P\ E\ r; adal' \in action\text{-}loc\ P\ E\ r \rrbracket \implies adal = adal'$
by(cases adal, cases adal')(fastforce elim: read-actions.cases action-loc-aux-cases)

lemma *addr-locsI*:

$P \vdash class\text{-}type\text{-}of\ hT\ has\ F:T\ (fm)\ in\ D \implies CField\ D\ F \in addr\text{-}locs\ P\ hT$
 $\llbracket hT = Array\text{-}type\ T\ n; n' < n \rrbracket \implies ACell\ n' \in addr\text{-}locs\ P\ hT$
by(cases hT)(auto dest: has-field-decl-above)

8.4.3 Orders

8.4.4 Action order

lemma *action-orderI*:

assumes $a \in actions\ E\ a' \in actions\ E$
and $\llbracket is\text{-}new\text{-}action\ (action\text{-}obs\ E\ a); is\text{-}new\text{-}action\ (action\text{-}obs\ E\ a') \rrbracket \implies a \leq a'$
and $\neg is\text{-}new\text{-}action\ (action\text{-}obs\ E\ a) \implies \neg is\text{-}new\text{-}action\ (action\text{-}obs\ E\ a') \wedge a \leq a'$
shows $E \vdash a \leq a'$
using *assms* **unfolding** *action-order-def* **by** simp

lemma *action-orderE*:

assumes $E \vdash a \leq a'$
obtains $a \in actions\ E\ a' \in actions\ E$
 $is\text{-}new\text{-}action\ (action\text{-}obs\ E\ a)\ is\text{-}new\text{-}action\ (action\text{-}obs\ E\ a') \longrightarrow a \leq a'$
 $| a \in actions\ E\ a' \in actions\ E$
 $\neg is\text{-}new\text{-}action\ (action\text{-}obs\ E\ a)\ \neg is\text{-}new\text{-}action\ (action\text{-}obs\ E\ a')\ a \leq a'$
using *assms* **unfolding** *action-order-def* **by**(simp split: if-split-asm)

lemma *refl-action-order*:

$refl\text{-}onP\ (actions\ E)\ (action\text{-}order\ E)$

by(rule refl-onPI)(auto elim: action-orderE intro: action-orderI)

lemma antisym-action-order:

antisymp (action-order E)

by(rule antisympI)(auto elim!: action-orderE)

lemma trans-action-order:

transp (action-order E)

by(rule transpI)(auto elim!: action-orderE intro: action-orderI)

lemma porder-action-order:

porder-on (actions E) (action-order E)

by(blast intro: porder-onI refl-action-order antisym-action-order trans-action-order)

lemma total-action-order:

total-onP (actions E) (action-order E)

by(rule total-onPI)(auto simp add: action-order-def)

lemma torder-action-order:

torder-on (actions E) (action-order E)

by(blast intro: torder-onI total-action-order porder-action-order)

lemma wf-action-order: wfP (action-order E)^{≠≠}

unfolding wfp-eq-minimal

proof(intro strip)

fix Q **and** x :: JMM-action

assume x ∈ Q

show ∃ z ∈ Q. ∀ y. (action-order E)^{≠≠} y z → y ∉ Q

proof(cases ∃ a ∈ Q. a ∈ actions E ∧ is-new-action (action-obs E a))

case True

then obtain a **where** a: a ∈ actions E ∧ is-new-action (action-obs E a) ∧ a ∈ Q **by** blast

define a' **where** a' = (LEAST a'. a' ∈ actions E ∧ is-new-action (action-obs E a') ∧ a' ∈ Q)

from a **have** a': a' ∈ actions E ∧ is-new-action (action-obs E a') ∧ a' ∈ Q

unfolding a'-def **by**(rule LeastI)

{ **fix** y

assume y-le-a': (action-order E)^{≠≠} y a'

have y ∉ Q

proof

assume y ∈ Q

with y-le-a' a' **have** y: y ∈ actions E ∧ is-new-action (action-obs E y) ∧ y ∈ Q

by(auto elim: action-orderE)

hence a' ≤ y **unfolding** a'-def **by**(rule Least-le)

with y-le-a' a' **show** False **by**(auto elim: action-orderE)

qed }

with a' **show** ?thesis **by** blast

next

case False

hence not-new: ∧a. [a ∈ Q; a ∈ actions E] ⇒ ¬ is-new-action (action-obs E a) **by** blast

show ?thesis

proof(cases Q ∩ actions E = {})

case True

with ⟨x ∈ Q⟩ **show** ?thesis **by**(auto elim: action-orderE)

next

case False


```

define a' where a' = (LEAST a'. a' ∈ Q ∧ a' ∈ actions E ∧ ¬ is-new-action (action-obs E a'))
from False obtain a where a ∈ Q ∧ a ∈ actions E by blast
with not-new[OF this] have a ∈ Q ∧ a ∈ actions E ∧ ¬ is-new-action (action-obs E a) by blast
hence a': a' ∈ Q ∧ a' ∈ actions E ∧ ¬ is-new-action (action-obs E a')
  unfolding a'-def by(rule LeastI)
{ fix y
  assume y-le-a': (action-order E)≠≠ y a'
  hence y ∈ actions E by(auto elim: action-orderE)
  have y ∉ Q
  proof
    assume y ∈ Q
    hence y-not-new: ¬ is-new-action (action-obs E y)
      using ⟨y ∈ actions E⟩ by(rule not-new)
    with ⟨y ∈ Q⟩ ⟨y ∈ actions E⟩ have a' ≤ y
      unfolding a'-def by -(rule Least-le, blast)
    with y-le-a' y-not-new show False by(auto elim: action-orderE)
  qed }
with a' show ?thesis by blast
qed
qed
qed

```

lemma *action-order-is-new-actionD*:

```

[[ E ⊢ a ≤ a a'; is-new-action (action-obs E a') ]] ⇒ is-new-action (action-obs E a)
by(auto elim: action-orderE)

```

8.4.5 Program order

lemma *program-orderI*:

```

assumes E ⊢ a ≤ a a' and action-tid E a = action-tid E a'
shows E ⊢ a ≤po a'
using assms unfolding program-order-def by auto

```

lemma *program-orderE*:

```

assumes E ⊢ a ≤po a'
obtains t obs obs'
where E ⊢ a ≤ a a'
and action-tid E a = t action-obs E a = obs
and action-tid E a' = t action-obs E a' = obs'
using assms unfolding program-order-def
by(cases lnth E a)(cases lnth E a', auto simp add: action-obs-def action-tid-def)

```

lemma *refl-on-program-order*:

```

refl-onP (actions E) (program-order E)
by(rule refl-onPI)(auto elim: action-orderE program-orderE intro: program-orderI refl-onPD[OF refl-action-order])

```

lemma *antisym-program-order*:

```

antisymp (program-order E)
using antisympD[OF antisym-action-order]
by(auto intro: antisympI elim!: program-orderE)

```

lemma *trans-program-order*:

```

transp (program-order E)
by(rule transpI)(auto elim!: program-orderE intro: program-orderI dest: transPD[OF trans-action-order])

```

lemma *porder-program-order*:

porder-on (actions E) (program-order E)

by(*blast intro: porder-onI refl-on-program-order antisym-program-order trans-program-order*)

lemma *total-program-order-on-tactions*:

total-onP (tactions E t) (program-order E)

by(*rule total-onPI*)(*auto elim: tactionsE simp add: program-order-def dest: total-onD[OF total-action-order]*)

8.4.6 Synchronization order

lemma *sync-orderI*:

$\llbracket E \vdash a \leq a'; a \in \text{sactions } P E; a' \in \text{sactions } P E \rrbracket \implies P, E \vdash a \leq_{so} a'$

unfolding *sync-order-def* **by** *blast*

lemma *sync-orderE*:

assumes $P, E \vdash a \leq_{so} a'$

obtains $a \in \text{sactions } P E \ a' \in \text{sactions } P E \ E \vdash a \leq a'$

using *assms* **unfolding** *sync-order-def* **by** *blast*

lemma *refl-on-sync-order*:

refl-onP (sactions P E) (sync-order P E)

by(*rule refl-onPI*)(*fastforce elim: sync-orderE intro: sync-orderI refl-onPD[OF refl-action-order]*)**+**

lemma *antisym-sync-order*:

antisymp (sync-order P E)

using *antisympD[OF antisym-action-order]*

by(*rule antisympI*)(*auto elim!: sync-orderE*)

lemma *trans-sync-order*:

transp (sync-order P E)

by(*rule transpI*)(*auto elim!: sync-orderE intro: sync-orderI dest: transPD[OF trans-action-order]*)

lemma *porder-sync-order*:

porder-on (sactions P E) (sync-order P E)

by(*blast intro: porder-onI refl-on-sync-order antisym-sync-order trans-sync-order*)

lemma *total-sync-order*:

total-onP (sactions P E) (sync-order P E)

apply(*rule total-onPI*)

apply(*simp add: sync-order-def*)

apply(*rule total-onPD[OF total-action-order]*)

apply *simp-all*

done

lemma *torder-sync-order*:

torder-on (sactions P E) (sync-order P E)

by(*blast intro: torder-onI porder-sync-order total-sync-order*)

8.4.7 Synchronizes with

lemma *sync-withI*:

$\llbracket P, E \vdash a \leq_{so} a'; P \vdash (\text{action-tid } E \ a, \text{action-obs } E \ a) \rightsquigarrow_{sw} (\text{action-tid } E \ a', \text{action-obs } E \ a') \rrbracket$

$\implies P, E \vdash a \leq_{sw} a'$

unfolding *sync-with-def* **by** *blast*

lemma *sync-withE*:

assumes $P, E \vdash a \leq_{sw} a'$

obtains $P, E \vdash a \leq_{so} a' \vdash (action-tid\ E\ a, action-obs\ E\ a) \rightsquigarrow_{sw} (action-tid\ E\ a', action-obs\ E\ a')$

using *assms* **unfolding** *sync-with-def* **by** *blast*

lemma *irrefl-synchronizes-with*:

irreflP (*synchronizes-with* P)

by(*rule* *irreflPI*)(*auto* *elim*: *synchronizes-with.cases*)

lemma *irrefl-sync-with*:

irreflP (*sync-with* $P\ E$)

by(*rule* *irreflPI*)(*auto* *elim*: *sync-withE* *intro*: *irreflPD*[*OF* *irrefl-synchronizes-with*])

lemma *antisym-sync-with*:

antisymp (*sync-with* $P\ E$)

using *antisymPD*[*OF* *antisym-sync-order*, *of* $P\ E$]

by -- (*rule* *antisymPI*, *auto* *elim*: *sync-withE*)

lemma *consistent-program-order-sync-order*:

order-consistent (*program-order* E) (*sync-order* $P\ E$)

apply(*rule* *order-consistent-subset*)

apply(*rule* *antisym-order-consistent-self*[*OF* *antisym-action-order*[*of* E]])

apply(*blast* *elim*: *program-orderE* *sync-orderE*)**+**

done

lemma *consistent-program-order-sync-with*:

order-consistent (*program-order* E) (*sync-with* $P\ E$)

by(*rule* *order-consistent-subset*[*OF* *consistent-program-order-sync-order*])(*blast* *elim*: *sync-withE*)**+**

8.4.8 Happens before

lemma *porder-happens-before*:

porder-on (*actions* E) (*happens-before* $P\ E$)

unfolding *po-sw-def* [*abs-def*]

by(*rule* *porder-on-sub-torder-on-tranclp-porder-onI*[*OF* *porder-program-order* *torder-sync-order* *consistent-program-order-sync-order*])(*auto* *elim*: *sync-withE*)

lemma *porder-tranclp-po-so*:

porder-on (*actions* E) $(\lambda a\ a'. program-order\ E\ a\ a' \vee sync-order\ P\ E\ a\ a')^{\wedge++}$

by(*rule* *porder-on-torder-on-tranclp-porder-onI*[*OF* *porder-program-order* *torder-sync-order* *consistent-program-order-sync-order*])
auto

lemma *happens-before-refl*:

assumes $a \in actions\ E$

shows $P, E \vdash a \leq_{hb} a$

using *porder-happens-before*[*of* $E\ P$]

by(*rule* *porder-onE*)(*erule* *refl-onPD*[*OF* *-* *assms*])

lemma *happens-before-into-po-so-tranclp*:

assumes $P, E \vdash a \leq_{hb} a'$

shows $(\lambda a\ a'. E \vdash a \leq_{po} a' \vee P, E \vdash a \leq_{so} a')^{\wedge++} a\ a'$

using *assms* **unfolding** *po-sw-def* [*abs-def*]

by(*induct*)(*blast elim: sync-withE intro: tranclp.trancl-into-trancl*)+

lemma *po-sw-into-action-order*:

po-sw P E a a' $\implies E \vdash a \leq a'$

by(*auto elim: program-orderE sync-withE sync-orderE simp add: po-sw-def*)

lemma *happens-before-into-action-order*:

assumes *P, E $\vdash a \leq_{hb} a'$*

shows *E $\vdash a \leq a'$*

using *assms*

by(*induct(blast intro: po-sw-into-action-order transPD[OF trans-action-order])*)+

lemma *action-order-consistent-with-happens-before*:

order-consistent (action-order E) (happens-before P E)

by(*blast intro: order-consistent-subset antisym-order-consistent-self antisym-action-order happens-before-into-action*)

lemma *happens-before-new-actionD*:

assumes *hb: P, E $\vdash a \leq_{hb} a'$*

and *new: is-new-action (action-obs E a')*

shows *is-new-action (action-obs E a) action-tid E a = action-tid E a' a $\leq a'$*

using *hb*

proof(*induct rule: converse-tranclp-induct*)

case (*base a*)

case 1 from new base show *?case*

by(*auto dest: po-sw-into-action-order elim: action-orderE*)

case 2 from new base show *?case*

by(*auto simp add: po-sw-def elim!: sync-withE elim: program-orderE synchronizes-with.cases*)

case 3 from new base show *?case*

by(*auto dest: po-sw-into-action-order elim: action-orderE*)

next

case (*step a a''*)

note *po-sw = $\langle po-sw P E a a' \rangle$*

and *new = $\langle is-new-action (action-obs E a'') \rangle$*

and *tid = $\langle action-tid E a'' = action-tid E a' \rangle$*

case 1 from new po-sw show *?case*

by(*auto dest: po-sw-into-action-order elim: action-orderE*)

case 2 from new po-sw tid show *?case*

by(*auto simp add: po-sw-def elim!: sync-withE elim: program-orderE synchronizes-with.cases*)

case 3 from new po-sw $\langle a'' \leq a' \rangle$ show *?case*

by(*auto dest!: po-sw-into-action-order elim!: action-orderE*)

qed

lemma *external-actions-not-new*:

$\llbracket a \in \text{external-actions } E; \text{is-new-action (action-obs } E \ a) \rrbracket \implies \text{False}$

by(*erule external-actions.cases*)(*simp*)

8.4.9 Most recent writes and sequential consistency

lemma *most-recent-write-for-fun*:

$\llbracket P, E \vdash ra \rightsquigarrow_{mrw} wa; P, E \vdash ra \rightsquigarrow_{mrw} wa' \rrbracket \implies wa = wa'$

apply(*erule most-recent-write-for.cases*)+

```

apply clarsimp
apply(erule meta-allE)+
apply(erule meta-impE)
  apply(rotate-tac 3)
  apply assumption
apply(erule (1) meta-impE)
apply(frule (1) action-loc-read-action-singleton)
  apply(rotate-tac 1)
  apply assumption
apply(fastforce dest: antisymPD[OF antisym-action-order] elim: write-actions.cases read-actions.cases)
done

```

lemma *THE-most-recent-writeI*: $P, E \vdash r \rightsquigarrow mrw w \implies (THE w. P, E \vdash r \rightsquigarrow mrw w) = w$
by(*blast dest: most-recent-write-for-fun*)+

lemma *most-recent-write-for-write-actionsD*:
assumes $P, E \vdash ra \rightsquigarrow mrw wa$
shows $wa \in write-actions E$
using *assms* **by** *cases*

lemma *most-recent-write-recent*:

```

 $\llbracket P, E \vdash r \rightsquigarrow mrw w; adal \in action-loc P E r; w' \in write-actions E; adal \in action-loc P E w' \rrbracket$ 
 $\implies E \vdash w' \leq_a w \vee E \vdash r \leq_a w'$ 
apply(erule most-recent-write-for.cases)
apply(drule (1) action-loc-read-action-singleton)
  apply(rotate-tac 1)
  apply assumption
apply clarsimp
done

```

lemma *is-write-seenI*:

```

 $\llbracket \bigwedge a ad al v. \llbracket a \in read-actions E; action-obs E a = NormalAction (ReadMem ad al v) \rrbracket$ 
 $\implies ws a \in write-actions E;$ 
 $\bigwedge a ad al v. \llbracket a \in read-actions E; action-obs E a = NormalAction (ReadMem ad al v) \rrbracket$ 
 $\implies (ad, al) \in action-loc P E (ws a);$ 
 $\bigwedge a ad al v. \llbracket a \in read-actions E; action-obs E a = NormalAction (ReadMem ad al v) \rrbracket$ 
 $\implies value-written P E (ws a) (ad, al) = v;$ 
 $\bigwedge a ad al v. \llbracket a \in read-actions E; action-obs E a = NormalAction (ReadMem ad al v) \rrbracket$ 
 $\implies \neg P, E \vdash a \leq_{hb} ws a;$ 
 $\bigwedge a ad al v. \llbracket a \in read-actions E; action-obs E a = NormalAction (ReadMem ad al v); is-volatile$ 
 $P al \rrbracket$ 
 $\implies \neg P, E \vdash a \leq_{so} ws a;$ 
 $\bigwedge a ad al v a'. \llbracket a \in read-actions E; action-obs E a = NormalAction (ReadMem ad al v);$ 
 $a' \in write-actions E; (ad, al) \in action-loc P E a'; P, E \vdash ws a \leq_{hb} a';$ 
 $P, E \vdash a' \leq_{hb} a \rrbracket \implies a' = ws a;$ 
 $\bigwedge a ad al v a'. \llbracket a \in read-actions E; action-obs E a = NormalAction (ReadMem ad al v);$ 
 $a' \in write-actions E; (ad, al) \in action-loc P E a'; is-volatile P al; P, E \vdash ws a \leq_{so} a';$ 
 $P, E \vdash a' \leq_{so} a \rrbracket \implies a' = ws a \rrbracket$ 
 $\implies is-write-seen P E ws$ 
unfolding is-write-seen-def
by(blast 30)

```

lemma *is-write-seenD*:

```

 $\llbracket is-write-seen P E ws; a \in read-actions E; action-obs E a = NormalAction (ReadMem ad al v) \rrbracket$ 

```

$\implies ws\ a \in \text{write-actions } E \wedge (ad, al) \in \text{action-loc } P\ E\ (ws\ a) \wedge \text{value-written } P\ E\ (ws\ a)\ (ad, al) = v \wedge \neg P, E \vdash a \leq_{hb} ws\ a \wedge (\text{is-volatile } P\ al \implies \neg P, E \vdash a \leq_{so} ws\ a) \wedge$

$(\forall a' \in \text{write-actions } E. (ad, al) \in \text{action-loc } P\ E\ a' \wedge (P, E \vdash ws\ a \leq_{hb} a' \wedge P, E \vdash a' \leq_{hb} a \vee \text{is-volatile } P\ al \wedge P, E \vdash ws\ a \leq_{so} a' \wedge P, E \vdash a' \leq_{so} a) \implies a' = ws\ a)$

unfolding *is-write-seen-def* **by** *blast*

lemma *thread-start-actions-okI*:

$(\bigwedge a. \llbracket a \in \text{actions } E; \neg \text{is-new-action } (\text{action-obs } E\ a) \rrbracket$

$\implies \exists i. i \leq a \wedge \text{action-obs } E\ i = \text{InitialThreadAction} \wedge \text{action-tid } E\ i = \text{action-tid } E\ a)$

$\implies \text{thread-start-actions-ok } E$

unfolding *thread-start-actions-ok-def* **by** *blast*

lemma *thread-start-actions-okD*:

$\llbracket \text{thread-start-actions-ok } E; a \in \text{actions } E; \neg \text{is-new-action } (\text{action-obs } E\ a) \rrbracket$

$\implies \exists i. i \leq a \wedge \text{action-obs } E\ i = \text{InitialThreadAction} \wedge \text{action-tid } E\ i = \text{action-tid } E\ a$

unfolding *thread-start-actions-ok-def* **by** *blast*

lemma *thread-start-actions-ok-prefix*:

$\llbracket \text{thread-start-actions-ok } E'; \text{lprefix } E\ E' \rrbracket \implies \text{thread-start-actions-ok } E$

apply(*clarsimp simp add: lprefix-conv-lappend*)

apply(*rule thread-start-actions-okI*)

apply(*drule-tac a=a in thread-start-actions-okD*)

apply(*simp add: actions-def*)

apply(*auto simp add: action-obs-def lnth-lappend1 actions-def action-tid-def le-less-trans[where y=enat a for a]*)

apply (*metis add.right-neutral add-strict-mono not-gr-zero*)

done

lemma *wf-execI* [*intro ?*]:

$\llbracket \text{is-write-seen } P\ E\ ws;$

$\text{thread-start-actions-ok } E \rrbracket$

$\implies P \vdash (E, ws) \checkmark$

by *simp*

lemma *wf-exec-is-write-seenD*:

$P \vdash (E, ws) \checkmark \implies \text{is-write-seen } P\ E\ ws$

by *simp*

lemma *wf-exec-thread-start-actions-okD*:

$P \vdash (E, ws) \checkmark \implies \text{thread-start-actions-ok } E$

by *simp*

lemma *sequentially-consistentI*:

$(\bigwedge r. r \in \text{read-actions } E \implies P, E \vdash r \rightsquigarrow_{mrw} ws\ r)$

$\implies \text{sequentially-consistent } P\ (E, ws)$

by *simp*

lemma *sequentially-consistentE*:

assumes *sequentially-consistent* $P\ (E, ws)\ a \in \text{read-actions } E$

obtains $P, E \vdash a \rightsquigarrow_{mrw} ws\ a$

using *assms* **by** *simp*

declare *sequentially-consistent.simps* [*simp del*]

8.4.10 Similar actions

Similar actions differ only in the values written/read.

inductive *sim-action* ::

$(\text{'addr}, \text{'thread-id}) \text{ obs-event action} \Rightarrow (\text{'addr}, \text{'thread-id}) \text{ obs-event action} \Rightarrow \text{bool}$
 $(\leftarrow \approx \rightarrow [50, 50] 51)$

where

InitialThreadAction: $\text{InitialThreadAction} \approx \text{InitialThreadAction}$
| *ThreadFinishAction*: $\text{ThreadFinishAction} \approx \text{ThreadFinishAction}$
| *NewHeapElem*: $\text{NormalAction} (\text{NewHeapElem } a \text{ } hT) \approx \text{NormalAction} (\text{NewHeapElem } a \text{ } hT)$
| *ReadMem*: $\text{NormalAction} (\text{ReadMem } ad \text{ } al \text{ } v) \approx \text{NormalAction} (\text{ReadMem } ad \text{ } al \text{ } v')$
| *WriteMem*: $\text{NormalAction} (\text{WriteMem } ad \text{ } al \text{ } v) \approx \text{NormalAction} (\text{WriteMem } ad \text{ } al \text{ } v')$
| *ThreadStart*: $\text{NormalAction} (\text{ThreadStart } t) \approx \text{NormalAction} (\text{ThreadStart } t)$
| *ThreadJoin*: $\text{NormalAction} (\text{ThreadJoin } t) \approx \text{NormalAction} (\text{ThreadJoin } t)$
| *SyncLock*: $\text{NormalAction} (\text{SyncLock } a) \approx \text{NormalAction} (\text{SyncLock } a)$
| *SyncUnlock*: $\text{NormalAction} (\text{SyncUnlock } a) \approx \text{NormalAction} (\text{SyncUnlock } a)$
| *ExternalCall*: $\text{NormalAction} (\text{ExternalCall } a \text{ } M \text{ } vs \text{ } v) \approx \text{NormalAction} (\text{ExternalCall } a \text{ } M \text{ } vs \text{ } v)$
| *ObsInterrupt*: $\text{NormalAction} (\text{ObsInterrupt } t) \approx \text{NormalAction} (\text{ObsInterrupt } t)$
| *ObsInterrupted*: $\text{NormalAction} (\text{ObsInterrupted } t) \approx \text{NormalAction} (\text{ObsInterrupted } t)$

definition *sim-actions* :: $(\text{'addr}, \text{'thread-id}) \text{ execution} \Rightarrow (\text{'addr}, \text{'thread-id}) \text{ execution} \Rightarrow \text{bool}$ $(\leftarrow [\approx]$
 $\rightarrow [51, 50] 51)$

where *sim-actions* = $\text{lList-all2 } (\lambda(t, a) (t', a'). t = t' \wedge a \approx a')$

lemma *sim-action-refl* [*intro!*, *simp*]:

$obs \approx obs$

apply(*cases* *obs*)

apply(*rename-tac* *obs'*)

apply(*case-tac* *obs'*)

apply(*auto* *intro*: *sim-action.intros*)

done

inductive-cases *sim-action-cases* [*elim!*]:

$\text{InitialThreadAction} \approx obs$

$\text{ThreadFinishAction} \approx obs$

$\text{NormalAction} (\text{NewHeapElem } a \text{ } hT) \approx obs$

$\text{NormalAction} (\text{ReadMem } ad \text{ } al \text{ } v) \approx obs$

$\text{NormalAction} (\text{WriteMem } ad \text{ } al \text{ } v) \approx obs$

$\text{NormalAction} (\text{ThreadStart } t) \approx obs$

$\text{NormalAction} (\text{ThreadJoin } t) \approx obs$

$\text{NormalAction} (\text{SyncLock } a) \approx obs$

$\text{NormalAction} (\text{SyncUnlock } a) \approx obs$

$\text{NormalAction} (\text{ObsInterrupt } t) \approx obs$

$\text{NormalAction} (\text{ObsInterrupted } t) \approx obs$

$\text{NormalAction} (\text{ExternalCall } a \text{ } M \text{ } vs \text{ } v) \approx obs$

$obs \approx \text{InitialThreadAction}$

$obs \approx \text{ThreadFinishAction}$

$obs \approx \text{NormalAction} (\text{NewHeapElem } a \text{ } hT)$

$obs \approx \text{NormalAction} (\text{ReadMem } ad \text{ } al \text{ } v')$

$obs \approx \text{NormalAction} (\text{WriteMem } ad \text{ } al \text{ } v')$

$obs \approx \text{NormalAction} (\text{ThreadStart } t)$

$obs \approx \text{NormalAction} (\text{ThreadJoin } t)$

$obs \approx \text{NormalAction} (\text{SyncLock } a)$

$obs \approx NormalAction (SyncUnlock a)$
 $obs \approx NormalAction (ObsInterrupt t)$
 $obs \approx NormalAction (ObsInterrupted t)$
 $obs \approx NormalAction (ExternalCall a M vs v)$

inductive-simps *sim-action-simps* [*simp*]:

$InitialThreadAction \approx obs$
 $ThreadFinishAction \approx obs$
 $NormalAction (NewHeapElem a hT) \approx obs$
 $NormalAction (ReadMem ad al v) \approx obs$
 $NormalAction (WriteMem ad al v) \approx obs$
 $NormalAction (ThreadStart t) \approx obs$
 $NormalAction (ThreadJoin t) \approx obs$
 $NormalAction (SyncLock a) \approx obs$
 $NormalAction (SyncUnlock a) \approx obs$
 $NormalAction (ObsInterrupt t) \approx obs$
 $NormalAction (ObsInterrupted t) \approx obs$
 $NormalAction (ExternalCall a M vs v) \approx obs$

$obs \approx InitialThreadAction$
 $obs \approx ThreadFinishAction$
 $obs \approx NormalAction (NewHeapElem a hT)$
 $obs \approx NormalAction (ReadMem ad al v')$
 $obs \approx NormalAction (WriteMem ad al v')$
 $obs \approx NormalAction (ThreadStart t)$
 $obs \approx NormalAction (ThreadJoin t)$
 $obs \approx NormalAction (SyncLock a)$
 $obs \approx NormalAction (SyncUnlock a)$
 $obs \approx NormalAction (ObsInterrupt t)$
 $obs \approx NormalAction (ObsInterrupted t)$
 $obs \approx NormalAction (ExternalCall a M vs v)$

lemma *sim-action-trans* [*trans*]:

$\llbracket obs \approx obs'; obs' \approx obs'' \rrbracket \implies obs \approx obs''$

by(*erule sim-action.cases*) *auto*

lemma *sim-action-sym* [*sym*]:

assumes $obs \approx obs'$

shows $obs' \approx obs$

using *assms* **by** *cases simp-all*

lemma *sim-actions-sym* [*sym*]:

$E [\approx] E' \implies E' [\approx] E$

unfolding *sim-actions-def*

by(*auto simp add: llist-all2-conv-all-lnth split-beta intro: sim-action-sym*)

lemma *sim-actions-action-obsD*:

$E [\approx] E' \implies action-obs E a \approx action-obs E' a$

unfolding *sim-actions-def action-obs-def*

by(*cases enat a < llength E*)(*auto dest: llist-all2-lnthD llist-all2-llengthD simp add: split-beta lnth-beyond split: enat.split*)

lemma *sim-actions-action-tidD*:

$E [\approx] E' \implies action-tid E a = action-tid E' a$

unfolding *sim-actions-def action-tid-def*

by(cases enat a < llength E)(auto dest: llist-all2-lnthD llist-all2-llengthD simp add: lnth-beyond split: enat.split)

lemma *action-loc-aux-sim-action:*

$a \approx a' \implies \text{action-loc-aux } P \ a = \text{action-loc-aux } P \ a'$

by(auto elim!: action-loc-aux-cases intro: action-loc-aux-intros)

lemma *eq-into-sim-actions:*

assumes $E = E'$

shows $E \ [\approx] \ E'$

unfolding *sim-actions-def assms*

by(rule llist-all2-refl)(auto)

8.4.11 Well-formedness conditions for execution sets

locale *executions-base =*

fixes $\mathcal{E} :: ('addr, 'thread-id) \text{ execution set}$

and $P :: 'm \text{ prog}$

locale *drf =*

executions-base $\mathcal{E} \ P$

for $\mathcal{E} :: ('addr, 'thread-id) \text{ execution set}$

and $P :: 'm \text{ prog} +$

assumes *\mathcal{E} -new-actions-for-fun:*

$\llbracket E \in \mathcal{E}; a \in \text{new-actions-for } P \ E \ \text{adal}; a' \in \text{new-actions-for } P \ E \ \text{adal} \rrbracket \implies a = a'$

and *\mathcal{E} -sequential-completion:*

$\llbracket E \in \mathcal{E}; P \vdash (E, ws) \checkmark; \bigwedge a. \llbracket a < r; a \in \text{read-actions } E \rrbracket \implies P, E \vdash a \rightsquigarrow_{mrw} ws \ a \rrbracket$

$\implies \exists E' \in \mathcal{E}. \exists ws'. P \vdash (E', ws') \checkmark \wedge \text{ltake } (\text{enat } r) \ E = \text{ltake } (\text{enat } r) \ E' \wedge \text{sequentially-consistent } P \ (E', ws') \wedge$

$\text{action-tid } E \ r = \text{action-tid } E' \ r \wedge \text{action-obs } E \ r \approx \text{action-obs } E' \ r \wedge$

$(r \in \text{actions } E \longrightarrow r \in \text{actions } E')$

locale *executions-aux =*

executions-base $\mathcal{E} \ P$

for $\mathcal{E} :: ('addr, 'thread-id) \text{ execution set}$

and $P :: 'm \text{ prog} +$

assumes *init-before-read:*

$\llbracket E \in \mathcal{E}; P \vdash (E, ws) \checkmark; r \in \text{read-actions } E; \text{adal} \in \text{action-loc } P \ E \ r;$

$\bigwedge a. \llbracket a < r; a \in \text{read-actions } E \rrbracket \implies P, E \vdash a \rightsquigarrow_{mrw} ws \ a \rrbracket$

$\implies \exists i < r. i \in \text{new-actions-for } P \ E \ \text{adal}$

and *\mathcal{E} -new-actions-for-fun:*

$\llbracket E \in \mathcal{E}; a \in \text{new-actions-for } P \ E \ \text{adal}; a' \in \text{new-actions-for } P \ E \ \text{adal} \rrbracket \implies a = a'$

locale *sc-legal =*

executions-aux $\mathcal{E} \ P$

for $\mathcal{E} :: ('addr, 'thread-id) \text{ execution set}$

and $P :: 'm \text{ prog} +$

assumes *\mathcal{E} -hb-completion:*

$\llbracket E \in \mathcal{E}; P \vdash (E, ws) \checkmark; \bigwedge a. \llbracket a < r; a \in \text{read-actions } E \rrbracket \implies P, E \vdash a \rightsquigarrow_{mrw} ws \ a \rrbracket$

$\implies \exists E' \in \mathcal{E}. \exists ws'. P \vdash (E', ws') \checkmark \wedge \text{ltake } (\text{enat } r) \ E = \text{ltake } (\text{enat } r) \ E' \wedge$

$(\forall a \in \text{read-actions } E'. \text{if } a < r \text{ then } ws' \ a = ws \ a \ \text{else } P, E' \vdash ws' \ a \leq_{hb} a) \wedge$

$\text{action-tid } E' \ r = \text{action-tid } E \ r \wedge$

$(\text{if } r \in \text{read-actions } E \text{ then } \text{sim-action else } (=)) (\text{action-obs } E' \ r) (\text{action-obs } E \ r) \wedge$

$$(r \in \text{actions } E \longrightarrow r \in \text{actions } E')$$

locale *jmm-consistent* =
drf?: *drf* \mathcal{E} *P* +
sc-legal \mathcal{E} *P*
for $\mathcal{E} :: ('addr, 'thread-id)$ *execution set*
and *P* :: *'m prog*

8.4.12 Legal executions

record (*'addr, 'thread-id*) *pre-justifying-execution* =
committed :: *JMM-action set*
justifying-exec :: (*'addr, 'thread-id*) *execution*
justifying-ws :: *write-seen*

record (*'addr, 'thread-id*) *justifying-execution* =
(*'addr, 'thread-id*) *pre-justifying-execution* +
action-translation :: *JMM-action* \Rightarrow *JMM-action*

type-synonym (*'addr, 'thread-id*) *justification* = *nat* \Rightarrow (*'addr, 'thread-id*) *justifying-execution*

definition *wf-action-translation-on* ::
(*'addr, 'thread-id*) *execution* \Rightarrow (*'addr, 'thread-id*) *execution* \Rightarrow *JMM-action set* \Rightarrow (*JMM-action* \Rightarrow
JMM-action) \Rightarrow *bool*

where

$$\begin{aligned} & \text{wf-action-translation-on } E \ E' \ A \ f \longleftrightarrow \\ & \text{inj-on } f \ (\text{actions } E) \wedge \\ & (\forall a \in A. \text{action-tid } E \ a = \text{action-tid } E' \ (f \ a) \wedge \text{action-obs } E \ a \approx \text{action-obs } E' \ (f \ a)) \end{aligned}$$

abbreviation *wf-action-translation* :: (*'addr, 'thread-id*) *execution* \Rightarrow (*'addr, 'thread-id*) *justifying-execution*
 \Rightarrow *bool*

where

$$\begin{aligned} & \text{wf-action-translation } E \ J \equiv \\ & \text{wf-action-translation-on } (\text{justifying-exec } J) \ E \ (\text{committed } J) \ (\text{action-translation } J) \end{aligned}$$

context

fixes *P* :: *'m prog*
and *E* :: (*'addr, 'thread-id*) *execution*
and *ws* :: *write-seen*
and *J* :: (*'addr, 'thread-id*) *justification*

begin

This context defines the causality constraints for the JMM. The weak versions are for the fixed JMM as presented by Sevcik and Aspinal at ECOOP 2008.

Committed actions are an ascending chain with all actions of *E* as a limit

definition *is-commit-sequence* :: *bool* **where**

$$\begin{aligned} & \text{is-commit-sequence} \longleftrightarrow \\ & \text{committed } (J \ 0) = \{\} \wedge \\ & (\forall n. \text{action-translation } (J \ n) \text{ ' committed } (J \ n) \subseteq \text{action-translation } (J \ (\text{Suc } n)) \text{ ' committed } (J \\ & (\text{Suc } n))) \wedge \\ & \text{actions } E = (\bigcup n. \text{action-translation } (J \ n) \text{ ' committed } (J \ n)) \end{aligned}$$

definition *justification-well-formed* :: *bool* **where**

$$\text{justification-well-formed} \longleftrightarrow (\forall n. P \vdash (\text{justifying-exec } (J \ n), \text{justifying-ws } (J \ n)) \checkmark)$$

definition *committed-subset-actions* :: **bool where** — JMM constraint 1
committed-subset-actions \longleftrightarrow $(\forall n. \text{committed } (J\ n) \subseteq \text{actions } (\text{justifying-exec } (J\ n)))$

definition *happens-before-committed* :: **bool where** — JMM constraint 2
happens-before-committed \longleftrightarrow
 $(\forall n. \text{happens-before } P (\text{justifying-exec } (J\ n)) \mid' \text{committed } (J\ n) =$
 $\text{inv-image } P (\text{happens-before } P\ E) (\text{action-translation } (J\ n)) \mid' \text{committed } (J\ n))$

definition *happens-before-committed-weak* :: **bool where** — relaxed JMM constraint
happens-before-committed-weak \longleftrightarrow
 $(\forall n. \forall r \in \text{read-actions } (\text{justifying-exec } (J\ n)) \cap \text{committed } (J\ n).$
 $\text{let } r' = \text{action-translation } (J\ n)\ r;$
 $w' = \text{ws } r';$
 $w = \text{inv-into } (\text{actions } (\text{justifying-exec } (J\ n))) (\text{action-translation } (J\ n))\ w' \text{ in}$
 $(P, E \vdash w' \leq_{hb} r' \longleftrightarrow P, \text{justifying-exec } (J\ n) \vdash w \leq_{hb} r) \wedge$
 $\neg P, \text{justifying-exec } (J\ n) \vdash r \leq_{hb} w)$

definition *sync-order-committed* :: **bool where** — JMM constraint 3
sync-order-committed \longleftrightarrow
 $(\forall n. \text{sync-order } P (\text{justifying-exec } (J\ n)) \mid' \text{committed } (J\ n) =$
 $\text{inv-image } P (\text{sync-order } P\ E) (\text{action-translation } (J\ n)) \mid' \text{committed } (J\ n))$

definition *value-written-committed* :: **bool where** — JMM constraint 4
value-written-committed \longleftrightarrow
 $(\forall n. \forall w \in \text{write-actions } (\text{justifying-exec } (J\ n)) \cap \text{committed } (J\ n).$
 $\text{let } w' = \text{action-translation } (J\ n)\ w$
 $\text{in } (\forall \text{adal} \in \text{action-loc } P\ E\ w'. \text{value-written } P (\text{justifying-exec } (J\ n))\ w\ \text{adal} = \text{value-written } P$
 $E\ w'\ \text{adal}))$

definition *write-seen-committed* :: **bool where** — JMM constraint 5
write-seen-committed \longleftrightarrow
 $(\forall n. \forall r' \in \text{read-actions } (\text{justifying-exec } (J\ n)) \cap \text{committed } (J\ n).$
 $\text{let } r = \text{action-translation } (J\ n)\ r';$
 $r'' = \text{inv-into } (\text{actions } (\text{justifying-exec } (J\ (\text{Suc } n)))) (\text{action-translation } (J\ (\text{Suc } n)))\ r$
 $\text{in } \text{action-translation } (J\ (\text{Suc } n)) (\text{justifying-ws } (J\ (\text{Suc } n))\ r'') = \text{ws } r)$

uncommitted reads see writes that happen before them – JMM constraint 6

definition *uncommitted-reads-see-hb* :: **bool where**
uncommitted-reads-see-hb \longleftrightarrow
 $(\forall n. \forall r' \in \text{read-actions } (\text{justifying-exec } (J\ (\text{Suc } n))).$
 $\text{action-translation } (J\ (\text{Suc } n))\ r' \in \text{action-translation } (J\ n) \mid' \text{committed } (J\ n) \vee$
 $P, \text{justifying-exec } (J\ (\text{Suc } n)) \vdash \text{justifying-ws } (J\ (\text{Suc } n))\ r' \leq_{hb} r')$

newly committed reads see already committed writes and write-seen relationship must not change any more – JMM constraint 7

definition *committed-reads-see-committed-writes* :: **bool where**
committed-reads-see-committed-writes \longleftrightarrow
 $(\forall n. \forall r' \in \text{read-actions } (\text{justifying-exec } (J\ (\text{Suc } n))) \cap \text{committed } (J\ (\text{Suc } n)).$
 $\text{let } r = \text{action-translation } (J\ (\text{Suc } n))\ r';$
 $\text{committed-}n = \text{action-translation } (J\ n) \mid' \text{committed } (J\ n)$
 $\text{in } r \in \text{committed-}n \vee$
 $(\text{action-translation } (J\ (\text{Suc } n)) (\text{justifying-ws } (J\ (\text{Suc } n))\ r') \in \text{committed-}n \wedge \text{ws } r \in$
 $\text{committed-}n))$

definition *committed-reads-see-committed-writes-weak* :: *bool* **where**
committed-reads-see-committed-writes-weak \longleftrightarrow
 $(\forall n. \forall r' \in \text{read-actions } (\text{justifying-exec } (J (\text{Suc } n)))) \cap \text{committed } (J (\text{Suc } n)).$
let $r = \text{action-translation } (J (\text{Suc } n)) \ r'$;
committed-n = $\text{action-translation } (J \ n) \ \text{'committed } (J \ n)$
in $r \in \text{committed-n} \vee \text{ws } r \in \text{committed-n}$

external actions must be committed as soon as hb-subsequent actions are committed – JMM constraint 9

definition *external-actions-committed* :: *bool* **where**
external-actions-committed \longleftrightarrow
 $(\forall n. \forall a \in \text{external-actions } (\text{justifying-exec } (J \ n)). \forall a' \in \text{committed } (J \ n).$
 $P.\text{justifying-exec } (J \ n) \vdash a \leq_{\text{hb}} a' \longrightarrow a \in \text{committed } (J \ n))$

well-formedness conditions for action translations

definition *wf-action-translations* :: *bool* **where**
wf-action-translations \longleftrightarrow
 $(\forall n. \text{wf-action-translation-on } (\text{justifying-exec } (J \ n)) \ E \ (\text{committed } (J \ n)) \ (\text{action-translation } (J \ n)))$

end

Rule 8 of the justification for the JMM is incorrect because there might be no transitive reduction of the happens-before relation for an infinite execution, if infinitely many initialisation actions have to be ordered before the start action of every thread. Hence, *is-justified-by* omits this constraint.

primrec *is-justified-by* ::
 $'m \text{ prog} \Rightarrow ('addr, 'thread-id) \text{ execution} \times \text{write-seen} \Rightarrow ('addr, 'thread-id) \text{ justification} \Rightarrow \text{bool}$
 $(\leftarrow \vdash - \text{justified}'\text{-by} \rightarrow [51, 50, 50] \ 50)$

where

$P \vdash (E, \text{ws}) \text{ justified-by } J \longleftrightarrow$
is-commit-sequence $E \ J \ \wedge$
justification-well-formed $P \ J \ \wedge$
committed-subset-actions $J \ \wedge$
happens-before-committed $P \ E \ J \ \wedge$
sync-order-committed $P \ E \ J \ \wedge$
value-written-committed $P \ E \ J \ \wedge$
write-seen-committed $\text{ws } J \ \wedge$
uncommitted-reads-see-hb $P \ J \ \wedge$
committed-reads-see-committed-writes $\text{ws } J \ \wedge$
external-actions-committed $P \ J \ \wedge$
wf-action-translations $E \ J$

Sevcik requires in the fixed JMM that external actions may only be committed when everything that happens before has already been committed. On the level of legality, this constraint is vacuous because it is always possible to delay committing external actions, so we omit it here.

primrec *is-weakly-justified-by* ::
 $'m \text{ prog} \Rightarrow ('addr, 'thread-id) \text{ execution} \times \text{write-seen} \Rightarrow ('addr, 'thread-id) \text{ justification} \Rightarrow \text{bool}$
 $(\leftarrow \vdash - \text{weakly}'\text{-justified}'\text{-by} \rightarrow [51, 50, 50] \ 50)$

where

$P \vdash (E, \text{ws}) \text{ weakly-justified-by } J \longleftrightarrow$
is-commit-sequence $E \ J \ \wedge$
justification-well-formed $P \ J \ \wedge$

committed-subset-actions $J \wedge$
happens-before-committed-weak $P E ws J \wedge$
 — no *sync-order* constraint
value-written-committed $P E J \wedge$
write-seen-committed $ws J \wedge$
uncommitted-reads-see-hb $P J \wedge$
committed-reads-see-committed-writes-weak $ws J \wedge$
wf-action-translations $E J$

Notion of conflict is strengthened to explicitly exclude volatile locations. Otherwise, the following program is not correctly synchronised:

```

volatile x = 0;
-----
r = x; | x = 1;

```

because in the SC execution [Init x 0, (t1, Read x 0), (t2, Write x 1)], the read and write are unrelated in hb, because synchronises-with is asymmetric for volatiles.

The JLS considers conflicting volatiles for data races, but this is only a remark on the DRF guarantee. See JMM mailing list posts #2477 to 2488.

definition *non-volatile-conflict* ::

$'m \text{ prog} \Rightarrow ('addr, 'thread-id) \text{ execution} \Rightarrow JMM\text{-action} \Rightarrow JMM\text{-action} \Rightarrow \text{bool}$
 $(\langle \cdot, - \vdash / (-) \dagger (-) \rangle [51, 50, 50, 50] 51)$

where

$P, E \vdash a \dagger a' \longleftrightarrow$
 $(a \in \text{read-actions } E \wedge a' \in \text{write-actions } E \vee$
 $a \in \text{write-actions } E \wedge a' \in \text{read-actions } E \vee$
 $a \in \text{write-actions } E \wedge a' \in \text{write-actions } E) \wedge$
 $(\exists ad \text{ al. } (ad, al) \in \text{action-loc } P E a \cap \text{action-loc } P E a' \wedge \neg \text{is-volatile } P a)$

definition *correctly-synchronized* :: $'m \text{ prog} \Rightarrow ('addr, 'thread-id) \text{ execution set} \Rightarrow \text{bool}$

where

$\text{correctly-synchronized } P \mathcal{E} \longleftrightarrow$
 $(\forall E \in \mathcal{E}. \forall ws. P \vdash (E, ws) \checkmark \longrightarrow \text{sequentially-consistent } P (E, ws)$
 $\longrightarrow (\forall a \in \text{actions } E. \forall a' \in \text{actions } E. P, E \vdash a \dagger a'$
 $\longrightarrow P, E \vdash a \leq_{hb} a' \vee P, E \vdash a' \leq_{hb} a))$

primrec *gen-legal-execution* ::

$'m \text{ prog} \Rightarrow ('addr, 'thread-id) \text{ execution} \times \text{write-seen} \Rightarrow ('addr, 'thread-id) \text{ justification} \Rightarrow \text{bool}$
 $\Rightarrow 'm \text{ prog} \Rightarrow ('addr, 'thread-id) \text{ execution set} \Rightarrow ('addr, 'thread-id) \text{ execution} \times \text{write-seen} \Rightarrow \text{bool}$

where

$\text{gen-legal-execution is-justification } P \mathcal{E} (E, ws) \longleftrightarrow$
 $E \in \mathcal{E} \wedge P \vdash (E, ws) \checkmark \wedge$
 $(\exists J. \text{is-justification } P (E, ws) J \wedge \text{range } (justifying-exec \circ J) \subseteq \mathcal{E})$

abbreviation *legal-execution* ::

$'m \text{ prog} \Rightarrow ('addr, 'thread-id) \text{ execution set} \Rightarrow ('addr, 'thread-id) \text{ execution} \times \text{write-seen} \Rightarrow \text{bool}$

where

$\text{legal-execution} \equiv \text{gen-legal-execution is-justified-by}$

abbreviation *weakly-legal-execution* ::

$'m \text{ prog} \Rightarrow ('addr, 'thread-id) \text{ execution set} \Rightarrow ('addr, 'thread-id) \text{ execution} \times \text{write-seen} \Rightarrow \text{bool}$

where

weakly-legal-execution \equiv *gen-legal-execution is-weakly-justified-by*

declare *gen-legal-execution.simps* [*simp del*]

lemma *sym-non-volatile-conflict*:

symP (non-volatile-conflict P E)

unfolding *non-volatile-conflict-def*

by(*rule symPI*) **blast**

lemma *legal-executionI*:

$\llbracket E \in \mathcal{E}; P \vdash (E, ws) \checkmark; \text{is-justification } P (E, ws) J; \text{range } (\text{justifying-exec} \circ J) \subseteq \mathcal{E} \rrbracket$

$\implies \text{gen-legal-execution is-justification } P \mathcal{E} (E, ws)$

unfolding *gen-legal-execution.simps* **by** *blast*

lemma *legal-executionE*:

assumes *gen-legal-execution is-justification P E (E, ws)*

obtains *J* **where** $E \in \mathcal{E} P \vdash (E, ws) \checkmark \text{is-justification } P (E, ws) J \text{range } (\text{justifying-exec} \circ J) \subseteq \mathcal{E}$

using *assms* **unfolding** *gen-legal-execution.simps* **by** *blast*

lemma *legal-ED*: *gen-legal-execution is-justification P E (E, ws) $\implies E \in \mathcal{E}$*

by(*erule legal-executionE*)

lemma *legal-wf-execD*:

gen-legal-execution is-justification P E Ews $\implies P \vdash Ews \checkmark$

by(*cases Ews*)(*auto elim: legal-executionE*)

lemma *correctly-synchronizedD*:

$\llbracket \text{correctly-synchronized } P \mathcal{E}; E \in \mathcal{E}; P \vdash (E, ws) \checkmark; \text{sequentially-consistent } P (E, ws) \rrbracket$

$\implies \forall a a'. a \in \text{actions } E \longrightarrow a' \in \text{actions } E \longrightarrow P, E \vdash a \dagger a' \longrightarrow P, E \vdash a \leq_{hb} a' \vee P, E \vdash a' \leq_{hb} a$

unfolding *correctly-synchronized-def* **by** *blast*

lemma *wf-action-translation-on-actionD*:

$\llbracket \text{wf-action-translation-on } E E' A f; a \in A \rrbracket$

$\implies \text{action-tid } E a = \text{action-tid } E' (f a) \wedge \text{action-obs } E a \approx \text{action-obs } E' (f a)$

unfolding *wf-action-translation-on-def* **by** *blast*

lemma *wf-action-translation-on-inj-onD*:

wf-action-translation-on E E' A f $\implies \text{inj-on } f (\text{actions } E)$

unfolding *wf-action-translation-on-def* **by** *simp*

lemma *wf-action-translation-on-action-locD*:

$\llbracket \text{wf-action-translation-on } E E' A f; a \in A \rrbracket$

$\implies \text{action-loc } P E a = \text{action-loc } P E' (f a)$

apply(*drule (1) wf-action-translation-on-actionD*)

apply(*cases (P, action-obs E a) rule: action-loc-aux.cases*)

apply *auto*

done

lemma *weakly-justified-write-seen-hb-read-committed*:

assumes $J: P \vdash (E, ws) \text{weakly-justified-by } J$

and $r: r \in \text{read-actions } (\text{justifying-exec } (J n)) r \in \text{committed } (J n)$

shows $ws (\text{action-translation } (J n) r) \in \text{action-translation } (J n) \text{' committed } (J n)$

using r

```

proof(induct n arbitrary: r)
  case 0
  from J have [simp]: committed (J 0) = {}
    by(simp add: is-commit-sequence-def)
  with 0 show ?case by simp
next
  case (Suc n)
  let ?E =  $\lambda n. \text{justifying-exec } (J n)$ 
    and ?ws =  $\lambda n. \text{justifying-ws } (J n)$ 
    and ?C =  $\lambda n. \text{committed } (J n)$ 
    and ? $\varphi$  =  $\lambda n. \text{action-translation } (J n)$ 

  note  $r = \langle r \in \text{read-actions } (?E (Suc n)) \rangle$ 
  hence  $r \in \text{actions } (?E (Suc n))$  by simp

  from J have wfan: wf-action-translation-on (?E n) E (?C n) (? $\varphi$  n)
    and wfaSn: wf-action-translation-on (?E (Suc n)) E (?C (Suc n)) (? $\varphi$  (Suc n))
    by(simp-all add: wf-action-translations-def)

  from wfaSn have injSn: inj-on (? $\varphi$  (Suc n)) (actions (?E (Suc n)))
    by(rule wf-action-translation-on-inj-onD)
  from J have C-sub-A: ?C (Suc n)  $\subseteq$  actions (?E (Suc n))
    by(simp add: committed-subset-actions-def)
  from J have CnCSn: ? $\varphi$  n ' ?C n  $\subseteq$  ? $\varphi$  (Suc n) ' ?C (Suc n)
    by(simp add: is-commit-sequence-def)

  from J have wsSn: is-write-seen P (?E (Suc n)) (?ws (Suc n))
    by(simp add: justification-well-formed-def)
  from r obtain ad al v where action-obs (?E (Suc n)) r = NormalAction (ReadMem ad al v) by
cases
  from is-write-seenD[OF wsSn r this]
  have wsSn: ?ws (Suc n)  $r \in \text{actions } (?E (Suc n))$  by simp

  show ?case
  proof(cases ? $\varphi$  (Suc n) r  $\in$  ? $\varphi$  n ' ?C n)
    case True
    then obtain r' where  $r': r' \in ?C n$ 
      and  $r-r': ?\varphi (Suc n) r = ?\varphi n r'$  by(auto)
    from  $r'$  wfan have action-tid (?E n)  $r' = \text{action-tid } E$  (? $\varphi$  n  $r'$ )
      and action-obs (?E n)  $r' \approx \text{action-obs } E$  (? $\varphi$  n  $r'$ )
      by(blast dest: wf-action-translation-on-actionD)+
    moreover from  $r'$  CnCSn have ? $\varphi$  (Suc n)  $r \in ?\varphi$  (Suc n) ' ?C (Suc n)
      unfolding  $r-r'$  by auto
    hence  $r \in ?C (Suc n)$ 
      unfolding inj-on-image-mem-iff[OF injSn  $\langle r \in \text{actions } (?E (Suc n)) \rangle$  C-sub-A].
    with wfaSn have action-tid (?E (Suc n))  $r = \text{action-tid } E$  (? $\varphi$  (Suc n)  $r$ )
      and action-obs (?E (Suc n))  $r \approx \text{action-obs } E$  (? $\varphi$  (Suc n)  $r$ )
      by(blast dest: wf-action-translation-on-actionD)+
    ultimately have tid: action-tid (?E n)  $r' = \text{action-tid } (?E (Suc n)) r$ 
      and obs: action-obs (?E n)  $r' \approx \text{action-obs } (?E (Suc n)) r$ 
      unfolding  $r-r'$  by(auto intro: sim-action-trans sim-action-sym)

  from J have ?C n  $\subseteq$  actions (?E n) by(simp add: committed-subset-actions-def)
  with  $r'$  have  $r' \in \text{actions } (?E n)$  by blast

```

with $r \text{ obs}$ **have** $r' \in \text{read-actions } (?E \ n)$
by $\text{cases}(\text{auto intro: read-actions.intros})$
hence $ws: ws \ (?\varphi \ n \ r') \in ?\varphi \ n \ ' \ ?C \ n$ **using** r' **by** (rule Suc)

have $r\text{-conv-inv: } r = \text{inv-into } (\text{actions } (?E \ (\text{Suc } n))) \ (?\varphi \ (\text{Suc } n)) \ (?\varphi \ n \ r')$
using $\langle r \in \text{actions } (?E \ (\text{Suc } n)) \rangle$ **unfolding** $r\text{-r}'[\text{symmetric}]$
by $(\text{simp add: inv-into-f-f}[OF \ \text{injSn}])$
with $\langle r' \in ?C \ n \rangle \ r \ J \ \langle r' \in \text{read-actions } (?E \ n) \rangle$
have $ws\text{-eq: } ?\varphi \ (\text{Suc } n) \ (?ws \ (\text{Suc } n) \ r) = ws \ (?\varphi \ n \ r')$
by $(\text{simp add: Let-def write-seen-committed-def})$
with $ws \ CnCSn$ **have** $?\varphi \ (\text{Suc } n) \ (?ws \ (\text{Suc } n) \ r) \in ?\varphi \ (\text{Suc } n) \ ' \ ?C \ (\text{Suc } n)$ **by** auto
hence $?ws \ (\text{Suc } n) \ r \in ?C \ (\text{Suc } n)$
by $(\text{subst } (\text{asm}) \ \text{inj-on-image-mem-iff}[OF \ \text{injSn } wsSn \ C\text{-sub-A}])$
moreover from $ws \ CnCSn$ **have** $ws \ (?\varphi \ (\text{Suc } n) \ r) \in ?\varphi \ (\text{Suc } n) \ ' \ ?C \ (\text{Suc } n)$
unfolding $r\text{-r}'$ **by** auto
ultimately show $?thesis$ **by** simp

next
case False
with $r \ \langle r \in ?C \ (\text{Suc } n) \rangle \ J$
have $ws \ (?\varphi \ (\text{Suc } n) \ r) \in ?\varphi \ n \ ' \ ?C \ n$
unfolding $\text{is-weakly-justified-by.simps Let-def committed-reads-see-committed-writes-weak-def}$
by blast
hence $ws \ (?\varphi \ (\text{Suc } n) \ r) \in ?\varphi \ (\text{Suc } n) \ ' \ ?C \ (\text{Suc } n)$
using $CnCSn$ **by** blast+
thus $?thesis$ **by** $(\text{simp add: inj-on-image-mem-iff}[OF \ \text{injSn } wsSn \ C\text{-sub-A}])$

qed
qed

lemma $\text{justified-write-seen-hb-read-committed:}$
assumes $J: P \vdash (E, ws) \ \text{justified-by } J$
and $r: r \in \text{read-actions } (\text{justifying-exec } (J \ n)) \ r \in \text{committed } (J \ n)$
shows $\text{justifying-ws } (J \ n) \ r \in \text{committed } (J \ n)$ **(is** $?thesis1$ **)**
and $ws \ (\text{action-translation } (J \ n) \ r) \in \text{action-translation } (J \ n) \ ' \ \text{committed } (J \ n)$ **(is** $?thesis2$ **)**

proof $-$
have $?thesis1 \wedge ?thesis2$ **using** r
proof $(\text{induct } n \ \text{arbitrary: } r)$
case 0
from J **have** $[\text{simp}]: \text{committed } (J \ 0) = \{\}$
by $(\text{simp add: is-commit-sequence-def})$
with 0 **show** $?case$ **by** simp

next
case $(\text{Suc } n)$
let $?E = \lambda n. \ \text{justifying-exec } (J \ n)$
and $?ws = \lambda n. \ \text{justifying-ws } (J \ n)$
and $?C = \lambda n. \ \text{committed } (J \ n)$
and $?\varphi = \lambda n. \ \text{action-translation } (J \ n)$

note $r = \langle r \in \text{read-actions } (?E \ (\text{Suc } n)) \rangle$
hence $r \in \text{actions } (?E \ (\text{Suc } n))$ **by** simp

from J **have** $wfan: wf\text{-action-translation-on } (?E \ n) \ E \ (?C \ n) \ (?\varphi \ n)$
and $wfaSn: wf\text{-action-translation-on } (?E \ (\text{Suc } n)) \ E \ (?C \ (\text{Suc } n)) \ (?\varphi \ (\text{Suc } n))$
by $(\text{simp-all add: wf-action-translations-def})$

from $wfaSn$ **have** $injSn$: $inj\text{-on } (?φ (Suc\ n)) (actions\ (?E\ (Suc\ n)))$
by(*rule wf-action-translation-on-inj-onD*)
from J **have** $C\text{-sub-A}$: $?C\ (Suc\ n) \subseteq actions\ (?E\ (Suc\ n))$
by(*simp add: committed-subset-actions-def*)
from J **have** $CnCSn$: $?φ\ n\ ' \ ?C\ n \subseteq ?φ\ (Suc\ n)\ ' \ ?C\ (Suc\ n)$
by(*simp add: is-commit-sequence-def*)

from J **have** $wsSn$: $is\text{-write-seen } P\ (?E\ (Suc\ n))\ (?ws\ (Suc\ n))$
by(*simp add: justification-well-formed-def*)

from r **obtain** $ad\ al\ v$ **where** $action\text{-obs } (?E\ (Suc\ n))\ r = NormalAction\ (ReadMem\ ad\ al\ v)$ **by**
cases

from $is\text{-write-seenD}[OF\ wsSn\ r\ this]$
have $wsSn$: $?ws\ (Suc\ n)\ r \in actions\ (?E\ (Suc\ n))$ **by** *simp*

show $?case$

proof(*cases ?φ (Suc n) r ∈ ?φ n ' ?C n*)

case *True*

then obtain r' **where** r' : $r' \in ?C\ n$

and $r\text{-}r'$: $?φ\ (Suc\ n)\ r = ?φ\ n\ r'$ **by**(*auto*)

from r' $wfan$ **have** $action\text{-tid } (?E\ n)\ r' = action\text{-tid } E\ (?φ\ n\ r')$

and $action\text{-obs } (?E\ n)\ r' \approx action\text{-obs } E\ (?φ\ n\ r')$

by(*blast dest: wf-action-translation-on-actionD*)+

moreover from r' $CnCSn$ **have** $?φ\ (Suc\ n)\ r \in ?φ\ (Suc\ n)\ ' \ ?C\ (Suc\ n)$

unfolding $r\text{-}r'$ **by** *auto*

hence $r \in ?C\ (Suc\ n)$

unfolding $inj\text{-on-image-mem-iff}[OF\ injSn\ \langle r \in actions\ (?E\ (Suc\ n)) \rangle\ C\text{-sub-A}]$.

with $wfaSn$ **have** $action\text{-tid } (?E\ (Suc\ n))\ r = action\text{-tid } E\ (?φ\ (Suc\ n)\ r)$

and $action\text{-obs } (?E\ (Suc\ n))\ r \approx action\text{-obs } E\ (?φ\ (Suc\ n)\ r)$

by(*blast dest: wf-action-translation-on-actionD*)+

ultimately have tid : $action\text{-tid } (?E\ n)\ r' = action\text{-tid } (?E\ (Suc\ n))\ r$

and obs : $action\text{-obs } (?E\ n)\ r' \approx action\text{-obs } (?E\ (Suc\ n))\ r$

unfolding $r\text{-}r'$ **by**(*auto intro: sim-action-trans sim-action-sym*)

from J **have** $?C\ n \subseteq actions\ (?E\ n)$ **by**(*simp add: committed-subset-actions-def*)

with r' **have** $r' \in actions\ (?E\ n)$ **by** *blast*

with $r\ obs$ **have** $r' \in read\text{-actions } (?E\ n)$

by *cases(auto intro: read-actions.intros)*

hence $?ws\ n\ r' \in ?C\ n \wedge ws\ (?φ\ n\ r') \in ?φ\ n\ ' \ ?C\ n$ **using** r' **by**(*rule Suc*)

then obtain ws : $ws\ (?φ\ n\ r') \in ?φ\ n\ ' \ ?C\ n$..

have $r\text{-conv-inv}$: $r = inv\text{-into } (actions\ (?E\ (Suc\ n)))\ (?φ\ (Suc\ n))\ (?φ\ n\ r')$

using $\langle r \in actions\ (?E\ (Suc\ n)) \rangle$ **unfolding** $r\text{-}r'$ [*symmetric*]

by(*simp add: inv-into-f-f[OF injSn]*)

with $\langle r' \in ?C\ n \rangle\ r\ J\ \langle r' \in read\text{-actions } (?E\ n) \rangle$

have $ws\text{-eq}$: $?φ\ (Suc\ n)\ (?ws\ (Suc\ n)\ r) = ws\ (?φ\ n\ r')$

by(*simp add: Let-def write-seen-committed-def*)

with $ws\ CnCSn$ **have** $?φ\ (Suc\ n)\ (?ws\ (Suc\ n)\ r) \in ?φ\ (Suc\ n)\ ' \ ?C\ (Suc\ n)$ **by** *auto*

hence $?ws\ (Suc\ n)\ r \in ?C\ (Suc\ n)$

by(*subst (asm) inj-on-image-mem-iff[OF injSn wsSn C-sub-A]*)

moreover from $ws\ CnCSn$ **have** $ws\ (?φ\ (Suc\ n)\ r) \in ?φ\ (Suc\ n)\ ' \ ?C\ (Suc\ n)$

unfolding $r\text{-}r'$ **by** *auto*

ultimately show $?thesis$ **by** *simp*

next

case *False*

```

with  $r \langle r \in ?C (Suc\ n) \rangle J$ 
have  $? \varphi (Suc\ n) (?ws (Suc\ n)\ r) \in ? \varphi\ n \text{ ' } ?C\ n$ 
  and  $ws (? \varphi (Suc\ n)\ r) \in ? \varphi\ n \text{ ' } ?C\ n$ 
  unfolding is-justified-by.simps Let-def committed-reads-see-committed-writes-def
  by blast+
hence  $? \varphi (Suc\ n) (?ws (Suc\ n)\ r) \in ? \varphi (Suc\ n) \text{ ' } ?C (Suc\ n)$ 
  and  $ws (? \varphi (Suc\ n)\ r) \in ? \varphi (Suc\ n) \text{ ' } ?C (Suc\ n)$ 
  using CnCSn by blast+
thus ?thesis by(simp add: inj-on-image-mem-iff[OF injSn wsSn C-sub-A])
qed
qed
thus ?thesis1 ?thesis2 by simp-all
qed

lemma is-justified-by-imp-is-weakly-justified-by:
  assumes justified:  $P \vdash (E, ws) \text{ justified-by } J$ 
  and wf:  $P \vdash (E, ws) \checkmark$ 
  shows  $P \vdash (E, ws) \text{ weakly-justified-by } J$ 
  unfolding is-weakly-justified-by.simps
proof(intro conjI)
  let  $?E = \lambda n. \text{justifying-exec } (J\ n)$ 
  and  $?ws = \lambda n. \text{justifying-ws } (J\ n)$ 
  and  $?C = \lambda n. \text{committed } (J\ n)$ 
  and  $? \varphi = \lambda n. \text{action-translation } (J\ n)$ 

  from justified
  show is-commit-sequence  $E\ J$  justification-well-formed  $P\ J$  committed-subset-actions  $J$ 
    value-written-committed  $P\ E\ J$  write-seen-committed  $ws\ J$  uncommitted-reads-see-hb  $P\ J$ 
    wf-action-translations  $E\ J$  by(simp-all)

  show happens-before-committed-weak  $P\ E\ ws\ J$ 
    unfolding happens-before-committed-weak-def Let-def
  proof(intro strip conjI)
    fix  $n\ r$ 
    assume  $r \in \text{read-actions } (?E\ n) \cap ?C\ n$ 
    hence read:  $r \in \text{read-actions } (?E\ n)$  and committed:  $r \in ?C\ n$  by simp-all
    with justified have committed-ws:  $?ws\ n\ r \in ?C\ n$ 
      and committed-ws':  $ws (? \varphi\ n\ r) \in ? \varphi\ n \text{ ' } ?C\ n$ 
      by(rule justified-write-seen-hb-read-committed)+
    from committed-ws' obtain  $w$  where  $w$ :  $ws (? \varphi\ n\ r) = ? \varphi\ n\ w$ 
      and committed-w:  $w \in ?C\ n$  by blast

    from committed-w justified have  $w \in \text{actions } (?E\ n)$  by(auto simp add: committed-subset-actions-def)
    moreover from justified have inj-on  $(? \varphi\ n) (\text{actions } (?E\ n))$ 
      by(auto simp add: wf-action-translations-def dest: wf-action-translation-on-inj-onD)
    ultimately have w-def:  $w = \text{inv-into } (\text{actions } (?E\ n)) (? \varphi\ n) (ws (? \varphi\ n\ r))$ 
      by(simp-all add: w)

    from committed committed-w
    have  $P, ?E\ n \vdash w \leq_{hb} r \longleftrightarrow (\text{happens-before } P (?E\ n) \mid \text{ ' } ?C\ n) w\ r$  by auto
    also have  $\dots \longleftrightarrow (\text{inv-image } P (\text{happens-before } P\ E) (? \varphi\ n) \mid \text{ ' } ?C\ n) w\ r$ 
      using justified by(simp add: happens-before-committed-def)
    also have  $\dots \longleftrightarrow P, E \vdash ? \varphi\ n\ w \leq_{hb} ? \varphi\ n\ r$  using committed committed-w by auto
    finally show  $P, E \vdash ws (? \varphi\ n\ r) \leq_{hb} ? \varphi\ n\ r \longleftrightarrow P, ?E\ n \vdash \text{inv-into } (\text{actions } (?E\ n)) (? \varphi\ n) (ws$ 

```

$(? \varphi \ n \ r)) \leq_{hb} r$

unfolding w [*symmetric*] **unfolding** w -def ..

have $P, ?E \ n \vdash \ r \leq_{hb} w \iff (\text{happens-before } P \ (?E \ n) \ |' \ ?C \ n) \ r \ w$

using *committed committed-w* **by** *auto*

also have $\dots \iff (\text{inv-image } P \ (\text{happens-before } P \ E) \ (? \varphi \ n) \ |' \ ?C \ n) \ r \ w$

using *justified* **by** (*simp add: happens-before-committed-def*)

also have $\dots \iff P, E \vdash \ ? \varphi \ n \ r \leq_{hb} ws \ (? \varphi \ n \ r)$ **using** *w committed committed-w* **by** *auto*

also {

from *read* **obtain** $ad \ al \ v$ **where** *action-obs* $(?E \ n) \ r = \text{NormalAction } (\text{ReadMem } ad \ al \ v)$ **by**

cases auto

with *justified committed* **obtain** v' **where** *obs'*: *action-obs* $E \ (? \varphi \ n \ r) = \text{NormalAction } (\text{ReadMem } ad \ al \ v')$

by (*fastforce simp add: wf-action-translations-def dest!: wf-action-translation-on-actionD*)

moreover from *committed justified* **have** $? \varphi \ n \ r \in \text{actions } E$

by (*auto simp add: is-commit-sequence-def*)

ultimately have *read'*: $? \varphi \ n \ r \in \text{read-actions } E$ **by** (*auto intro: read-actions.intros*)

from *wf* **have** *is-write-seen* $P \ E \ ws$ **by** (*rule wf-exec-is-write-seenD*)

from *is-write-seenD* [*OF this read' obs'*]

have $\neg P, E \vdash \ ? \varphi \ n \ r \leq_{hb} ws \ (? \varphi \ n \ r)$ **by** *simp* }

ultimately show $\neg P, ?E \ n \vdash \ r \leq_{hb} \text{inv-into } (\text{actions } (?E \ n)) \ (? \varphi \ n) \ (ws \ (? \varphi \ n \ r))$

unfolding w -def **by** *simp*

qed

from *justified* **have** *committed-reads-see-committed-writes* $ws \ J$ **by** *simp*

thus *committed-reads-see-committed-writes-weak* $ws \ J$

by (*auto simp add: committed-reads-see-committed-writes-def committed-reads-see-committed-writes-weak-def*)

qed

corollary *legal-imp-weakly-legal-execution*:

legal-execution $P \ \mathcal{E} \ Ews \implies \text{weakly-legal-execution } P \ \mathcal{E} \ Ews$

by (*cases Ews*) (*auto 4 4 simp add: gen-legal-execution.simps simp del: is-justified-by.simps is-weakly-justified-by.simps intro: is-justified-by-imp-is-weakly-justified-by*)

lemma *drop-0th-justifying-exec*:

assumes $P \vdash (E, ws) \text{ justified-by } J$

and $wf: P \vdash (E', ws') \checkmark$

shows $P \vdash (E, ws) \text{ justified-by } (J(0 := (\text{committed} = \{\}, \text{justifying-exec} = E', \text{justifying-ws} = ws', \text{action-translation} = \text{id})))$

(**is** - \vdash - *justified-by* $?J$)

using *assms*

unfolding *is-justified-by.simps is-commit-sequence-def*

justification-well-formed-def committed-subset-actions-def happens-before-committed-def

sync-order-committed-def value-written-committed-def write-seen-committed-def uncommitted-reads-see-hb-def

committed-reads-see-committed-writes-def external-actions-committed-def wf-action-translations-def

proof (*intro conjI strip*)

let $?E = \lambda n. \text{justifying-exec } (?J \ n)$

and $? \varphi = \lambda n. \text{action-translation } (?J \ n)$

and $?C = \lambda n. \text{committed } (?J \ n)$

and $?ws = \lambda n. \text{justifying-ws } (?J \ n)$

show $?C \ 0 = \{\}$ **by** *simp*

from *assms* **have** $C-0: \text{committed } (J \ 0) = \{\}$ **by** (*simp add: is-commit-sequence-def*)

hence $(\bigcup n. ?\varphi n \text{ ' } ?C n) = (\bigcup n. \text{action-translation } (J n) \text{ ' committed } (J n))$
 by $-(\text{rule SUP-cong, simp-all})$
 also have $\dots = \text{actions } E$ using $\text{assms by}(\text{simp add: is-commit-sequence-def})$
 finally show $\text{actions } E = (\bigcup n. ?\varphi n \text{ ' } ?C n) ..$

```

fix n
{ fix r'
  assume r' ∈ read-actions (?E (Suc n))
  thus ?φ (Suc n) r' ∈ ?φ n ' ?C n ∨ P, ?E (Suc n) ⊢ ?ws (Suc n) r' ≤hb r'
    using assms by(auto dest!: bspec simp add: uncommitted-reads-see-hb-def is-commit-sequence-def)
}
{ fix r'
  assume r': r' ∈ read-actions (?E (Suc n)) ∩ ?C (Suc n)

  have n ≠ 0
  proof
    assume n = 0
    hence r' ∈ read-actions (justifying-exec (J 1)) ∩ committed (J 1) using r' by simp
    hence action-translation (J 1) r' ∈ action-translation (J 0) ' committed (J 0) ∨
      ws (action-translation (J 1) r') ∈ action-translation (J 0) ' committed (J 0) using assms
    unfolding One-nat-def is-justified-by.simps Let-def committed-reads-see-committed-writes-def
    by(metis (lifting))
    thus False unfolding C-0 by simp
  qed
  thus let r = ?φ (Suc n) r'; committed-n = ?φ n ' ?C n
    in r ∈ committed-n ∨
      (?φ (Suc n) (?ws (Suc n) r') ∈ committed-n ∧ ws r ∈ committed-n)
    using assms r' by(simp add: committed-reads-see-committed-writes-def) }
{ fix a a'
  assume a ∈ external-actions (?E n)
  and a' ∈ ?C n P, ?E n ⊢ a ≤hb a'
  moreover hence n > 0 by(simp split: if-split-asm)
  ultimately show a ∈ ?C n using assms
  by(simp add: external-actions-committed-def) blast }

```

```

from assms have wf-action-translation E (?J 0)
  by(simp add: wf-action-translations-def wf-action-translation-on-def)
thus wf-action-translation E (?J n) using assms by(simp add: wf-action-translations-def)
qed auto

```

lemma *drop-0th-weakly-justifying-exec*:

```

assumes P ⊢ (E, ws) weakly-justified-by J
and wf: P ⊢ (E', ws') √
shows P ⊢ (E, ws) weakly-justified-by (J(0 := (|committed = {}, justifying-exec = E', justifying-ws
= ws', action-translation = id)))
(is - ⊢ - weakly-justified-by ?J)
using assms
unfolding is-weakly-justified-by.simps is-commit-sequence-def
justification-well-formed-def committed-subset-actions-def happens-before-committed-weak-def
value-written-committed-def write-seen-committed-def uncommitted-reads-see-hb-def
committed-reads-see-committed-writes-weak-def external-actions-committed-def wf-action-translations-def
proof(intro conjI strip)
  let ?E = λn. justifying-exec (?J n)
  and ?φ = λn. action-translation (?J n)

```

and $?C = \lambda n. \text{committed } (?J n)$
 and $?ws = \lambda n. \text{justifying-ws } (?J n)$

show $?C 0 = \{\}$ by *simp*

from *assms* have $C-0: \text{committed } (J 0) = \{\}$ by(*simp add: is-commit-sequence-def*)
 hence $(\bigcup n. ?\varphi n \text{ ' } ?C n) = (\bigcup n. \text{action-translation } (J n) \text{ ' committed } (J n))$
 by $-(\text{rule SUP-cong, simp-all})$
 also have $\dots = \text{actions } E$ using *assms* by(*simp add: is-commit-sequence-def*)
 finally show $\text{actions } E = (\bigcup n. ?\varphi n \text{ ' } ?C n) ..$

fix n

{ fix r'
 assume $r' \in \text{read-actions } (?E (Suc n))$
 thus $?\varphi (Suc n) r' \in ?\varphi n \text{ ' } ?C n \vee P, ?E (Suc n) \vdash ?ws (Suc n) r' \leq_{hb} r'$
 using *assms* by(*auto dest!: bspec simp add: uncommitted-reads-see-hb-def is-commit-sequence-def*)

}

{ fix r'
 assume $r': r' \in \text{read-actions } (?E (Suc n)) \cap ?C (Suc n)$

have $n \neq 0$

proof

assume $n = 0$

hence $r' \in \text{read-actions } (\text{justifying-exec } (J 1)) \cap \text{committed } (J 1)$ using r' by *simp*

hence $\text{action-translation } (J 1) r' \in \text{action-translation } (J 0) \text{ ' committed } (J 0) \vee$

$ws (\text{action-translation } (J 1) r') \in \text{action-translation } (J 0) \text{ ' committed } (J 0)$ using *assms*

unfolding *One-nat-def is-weakly-justified-by.simps Let-def committed-reads-see-committed-writes-weak-def*
 by(*metis (lifting)*)

thus *False* unfolding *C-0* by *simp*

qed

thus let $r = ?\varphi (Suc n) r'$; $\text{committed-n} = ?\varphi n \text{ ' } ?C n$

in $r \in \text{committed-n} \vee ws r \in \text{committed-n}$

using *assms* r' by(*simp add: committed-reads-see-committed-writes-weak-def*) }

from *assms* have $\text{wf-action-translation } E (?J 0)$

by(*simp add: wf-action-translations-def wf-action-translation-on-def*)

thus $\text{wf-action-translation } E (?J n)$ using *assms* by(*simp add: wf-action-translations-def*)

qed *auto*

8.4.13 Executions with common prefix

lemma *actions-change-prefix*:

assumes *read*: $a \in \text{actions } E$

and *prefix*: $\text{ltake } n E [\approx] \text{ltake } n E'$

and *rn*: $\text{enat } a < n$

shows $a \in \text{actions } E'$

using *l1ist-all2-llengthD[OF prefix[unfolded sim-actions-def]] read rn*

by(*simp add: actions-def min-def split: if-split-asm*)

lemma *action-obs-change-prefix*:

assumes *prefix*: $\text{ltake } n E [\approx] \text{ltake } n E'$

and *rn*: $\text{enat } a < n$

shows $\text{action-obs } E a \approx \text{action-obs } E' a$

proof –

from rn **have** $action-obs\ E\ a = action-obs\ (ltake\ n\ E)\ a$
 by(*simp add: action-obs-def lnth-ltake*)
also from $prefix$ **have** $\dots \approx action-obs\ (ltake\ n\ E')\ a$
 by(*rule sim-actions-action-obsD*)
also have $\dots = action-obs\ E'\ a$ **using** rn
 by(*simp add: action-obs-def lnth-ltake*)
finally show *?thesis* .
qed

lemma *action-obs-change-prefix-eq*:
assumes $prefix: ltake\ n\ E = ltake\ n\ E'$
and $rn: enat\ a < n$
shows $action-obs\ E\ a = action-obs\ E'\ a$

proof –

from rn **have** $action-obs\ E\ a = action-obs\ (ltake\ n\ E)\ a$
 by(*simp add: action-obs-def lnth-ltake*)
also from $prefix$ **have** $\dots = action-obs\ (ltake\ n\ E')\ a$
 by(*simp add: action-obs-def*)
also have $\dots = action-obs\ E'\ a$ **using** rn
 by(*simp add: action-obs-def lnth-ltake*)
finally show *?thesis* .

qed

lemma *read-actions-change-prefix*:
assumes $read: r \in read-actions\ E$
and $prefix: ltake\ n\ E [\approx] ltake\ n\ E'\ enat\ r < n$
shows $r \in read-actions\ E'$
using $read\ action-obs-change-prefix[OF\ prefix]\ actions-change-prefix[OF\ -\ prefix]$
by(*cases*)(*auto intro: read-actions.intros*)

lemma *sim-action-is-write-action-eq*:
assumes $obs \approx obs'$
shows $is-write-action\ obs \longleftrightarrow is-write-action\ obs'$
using *assms by cases simp-all*

lemma *write-actions-change-prefix*:
assumes $write: w \in write-actions\ E$
and $prefix: ltake\ n\ E [\approx] ltake\ n\ E'\ enat\ w < n$
shows $w \in write-actions\ E'$
using $write\ action-obs-change-prefix[OF\ prefix]\ actions-change-prefix[OF\ -\ prefix]$
by(*cases*)(*auto intro: write-actions.intros dest: sim-action-is-write-action-eq*)

lemma *action-loc-change-prefix*:
assumes $ltake\ n\ E [\approx] ltake\ n\ E'\ enat\ a < n$
shows $action-loc\ P\ E\ a = action-loc\ P\ E'\ a$
using $action-obs-change-prefix[OF\ assms]$
by(*fastforce elim!: action-loc-aux-cases intro: action-loc-aux-intros*)

lemma *sim-action-is-new-action-eq*:
assumes $obs \approx obs'$
shows $is-new-action\ obs = is-new-action\ obs'$
using *assms by cases auto*

lemma *action-order-change-prefix*:

assumes $ao: E \vdash a \leq a'$
and $prefix: ltake\ n\ E \approx ltake\ n\ E'$
and $an: enat\ a < n$
and $a'n: enat\ a' < n$
shows $E' \vdash a \leq a'$
using $ao\ actions-change-prefix[OF\ -\ prefix\ an]\ actions-change-prefix[OF\ -\ prefix\ a'n]\ action-obs-change-prefix[OF\ prefix\ an]\ action-obs-change-prefix[OF\ prefix\ a'n]$
by($auto\ simp\ add: action-order-def\ split: if-split-asm\ dest: sim-action-is-new-action-eq$)

lemma *value-written-change-prefix*:
assumes $eq: ltake\ n\ E = ltake\ n\ E'$
and $an: enat\ a < n$
shows $value-written\ P\ E\ a = value-written\ P\ E'\ a$
using $action-obs-change-prefix-eq[OF\ eq\ an]$
by($simp\ add: value-written-def\ fun-eq-iff$)

lemma *action-tid-change-prefix*:
assumes $prefix: ltake\ n\ E \approx ltake\ n\ E'$
and $an: enat\ a < n$
shows $action-tid\ E\ a = action-tid\ E'\ a$

proof –
from an **have** $action-tid\ E\ a = action-tid\ (ltake\ n\ E)\ a$
by($simp\ add: action-tid-def\ lnth-ltake$)
also from $prefix$ **have** $\dots = action-tid\ (ltake\ n\ E')\ a$
by($rule\ sim-actions-action-tidD$)
also from an **have** $\dots = action-tid\ E'\ a$
by($simp\ add: action-tid-def\ lnth-ltake$)
finally show $?thesis$.
qed

lemma *program-order-change-prefix*:
assumes $po: E \vdash a \leq_{po} a'$
and $prefix: ltake\ n\ E \approx ltake\ n\ E'$
and $an: enat\ a < n$
and $a'n: enat\ a' < n$
shows $E' \vdash a \leq_{po} a'$
using $po\ action-order-change-prefix[OF\ -\ prefix\ an\ a'n]\ action-tid-change-prefix[OF\ prefix\ an]\ action-tid-change-prefix[OF\ prefix\ a'n]$
by($auto\ elim!: program-orderE\ intro: program-orderI$)

lemma *sim-action-sactionD*:
assumes $obs \approx obs'$
shows $saction\ P\ obs \longleftrightarrow saction\ P\ obs'$
using $assms$ **by** $cases\ simp-all$

lemma *sactions-change-prefix*:
assumes $sync: a \in sactions\ P\ E$
and $prefix: ltake\ n\ E \approx ltake\ n\ E'$
and $rn: enat\ a < n$
shows $a \in sactions\ P\ E'$
using $sync\ action-obs-change-prefix[OF\ prefix\ rn]\ actions-change-prefix[OF\ -\ prefix\ rn]$
unfolding $sactions-def$ **by**($simp\ add: sim-action-sactionD$)

lemma *sync-order-change-prefix*:

assumes *so*: $P, E \vdash a \leq_{so} a'$
and *prefix*: $l\text{take } n \ E \ [\approx] \ l\text{take } n \ E'$
and *an*: $\text{enat } a < n$
and *a'n*: $\text{enat } a' < n$
shows $P, E' \vdash a \leq_{so} a'$

using *so* *action-order-change-prefix*[*OF* - *prefix an a'n*] *sactions-change-prefix*[*OF* - *prefix an*, of *P*]
sactions-change-prefix[*OF* - *prefix a'n*, of *P*]

by(*simp add: sync-order-def*)

lemma *sim-action-synchronizes-withD*:

assumes $\text{obs} \approx \text{obs}' \ \text{obs}'' \approx \text{obs}'''$
shows $P \vdash (t, \text{obs}) \rightsquigarrow_{sw} (t', \text{obs}'') \iff P \vdash (t, \text{obs}') \rightsquigarrow_{sw} (t', \text{obs}''')$

using *assms*

by(*auto elim!: sim-action.cases synchronizes-with.cases intro: synchronizes-with.intros*)

lemma *sync-with-change-prefix*:

assumes *sw*: $P, E \vdash a \leq_{sw} a'$
and *prefix*: $l\text{take } n \ E \ [\approx] \ l\text{take } n \ E'$
and *an*: $\text{enat } a < n$
and *a'n*: $\text{enat } a' < n$
shows $P, E' \vdash a \leq_{sw} a'$

using *sw* *sync-order-change-prefix*[*OF* - *prefix an a'n*, of *P*]

action-tid-change-prefix[*OF prefix an*] *action-tid-change-prefix*[*OF prefix a'n*]
action-obs-change-prefix[*OF prefix an*] *action-obs-change-prefix*[*OF prefix a'n*]

by(*auto simp add: sync-with-def dest: sim-action-synchronizes-withD*)

lemma *po-sw-change-prefix*:

assumes *posw*: $\text{po-sw } P \ E \ a \ a'$
and *prefix*: $l\text{take } n \ E \ [\approx] \ l\text{take } n \ E'$
and *an*: $\text{enat } a < n$
and *a'n*: $\text{enat } a' < n$
shows $\text{po-sw } P \ E' \ a \ a'$

using *posw* *sync-with-change-prefix*[*OF* - *prefix an a'n*, of *P*] *program-order-change-prefix*[*OF* - *prefix an a'n*]

by(*auto simp add: po-sw-def*)

lemma *happens-before-new-not-new*:

assumes *tsa-ok*: *thread-start-actions-ok* *E*
and *a*: $a \in \text{actions } E$
and *a'*: $a' \in \text{actions } E$
and *new-a*: *is-new-action* (*action-obs* *E* *a*)
and *new-a'*: $\neg \text{is-new-action}$ (*action-obs* *E* *a'*)
shows $P, E \vdash a \leq_{hb} a'$

proof –

from *thread-start-actions-okD*[*OF tsa-ok a' new-a'*]

obtain *i* **where** $i \leq a'$

and *obs-i*: *action-obs* *E* *i* = *InitialThreadAction*

and *action-tid* *E* *i* = *action-tid* *E* *a'* **by** *auto*

from $\langle i \leq a' \rangle \ a'$ **have** $i \in \text{actions } E$

by(*auto simp add: actions-def le-less-trans*[**where** $y = \text{enat } a'$])

with $\langle i \leq a' \rangle \ \text{obs-i } a' \ \text{new-a}'$ **have** $E \vdash i \leq a \ a'$ **by**(*simp add: action-order-def*)

hence $E \vdash i \leq_{po} a'$ **using** $\langle \text{action-tid } E \ i = \text{action-tid } E \ a' \rangle$
by(rule program-orderI)

moreover {
from $\langle i \in \text{actions } E \rangle \text{ obs-}i$
have $i \in \text{sactions } P \ E$ **by**(auto intro: sactionsI)
from $a \langle i \in \text{actions } E \rangle \text{ new-}a \ \text{obs-}i$ **have** $E \vdash a \leq a \ i$ **by**(simp add: action-order-def)
moreover from $a \ \text{new-}a$ **have** $a \in \text{sactions } P \ E$ **by**(auto intro: sactionsI)
ultimately have $P, E \vdash a \leq_{so} i$ **using** $\langle i \in \text{sactions } P \ E \rangle$ **by**(rule sync-orderI)
moreover from $\text{new-}a \ \text{obs-}i$ **have** $P \vdash (\text{action-tid } E \ a, \ \text{action-obs } E \ a) \rightsquigarrow_{sw} (\text{action-tid } E \ i, \ \text{action-obs } E \ i)$
by cases(auto intro: synchronizes-with.intros)
ultimately have $P, E \vdash a \leq_{sw} i$ **by**(rule sync-withI) }
ultimately show ?thesis **unfolding** po-sw-def [abs-def] **by**(blast intro: tranclp.r-into-trancl tranclp-trans)
qed

lemma happens-before-change-prefix:

assumes hb: $P, E \vdash a \leq_{hb} a'$
and tsa-ok: thread-start-actions-ok E'
and prefix: $\text{ltake } n \ E \ [\approx] \ \text{ltake } n \ E'$
and an: $\text{enat } a < n$
and a'n: $\text{enat } a' < n$
shows $P, E' \vdash a \leq_{hb} a'$
using hb an a'n
proof induct
case (base a')
thus ?case **by**(rule tranclp.r-into-trancl[**where** $r = \text{po-sw } P \ E'$, OF po-sw-change-prefix[OF - prefix]])
next
case (step a' a'')
show ?case
proof(cases is-new-action (action-obs $E \ a'$) $\wedge \neg$ is-new-action (action-obs $E \ a''$))
case False
from $\langle \text{po-sw } P \ E \ a' \ a'' \rangle$ **have** $E \vdash a' \leq a \ a''$ **by**(rule po-sw-into-action-order)
with $\langle \text{enat } a'' < n \rangle$ False **have** $\text{enat } a' < n$
by(safe elim!: action-orderE)(metis Suc-leI Suc-n-not-le-n enat-ord-simps(2) le-trans nat-neq-iff xtrans(10))+
with $\langle \text{enat } a < n \rangle$ **have** $P, E' \vdash a \leq_{hb} a'$ **by**(rule step)
moreover from $\langle \text{po-sw } P \ E \ a' \ a'' \rangle$ prefix $\langle \text{enat } a' < n \rangle \ \langle \text{enat } a'' < n \rangle$
have po-sw $P \ E' \ a' \ a''$ **by**(rule po-sw-change-prefix)
ultimately show ?thesis ..
next
case True
then obtain new-a': is-new-action (action-obs $E \ a'$)
and \neg is-new-action (action-obs $E \ a''$) ..
from $\langle P, E \vdash a \leq_{hb} a' \rangle$ new-a'
have new-a: is-new-action (action-obs $E \ a$)
and tid: action-tid $E \ a = \text{action-tid } E \ a'$
and $a \leq a'$ **by**(rule happens-before-new-actionD)+

note tsa-ok **moreover**
from porder-happens-before[of $E \ P$] **have** $a \in \text{actions } E$
by(rule porder-onE)(erule refl-onPD1, rule $\langle P, E \vdash a \leq_{hb} a' \rangle$)
hence $a \in \text{actions } E'$ **using** an **by**(rule actions-change-prefix[OF - prefix])

moreover
from $\langle po\text{-}sw\ P\ E\ a'\ a'' \rangle\ refl\text{-}on\text{-}program\text{-}order[of\ E]\ refl\text{-}on\text{-}sync\text{-}order[of\ P\ E]$
have $a'' \in actions\ E$
unfolding $po\text{-}sw\text{-}def$ **by**($auto\ dest: refl\text{-}onPD2\ elim!: sync\text{-}withE$)
hence $a'' \in actions\ E'$ **using** $\langle enat\ a'' < n \rangle$ **by**($rule\ actions\text{-}change\text{-}prefix[OF\ \text{-}\ prefix]$)
moreover
from $new\text{-}a\ action\text{-}obs\text{-}change\text{-}prefix[OF\ prefix\ an]$
have $is\text{-}new\text{-}action\ (action\text{-}obs\ E'\ a)$ **by**($cases$) $auto$
moreover
from $\langle \neg\ is\text{-}new\text{-}action\ (action\text{-}obs\ E\ a'') \rangle\ action\text{-}obs\text{-}change\text{-}prefix[OF\ prefix\ \langle enat\ a'' < n \rangle]$
have $\neg\ is\text{-}new\text{-}action\ (action\text{-}obs\ E'\ a'')$ **by**($auto\ elim: is\text{-}new\text{-}action.cases$)
ultimately show $P, E' \vdash a \leq_{hb} a''$ **by**($rule\ happens\text{-}before\text{-}new\text{-}not\text{-}new$)
qed
qed

lemma $thread\text{-}start\text{-}actions\text{-}ok\text{-}change$:
assumes $tsa: thread\text{-}start\text{-}actions\text{-}ok\ E$
and $sim: E [\approx] E'$
shows $thread\text{-}start\text{-}actions\text{-}ok\ E'$
proof($rule\ thread\text{-}start\text{-}actions\text{-}okI$)
fix a
assume $a \in actions\ E' \neg\ is\text{-}new\text{-}action\ (action\text{-}obs\ E'\ a)$
from sim **have** $len\text{-}eq: llength\ E = llength\ E'$ **by**($simp\ add: sim\text{-}actions\text{-}def$)($rule\ llist\text{-}all2\text{-}llengthD$)
with sim **have** $sim': ltake\ (llength\ E)\ E [\approx] ltake\ (llength\ E)\ E'$ **by**($simp\ add: ltake\text{-}all$)

from $\langle a \in actions\ E' \rangle\ len\text{-}eq$ **have** $enat\ a < llength\ E$ **by**($simp\ add: actions\text{-}def$)
with $\langle a \in actions\ E' \rangle\ sim'$ [$symmetric$] **have** $a \in actions\ E$ **by**($rule\ actions\text{-}change\text{-}prefix$)
moreover have $\neg\ is\text{-}new\text{-}action\ (action\text{-}obs\ E\ a)$
using $action\text{-}obs\text{-}change\text{-}prefix[OF\ sim'\ \langle enat\ a < llength\ E \rangle]\ \langle \neg\ is\text{-}new\text{-}action\ (action\text{-}obs\ E'\ a) \rangle$
by($auto\ elim!: is\text{-}new\text{-}action.cases$)
ultimately obtain i **where** $i \leq a\ action\text{-}obs\ E\ i = InitialThreadAction\ action\text{-}tid\ E\ i = action\text{-}tid\ E\ a$
by($blast\ dest: thread\text{-}start\text{-}actions\text{-}okD[OF\ tsa]$)
thus $\exists i \leq a. action\text{-}obs\ E'\ i = InitialThreadAction \wedge action\text{-}tid\ E'\ i = action\text{-}tid\ E'\ a$
using $action\text{-}tid\text{-}change\text{-}prefix[OF\ sim',\ of\ i]\ action\text{-}tid\text{-}change\text{-}prefix[OF\ sim',\ of\ a]\ \langle enat\ a < llength\ E \rangle$
 $action\text{-}obs\text{-}change\text{-}prefix[OF\ sim',\ of\ i]$
by($cases\ llength\ E$)($auto\ intro!: exI[\mathbf{where}\ x=i]$)
qed

context $executions\text{-}aux$ **begin**

lemma $\mathcal{E}\text{-}new\text{-}same\text{-}addr\text{-}singleton$:
assumes $E: E \in \mathcal{E}$
shows $\exists a. new\text{-}actions\text{-}for\ P\ E\ adal \subseteq \{a\}$
by($blast\ dest: \mathcal{E}\text{-}new\text{-}actions\text{-}for\text{-}fun[OF\ E]$)

lemma $new\text{-}action\text{-}before\text{-}read$:
assumes $E: E \in \mathcal{E}$
and $wf: P \vdash (E, ws) \checkmark$
and $ra: ra \in read\text{-}actions\ E$
and $adal: adal \in action\text{-}loc\ P\ E\ ra$
and $new: wa \in new\text{-}actions\text{-}for\ P\ E\ adal$
and $sc: \bigwedge a. \llbracket a < ra; a \in read\text{-}actions\ E \rrbracket \implies P, E \vdash a \rightsquigarrow_{mrw} ws\ a$

shows $wa < ra$
using \mathcal{E} -new-same-addr-singleton[OF E, of adal] init-before-read[OF E wf ra adal sc] new
by auto

lemma *mrw-before*:

assumes $E: E \in \mathcal{E}$
and $wf: P \vdash (E, ws) \checkmark$
and $mrw: P, E \vdash r \rightsquigarrow_{mrw} w$
and $sc: \bigwedge a. \llbracket a < r; a \in \text{read-actions } E \rrbracket \implies P, E \vdash a \rightsquigarrow_{mrw} ws a$
shows $w < r$

using *mrw read-actions-not-write-actions*[of r E]

apply *cases*

apply(*erule action-orderE*)

apply(*erule (1) new-action-before-read*[OF E wf])

apply(*simp add: new-actions-for-def*)

apply(*erule (1) sc*)

apply(*cases w = r*)

apply auto

done

lemma *mrw-change-prefix*:

assumes $E': E' \in \mathcal{E}$
and $mrw: P, E \vdash r \rightsquigarrow_{mrw} w$
and *tsa-ok: thread-start-actions-ok* E'
and *prefix: ltake n E* \approx *ltake n E'*
and *an: enat* $r < n$
and *a'n: enat* $w < n$
shows $P, E' \vdash r \rightsquigarrow_{mrw} w$

using *mrw*

proof *cases*

fix *adal*

assume $r: r \in \text{read-actions } E$

and *adal-r: adal* $\in \text{action-loc } P E r$

and *war: E* $\vdash w \leq_a r$

and $w \in \text{write-actions } E$

and *adal-w: adal* $\in \text{action-loc } P E w$

and $mrw: \bigwedge wa'. \llbracket wa' \in \text{write-actions } E; adal \in \text{action-loc } P E wa' \rrbracket$
 $\implies E \vdash wa' \leq_a w \vee E \vdash r \leq_a wa'$

show *?thesis*

proof(*rule most-recent-write-for.intros*)

from *r prefix an* **show** $r': r \in \text{read-actions } E'$

by(*rule read-actions-change-prefix*)

from *adal-r* **show** $adal \in \text{action-loc } P E' r$

by(*simp add: action-loc-change-prefix*[OF *prefix*[*symmetric*] *an*])

from *war prefix a'n an* **show** $E' \vdash w \leq_a r$ **by**(*rule action-order-change-prefix*)

from w *prefix a'n* **show** $w': w \in \text{write-actions } E'$ **by**(*rule write-actions-change-prefix*)

from *adal-w* **show** $adal-w': adal \in \text{action-loc } P E' w$ **by**(*simp add: action-loc-change-prefix*[OF *prefix*[*symmetric*] *a'n*])

fix wa'

assume $wa': wa' \in \text{write-actions } E'$

and *adal-wa': adal* $\in \text{action-loc } P E' wa'$

show $E' \vdash wa' \leq_a w \vee E' \vdash r \leq_a wa'$

proof(*cases enat wa' < n*)

```

case True
note wa'n = this
with wa' prefix[symmetric] have wa' ∈ write-actions E by(rule write-actions-change-prefix)
moreover from adal-wa' have adal ∈ action-loc P E wa'
  by(simp add: action-loc-change-prefix[OF prefix wa'n])
ultimately have E ⊢ wa' ≤ a w ∨ E ⊢ r ≤ a wa' by(rule mrw)
thus ?thesis
proof
  assume E ⊢ wa' ≤ a w
  hence E' ⊢ wa' ≤ a w using prefix wa'n a'n by(rule action-order-change-prefix)
  thus ?thesis ..
next
  assume E ⊢ r ≤ a wa'
  hence E' ⊢ r ≤ a wa' using prefix an wa'n by(rule action-order-change-prefix)
  thus ?thesis ..
qed
next
case False note wa'n = this
show ?thesis
proof(cases is-new-action (action-obs E' wa'))
  case False
  hence E' ⊢ r ≤ a wa' using wa'n r' wa' an
    by(auto intro!: action-orderI) (metis enat-ord-code(1) linorder-le-cases order-le-less-trans)
  thus ?thesis ..
next
  case True
with wa' adal-wa' have new: wa' ∈ new-actions-for P E' adal by(simp add: new-actions-for-def)
show ?thesis
proof(cases is-new-action (action-obs E' w))
  case True
  with adal-w' a'n w' have w ∈ new-actions-for P E' adal by(simp add: new-actions-for-def)
  with E' new have wa' = w by(rule  $\mathcal{E}$ -new-actions-for-fun)
  thus ?thesis using w' by(auto intro: refl-onPD[OF refl-action-order])
next
  case False
  with True wa' w' show ?thesis by(auto intro!: action-orderI)
qed
qed
qed
qed
qed

```

lemma *action-order-read-before-write*:

```

assumes E: E ∈  $\mathcal{E}$  P ⊢ (E, ws)  $\checkmark$ 
and ao: E ⊢ w ≤ a r
and r: r ∈ read-actions E
and w: w ∈ write-actions E
and adal: adal ∈ action-loc P E r adal ∈ action-loc P E w
and sc:  $\bigwedge a. \llbracket a < r; a \in \text{read-actions } E \rrbracket \implies P, E \vdash a \rightsquigarrow_{mrw} ws a$ 
shows w < r
using ao
proof(cases rule: action-orderE)
  case 1
  from init-before-read[OF E r adal(1) sc]

```

```

obtain  $i$  where  $i < r$   $i \in \text{new-actions-for } P \ E \text{ adal}$  by blast
moreover from  $\langle \text{is-new-action } (\text{action-obs } E \ w) \rangle \text{ adal}(2) \langle w \in \text{actions } E \rangle$ 
have  $w \in \text{new-actions-for } P \ E \text{ adal}$  by (simp add: new-actions-for-def)
ultimately show  $w < r$  using  $E$  by (auto dest:  $\mathcal{E}$ -new-actions-for-fun)
next
  case 2
  with  $r \ w$  show ?thesis
    by (cases w = r) (auto dest: read-actions-not-write-actions)
qed

end

end

```

8.5 The data race free guarantee of the JMM

```

theory JMM-DRF
imports
  JMM-Spec
begin

```

```

context drf begin

```

```

lemma drf-lemma:

```

```

  assumes wf:  $P \vdash (E, \text{ws}) \checkmark$ 
  and  $E: E \in \mathcal{E}$ 
  and sync: correctly-synchronized  $P \ \mathcal{E}$ 
  and read-before:  $\bigwedge r. r \in \text{read-actions } E \implies P, E \vdash \text{ws } r \leq_{hb} r$ 
  shows sequentially-consistent  $P \ (E, \text{ws})$ 

```

```

proof (rule ccontr)

```

```

  let  $?Q = \{r. r \in \text{read-actions } E \wedge \neg P, E \vdash r \rightsquigarrow_{mrw} \text{ws } r\}$ 

```

```

  assume  $\neg ?thesis$ 

```

```

  then obtain  $r$  where  $r \in \text{read-actions } E \wedge \neg P, E \vdash r \rightsquigarrow_{mrw} \text{ws } r$ 
    by (auto simp add: sequentially-consistent-def)

```

```

  hence  $r \in ?Q$  by simp

```

```

  with wf-action-order[of  $E$ ] obtain  $r'$ 

```

```

    where  $r' \in ?Q$ 

```

```

    and  $(\text{action-order } E)^{\neq} \hat{=}^* r' \ r$ 

```

```

    and  $r'\text{-min}$ :  $\bigwedge a. (\text{action-order } E)^{\neq} a \ r' \implies a \notin ?Q$ 

```

```

    by (rule wfP-minimalE) blast

```

```

  from  $\langle r' \in ?Q \rangle$  have  $r': r' \in \text{read-actions } E$ 

```

```

    and not-mrw:  $\neg P, E \vdash r' \rightsquigarrow_{mrw} \text{ws } r'$  by blast+

```

```

  from  $r'$  obtain  $ad \ al \ v$  where  $\text{obs-}r': \text{action-obs } E \ r' = \text{NormalAction } (\text{ReadMem } ad \ al \ v)$ 

```

```

    by (cases) auto

```

```

  from wf have  $\text{ws}$ : is-write-seen  $P \ E \ \text{ws}$ 

```

```

    and tso-ok: thread-start-actions-ok  $E$ 

```

```

    by (rule wf-exec-is-write-seenD wf-exec-thread-start-actions-okD) $+$ 

```

```

  from is-write-seenD[OF  $\text{ws } r' \ \text{obs-}r'$ ]

```

```

  have  $\text{ws-r}$ :  $\text{ws } r' \in \text{write-actions } E$ 

```

```

    and adal:  $(ad, al) \in \text{action-loc } P \ E \ (\text{ws } r')$ 

```

```

    and  $v$ :  $v = \text{value-written } P \ E \ (\text{ws } r') \ (ad, al)$ 

```

and not-hb: $\neg P, E \vdash r' \leq_{hb} ws r'$ **by** *auto*
from r' **have** $P, E \vdash ws r' \leq_{hb} r'$ **by** (*rule read-before*)
hence $E \vdash ws r' \leq_a r'$ **by** (*rule happens-before-into-action-order*)
from *not-mrw*
have $\exists w'. w' \in \text{write-actions } E \wedge (ad, al) \in \text{action-loc } P E w' \wedge$
 $\neg P, E \vdash w' \leq_{hb} ws r' \wedge \neg P, E \vdash w' \leq_{so} ws r' \wedge$
 $\neg P, E \vdash r' \leq_{hb} w' \wedge \neg P, E \vdash r' \leq_{so} w' \wedge E \vdash w' \leq_a r'$
proof (*rule contrapos-np*)
assume *inbetween:* $\neg ?thesis$
note r'
moreover from *obs-r'* **have** $(ad, al) \in \text{action-loc } P E r'$ **by** *simp*
moreover note $\langle E \vdash ws r' \leq_a r' \rangle$ *ws-r adal*
moreover
{ fix w'
assume $w' \in \text{write-actions } E (ad, al) \in \text{action-loc } P E w'$
with *inbetween* **have** $P, E \vdash w' \leq_{hb} ws r' \vee P, E \vdash w' \leq_{so} ws r' \vee P, E \vdash r' \leq_{hb} w' \vee P, E \vdash r'$
 $\leq_{so} w' \vee \neg E \vdash w' \leq_a r'$ **by** *simp*
moreover from *total-onPD*[*OF total-action-order, of w' E r'*] $\langle w' \in \text{write-actions } E \rangle r'$
have $E \vdash w' \leq_a r' \vee E \vdash r' \leq_a w'$ **by** (*auto dest: read-actions-not-write-actions*)
ultimately have $E \vdash w' \leq_a ws r' \vee E \vdash r' \leq_a w'$ **unfolding** *sync-order-def*
by (*blast intro: happens-before-into-action-order*) **}**
ultimately show $P, E \vdash r' \rightsquigarrow_{mrw} ws r'$ **by** (*rule most-recent-write-for.intros*)
qed
then obtain w' **where** $w': w' \in \text{write-actions } E$
and *adal-w'*: $(ad, al) \in \text{action-loc } P E w'$
and $\neg P, E \vdash w' \leq_{hb} ws r' \neg P, E \vdash r' \leq_{hb} w' E \vdash w' \leq_a r'$
and *so:* $\neg P, E \vdash w' \leq_{so} ws r' \neg P, E \vdash r' \leq_{so} w'$ **by** *blast*

have $ws r' \neq w'$ **using** $\langle \neg P, E \vdash w' \leq_{hb} ws r' \rangle$ *ws-r*
by (*auto intro: happens-before-refl*)

have *vol:* $\neg \text{is-volatile } P al$
proof
assume *vol-al:* *is-volatile P al*
with r' *obs-r'* **have** $r' \in \text{sactions } P E$ **by** *cases*(*rule sactionsI, simp-all*)
moreover from w' *vol-al adal-w'* **have** $w' \in \text{sactions } P E$
by (*cases*)(*auto intro: sactionsI elim!: is-write-action.cases*)
ultimately have $P, E \vdash w' \leq_{so} r' \vee w' = r' \vee P, E \vdash r' \leq_{so} w'$
using *total-sync-order*[*of P E*] **by** (*blast dest: total-onPD*)
moreover have $w' \neq r'$ **using** $w' r'$ **by** (*auto dest: read-actions-not-write-actions*)
ultimately have $P, E \vdash w' \leq_{so} r'$ **using** $\langle \neg P, E \vdash r' \leq_{so} w' \rangle$ **by** *simp*
moreover from *ws-r vol-al adal* **have** $ws r' \in \text{sactions } P E$
by (*cases*)(*auto intro: sactionsI elim!: is-write-action.cases*)
with *total-sync-order*[*of P E*] $\langle w' \in \text{sactions } P E \rangle \langle \neg P, E \vdash w' \leq_{so} ws r' \rangle \langle ws r' \neq w' \rangle$
have $P, E \vdash ws r' \leq_{so} w'$ **by** (*blast dest: total-onPD*)
ultimately show *False*
using *is-write-seenD*[*OF ws r' obs-r'*] w' *adal-w' vol-al* $\langle ws r' \neq w' \rangle$ **by** *auto*
qed

{ fix a
assume $a < r'$ **and** $a \in \text{read-actions } E$
hence (*action-order E*) $\neq\neq a r'$ **using** r' *obs-r'* **by** (*auto intro: action-orderI*)
from r' -*min*[*OF this*] $\langle a \in \text{read-actions } E \rangle$
have $P, E \vdash a \rightsquigarrow_{mrw} ws a$ **by** *simp* **}**

from \mathcal{E} -sequential-completion[$OF\ E\ wf\ this,\ of\ r'$] r'
obtain $E'\ ws'$ **where** $E' \in \mathcal{E}\ P \vdash (E', ws')$ \surd
and eq : $ltake\ (enat\ r')\ E = ltake\ (enat\ r')\ E'$
and sc' : sequentially-consistent $P\ (E', ws')$
and r'' : $action-tid\ E\ r' = action-tid\ E'\ r'\ action-obs\ E\ r' \approx action-obs\ E'\ r'$
and $r' \in actions\ E'$
by *auto*

from $\langle P \vdash (E', ws') \surd \rangle$ **have** $t\text{-}ok'$: *thread-start-actions-ok* E'
by(*rule wf-exec-thread-start-actions-okD*)

from $\langle r' \in read\text{-}actions\ E \rangle$ **have** $enat\ r' < llength\ E$ **by**(*auto elim: read-actions.cases actionsE*)
moreover from $\langle r' \in actions\ E' \rangle$ **have** $enat\ r' < llength\ E'$ **by**(*auto elim: actionsE*)
ultimately have eq' : $ltake\ (enat\ (Suc\ r'))\ E [\approx] ltake\ (enat\ (Suc\ r'))\ E'$
using eq [*THEN eq-into-sim-actions*] r''
by(*auto simp add: ltake-Suc-conv-snoc-lnth sim-actions-def split-beta action-tid-def action-obs-def*
intro!: llist-all2-lappendI)

from r' **have** r'' : $r' \in read\text{-}actions\ E'$
by(*rule read-actions-change-prefix[OF -eq'] simp*)
from $obs\text{-}r'$ **have** $(ad, al) \in action\text{-}loc\ P\ E\ r'$ **by** *simp*
hence $adal\text{-}r''$: $(ad, al) \in action\text{-}loc\ P\ E'\ r'$
by(*subst (asm) action-loc-change-prefix[OF eq'] simp*)

from $\langle \neg P, E \vdash w' \leq_{hb}\ ws\ r' \rangle$
have $\neg is\text{-}new\text{-}action\ (action\text{-}obs\ E\ w')$
proof(*rule contrapos-nn*)
assume $new\text{-}w'$: $is\text{-}new\text{-}action\ (action\text{-}obs\ E\ w')$
show $P, E \vdash w' \leq_{hb}\ ws\ r'$
proof(*cases is-new-action (action-obs E (ws r'))*)
case *True*
with $adal\ new\text{-}w'\ adal\text{-}w'\ w'\ ws\text{-}r$
have $ws\ r' \in new\text{-}actions\text{-}for\ P\ E\ (ad, al)\ w' \in new\text{-}actions\text{-}for\ P\ E\ (ad, al)$
by(*auto simp add: new-actions-for-def*)
with $\langle E \in \mathcal{E} \rangle$ **have** $ws\ r' = w'$ **by**(*rule E-new-actions-for-fun*)
thus *?thesis* **using** w' **by**(*auto intro: happens-before-refl*)
next
case *False*
with $t\text{-}ok\ w'\ ws\text{-}r\ new\text{-}w'$
show *?thesis* **by**(*auto intro: happens-before-new-not-new*)
qed

qed
with $\langle E \vdash w' \leq_a\ r' \rangle$ **have** $w' \leq r'$ **by**(*auto elim!: action-orderE*)
moreover from $w'\ r'$ **have** $w' \neq r'$ **by**(*auto intro: read-actions-not-write-actions*)
ultimately have $w' < r'$ **by** *simp*
with w' **have** $w' \in write\text{-}actions\ E'$
by(*auto intro: write-actions-change-prefix[OF - eq']*)
hence $w' \in actions\ E'$ **by** *simp*

from $adal\text{-}w'\ \langle w' < r' \rangle$
have $(ad, al) \in action\text{-}loc\ P\ E'\ w'$
by(*subst action-loc-change-prefix[symmetric, OF eq'] simp-all*)

from $vol\ \langle r' \in read\text{-}actions\ E' \rangle\ \langle w' \in write\text{-}actions\ E' \rangle\ \langle (ad, al) \in action\text{-}loc\ P\ E'\ w' \rangle\ adal\text{-}r''$

have $P, E' \vdash r' \dagger w'$ **unfolding** *non-volatile-conflict-def* **by** *auto*
with *sync* $\langle E' \in \mathcal{E} \rangle \langle P \vdash (E', ws') \checkmark \rangle sc' \langle r' \in \text{actions } E' \rangle \langle w' \in \text{actions } E' \rangle$
have $hb'-r'-w': P, E' \vdash r' \leq_{hb} w' \vee P, E' \vdash w' \leq_{hb} r'$
by(*rule correctly-synchronizedD*[*rule-format*])
hence $P, E \vdash r' \leq_{hb} w' \vee P, E \vdash w' \leq_{hb} r'$ **using** $\langle w' < r' \rangle$
by(*auto intro: happens-before-change-prefix*[*OF - tsa-ok eq'*[*symmetric*]])
with $\langle \neg P, E \vdash r' \leq_{hb} w' \rangle$ **have** $P, E \vdash w' \leq_{hb} r'$ **by** *simp*

have $P, E \vdash ws \ r' \leq_{hb} w'$
proof(*cases is-new-action* (*action-obs* E ($ws \ r'$)))
case *False*
with $\langle E \vdash ws \ r' \leq_a r' \rangle$ **have** $ws \ r' \leq r'$ **by**(*auto elim!*: *action-orderE*)
moreover from $ws-r \ r'$ **have** $ws \ r' \neq r'$ **by**(*auto dest: read-actions-not-write-actions*)
ultimately have $ws \ r' < r'$ **by** *simp*
with $ws-r$ **have** $ws \ r' \in \text{write-actions } E'$
by(*auto intro: write-actions-change-prefix*[*OF - eq'*])
hence $ws \ r' \in \text{actions } E'$ **by** *simp*

from *adal* $\langle ws \ r' < r' \rangle$
have $(ad, al) \in \text{action-loc } P \ E' \ (ws \ r')$
by(*subst action-loc-change-prefix*[*symmetric, OF eq'*]) *simp-all*
hence $P, E' \vdash ws \ r' \dagger w'$
using $\langle ws \ r' \in \text{write-actions } E' \rangle \langle w' \in \text{write-actions } E' \rangle \langle (ad, al) \in \text{action-loc } P \ E' \ w' \rangle$ *vol*
unfolding *non-volatile-conflict-def* **by** *auto*
with *sync* $\langle E' \in \mathcal{E} \rangle \langle P \vdash (E', ws') \checkmark \rangle sc' \langle ws \ r' \in \text{actions } E' \rangle \langle w' \in \text{actions } E' \rangle$
have $P, E' \vdash ws \ r' \leq_{hb} w' \vee P, E' \vdash w' \leq_{hb} ws \ r'$
by(*rule correctly-synchronizedD*[*rule-format*])
thus $P, E \vdash ws \ r' \leq_{hb} w'$ **using** $\langle w' < r' \rangle \langle ws \ r' < r' \rangle \langle \neg P, E \vdash w' \leq_{hb} ws \ r' \rangle$
by(*auto dest: happens-before-change-prefix*[*OF - tsa-ok eq'*[*symmetric*]])

next
case *True*
with *tsa-ok* $ws-r \ w' \langle \neg \text{is-new-action} \ (\text{action-obs } E \ w') \rangle$
show $P, E \vdash ws \ r' \leq_{hb} w'$ **by**(*auto intro: happens-before-new-not-new*)

qed
moreover
from *wf* **have** *is-write-seen* $P \ E \ ws$ **by**(*rule wf-exec-is-write-seenD*)
ultimately have $w' = ws \ r'$
using *is-write-seenD*[*OF* $\langle \text{is-write-seen } P \ E \ ws \rangle \langle r' \in \text{read-actions } E \rangle \text{obs-}r'$]
 $\langle w' \in \text{write-actions } E \rangle \langle (ad, al) \in \text{action-loc } P \ E \ w' \rangle \langle P, E \vdash w' \leq_{hb} r' \rangle$
by *auto*
with *porder-happens-before*[*of* $E \ P$] $\langle \neg P, E \vdash w' \leq_{hb} ws \ r' \rangle$ $ws-r$ **show** *False*
by(*auto dest: refl-onPD*[**where** $a=ws \ r'$] *elim!*: *porder-onE*)

qed

lemma *justified-action-committedD*:
assumes *justified*: $P \vdash (E, ws)$ *weakly-justified-by* J
and $a: a \in \text{actions } E$
obtains $n \ a'$ **where** $a = \text{action-translation } (J \ n) \ a' \ a' \in \text{committed } (J \ n)$

proof(*atomize-elim*)
from *justified* **have** $\text{actions } E = (\bigcup n. \text{action-translation } (J \ n) \ \text{committed } (J \ n))$
by(*simp add: is-commit-sequence-def*)
with a **show** $\exists n \ a'. a = \text{action-translation } (J \ n) \ a' \wedge a' \in \text{committed } (J \ n)$ **by** *auto*

qed

theorem *drf-weak*:

assumes *sync*: *correctly-synchronized* $P \ \mathcal{E}$
and *legal*: *weakly-legal-execution* $P \ \mathcal{E} \ (E, ws)$
shows *sequentially-consistent* $P \ (E, ws)$
using *legal-wf-execD*[*OF legal*] *legal-ED*[*OF legal*] *sync*
proof(*rule drf-lemma*)
fix r
assume $r \in \text{read-actions } E$

from *legal* **obtain** J **where** $E: E \in \mathcal{E}$
and *wf-exec*: $P \vdash (E, ws) \checkmark$
and $J: P \vdash (E, ws) \text{ weakly-justified-by } J$
and *range-J*: $\text{range } (justifying-exec \circ J) \subseteq \mathcal{E}$
by(*rule legal-executionE*)

let $?E = \lambda n. justifying-exec \ (J \ n)$
and $?ws = \lambda n. justifying-ws \ (J \ n)$
and $?C = \lambda n. committed \ (J \ n)$
and $? \varphi = \lambda n. action-translation \ (J \ n)$

from $\langle r \in \text{read-actions } E \rangle$ **have** $r \in \text{actions } E$ **by** *simp*
with J **obtain** $n \ r'$ **where** $r: r = action-translation \ (J \ n) \ r'$
and $r': r' \in ?C \ n$ **by**(*rule justified-action-committedD*)

note $\langle r \in \text{read-actions } E \rangle$
moreover from J **have** *wfan*: *wf-action-translation-on* $(?E \ n) \ E \ (?C \ n) \ (? \varphi \ n)$
by(*simp add: wf-action-translations-def*)
hence *action-obs* $(?E \ n) \ r' \approx action-obs \ E \ r$ **using** r' **unfolding** r
by(*blast dest: wf-action-translation-on-actionD*)
moreover from $J \ r'$ **have** $r' \in \text{actions } (?E \ n)$
by(*auto simp add: committed-subset-actions-def*)
ultimately have $r' \in \text{read-actions } (?E \ n)$ **unfolding** r
by *cases*(*auto intro: read-actions.intros*)
hence $P, E \vdash ws \ (? \varphi \ n \ r') \leq_{hb} ? \varphi \ n \ r'$ **using** $\langle r' \in ?C \ n \rangle$
proof(*induct n arbitrary: r'*)
case 0
from J **have** $?C \ 0 = \{\}$ **by**(*simp add: is-commit-sequence-def*)
with 0 **have** *False* **by** *simp*
thus *?case ..*
next
case $(Suc \ n \ r)$
note $r = \langle r \in \text{read-actions } (?E \ (Suc \ n)) \rangle$
from J **have** *wfan*: *wf-action-translation-on* $(?E \ n) \ E \ (?C \ n) \ (? \varphi \ n)$
and *wfaSn*: *wf-action-translation-on* $(?E \ (Suc \ n)) \ E \ (?C \ (Suc \ n)) \ (? \varphi \ (Suc \ n))$
by(*simp-all add: wf-action-translations-def*)

from *wfaSn* **have** *injSn*: *inj-on* $(? \varphi \ (Suc \ n)) \ (\text{actions } (?E \ (Suc \ n)))$
by(*rule wf-action-translation-on-inj-onD*)
from J **have** $C\text{-sub-A}: ?C \ (Suc \ n) \subseteq \text{actions } (?E \ (Suc \ n))$
by(*simp add: committed-subset-actions-def*)

from J **have** *wf*: $P \vdash (?E \ (Suc \ n), ?ws \ (Suc \ n)) \checkmark$ **by**(*simp add: justification-well-formed-def*)
moreover from *range-J* **have** $?E \ (Suc \ n) \in \mathcal{E}$ **by** *auto*
ultimately have *sc*: *sequentially-consistent* $P \ (?E \ (Suc \ n), ?ws \ (Suc \ n))$ **using** *sync*

proof(*rule drf-lemma*)
fix r'
assume $r': r' \in \text{read-actions } (?E \text{ (Suc } n))$
hence $r' \in \text{actions } (?E \text{ (Suc } n))$ **by** *simp*

show $P, ?E \text{ (Suc } n) \vdash ?ws \text{ (Suc } n) r' \leq_{hb} r'$
proof(*cases* $? \varphi \text{ (Suc } n) r' \in ? \varphi n \text{ ' } ?C n$)
case *True*
then obtain r'' **where** $r'': r'' \in ?C n$
and $r'-r'': ? \varphi \text{ (Suc } n) r' = ? \varphi n r''$ **by**(*auto*)
from r'' *wfan* **have** $\text{action-tid } (?E n) r'' = \text{action-tid } E \text{ (?} \varphi n r'')$
and $\text{action-obs } (?E n) r'' \approx \text{action-obs } E \text{ (?} \varphi n r'')$
by(*blast dest: wf-action-translation-on-actionD*)**+**
moreover from J **have** $? \varphi n \text{ ' } ?C n \subseteq ? \varphi \text{ (Suc } n) \text{ ' } ?C \text{ (Suc } n)$
by(*simp add: is-commit-sequence-def*)
with r'' **have** $? \varphi \text{ (Suc } n) r' \in ? \varphi \text{ (Suc } n) \text{ ' } ?C \text{ (Suc } n)$
unfolding $r'-r''$ **by** *auto*
hence $r' \in ?C \text{ (Suc } n)$
unfolding *inj-on-image-mem-iff*[*OF injSn* $\langle r' \in \text{actions } (?E \text{ (Suc } n)) \rangle$ *C-sub-A*]
with *wfaSn* **have** $\text{action-tid } (?E \text{ (Suc } n)) r' = \text{action-tid } E \text{ (?} \varphi \text{ (Suc } n) r')$
and $\text{action-obs } (?E \text{ (Suc } n)) r' \approx \text{action-obs } E \text{ (?} \varphi \text{ (Suc } n) r')$
by(*blast dest: wf-action-translation-on-actionD*)**+**
ultimately have $\text{tid: action-tid } (?E n) r'' = \text{action-tid } (?E \text{ (Suc } n)) r'$
and $\text{obs: action-obs } (?E n) r'' \approx \text{action-obs } (?E \text{ (Suc } n)) r'$
unfolding $r'-r''$ **by**(*auto intro: sim-action-trans sim-action-sym*)

from J **have** $?C n \subseteq \text{actions } (?E n)$ **by**(*simp add: committed-subset-actions-def*)
with r'' **have** $r'' \in \text{actions } (?E n)$ **by** *blast*
with r' *obs* **have** $r'' \in \text{read-actions } (?E n)$
by *cases(auto intro: read-actions.intros)*
hence $hb'': P, E \vdash ws \text{ (?} \varphi n r'') \leq_{hb} ? \varphi n r''$
using $\langle r'' \in ?C n \rangle$ **by**(*rule Suc*)

have *r-conv-inv*: $r' = \text{inv-into } (\text{actions } (?E \text{ (Suc } n))) \text{ (?} \varphi \text{ (Suc } n)) \text{ (?} \varphi n r'')$
using $\langle r' \in \text{actions } (?E \text{ (Suc } n)) \rangle$ **unfolding** $r'-r''$ [*symmetric*]
by(*simp add: inv-into-f-f*[*OF injSn*])
with $\langle r'' \in ?C n \rangle$ $r' J \langle r'' \in \text{read-actions } (?E n) \rangle$
have *ws-eq*[*symmetric*]: $? \varphi \text{ (Suc } n) (?ws \text{ (Suc } n) r') = ws \text{ (?} \varphi n r'')$
by(*simp add: write-seen-committed-def*)
with $r'-r''$ [*symmetric*] hb'' **have** $P, E \vdash ? \varphi \text{ (Suc } n) (?ws \text{ (Suc } n) r') \leq_{hb} ? \varphi \text{ (Suc } n) r'$ **by**
simp

moreover

from J $r' \langle r' \in \text{committed } (J \text{ (Suc } n)) \rangle$
have $ws \text{ (?} \varphi \text{ (Suc } n) r') \in ? \varphi \text{ (Suc } n) \text{ ' } ?C \text{ (Suc } n)$
by(*rule weakly-justified-write-seen-hb-read-committed*)
then obtain w' **where** $w': ws \text{ (?} \varphi \text{ (Suc } n) r') = ? \varphi \text{ (Suc } n) w'$
and *committed-w*: $w' \in ?C \text{ (Suc } n)$ **by** *blast*
with *C-sub-A* **have** $w'\text{-action: } w' \in \text{actions } (?E \text{ (Suc } n))$ **by** *auto*

hence $w'\text{-def: } w' = \text{inv-into } (\text{actions } (?E \text{ (Suc } n))) \text{ (?} \varphi \text{ (Suc } n)) \text{ (ws \text{ (?} \varphi \text{ (Suc } n) r'))}$
using *injSn* **unfolding** w' **by** *simp*

from $J r' \langle r' \in \text{committed } (J (Suc n)) \rangle$
have $hb\text{-eq}: P, E \vdash ws (\varphi (Suc n) r') \leq_{hb} \varphi (Suc n) r' \longleftrightarrow P, ?E (Suc n) \vdash w' \leq_{hb} r'$
unfolding $w'\text{-def}$ **by**($\text{simp add: happens-before-committed-weak-def}$)

from r' **obtain** $ad\ al\ v$ **where** $\text{action-obs } (?E (Suc n)) r' = \text{NormalAction } (\text{ReadMem } ad\ al\ v)$ **by**(cases)

from $\text{is-write-seenD}[OF\ wf\ \text{exec-is-write-seenD}[OF\ wf]\ r'\ \text{this}]$
have $?ws (Suc n) r' \in \text{actions } (?E (Suc n))$ **by**(auto)
with injSn **have** $w' = ?ws (Suc n) r'$
unfolding $w'\text{-def}$ $ws\text{-eq}[\text{folded } r'\text{-}r'']$ **by**(rule inv-into-f-f)
thus $?thesis$ **using** $hb''\ hb\text{-eq}\ w'\text{-action}\ r'\text{-}r''[\text{symmetric}]$ $w'\ \text{injSn}$ **by** simp

next

case False

with $J r'$ **show** $?thesis$ **by**($\text{auto simp add: uncommitted-reads-see-hb-def}$)

qed

qed

from r **have** $r \in \text{actions } (?E (Suc n))$ **by** simp
let $?w = \text{inv-into } (\text{actions } (?E (Suc n))) (\varphi (Suc n)) (ws (\varphi (Suc n) r))$
from $J r \langle r \in ?C (Suc n) \rangle$ **have** $ws\text{-rE-comm}: ws (\varphi (Suc n) r) \in ?\varphi (Suc n) \text{ ' } ?C (Suc n)$
by($\text{rule weakly-justified-write-seen-hb-read-committed}$)
hence $?w \in ?C (Suc n)$ **using** $C\text{-sub-A}$ **by**($\text{auto simp add: inv-into-f-f}[OF\ \text{injSn}]$)
with $C\text{-sub-A}$ **have** $w: ?w \in \text{actions } (?E (Suc n))$ **by** blast

from $ws\text{-rE-comm}\ C\text{-sub-A}$ **have** $w\text{-eq}: ?\varphi (Suc n) ?w = ws (\varphi (Suc n) r)$
by($\text{auto simp: f-inv-into-f}[\text{where } f = ?\varphi (Suc n)]$)

from r **obtain** $ad\ al\ v$

where $\text{obsr}: \text{action-obs } (?E (Suc n)) r = \text{NormalAction } (\text{ReadMem } ad\ al\ v)$ **by** cases

hence $ad\text{-}r: (ad, al) \in \text{action-loc } P (?E (Suc n)) r$ **by** simp

from $J\ wfaSn \langle r \in ?C (Suc n) \rangle$

have $\text{obs-sim}: \text{action-obs } (?E (Suc n)) r \approx \text{action-obs } E (\varphi (Suc n) r) \varphi (Suc n) r \in \text{actions } E$

by($\text{auto dest: wf-action-translation-on-actionD simp add: committed-subset-actions-def is-commit-sequence-def}$)

with obsr **have** $rE: ?\varphi (Suc n) r \in \text{read-actions } E$ **by**($\text{fastforce intro: read-actions.intros}$)

from $\text{obs-sim}\ \text{obsr}$ **obtain** v'

where $\text{obsrE}: \text{action-obs } E (\varphi (Suc n) r) = \text{NormalAction } (\text{ReadMem } ad\ al\ v')$ **by** auto

from $wf\text{-exec}$ **have** $\text{is-write-seen } P\ E\ ws$ **by**($\text{rule wf-exec-is-write-seenD}$)

from $\text{is-write-seenD}[OF\ \text{this}\ rE\ \text{obsrE}]$

have $ws (\varphi (Suc n) r) \in \text{write-actions } E$

and $(ad, al) \in \text{action-loc } P\ E\ (ws (\varphi (Suc n) r))$

and $n\text{hb}: \neg P, E \vdash ?\varphi (Suc n) r \leq_{hb} ws (\varphi (Suc n) r)$

and $\text{vol}: \text{is-volatile } P\ al \implies \neg P, E \vdash ?\varphi (Suc n) r \leq_{so} ws (\varphi (Suc n) r)$ **by** simp-all

show $?case$

proof($\text{cases is-volatile } P\ al$)

case False

from $wf\text{-action-translation-on-actionD}[OF\ wfaSn \langle ?w \in ?C (Suc n) \rangle]$

have $\text{action-obs } (?E (Suc n)) ?w \approx \text{action-obs } E (\varphi (Suc n) ?w)$ **by** simp

with $w\text{-eq}$ **have** $\text{obs-sim-w}: \text{action-obs } (?E (Suc n)) ?w \approx \text{action-obs } E (ws (\varphi (Suc n) r))$ **by**

simp

with $\langle ws (\varphi (Suc n) r) \in \text{write-actions } E \rangle \langle ?w \in \text{actions } (?E (Suc n)) \rangle$

have $?w \in \text{write-actions } (?E (Suc n))$

by $\text{cases}(\text{fastforce intro: write-actions.intros is-write-action.intros elim!: is-write-action.cases})$

from $\langle (ad, al) \in \text{action-loc } P\ E\ (ws (\varphi (Suc n) r)) \rangle \text{obs-sim-w}$

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have (ad, al) ∈ action-loc P (?E (Suc n)) ?w by cases(auto intro: action-loc-aux-intros)
with r adal-r ‹?w ∈ write-actions (?E (Suc n))› False
have P, ?E (Suc n) ⊢ r † ?w by(auto simp add: non-volatile-conflict-def)
with sc ‹r ∈ actions (?E (Suc n))› w
have P, ?E (Suc n) ⊢ r ≤hb ?w ∨ P, ?E (Suc n) ⊢ ?w ≤hb r
  by(rule correctly-synchronizedD[rule-format, OF sync ‹?E (Suc n) ∈ ℰ› wf])
moreover from J r ‹r ∈ ?C (Suc n)›
have P, ?E (Suc n) ⊢ ?w ≤hb r ↔ P, E ⊢ ws (?φ (Suc n) r) ≤hb ?φ (Suc n) r
  and ¬ P, ?E (Suc n) ⊢ r ≤hb ?w
  by(simp-all add: happens-before-committed-weak-def)
ultimately show ?thesis by auto
next
case True
with rE obsrE have ?φ (Suc n) r ∈ sactions P E by cases (auto intro: sactionsI)
moreover from ‹ws (?φ (Suc n) r) ∈ write-actions E› ‹(ad, al) ∈ action-loc P E (ws (?φ (Suc n) r))› True
have ws (?φ (Suc n) r) ∈ sactions P E by cases(auto intro!: sactionsI elim: is-write-action.cases)
moreover have ?φ (Suc n) r ≠ ws (?φ (Suc n) r)
  using ‹ws (?φ (Suc n) r) ∈ write-actions E› rE by(auto dest: read-actions-not-write-actions)
ultimately have P, E ⊢ ws (?φ (Suc n) r) ≤so ?φ (Suc n) r
  using total-sync-order[of P E] vol[OF True] by(auto dest: total-onPD)
moreover from ‹ws (?φ (Suc n) r) ∈ write-actions E› ‹(ad, al) ∈ action-loc P E (ws (?φ (Suc n) r))› True
have P ⊢ (action-tid E (ws (?φ (Suc n) r)), action-obs E (ws (?φ (Suc n) r))) ∼sw
  (action-tid E (?φ (Suc n) r), action-obs E (?φ (Suc n) r))
  by cases(fastforce elim!: is-write-action.cases intro: synchronizes-with.intros addr-locsI simp
  add: obsrE)
ultimately have P, E ⊢ ws (?φ (Suc n) r) ≤sw ?φ (Suc n) r by(rule sync-withI)
thus ?thesis unfolding po-sw-def by blast
qed
qed
thus P, E ⊢ ws r ≤hb r unfolding r .
qed

```

corollary drf:

```

[[ correctly-synchronized P ℰ; legal-execution P ℰ (E, ws) ]]
⇒ sequentially-consistent P (E, ws)

```

by(erule drf-weak)(rule legal-imp-weakly-legal-execution)

end

end

8.6 Sequentially consistent executions are legal

theory SC-Legal **imports**

JMM-Spec

begin

context executions-base **begin**

primrec commit-for-sc :: 'm prog ⇒ ('addr, 'thread-id) execution × write-seen ⇒ ('addr, 'thread-id) justification

where

commit-for-sc $P (E, ws) n =$
 (if *enat* $n \leq \text{length } E$ then
 let $(E', ws') = \text{SOME } (E', ws')$. $E' \in \mathcal{E} \wedge P \vdash (E', ws') \checkmark \wedge \text{enat } n \leq \text{length } E' \wedge$
 $\text{ltake } (\text{enat } (n - 1)) E = \text{ltake } (\text{enat } (n - 1)) E' \wedge$
 $(n > 0 \longrightarrow \text{action-tid } E' (n - 1) = \text{action-tid } E (n - 1) \wedge$
 (if $n - 1 \in \text{read-actions } E$ then *sim-action* else (=))
 $(\text{action-obs } E' (n - 1)) (\text{action-obs } E (n - 1)) \wedge$
 $(\forall i < n - 1. i \in \text{read-actions } E \longrightarrow ws' i = ws i)) \wedge$
 $(\forall r \in \text{read-actions } E'. n - 1 \leq r \longrightarrow P, E' \vdash ws' r \leq_{hb} r)$
 in (*committed* = $\{..<n\}$, *justifying-exec* = E' , *justifying-ws* = ws' , *action-translation* = *id*)
 else (*committed* = *actions* E , *justifying-exec* = E , *justifying-ws* = ws , *action-translation* = *id*))

end

context *sc-legal* begin

lemma *commit-for-sc-correct*:

assumes $E: E \in \mathcal{E}$

and *wf*: $P \vdash (E, ws) \checkmark$

and *sc*: *sequentially-consistent* $P (E, ws)$

shows *wf-action-translation-commit-for-sc*:

$\bigwedge n. \text{wf-action-translation } E (\text{commit-for-sc } P (E, ws) n) (\text{is } \bigwedge n. \text{?thesis1 } n)$

and *commit-for-sc-in-E*:

$\bigwedge n. \text{justifying-exec } (\text{commit-for-sc } P (E, ws) n) \in \mathcal{E} (\text{is } \bigwedge n. \text{?thesis2 } n)$

and *commit-for-sc-wf*:

$\bigwedge n. P \vdash (\text{justifying-exec } (\text{commit-for-sc } P (E, ws) n), \text{justifying-ws } (\text{commit-for-sc } P (E, ws) n))$

\checkmark

(*is* $\bigwedge n. \text{?thesis3 } n$)

and *commit-for-sc-justification*:

$P \vdash (E, ws) \text{justified-by } \text{commit-for-sc } P (E, ws) (\text{is } \text{?thesis4})$

proof –

let $\text{?}\varphi = \text{commit-for-sc } P (E, ws)$

note [*simp*] = *split-beta*

from *wf* have *tsok*: *thread-start-actions-ok* E by *simp*

let $\text{?}P = \lambda n (E', ws'). E' \in \mathcal{E} \wedge P \vdash (E', ws') \checkmark \wedge (\text{enat } n \leq \text{length } E \longrightarrow \text{enat } n \leq \text{length } E') \wedge$

$\text{ltake } (\text{enat } (n - 1)) E = \text{ltake } (\text{enat } (n - 1)) E' \wedge$

$(n > 0 \longrightarrow \text{action-tid } E' (n - 1) = \text{action-tid } E (n - 1) \wedge$

(if $n - 1 \in \text{read-actions } E$ then *sim-action* else (=))

$(\text{action-obs } E' (n - 1)) (\text{action-obs } E (n - 1)) \wedge$

$(\forall i < n - 1. i \in \text{read-actions } E \longrightarrow ws' i = ws i)) \wedge$

$(\forall r \in \text{read-actions } E'. n - 1 \leq r \longrightarrow P, E' \vdash ws' r \leq_{hb} r)$

define $E' ws'$ where $E' n = \text{fst } (Eps (\text{?}P n))$ and $ws' n = \text{snd } (Eps (\text{?}P n))$ for n

hence [*simp*]:

$\bigwedge n. \text{commit-for-sc } P (E, ws) n =$

(if *enat* $n \leq \text{length } E$

 then (*committed* = $\{..<n\}$, *justifying-exec* = $E' n$, *justifying-ws* = $ws' n$, *action-translation* = *id*)

 else (*committed* = *actions* E , *justifying-exec* = E , *justifying-ws* = ws , *action-translation* = *id*))

by *simp*

note [*simp del*] = *commit-for-sc.simps*

have $(\forall n. \text{?thesis1 } n) \wedge (\forall n. \text{?thesis2 } n) \wedge (\forall n. \text{?thesis3 } n) \wedge \text{?thesis4}$

unfolding *is-justified-by.simps is-commit-sequence-def justification-well-formed-def committed-subset-actions-def happens-before-committed-def sync-order-committed-def value-written-committed-def uncommitted-reads-see-hb-def*

committed-reads-see-committed-writes-def external-actions-committed-def wf-action-translations-def write-seen-committed-def

proof (*intro conjI strip LetI*)

show *committed* ($?φ\ 0$) = {}

by (*auto simp add: actions-def zero-enat-def[symmetric]*)

show *actions-E*: *actions* $E = (\bigcup n. \text{action-translation } (?φ\ n) \text{ ' committed } (?φ\ n))$

by (*auto simp add: actions-def less-le-trans[where y=enat n for n] split: if-split-asm*)

hence *committed-subset-E*: $\bigwedge n. \text{action-translation } (?φ\ n) \text{ ' committed } (?φ\ n) \subseteq \text{actions } E$ **by** *fastforce*

{ **fix** n

have $?P\ n\ (Eps\ (?P\ n))$

proof (*cases* n)

case 0

from $\mathcal{E}\text{-hb-completion}[OF\ E\ wf,\ of\ 0]$ **have** $\exists Ews. ?P\ 0\ Ews$

by (*fastforce simp add: zero-enat-def[symmetric]*)

thus *?thesis unfolding* 0 **by** (*rule someI-ex*)

next

case ($Suc\ n'$)

moreover

from *sc* **have** $sc': \bigwedge a. [\![\ a < n'; a \in \text{read-actions } E \]\!] \implies P, E \vdash a \rightsquigarrow mrw\ ws\ a$

by (*simp add: sequentially-consistent-def*)

from $\mathcal{E}\text{-hb-completion}[OF\ E\ wf\ this,\ of\ n']$

obtain $E'\ ws'$ **where** $E' \in \mathcal{E}$ **and** $P \vdash (E', ws') \checkmark$

and *eq*: $ltake\ (enat\ n')\ E = ltake\ (enat\ n')\ E'$

and *hb*: $\forall a \in \text{read-actions } E'. \text{if } a < n' \text{ then } ws'\ a = ws\ a \text{ else } P, E' \vdash ws'\ a \leq_{hb}\ a$

and *n-sim*: $\text{action-tid } E'\ n' = \text{action-tid } E\ n'$

(*if* $n' \in \text{read-actions } E$ *then* *sim-action* *else* (=)) (*action-obs* $E'\ n'$) (*action-obs* $E\ n'$)

and $n: n' \in \text{actions } E \implies n' \in \text{actions } E'$ **by** *blast*

moreover {

assume $enat\ n \leq llength\ E$

with $n\ Suc$ **have** $enat\ n \leq llength\ E'$

by (*simp add: actions-def Suc-ile-eq*) }

moreover {

fix i

assume $i \in \text{read-actions } E$

moreover from *eq* **have** $ltake\ (enat\ n')\ E [\approx] ltake\ (enat\ n')\ E'$

by (*rule eq-into-sim-actions*)

moreover assume $i < n'$

hence $enat\ i < enat\ n'$ **by** *simp*

ultimately have $i \in \text{read-actions } E'$ **by** (*rule read-actions-change-prefix*)

with *hb*[*rule-format, OF this*] $\langle i < n' \rangle$

have $ws'\ i = ws\ i$ **by** *simp* }

ultimately have $?P\ n\ (E', ws')$ **by** *simp*

thus *?thesis* **by** (*rule someI*)

qed }

hence P [*simplified*]: $\bigwedge n. ?P\ n\ (E'\ n,\ ws'\ n)$ **by** (*simp add: E'-def ws'-def*)

{ **fix** n

assume $n\text{-E}$: $enat\ n \leq llength\ E$

have $ltake\ (enat\ n)\ (E'\ n) [\approx] ltake\ (enat\ n)\ E$ **unfolding** *sim-actions-def*

```

proof(rule llist-all2-all-lnthI)
  show llength (ltake (enat n) (E' n)) = llength (ltake (enat n) E)
    using n-E P[of n] by(clarsimp simp add: min-def)
next
  fix n'
  assume n': enat n' < llength (ltake (enat n) (E' n))
  show ( $\lambda(t, a) (t', a'). t = t' \wedge a \approx a'$ ) (lnth (ltake (enat n) (E' n)) n') (lnth (ltake (enat n)
E) n')
  proof(cases n = Suc n')
    case True
      with P[of n] show ?thesis
        by(simp add: action-tid-def action-obs-def lnth-ltake split: if-split-asm)
    next
      case False
        with n' have n' < n - 1 by auto
        moreover from P[of n] have lnth (ltake (enat (n - 1)) (E' n)) n' = lnth (ltake (enat (n -
1)) E) n' by simp
        ultimately show ?thesis by(simp add: lnth-ltake)
      qed
    qed }
note sim = this
note len-eq = llist-all2-llengthD[OF this[unfolded sim-actions-def]]

{ fix n
  show wf-action-translation E (? $\varphi$  n)
  proof(cases enat n  $\leq$  llength E)
    case False thus ?thesis by(simp add: wf-action-translation-on-def)
  next
    case True
      hence {.. $n$ }  $\subseteq$  actions E
        by(auto simp add: actions-def min-def less-le-trans[where y=enat n] split: if-split-asm)
      moreover
        from True len-eq[OF True] have {.. $n$ }  $\subseteq$  actions (E' n)
          by(auto simp add: actions-def min-def less-le-trans[where y=enat n] split: if-split-asm)
        moreover {
          fix a assume a < n
          moreover from sim[OF True]
          have action-tid (ltake (enat n) (E' n)) a = action-tid (ltake (enat n) E) a
            action-obs (ltake (enat n) (E' n)) a  $\approx$  action-obs (ltake (enat n) E) a
              by(rule sim-actions-action-tidD sim-actions-action-obsD)+
          ultimately have action-tid (E' n) a = action-tid E a action-obs (E' n) a  $\approx$  action-obs E a
            by(simp-all add: action-tid-def action-obs-def lnth-ltake) }
          ultimately show ?thesis by(auto simp add: wf-action-translation-on-def del: subsetI)
        qed
      thus wf-action-translation E (? $\varphi$  n) . }
note wfa = this

{ fix n from P E show justifying-exec (? $\varphi$  n)  $\in$   $\mathcal{E}$ 
  by(cases enat n  $\leq$  llength E) simp-all }
note En = this

{ fix n from P wf show P  $\vdash$  (justifying-exec (? $\varphi$  n), justifying-ws (? $\varphi$  n))  $\checkmark$ 
  by(cases enat n  $\leq$  llength E) simp-all
  thus P  $\vdash$  (justifying-exec (? $\varphi$  n), justifying-ws (? $\varphi$  n))  $\checkmark$  . }

```

note $wfn = this$

```
{ fix n show action-translation (?φ n) ‘ committed (?φ n) ⊆ action-translation (?φ (Suc n)) ‘
committed (?φ (Suc n))
  by(auto simp add: actions-def less-le-trans[where y=enat n]) (metis Suc-ile-eq order-less-imp-le)
}
```

note $committed-subset = this$

```
{ fix n
from len-eq[of n] have enat n ≤ llength E ⇒ {.. $n$ } ⊆ actions (E' n)
  by(auto simp add: E'-def actions-def min-def less-le-trans[where y=enat n] split: if-split-asm)
thus committed (?φ n) ⊆ actions (justifying-exec (?φ n))
  by(simp add: actions-def E'-def) }
```

note $committed-actions = this$

fix n

show $happens-before P (justifying-exec (?φ n)) |‘ committed (?φ n) =$
 $inv-imageP (happens-before P E) (action-translation (?φ n)) |‘ committed (?φ n)$

proof(cases $enat n ≤ llength E$)

case *False* **thus** $?thesis$ **by** *simp*

next

case *True* **thus** $?thesis$

proof(safe intro!: ext)

fix $a b$

assume $hb: P, justifying-exec (?φ n) ⊢ a ≤ hb b$

and $a: a ∈ committed (?φ n)$

and $b: b ∈ committed (?φ n)$

from hb *True* **have** $P, E' n ⊢ a ≤ hb b$ **by**(simp add: E'-def)

moreover **note** $tsok sim[OF True]$

moreover **from** $a b$ *True*

have $enat a < enat n$ $enat b < enat n$ **by** *simp-all*

ultimately **have** $P, E ⊢ a ≤ hb b$ **by**(rule happens-before-change-prefix)

thus $P, E ⊢ action-translation (?φ n) a ≤ hb action-translation (?φ n) b$ **by** *simp*

next

fix $a b$

assume $a: a ∈ committed (?φ n)$

and $b: b ∈ committed (?φ n)$

and $hb: P, E ⊢ action-translation (?φ n) a ≤ hb action-translation (?φ n) b$

from hb *True* **have** $P, E ⊢ a ≤ hb b$ **by** *simp*

moreover **from** $wfn[of n]$ *True* **have** $thread-start-actions-ok (E' n)$ **by**(simp)

moreover **from** $sim[OF True]$ **have** $ltake (enat n) E [≈] ltake (enat n) (E' n)$

by(rule sim-actions-sym)

moreover **from** $a b$ *True* **have** $enat a < enat n$ $enat b < enat n$ **by** *simp-all*

ultimately **have** $P, E' n ⊢ a ≤ hb b$ **by**(rule happens-before-change-prefix)

thus $P, justifying-exec (?φ n) ⊢ a ≤ hb b$ **using** *True* **by**(simp add: E'-def)

qed

qed

show $sync-order P (justifying-exec (?φ n)) |‘ committed (?φ n) =$

$inv-imageP (sync-order P E) (action-translation (?φ n)) |‘ committed (?φ n)$

proof(cases $enat n ≤ llength E$)

case *False* **thus** $?thesis$ **by** *simp*

next

case *True* **thus** $?thesis$


```

proof(safe intro!: ext)
  fix a b
  assume hb:  $P, \text{justifying-exec } (? \varphi \ n) \vdash a \leq_{so} b$ 
    and a:  $a \in \text{committed } (? \varphi \ n)$ 
    and b:  $b \in \text{committed } (? \varphi \ n)$ 
  from hb True have  $P, E' \ n \vdash a \leq_{so} b$  by(simp add: E'-def)
  moreover note sim[OF True]
  moreover from a b True
  have enat a < enat n enat b < enat n by simp-all
  ultimately have  $P, E \vdash a \leq_{so} b$  by(rule sync-order-change-prefix)
  thus  $P, E \vdash \text{action-translation } (? \varphi \ n) \ a \leq_{so} \text{action-translation } (? \varphi \ n) \ b$  by simp
next
  fix a b
  assume a:  $a \in \text{committed } (? \varphi \ n)$ 
    and b:  $b \in \text{committed } (? \varphi \ n)$ 
    and hb:  $P, E \vdash \text{action-translation } (? \varphi \ n) \ a \leq_{so} \text{action-translation } (? \varphi \ n) \ b$ 
  from hb True have  $P, E \vdash a \leq_{so} b$  by simp
  moreover from sim[OF True] have ltake (enat n) E  $\approx$  ltake (enat n) (E' n)
    by(rule sim-actions-sym)
  moreover from a b True have enat a < enat n enat b < enat n by simp-all
  ultimately have  $P, E' \ n \vdash a \leq_{so} b$  by(rule sync-order-change-prefix)
  thus  $P, \text{justifying-exec } (? \varphi \ n) \vdash a \leq_{so} b$  using True by(simp add: E'-def)
qed
qed

{ fix w w' adal
  assume w:  $w \in \text{write-actions } (\text{justifying-exec } (? \varphi \ n)) \cap \text{committed } (? \varphi \ n)$ 
    and w':  $w' = \text{action-translation } (? \varphi \ n) \ w$ 
    and adal:  $\text{adal} \in \text{action-loc } P \ E \ w'$ 
  show value-written P (justifying-exec (?  $\varphi$  n)) w adal = value-written P E w' adal
  proof(cases enat n  $\leq$  llength E)
    case False thus ?thesis using w' by simp
  next
    case True
    note n-E = this
    have action-obs E w = action-obs (E' n) w
    proof(cases w < n - 1)
      case True
      with P[of n] w' n-E show ?thesis
      by(clarsimp simp add: action-obs-change-prefix-eq)
    next
      case False
      with w True have w = n - 1 n > 0 by auto
      moreover
      with True have w  $\in$  actions E
      by(simp add: actions-def)(metis Suc-ile-eq Suc-pred)
      with True w wf-action-translation-on-actionD[OF wfa, of w n] w'
      have w'  $\in$  write-actions E
      by(auto intro!: write-actions.intros elim!: write-actions.cases is-write-action.cases)
      hence w'  $\notin$  read-actions E by(blast dest: read-actions-not-write-actions)
      ultimately show ?thesis using P[of n] w' True byclarsimp
    qed
  with True w' show ?thesis by(cases adal)(simp add: value-written.simps)
qed }

```

```

{ fix r' r r''
  assume r': r' ∈ read-actions (justifying-exec (commit-for-sc P (E, ws) n)) ∩ committed (?φ n)
    and r: r = action-translation (?φ n) r'
    and r'': r'' = inv-into (actions (justifying-exec (?φ (Suc n)))) (action-translation (?φ (Suc n)))
r
  from r' r committed-subset[of n] have r ∈ actions E
  by(auto split: if-split-asm elim!: read-actions.cases simp add: actions-def Suc-ile-eq less-trans[where
y=enat n])
  with r' r have r-actions: r ∈ read-actions E
  by(fastforce dest: wf-action-translation-on-actionD[OF wfa] split: if-split-asm elim!: read-actions.cases
intro: read-actions.intros)
  moreover from r' committed-subset[of n] committed-actions[of Suc n]
  have r' ∈ actions (justifying-exec (?φ (Suc n))) by(auto split: if-split-asm elim: read-actions.cases)
  ultimately have r'' = r' using r' r r'' by(cases enat (Suc n) ≤ llength E) simp-all
  moreover from r' have r' < n
  by(simp add: actions-def split: if-split-asm)(metis enat-ord-code(2) linorder-linear order-less-le-trans)
  ultimately show action-translation (?φ (Suc n)) (justifying-ws (?φ (Suc n)) r'') = ws r
    using P[of Suc n] r' r r-actions by(clarsimp split: if-split-asm) }

{ fix r'
  assume r': r' ∈ read-actions (justifying-exec (?φ (Suc n)))
  show action-translation (?φ (Suc n)) r' ∈ action-translation (?φ n) ' committed (?φ n) ∨
    P.justifying-exec (?φ (Suc n)) ⊢ justifying-ws (?φ (Suc n)) r' ≤hb r' (is ?committed ∨ ?hb)
  proof(cases r' < n)
    case True
      hence ?committed using r'
      by(auto elim!: actionsE split: if-split-asm dest!: read-actions-actions)(metis Suc-ile-eq linorder-not-le
not-less-iff-gr-or-eq)
      thus ?thesis ..
    next
      case False
      hence r' ≥ n by simp
      hence enat (Suc n) ≤ llength E using False r'
      by(auto split: if-split-asm dest!: read-actions-actions elim!: actionsE) (metis Suc-ile-eq
enat-ord-code(2) not-le-imp-less order-less-le-trans)
      hence ?hb using P[of Suc n] r' ⟨r' ≥ n⟩ by simp
      thus ?thesis ..
    qed }

{ fix r' r C-n
  assume r': r' ∈ read-actions (justifying-exec (?φ (Suc n))) ∩ committed (?φ (Suc n))
    and r: r = action-translation (?φ (Suc n)) r'
    and C-n: C-n = action-translation (?φ n) ' committed (?φ n)
  show r ∈ C-n ∨ action-translation (?φ (Suc n)) (justifying-ws (?φ (Suc n)) r') ∈ C-n ∧ ws r ∈
C-n
    (is - ∨ (?C-ws-n ∧ ?C-ws))
  proof(cases r ∈ C-n)
    case True thus ?thesis ..
  next
    case False
    with r' r C-n have [simp]: r' = n
    apply(auto split: if-split-asm dest!: read-actions-actions elim!: actionsE)
    apply(metis enat-ord-code(1) less-SucI less-eq-Suc-le not-less-eq-eq order-trans)

```

by (metis Suc-ile-eq enat-ord-code(1) leD leI linorder-cases)
 from r' have len-E: enat (Suc n) \leq llength E
 by (clarsimp simp add: actions-def Suc-ile-eq split: if-split-asm)
 with r' P[of Suc n] have P,justifying-exec ($? \varphi$ (Suc n)) \vdash ws' (Suc n) $r' \leq_{hb} r'$ by (simp)
 hence justifying-exec ($? \varphi$ (Suc n)) \vdash ws' (Suc n) $r' \leq_a r'$ by (rule happens-before-into-action-order)
 moreover from r' have $r' \in$ read-actions (justifying-exec ($? \varphi$ (Suc n))) by simp
 moreover then obtain ad al v where action-obs (justifying-exec ($? \varphi$ (Suc n))) $r' =$ NormalAction (ReadMem ad al v)
 by cases auto
 with wfn[of Suc n] $\langle r' \in$ read-actions \rightarrow len-E obtain adal
 where ws' (Suc n) $r' \in$ write-actions (justifying-exec ($? \varphi$ (Suc n)))
 and adal \in action-loc P (justifying-exec ($? \varphi$ (Suc n))) r'
 and adal \in action-loc P (justifying-exec ($? \varphi$ (Suc n))) (ws' (Suc n) r')
 by (clarsimp)(auto dest: is-write-seenD)
 moreover {
 from En[of Suc n] len-E have E' (Suc n) $\in \mathcal{E}$ by simp
 moreover
 fix a assume a \in read-actions (justifying-exec ($? \varphi$ (Suc n))) and a $< r'$
 hence a \in read-actions (E' (Suc n)) enat a $<$ enat (Suc n) using len-E by simp-all
 with sim[OF len-E] have a: a \in read-actions E by \neg (rule read-actions-change-prefix)
 with $\langle a < r' \rangle$ have mrw: P,E \vdash a \rightsquigarrow mrw ws a using sc by (simp add: sequentially-consistent-def)
 from P[of Suc n] $\langle a < r' \rangle$ a len-E have ws a = ws' (Suc n) a by simp
 with mrw have mrw': P,E \vdash a \rightsquigarrow mrw ws' (Suc n) a by simp
 moreover from wfn[of Suc n] wf len-E have thread-start-actions-ok (E' (Suc n)) by (simp)
 moreover note sim[OF len-E, symmetric]
 moreover from E wf mrw' have ws' (Suc n) a $<$ a
 by (rule mrw-before)(erule sequentially-consistentE[OF sc])
 with $\langle a < r' \rangle$ have ws' (Suc n) a $<$ r' by simp
 ultimately have P,E' (Suc n) \vdash a \rightsquigarrow mrw ws' (Suc n) a using $\langle a < r' \rangle$
 by \neg (rule mrw-change-prefix, simp+)
 hence P,justifying-exec ($? \varphi$ (Suc n)) \vdash a \rightsquigarrow mrw justifying-ws ($? \varphi$ (Suc n)) a using len-E by
 simp
 }
 ultimately have ws' (Suc n) $r' < r'$ by (rule action-order-read-before-write[OF En wfn])
 with len-E C-n have $?C$ -ws-n byclarsimp (metis Suc-ile-eq linorder-le-cases order-less-irrefl
 order-trans)
 moreover
 from r' have $r' \in$ committed ($? \varphi$ (Suc n)) by blast
 with r' r len-E wf-action-translation-on-actionD[OF wfa this] committed-subset-E[of Suc n]
 have r \in read-actions E by (fastforce elim!: read-actions.cases intro: read-actions.intros split:
 if-split-asm)
 with sc obtain P,E \vdash r \rightsquigarrow mrw ws r by (rule sequentially-consistentE)
 with E wf have ws r $<$ r by (rule mrw-before)(rule sequentially-consistentE[OF sc])
 with C-n len-E r have $?C$ -ws by (auto simp add: Suc-ile-eq)
 ultimately show ?thesis by simp
 qed }
 { fix a a'
 assume a: a \in external-actions (justifying-exec ($? \varphi$ n))
 and a': a' \in committed ($? \varphi$ n)
 and hb: P,justifying-exec ($? \varphi$ n) \vdash a \leq_{hb} a'
 from hb have justifying-exec ($? \varphi$ n) \vdash a \leq_a a'
 by (rule happens-before-into-action-order)
 with a have a \leq a' by (auto elim!: action-orderE dest: external-actions-not-new)

```

    with a' a show a ∈ committed (?φ n) by(auto elim: external-actions.cases) }
  qed
  thus ∧n. ?thesis1 n ∧n. ?thesis2 n ∧n. ?thesis3 n ?thesis4
    by blast+
  qed

theorem SC-is-legal:
  assumes E: E ∈ ℰ
  and wf: P ⊢ (E, ws) √
  and sc: sequentially-consistent P (E, ws)
  shows legal-execution P ℰ (E, ws)
using E wf
apply(rule legal-executionI)
  apply(rule commit-for-sc-correct[OF assms])
apply clarify
apply(unfold o-apply)
apply(rule commit-for-sc-in-ℰ[OF assms])
done

end

context jmm-consistent begin

theorem consistent:
  assumes E ∈ ℰ P ⊢ (E, ws) √
  shows ∃ E ∈ ℰ. ∃ ws. legal-execution P ℰ (E, ws)
proof -
  from ℰ-sequential-completion[OF assms, of 0]
  obtain E' ws' where E' ∈ ℰ P ⊢ (E', ws') √ sequentially-consistent P (E', ws') by auto
  moreover hence legal-execution P ℰ (E', ws') by(rule SC-is-legal)
  ultimately show ?thesis by blast
qed

end

end

```

8.7 Non-speculative prefixes of executions

theory *Non-Speculative* imports

JMM-Spec

../Framework/FWLTS

begin

declare *addr-locsI* [*simp*]

8.7.1 Previously written values

fun *w-value* ::

'm prog ⇒ ('addr × addr-loc) ⇒ 'addr val set) ⇒ ('addr, 'thread-id) obs-event action

⇒ ('addr × addr-loc) ⇒ 'addr val set)

where

w-value P vs (NormalAction (WriteMem ad al v)) = vs((ad, al) := insert v (vs (ad, al)))

| *w-value* P vs (NormalAction (NewHeapElem ad hT)) =

$(\lambda(ad', al). \text{if } ad = ad' \wedge al \in \text{addr-locs } P \text{ hT}$
 $\text{then insert (addr-loc-default } P \text{ hT } al) (vs (ad, al))$
 $\text{else } vs (ad', al))$
 | $w\text{-value } P \text{ vs } - = vs$

lemma *w-value-cases*:

obtains $ad \ al \ v$ **where** $x = \text{NormalAction (WriteMem } ad \ al \ v)$
 | $ad \ hT$ **where** $x = \text{NormalAction (NewHeapElem } ad \ hT)$
 | $ad \ M \ vs \ v$ **where** $x = \text{NormalAction (ExternalCall } ad \ M \ vs \ v)$
 | $ad \ al \ v$ **where** $x = \text{NormalAction (ReadMem } ad \ al \ v)$
 | t **where** $x = \text{NormalAction (ThreadStart } t)$
 | t **where** $x = \text{NormalAction (ThreadJoin } t)$
 | ad **where** $x = \text{NormalAction (SyncLock } ad)$
 | ad **where** $x = \text{NormalAction (SyncUnlock } ad)$
 | t **where** $x = \text{NormalAction (ObsInterrupt } t)$
 | t **where** $x = \text{NormalAction (ObsInterrupted } t)$
 | $x = \text{InitialThreadAction}$
 | $x = \text{ThreadFinishAction}$

by *pat-completeness*

abbreviation *w-values* ::

$'m \text{ prog} \Rightarrow (('addr \times \text{addr-loc}) \Rightarrow 'addr \text{ val set}) \Rightarrow ('addr, 'thread-id) \text{ obs-event action list}$
 $\Rightarrow (('addr \times \text{addr-loc}) \Rightarrow 'addr \text{ val set})$

where *w-values* $P \equiv \text{foldl (w-value } P)$

lemma *in-w-valuesD*:

assumes $w: v \in w\text{-values } P \text{ vs0 obs (ad, al)}$

and $v: v \notin \text{vs0 (ad, al)}$

shows $\exists \text{obs}' \ wa \ \text{obs}''. \text{obs} = \text{obs}' @ \ wa \ \# \ \text{obs}'' \wedge \text{is-write-action } wa \wedge (ad, al) \in \text{action-loc-aux } P$
 $wa \wedge$

$\text{value-written-aux } P \ wa \ al = v$

(is ?concl obs)

using w

proof(*induction obs rule: rev-induct*)

case Nil thus ?case using v by simp

next

case (snoc ob obs)

from snoc.IH show ?case

proof(*cases v \in w-values P vs0 obs (ad, al)*)

case False thus ?thesis using $\langle v \in w\text{-values } P \text{ vs0 (obs @ [ob]) (ad, al) \rangle$

by(*cases ob rule: w-value-cases*)(*auto 4 4 intro: action-loc-aux-intros split: if-split-asm simp add:*

addr-locs-def split: htype.split-asm)

qed fastforce

qed

lemma *w-values-WriteMemD*:

assumes $\text{NormalAction (WriteMem } ad \ al \ v) \in \text{set obs}$

shows $v \in w\text{-values } P \text{ vs0 obs (ad, al)}$

using *assms*

apply(*induct obs rule: rev-induct*)

apply *simp*

apply *clarsimp*

apply(*erule disjE*)

apply *clarsimp*

```

apply clarsimp
apply(case-tac x rule: w-value-cases)
apply auto
done

```

lemma *w-values-new-actionD*:

assumes *NormalAction (NewHeapElem ad hT) ∈ set obs (ad, al) ∈ action-loc-aux P (NormalAction (NewHeapElem ad hT))*

shows *addr-loc-default P hT al ∈ w-values P vs0 obs (ad, al)*

using *assms*

apply(*induct obs rule: rev-induct*)

apply *simp*

apply *clarsimp*

apply(*rename-tac w' obs*)

apply(*case-tac w' rule: w-value-cases*)

apply(*auto simp add: split-beta*)

done

lemma *w-value-mono: vs0 adal ⊆ w-value P vs0 ob adal*

by(*cases ob rule: w-value-cases*)(*auto split: if-split-asm simp add: split-beta*)

lemma *w-values-mono: vs0 adal ⊆ w-values P vs0 obs adal*

by(*induct obs rule: rev-induct*)(*auto del: subsetI intro: w-value-mono subset-trans*)

lemma *w-value-greater: vs0 ≤ w-value P vs0 ob*

by(*rule le-funI*)(*rule w-value-mono*)

lemma *w-values-greater: vs0 ≤ w-values P vs0 obs*

by(*rule le-funI*)(*rule w-values-mono*)

lemma *w-values-eq-emptyD*:

assumes *w-values P vs0 obs adal = {}*

and *w ∈ set obs* **and** *is-write-action w* **and** *adal ∈ action-loc-aux P w*

shows *False*

using *assms(4) assms(1-3)*

apply(*cases rule: action-loc-aux-cases*)

apply(*auto dest!: w-values-new-actionD[where ?vs0.0=vs0 and P=P] w-values-WriteMemD[where ?vs0.0=vs0 and P=P]*)

apply *blast*

done

8.7.2 Coinductive version of non-speculative prefixes

coinductive *non-speculative* ::

'm prog ⇒ ('addr × addr-loc ⇒ 'addr val set) ⇒ ('addr, 'thread-id) obs-event action llist ⇒ bool
for *P* :: *'m prog*

where

LNil: non-speculative P vs LNil

| *LCons:*

\llbracket *case ob of NormalAction (ReadMem ad al v) ⇒ v ∈ vs (ad, al) | - ⇒ True;*

non-speculative P (w-value P vs ob) obs \rrbracket

\implies *non-speculative P vs (LCons ob obs)*

inductive-simps *non-speculative-simps* [*simp*]:
non-speculative P vs LNil
non-speculative P vs (LCons ob obs)

lemma *non-speculative-lappend*:

assumes *lfinite obs*

shows *non-speculative P vs (lappend obs obs') \longleftrightarrow*

non-speculative P vs obs \wedge non-speculative P (w-values P vs (list-of obs)) obs'

(**is** *?concl vs obs*)

using *assms*

proof(*induct arbitrary: vs*)

case *lfinite-LNil* **thus** *?case* **by** *simp*

next

case (*lfinite-LConsI obs ob*)

have *?concl (w-value P vs ob) obs* **by** *fact*

thus *?case* **using** \langle *lfinite obs* \rangle **by** *simp*

qed

lemma

assumes *non-speculative P vs obs*

shows *non-speculative-ltake: non-speculative P vs (ltake n obs) (is ?thesis1)*

and *non-speculative-ldrop: non-speculative P (w-values P vs (list-of (ltake n obs))) (ldrop n obs) (is ?thesis2)*

proof –

note *assms*

also have *obs = lappend (ltake n obs) (ldrop n obs)* **by**(*simp add: lappend-ltake-ldrop*)

finally have *?thesis1 \wedge ?thesis2*

by(*cases n*)(*simp-all add: non-speculative-lappend del: lappend-ltake-enat-ldropn*)

thus *?thesis1 ?thesis2* **by** *blast+*

qed

lemma *non-speculative-coinduct-append* [*consumes 1, case-names non-speculative, case-conclusion non-speculative LNil lappend*]:

assumes *major: X vs obs*

and *step: \bigwedge vs obs. X vs obs*

\implies *obs = LNil \vee*

(\exists *obs' obs''*. *obs = lappend obs' obs'' \wedge obs' \neq LNil \wedge non-speculative P vs obs' \wedge*
(lfinite obs' \longrightarrow (X (w-values P vs (list-of obs')) obs'' \vee
non-speculative P (w-values P vs (list-of obs')) obs'')))

(**is** \bigwedge vs obs. $- \implies - \vee$ *?step vs obs*)

shows *non-speculative P vs obs*

proof –

from *major*

have \exists *obs' obs''*. *obs = lappend (llist-of obs') obs'' \wedge non-speculative P vs (llist-of obs') \wedge*
X (w-values P vs obs') obs''

by(*auto intro: exI[where x=[]]*)

thus *?thesis*

proof(*coinduct*)

case (*non-speculative vs obs*)

then obtain *obs' obs''*

where *obs: obs = lappend (llist-of obs') obs''*

and *sc-obs': non-speculative P vs (llist-of obs')*

and *X: X (w-values P vs obs') obs''* **by** *blast*

```

show ?case
proof(cases obs')
  case Nil
  with X have X vs obs'' by simp
  from step[OF this] show ?thesis
proof
  assume obs'' = LNil
  with Nil obs show ?thesis by simp
next
  assume ?step vs obs''
  then obtain obs''' obs''''
    where obs'': obs'' = lappend obs''' obs'''' and obs''' ≠ LNil
    and sc-obs''': non-speculative P vs obs'''
    and fin: lfinite obs''' ⇒ X (w-values P vs (list-of obs''')) obs'''' ∨
              non-speculative P (w-values P vs (list-of obs''')) obs''''
  by blast
  from ⟨obs''' ≠ LNil⟩ obtain ob obs'''' where obs''': obs''' = LCons ob obs''''
  unfolding neq-LNil-conv by blast
  with Nil obs'' obs have concl1: obs = LCons ob (lappend obs'''' obs''') by simp
  have concl2: case ob of NormalAction (ReadMem ad al v) ⇒ v ∈ vs (ad, al) | - ⇒ True
  using sc-obs''' obs'''' by simp

show ?thesis
proof(cases lfinite obs''')
  case False
  hence lappend obs'''' obs'''' = obs'''' using obs''' by (simp add: lappend-inf)
  hence non-speculative P (w-value P vs ob) (lappend obs'''' obs''')
    using sc-obs''' obs'''' by simp
  with concl1 concl2 have ?LCons by blast
  thus ?thesis by simp
next
  case True
  with obs''' obtain obs'''' where obs''': obs'''' = llist-of obs''''
  by simp(auto simp add: lfinite-eq-range-llist-of)
  from fin[OF True] have ?LCons
proof
  assume X: X (w-values P vs (list-of obs''')) obs''''
  hence X (w-values P (w-value P vs ob) obs'''' obs''')
    using obs'''' obs'''' by simp
  moreover from obs''''
  have lappend obs'''' obs'''' = lappend (llist-of obs''') obs'''' by simp
  moreover have non-speculative P (w-value P vs ob) (llist-of obs''')
    using sc-obs''' obs'''' obs'''' by simp
  ultimately show ?thesis using concl1 concl2 by blast
next
  assume non-speculative P (w-values P vs (list-of obs''')) obs''''
  with sc-obs''' obs'''' obs''''
  have non-speculative P (w-value P vs ob) (lappend obs'''' obs''')
    by (simp add: non-speculative-lappend)
  with concl1 concl2 show ?thesis by blast
qed
thus ?thesis by simp
qed
qed

```



```

next
  case (Cons ob obs'')
  hence obs = LCons ob (lappend (llist-of obs'') obs'')
  using obs by simp
  moreover from sc-obs' Cons
  have case ob of NormalAction (ReadMem ad al v)  $\Rightarrow$   $v \in vs (ad, al) \mid - \Rightarrow$  True
  and non-speculative P (w-value P vs ob) (llist-of obs'') by simp-all
  moreover from X Cons have X (w-values P (w-value P vs ob) obs'') obs'' by simp
  ultimately show ?thesis by blast
qed
qed
qed

lemma non-speculative-coinduct-append-wf
  [consumes 2, case-names non-speculative, case-conclusion non-speculative LNil lappend]:
  assumes major: X vs obs a
  and wf: wf R
  and step:  $\bigwedge vs\ obs\ a.\ X\ vs\ obs\ a$ 
   $\Rightarrow obs = LNil \vee$ 
  ( $\exists obs'\ obs'' a'. obs = lappend\ obs'\ obs'' \wedge non-speculative\ P\ vs\ obs' \wedge (obs' = LNil \longrightarrow (a', a)$ 
 $\in R) \wedge$ 
  ( $lfinite\ obs' \longrightarrow X\ (w-values\ P\ vs\ (list-of\ obs'))\ obs''\ a' \vee$ 
  non-speculative P (w-values P vs (list-of obs')) obs''))
  (is  $\bigwedge vs\ obs\ a.\ - \Longrightarrow - \vee ?step\ vs\ obs\ a$ )
  shows non-speculative P vs obs
proof -
  { fix vs obs a
    assume X vs obs a
    with wf
    have obs = LNil  $\vee (\exists obs'\ obs''. obs = lappend\ obs'\ obs'' \wedge obs' \neq LNil \wedge non-speculative\ P\ vs$ 
  obs'  $\wedge$ 
  ( $lfinite\ obs' \longrightarrow (\exists a.\ X\ (w-values\ P\ vs\ (list-of\ obs'))\ obs''\ a) \vee$ 
  non-speculative P (w-values P vs (list-of obs')) obs''))
  (is  $- \vee ?step-concl\ vs\ obs$ )
  proof (induct a arbitrary: vs obs rule: wf-induct[consumes 1, case-names wf])
  case (wf a)
  note IH = wf.hyps[rule-format]
  from step[OF  $\langle X\ vs\ obs\ a \rangle$ ]
  show ?case
  proof
    assume obs = LNil thus ?thesis ..
  next
    assume ?step vs obs a
    then obtain obs' obs'' a'
      where obs: obs = lappend obs' obs''
      and sc-obs': non-speculative P vs obs'
      and decr: obs' = LNil  $\Longrightarrow (a', a) \in R$ 
      and fin: lfinite obs'  $\Longrightarrow$ 
      X (w-values P vs (list-of obs')) obs'' a'  $\vee$ 
      non-speculative P (w-values P vs (list-of obs')) obs''
      by blast
    show ?case
  proof (cases obs' = LNil)
  case True

```

```

hence lfinite obs' by simp
from fin[OF this] show ?thesis
proof
  assume X: X (w-values P vs (list-of obs')) obs'' a'
  from True have  $(a', a) \in R$  by (rule decr)
  from IH[OF this X] show ?thesis
  proof
    assume  $obs'' = LNil$ 
    with True obs have  $obs = LNil$  by simp
    thus ?thesis ..
  next
    assume ?step-concl (w-values P vs (list-of obs')) obs''
    hence ?step-concl vs obs using True obs by simp
    thus ?thesis ..
  qed
next
  assume non-speculative P (w-values P vs (list-of obs')) obs''
  thus ?thesis using obs True
  by cases(auto cong: action.case-cong obs-event.case-cong intro: exI[where x=LCons x LNil
for x])
  qed
next
  case False
  with obs sc-obs' fin show ?thesis by auto
  qed
qed
qed
qed }
note step' = this

from major show ?thesis
proof (coinduction arbitrary: vs obs a rule: non-speculative-coinduct-append)
  case (non-speculative vs obs)
  thus ?case by simp(rule step')
  qed
qed

lemma non-speculative-nthI:
   $(\bigwedge i \text{ ad al } v. \llbracket \text{enat } i < \text{llength } obs; \text{lnth } obs \ i = \text{NormalAction } (\text{ReadMem } \text{ad } \text{al } v);$ 
     $\text{non-speculative } P \text{ vs } (\text{ltake } (\text{enat } i) \text{ obs}) \rrbracket$ 
     $\implies v \in \text{w-values } P \text{ vs } (\text{list-of } (\text{ltake } (\text{enat } i) \text{ obs})) (\text{ad}, \text{al}))$ 
     $\implies \text{non-speculative } P \text{ vs } obs$ 
proof (coinduction arbitrary: vs obs rule: non-speculative.coinduct)
  case (non-speculative vs obs)
  hence nth:
     $\bigwedge i \text{ ad al } v. \llbracket \text{enat } i < \text{llength } obs; \text{lnth } obs \ i = \text{NormalAction } (\text{ReadMem } \text{ad } \text{al } v);$ 
       $\text{non-speculative } P \text{ vs } (\text{ltake } (\text{enat } i) \text{ obs}) \rrbracket$ 
       $\implies v \in \text{w-values } P \text{ vs } (\text{list-of } (\text{ltake } (\text{enat } i) \text{ obs})) (\text{ad}, \text{al})$  by blast
  show ?case
  proof (cases obs)
  case LNil thus ?thesis by simp
  next
  case (LCons ob obs')
  { fix ad al v

```

```

assume  $ob = \text{NormalAction } (\text{ReadMem } ad \ al \ v)$ 
with  $nth[of \ 0 \ ad \ al \ v] \ LCons$ 
have  $v \in vs \ (ad, \ al) \ \mathbf{by}(simp \ add: \ zero-enat-def[symmetric]) \ }$ 
note  $base = this$ 
moreover {
  fix  $i \ ad \ al \ v$ 
  assume  $enat \ i < llength \ obs' \ lnth \ obs' \ i = \text{NormalAction } (\text{ReadMem } ad \ al \ v)$ 
  and  $non-speculative \ P \ (w-value \ P \ vs \ ob) \ (ltake \ (enat \ i) \ obs')$ 
  with  $LCons \ nth[of \ Suc \ i \ ad \ al \ v] \ base$ 
  have  $v \in w-values \ P \ (w-value \ P \ vs \ ob) \ (list-of \ (ltake \ (enat \ i) \ obs')) \ (ad, \ al)$ 
  by}(clarsimp \ simp \ add: \ eSuc-enat[symmetric] \ split: \ obs-event.split \ action.split) \ }
  ultimately have  $?LCons \ \mathbf{using} \ LCons \ \mathbf{by}(simp \ split: \ action.split \ obs-event.split)$ 
  thus  $?thesis \ ..$ 
qed
qed

locale  $executions-sc-hb =$ 
   $executions-base \ \mathcal{E} \ P$ 
  for  $\mathcal{E} :: ('addr, 'thread-id) \ \text{execution set}$ 
  and  $P :: 'm \ \text{prog} +$ 
  assumes  $\mathcal{E}\text{-new-actions-for-fun:}$ 
   $\llbracket E \in \mathcal{E}; a \in \text{new-actions-for } P \ E \ adal; a' \in \text{new-actions-for } P \ E \ adal \rrbracket \implies a = a'$ 
  and  $\mathcal{E}\text{-ex-new-action:}$ 
   $\llbracket E \in \mathcal{E}; ra \in \text{read-actions } E; adal \in \text{action-loc } P \ E \ ra; non-speculative \ P \ (\lambda-. \ \{\}) \ (ltake \ (enat \ ra) \ (lmap \ snd \ E)) \rrbracket$ 
   $\implies \exists wa. wa \in \text{new-actions-for } P \ E \ adal \wedge wa < ra$ 
begin

lemma  $\mathcal{E}\text{-new-same-addr-singleton:}$ 
  assumes  $E: E \in \mathcal{E}$ 
  shows  $\exists a. \text{new-actions-for } P \ E \ adal \subseteq \{a\}$ 
by}(blast \ dest: \ \mathcal{E}\text{-new-actions-for-fun}[OF \ E])

lemma  $\text{new-action-before-read:}$ 
  assumes  $E: E \in \mathcal{E}$ 
  and  $ra: ra \in \text{read-actions } E$ 
  and  $adal: adal \in \text{action-loc } P \ E \ ra$ 
  and  $new: wa \in \text{new-actions-for } P \ E \ adal$ 
  and  $sc: non-speculative \ P \ (\lambda-. \ \{\}) \ (ltake \ (enat \ ra) \ (lmap \ snd \ E))$ 
  shows  $wa < ra$ 
using  $\mathcal{E}\text{-new-same-addr-singleton}[OF \ E, \ of \ adal] \ \mathcal{E}\text{-ex-new-action}[OF \ E \ ra \ adal \ sc] \ new$ 
by  $auto$ 

lemma  $\text{most-recent-write-exists:}$ 
  assumes  $E: E \in \mathcal{E}$ 
  and  $ra: ra \in \text{read-actions } E$ 
  and  $sc: non-speculative \ P \ (\lambda-. \ \{\}) \ (ltake \ (enat \ ra) \ (lmap \ snd \ E))$ 
  shows  $\exists wa. P, E \vdash ra \rightsquigarrow_{mrw} wa$ 
proof –
  from  $ra$  obtain  $ad \ al$  where
   $adal: (ad, \ al) \in \text{action-loc } P \ E \ ra$ 
  by}(rule \ read-action-action-locE)

define  $Q$  where  $Q = \{a. a \in \text{write-actions } E \wedge (ad, \ al) \in \text{action-loc } P \ E \ a \wedge E \vdash a \leq a \ ra\}$ 

```

```

let ?A = new-actions-for P E (ad, al)
let ?B = {a. a ∈ actions E ∧ (∃ v'. action-obs E a = NormalAction (WriteMem ad al v')) ∧ a ≤
ra}

have Q ⊆ ?A ∪ ?B unfolding Q-def
  by(auto elim!: write-actions.cases action-loc-aux-cases simp add: new-actions-for-def elim: ac-
tion-orderE)
moreover from  $\mathcal{E}$ -new-same-addr-singleton[OF E, of (ad, al)]
have finite ?A by(blast intro: finite-subset)
moreover have finite ?B by auto
ultimately have finQ: finite Q
  by(blast intro: finite-subset)

from  $\mathcal{E}$ -ex-new-action[OF E ra adal sc] ra obtain wa
  where wa: wa ∈ Q unfolding Q-def
  by(fastforce elim!: new-actionsE is-new-action.cases read-actions.cases intro: write-actionsI ac-
tion-orderI)

define wa' where wa' = Max-torder (action-order E) Q

from wa have Q ≠ {} Q ⊆ actions E by(auto simp add: Q-def)
with finQ have wa' ∈ Q unfolding wa'-def
  by(rule Max-torder-in-set[OF torder-action-order])
hence E ⊢ wa' ≤a ra wa' ∈ write-actions E
  and (ad, al) ∈ action-loc P E wa' by(simp-all add: Q-def)
with ra adal have P, E ⊢ ra ~mrw wa'
proof
  fix wa''
  assume wa'': wa'' ∈ write-actions E (ad, al) ∈ action-loc P E wa''
  from ⟨wa'' ∈ write-actions E⟩ ra
  have ra ≠ wa'' by(auto dest: read-actions-not-write-actions)
  show E ⊢ wa'' ≤a wa' ∨ E ⊢ ra ≤a wa''
  proof(rule disjCI)
    assume ¬ E ⊢ ra ≤a wa''
    with total-onPD[OF total-action-order, of ra E wa'']
      ⟨ra ≠ wa''⟩ ⟨ra ∈ read-actions E⟩ ⟨wa'' ∈ write-actions E⟩
    have E ⊢ wa'' ≤a ra by simp
    with wa'' have wa'' ∈ Q by(simp add: Q-def)
    with finQ show E ⊢ wa'' ≤a wa'
      using ⟨Q ⊆ actions E⟩ unfolding wa'-def
      by(rule Max-torder-above[OF torder-action-order])
  qed
  qed
  thus ?thesis ..
qed

lemma mrw-before:
  assumes E: E ∈  $\mathcal{E}$ 
  and mrw: P, E ⊢ r ~mrw w
  and sc: non-speculative P (λ-. {}) (ltake (enat r) (lmap snd E))
  shows w < r
using mrw read-actions-not-write-actions[of r E]
apply cases
apply(erule action-orderE)

```

```

apply(erule (1) new-action-before-read[OF E])
apply(simp add: new-actions-for-def)
apply(rule sc)
apply(cases w = r)
apply auto
done

```

lemma *sequentially-consistent-most-recent-write-for*:

```

assumes E: E ∈  $\mathcal{E}$ 
and sc: non-speculative P (λ-. {}) (lmap snd E)
shows sequentially-consistent P (E, λr. THE w. P,E ⊢ r ~mrw w)
proof(rule sequentially-consistentI)
  fix r
  assume r: r ∈ read-actions E
  from sc have sc': non-speculative P (λ-. {}) (ltake (enat r) (lmap snd E))
    by(rule non-speculative-ltake)
  from most-recent-write-exists[OF E r this]
  obtain w where P,E ⊢ r ~mrw w ..
  thus P,E ⊢ r ~mrw THE w. P,E ⊢ r ~mrw w
    by(simp add: THE-most-recent-writeI)
qed

```

end

```

locale jmm-multithreaded = multithreaded-base +
  constrains final :: 'x ⇒ bool
  and r :: ('l, 'thread-id, 'x, 'm, 'w, ('addr, 'thread-id) obs-event action) semantics
  and convert-RA :: 'l released-locks ⇒ ('addr, 'thread-id) obs-event action list
  fixes P :: 'md prog

```

end

8.8 Sequentially consistent completion of executions in the JMM

theory *SC-Completion*

imports

Non-Speculative

begin

8.8.1 Most recently written values

fun *mrw-value* ::

```

  'm prog ⇒ (('addr × addr-loc) → ('addr val × bool)) ⇒ ('addr, 'thread-id) obs-event action
  ⇒ (('addr × addr-loc) → ('addr val × bool))

```

where

```

  mrw-value P vs (NormalAction (WriteMem ad al v)) = vs((ad, al) ↦ (v, True))
| mrw-value P vs (NormalAction (NewHeapElem ad hT)) =
  (λ(ad', al). if ad = ad' ∧ al ∈ addr-locs P hT ∧ (case vs (ad, al) of None ⇒ True | Some (v, b)
  ⇒ ¬ b)
    then Some (addr-loc-default P hT al, False)
    else vs (ad', al))
| mrw-value P vs - = vs

```

lemma *mrw-value-cases*:

obtains *ad al v* **where** $x = \text{NormalAction } (\text{WriteMem } ad \ al \ v)$
 | *ad hT* **where** $x = \text{NormalAction } (\text{NewHeapElem } ad \ hT)$
 | *ad M vs v* **where** $x = \text{NormalAction } (\text{ExternalCall } ad \ M \ vs \ v)$
 | *ad al v* **where** $x = \text{NormalAction } (\text{ReadMem } ad \ al \ v)$
 | *t* **where** $x = \text{NormalAction } (\text{ThreadStart } t)$
 | *t* **where** $x = \text{NormalAction } (\text{ThreadJoin } t)$
 | *ad* **where** $x = \text{NormalAction } (\text{SyncLock } ad)$
 | *ad* **where** $x = \text{NormalAction } (\text{SyncUnlock } ad)$
 | *t* **where** $x = \text{NormalAction } (\text{ObsInterrupt } t)$
 | *t* **where** $x = \text{NormalAction } (\text{ObsInterrupted } t)$
 | $x = \text{InitialThreadAction}$
 | $x = \text{ThreadFinishAction}$

by *pat-completeness*

abbreviation *mrw-values* ::

$'m \text{ prog} \Rightarrow (('addr \times \text{addr-loc}) \rightarrow ('addr \ \text{val} \times \text{bool})) \Rightarrow ('addr, 'thread-id) \text{ obs-event action list}$
 $\Rightarrow (('addr \times \text{addr-loc}) \rightarrow ('addr \ \text{val} \times \text{bool}))$

where *mrw-values* $P \equiv \text{foldl } (\text{mrw-value } P)$

lemma *mrw-values-eq-SomeD*:

assumes *mrw*: *mrw-values* $P \ \text{vs0} \ \text{obs} \ (ad, al) = \lfloor (v, b) \rfloor$

and *vs0* $(ad, al) = \lfloor (v, b) \rfloor \implies \exists wa. wa \in \text{set } \text{obs} \wedge \text{is-write-action } wa \wedge (ad, al) \in \text{action-loc-aux } P \ wa \wedge (b \longrightarrow \neg \text{is-new-action } wa)$

shows $\exists \text{obs}' \ wa \ \text{obs}''. \text{obs} = \text{obs}' \ @ \ wa \ \# \ \text{obs}'' \wedge \text{is-write-action } wa \wedge (ad, al) \in \text{action-loc-aux } P \ wa \wedge$

$\text{value-written-aux } P \ wa \ al = v \wedge (\text{is-new-action } wa \longleftrightarrow \neg b) \wedge$

$(\forall ob \in \text{set } \text{obs}''. \text{is-write-action } ob \longrightarrow (ad, al) \in \text{action-loc-aux } P \ ob \longrightarrow \text{is-new-action } ob \wedge$

$b)$

(is ?concl obs)

using *assms*

proof(*induct obs rule: rev-induct*)

case Nil thus ?case by simp

next

case (*snoc ob obs*)

note $mrw = \langle \text{mrw-values } P \ \text{vs0} \ (\text{obs} \ @ \ [ob]) \ (ad, al) = \lfloor (v, b) \rfloor \rangle$

show *?case*

proof(*cases is-write-action ob* $\wedge (ad, al) \in \text{action-loc-aux } P \ ob \wedge (\text{is-new-action } ob \longrightarrow \neg b)$)

case True thus ?thesis using *mrw*

by(*fastforce elim!: is-write-action.cases intro: action-loc-aux-intros split: if-split-asm*)

next

case False

with *mrw* **have** *mrw-values* $P \ \text{vs0} \ \text{obs} \ (ad, al) = \lfloor (v, b) \rfloor$

by(*cases ob rule: mrw-value-cases*)(*auto split: if-split-asm simp add: addr-locs-def split: htype.split-asm*)

moreover

{ **assume** *vs0* $(ad, al) = \lfloor (v, b) \rfloor$

hence $\exists wa. wa \in \text{set } (\text{obs} \ @ \ [ob]) \wedge \text{is-write-action } wa \wedge (ad, al) \in \text{action-loc-aux } P \ wa \wedge (b \longrightarrow \neg \text{is-new-action } wa)$

by(*rule snoc*)

with False **have** $\exists wa. wa \in \text{set } \text{obs} \wedge \text{is-write-action } wa \wedge (ad, al) \in \text{action-loc-aux } P \ wa \wedge (b \longrightarrow \neg \text{is-new-action } wa)$

by *auto* }

ultimately **have** *?concl obs* **by**(*rule snoc*)

```

  thus ?thesis using False mrw by fastforce
qed
qed

```

lemma *mrw-values-WriteMemD*:

```

  assumes NormalAction (WriteMem ad al v') ∈ set obs
  shows ∃ v. mrw-values P vs0 obs (ad, al) = Some (v, True)
using assms
apply(induct obs rule: rev-induct)
  apply simp
apply clarsimp
apply(erule disjE)
  apply clarsimp
apply clarsimp
apply(case-tac x rule: mrw-value-cases)
apply simp-all
done

```

lemma *mrw-values-new-actionD*:

```

  assumes w ∈ set obs is-new-action w adal ∈ action-loc-aux P w
  shows ∃ v b. mrw-values P vs0 obs adal = Some (v, b)
using assms
apply(induct obs rule: rev-induct)
  apply simp
apply clarsimp
apply(erule disjE)
  apply(fastforce simp add: split-beta elim!: action-loc-aux-cases is-new-action.cases)
apply clarsimp
apply(rename-tac w' obs' v b)
apply(case-tac w' rule: mrw-value-cases)
apply(auto simp add: split-beta)
done

```

lemma *mrw-value-dom-mono*:

```

  dom vs ⊆ dom (mrw-value P vs ob)
by(cases ob rule: mrw-value-cases) auto

```

lemma *mrw-values-dom-mono*:

```

  dom vs ⊆ dom (mrw-values P vs obs)
by(induct obs arbitrary: vs)(auto intro: subset-trans[OF mrw-value-dom-mono] del: subsetI)

```

lemma *mrw-values-eq-NoneD*:

```

  assumes mrw-values P vs0 obs adal = None
  and w ∈ set obs and is-write-action w and adal ∈ action-loc-aux P w
  shows False
using assms
apply –
apply(erule is-write-action.cases)
apply(fastforce dest: mrw-values-WriteMemD[where ?vs0.0=vs0 and P=P] mrw-values-new-actionD[where ?vs0.0=vs0] elim: action-loc-aux-cases)+
done

```

lemma *mrw-values-mrw*:

```

  assumes mrw: mrw-values P vs0 (map snd obs) (ad, al) = [(v, b)]

```

and *initial*: $vs0 (ad, al) = \lfloor (v, b) \rfloor \implies \exists wa. wa \in set (map\ snd\ obs) \wedge is\text{-}write\text{-}action\ wa \wedge (ad, al) \in action\text{-}loc\text{-}aux\ P\ wa \wedge (b \longrightarrow \neg is\text{-}new\text{-}action\ wa)$

shows $\exists i. i < length\ obs \wedge P, llist\text{-}of (obs @ [(t, NormalAction (ReadMem\ ad\ al\ v))]) \vdash length\ obs \rightsquigarrow mrw\ i \wedge value\text{-}written\ P (l\text{-}list\text{-}of\ obs)\ i (ad, al) = v$

proof –

from *mrw-values-eq-SomeD*[*OF mrw initial*]

obtain *obs'* *wa* *obs''* **where** $obs: map\ snd\ obs = obs' @ wa \# obs''$

and *wa*: *is-write-action wa*

and *adal*: $(ad, al) \in action\text{-}loc\text{-}aux\ P\ wa$

and *written*: *value-written-aux P wa al = v*

and *new*: $is\text{-}new\text{-}action\ wa \longleftrightarrow \neg b$

and *last*: $\bigwedge ob. \llbracket ob \in set\ obs''; is\text{-}write\text{-}action\ ob; (ad, al) \in action\text{-}loc\text{-}aux\ P\ ob \rrbracket \implies is\text{-}new\text{-}action\ ob \wedge b$

by *blast*

let *?i* = *length obs'*

let *?E* = *l\text{-}list\text{-}of (obs @ [(t, NormalAction (ReadMem ad al v))])*

from *obs* **have** *len*: $length (map\ snd\ obs) = Suc (length\ obs') + length\ obs''$ **by** *simp*

hence *?i* < *length obs* **by** *simp*

moreover

hence *obs-i*: *action-obs ?E ?i = wa* **using** *len obs*

by(*auto simp add: action-obs-def map-eq-append-conv*)

have *P, ?E* $\vdash length\ obs \rightsquigarrow mrw\ ?i$

proof(*rule most-recent-write-for.intros*)

show $length\ obs \in read\text{-}actions\ ?E$

by(*auto intro: read-actions.intros simp add: actions-def action-obs-def*)

show $(ad, al) \in action\text{-}loc\ P\ ?E (length\ obs)$

by(*simp add: action-obs-def lnth-l\text{-}list\text{-}of*)

show $?E \vdash length\ obs' \leq_a length\ obs$ **using** *len*

by–(*rule action-orderI, auto simp add: actions-def action-obs-def nth-append*)

show $?i \in write\text{-}actions\ ?E$ **using** *len obs wa*

by–(*rule write-actions.intros, auto simp add: actions-def action-obs-def nth-append map-eq-append-conv*)

show $(ad, al) \in action\text{-}loc\ P\ ?E\ ?i$ **using** *obs-i adal* **by** *simp*

fix *wa'*

assume *wa'*: $wa' \in write\text{-}actions\ ?E$

and *adal'*: $(ad, al) \in action\text{-}loc\ P\ ?E\ wa'$

from *wa'* $\langle ?i \in write\text{-}actions\ ?E \rangle$

have $wa' \in actions\ ?E\ ?i \in actions\ ?E$ **by** *simp-all*

hence $?E \vdash wa' \leq_a ?i$

proof(*rule action-orderI*)

assume *new-wa'*: *is-new-action (action-obs ?E wa')*

and *new-i*: *is-new-action (action-obs ?E ?i)*

from *new-i obs-i new* **have** $b: \neg b$ **by** *simp*

show $wa' \leq ?i$

proof(*rule ccontr*)

assume $\neg ?thesis$

hence $?i < wa'$ **by** *simp*

hence $snd (obs ! wa') \in set\ obs''$ **using** *obs wa' unfolding in-set-conv-nth*

by –(*rule exI[where x=wa' – Suc (length obs')], auto elim!: write-actions.cases actionsE simp add: action-obs-def lnth-l\text{-}list\text{-}of actions-def nth-append map-eq-append-conv nth-Cons' split: if-split-asm*)


```

moreover from  $wa'$  have is-write-action ( $snd$  ( $obs ! wa'$ ))
  by cases(auto simp add: action-obs-def nth-append actions-def split: if-split-asm)
moreover from  $adal'$   $wa'$  have  $(ad, al) \in action-loc-aux P$  ( $snd$  ( $obs ! wa'$ ))
  by(auto simp add: action-obs-def nth-append nth-Cons' actions-def split: if-split-asm elim!:
write-actions.cases)
  ultimately show False using last[of snd (obs ! wa')] b by simp
qed
next
assume  $new-wa': \neg is-new-action$  ( $action-obs ?E wa'$ )
with  $wa'$   $adal'$  obtain  $v'$  where NormalAction ( $WriteMem ad al v'$ )  $\in set$  ( $map snd obs$ )
  unfolding in-set-conv-nth
  by (fastforce elim!: write-actions.cases is-write-action.cases simp add: action-obs-def actions-def
nth-append split: if-split-asm intro!: exI[where x=wa'])
from mrw-values-WriteMemD[OF this, of P vs0] mrw have  $b$  by simp
with  $new\ obs-i$  have  $\neg is-new-action$  ( $action-obs ?E ?i$ ) by simp
moreover
have  $wa' \leq ?i$ 
proof(rule ccontr)
  assume  $\neg ?thesis$ 
  hence  $?i < wa'$  by simp
  hence  $snd (obs ! wa') \in set\ obs''$  using  $obs\ wa'$  unfolding in-set-conv-nth
    by  $\neg(rule\ exI[where\ x=wa' - Suc\ (length\ obs')], auto\ elim!: write-actions.cases\ ac-$ 
tionsE simp add: action-obs-def lnth-llist-of actions-def nth-append map-eq-append-conv nth-Cons'
split: if-split-asm)
  moreover from  $wa'$  have is-write-action ( $snd (obs ! wa')$ )
    by cases(auto simp add: action-obs-def nth-append actions-def split: if-split-asm)
  moreover from  $adal'$   $wa'$  have  $(ad, al) \in action-loc-aux P$  ( $snd (obs ! wa')$ )
    by(auto simp add: action-obs-def nth-append nth-Cons' actions-def split: if-split-asm elim!:
write-actions.cases)
  ultimately have is-new-action ( $snd (obs ! wa')$ ) using last[of snd (obs ! wa')] by simp
  moreover from  $new-wa'$   $wa'$  have  $\neg is-new-action$  ( $snd (obs ! wa')$ )
    by(auto elim!: write-actions.cases simp add: action-obs-def nth-append actions-def split:
if-split-asm)
  ultimately show False by contradiction
qed
ultimately
show  $\neg is-new-action$  ( $action-obs ?E ?i$ )  $\wedge wa' \leq ?i$  by blast
qed
thus  $?E \vdash wa' \leq a\ ?i \vee ?E \vdash length\ obs \leq a\ wa' ..$ 
qed
moreover from written  $\langle ?i < length\ obs \rangle obs-i$ 
have value-written  $P$  (llist-of  $obs$ )  $?i$  ( $ad, al$ )  $= v$ 
  by(simp add: value-written-def action-obs-def nth-append)
ultimately show ?thesis by blast
qed

lemma mrw-values-no-write-unchanged:
  assumes no-write:  $\bigwedge w. \llbracket w \in set\ obs; is-write-action\ w; adal \in action-loc-aux\ P\ w \rrbracket$ 
   $\implies case\ vs\ adal\ of\ None \implies False \mid Some\ (v, b) \implies b \wedge is-new-action\ w$ 
  shows mrw-values  $P\ vs\ obs\ adal = vs\ adal$ 
using assms
proof(induct obs arbitrary: vs)
  case Nil show ?case by simp
next

```

```

case (Cons ob obs)
from Cons.premis[of ob]
have mrw-value P vs ob adal = vs adal
  apply(cases adal)
  apply(cases ob rule: mrw-value-cases, fastforce+)
  apply(auto simp add: addr-locs-def split: htype.split-asm)
  apply blast+
  done
moreover
have mrw-values P (mrw-value P vs ob) obs adal = mrw-value P vs ob adal
proof(rule Cons.hypos)
  fix w
  assume w ∈ set obs is-write-action w adal ∈ action-loc-aux P w
  with Cons.premis[of w] ⟨mrw-value P vs ob adal = vs adal⟩
  show case mrw-value P vs ob adal of None ⇒ False | [(v, b)] ⇒ b ∧ is-new-action w by simp
  qed
ultimately show ?case by simp
qed

```

8.8.2 Coinductive version of sequentially consistent prefixes

```

coinductive ta-seq-consist ::
  'm prog ⇒ ('addr × addr-loc → 'addr val × bool) ⇒ ('addr, 'thread-id) obs-event action llist ⇒ bool
for P :: 'm prog
where
  LNil: ta-seq-consist P vs LNil
  | LCons:
    [[ case ob of NormalAction (ReadMem ad al v) ⇒ ∃ b. vs (ad, al) = [(v, b)] | - ⇒ True;
      ta-seq-consist P (mrw-value P vs ob) obs ]
    ⇒ ta-seq-consist P vs (LCons ob obs)

```

inductive-simps ta-seq-consist-simps [simp]:

```

ta-seq-consist P vs LNil
ta-seq-consist P vs (LCons ob obs)

```

lemma ta-seq-consist-lappend:

```

assumes lfinite obs
shows ta-seq-consist P vs (lappend obs obs') ↔
  ta-seq-consist P vs obs ∧ ta-seq-consist P (mrw-values P vs (list-of obs)) obs'
(is ?concl vs obs)

```

using assms

proof(induct arbitrary: vs)

```

case lfinite-LNil thus ?case by simp

```

next

```

case (lfinite-LConsI obs ob)

```

```

have ?concl (mrw-value P vs ob) obs by fact

```

```

thus ?case using ⟨lfinite obs⟩ by(simp split: action.split add: list-of-LCons)

```

qed

lemma

```

assumes ta-seq-consist P vs obs

```

```

shows ta-seq-consist-ltake: ta-seq-consist P vs (ltake n obs) (is ?thesis1)

```

```

and ta-seq-consist-ldrop: ta-seq-consist P (mrw-values P vs (list-of (ltake n obs))) (ldrop n obs) (is ?thesis2)

```

proof –

note *assms*

also have $obs = lappend (ltake\ n\ obs) (ldrop\ n\ obs)$ **by** (*simp add: lappend-ltake-ldrop*)

finally have $?thesis1 \wedge ?thesis2$

by (*cases n*) (*simp-all add: ta-seq-consist-lappend del: lappend-ltake-enat-ldropn*)

thus $?thesis1\ ?thesis2$ **by** *blast+*

qed

lemma *ta-seq-consist-coinduct-append* [*consumes 1, case-names ta-seq-consist, case-conclusion ta-seq-consist LNil lappend*]:

assumes major: $X\ vs\ obs$

and step: $\bigwedge\ vs\ obs.\ X\ vs\ obs$

$\implies obs = LNil \vee$

$(\exists\ obs'\ obs''.\ obs = lappend\ obs'\ obs'' \wedge obs' \neq LNil \wedge ta-seq-consist\ P\ vs\ obs' \wedge$
 $(lfinite\ obs' \implies (X\ (mrw-values\ P\ vs\ (list-of\ obs'))\ obs'' \vee$
 $ta-seq-consist\ P\ (mrw-values\ P\ vs\ (list-of\ obs'))\ obs''))$

(**is** $\bigwedge\ vs\ obs.\ - \implies - \vee ?step\ vs\ obs$)

shows $ta-seq-consist\ P\ vs\ obs$

proof –

from major

have $\exists\ obs'\ obs''.\ obs = lappend\ (llist-of\ obs')\ obs'' \wedge ta-seq-consist\ P\ vs\ (llist-of\ obs') \wedge$
 $X\ (mrw-values\ P\ vs\ obs')\ obs''$

by (*auto intro: exI[where x=[]]*)

thus $?thesis$

proof (*coinduct*)

case (*ta-seq-consist vs obs*)

then obtain $obs'\ obs''$

where $obs: obs = lappend\ (llist-of\ obs')\ obs''$

and $sc-obs': ta-seq-consist\ P\ vs\ (llist-of\ obs')$

and $X: X\ (mrw-values\ P\ vs\ obs')\ obs''$ **by** *blast*

show $?case$

proof (*cases obs'*)

case *Nil*

with X **have** $X\ vs\ obs''$ **by** *simp*

from *step[OF this]* **show** $?thesis$

proof

assume $obs'' = LNil$

with *Nil obs* **show** $?thesis$ **by** *simp*

next

assume $?step\ vs\ obs''$

then obtain $obs''' obs''''$

where $obs''': obs'' = lappend\ obs''' obs''''$ **and** $obs''' \neq LNil$

and $sc-obs''': ta-seq-consist\ P\ vs\ obs'''$

and $fin: lfinite\ obs''' \implies X\ (mrw-values\ P\ vs\ (list-of\ obs'''))\ obs'''' \vee$
 $ta-seq-consist\ P\ (mrw-values\ P\ vs\ (list-of\ obs'''))\ obs''''$

by *blast*

from $\langle obs'''' \neq LNil \rangle$ **obtain** $ob\ obs''''''$ **where** $obs''''': obs'''' = LCons\ ob\ obs''''''$

unfolding *neq-LNil-conv* **by** *blast*

with *Nil obs'' obs* **have** *concl1*: $obs = LCons\ ob\ (lappend\ obs'''''' obs''''')$ **by** *simp*

have *concl2*: *case ob of NormalAction (ReadMem ad al v) $\Rightarrow \exists b.\ vs\ (ad, al) = [(v, b)] \mid - \Rightarrow$*

True

using $sc-obs''' obs''''$ **by** *simp*

```

show ?thesis
proof(cases lfinite obs''')
  case False
  hence lappend obs'''' obs'''' = obs'''' using obs''' by(simp add: lappend-inf)
  hence ta-seq-consist P (mrw-value P vs ob) (lappend obs'''' obs''')
    using sc-obs''' obs''' by simp
  with concl1 concl2 have ?LCons by blast
  thus ?thesis by simp
next
case True
with obs''' obtain obs'''' where obs''': obs'''' = llist-of obs''''
  by simp(auto simp add: lfinite-eq-range-llist-of)
from fin[OF True] have ?LCons
proof
  assume X: X (mrw-values P vs (list-of obs''')) obs''''
  hence X (mrw-values P (mrw-value P vs ob) obs''''') obs''''
    using obs'''' obs''' by simp
  moreover from obs''''
  have lappend obs'''' obs'''' = lappend (llist-of obs''''') obs'''' by simp
  moreover have ta-seq-consist P (mrw-value P vs ob) (llist-of obs''''')
    using sc-obs''' obs''' obs'''' by simp
  ultimately show ?thesis using concl1 concl2 by blast
next
assume ta-seq-consist P (mrw-values P vs (list-of obs''')) obs''''
with sc-obs''' obs'''' obs'''
have ta-seq-consist P (mrw-value P vs ob) (lappend obs'''' obs''')
  by(simp add: ta-seq-consist-lappend)
with concl1 concl2 show ?thesis by blast
qed
thus ?thesis by simp
qed
qed
next
case (Cons ob obs''')
hence obs = LCons ob (lappend (llist-of obs''') obs'')
  using obs by simp
moreover from sc-obs' Cons
have case ob of NormalAction (ReadMem ad al v)  $\Rightarrow \exists b. vs (ad, al) = [(v, b)] \mid - \Rightarrow True$ 
  and ta-seq-consist P (mrw-value P vs ob) (llist-of obs''') by simp-all
moreover from X Cons have X (mrw-values P (mrw-value P vs ob) obs''') obs'' by simp
ultimately show ?thesis by blast
qed
qed
qed

```

lemma *ta-seq-consist-coinduct-append-wf*
[consumes 2, case-names ta-seq-consist, case-conclusion ta-seq-consist LNil lappend]:
assumes major: $X vs obs a$
and wf: wf R
and step: $\bigwedge vs obs a. X vs obs a \Rightarrow obs = LNil \vee (\exists obs' obs'' a'. obs = lappend obs' obs'' \wedge ta-seq-consist P vs obs' \wedge (obs' = LNil \longrightarrow (a', a) \in R) \wedge (lfinite obs' \longrightarrow X (mrw-values P vs (list-of obs')) obs'' a' \vee$

```

                                ta-seq-consist P (mrw-values P vs (list-of obs')) obs'')
(is  $\wedge$  vs obs a. -  $\implies$  -  $\vee$  ?step vs obs a)
shows ta-seq-consist P vs obs
proof -
{ fix vs obs a
  assume X vs obs a
  with wf
  have obs = LNil  $\vee$  ( $\exists$  obs' obs''. obs = lappend obs' obs''  $\wedge$  obs'  $\neq$  LNil  $\wedge$  ta-seq-consist P vs obs'
 $\wedge$ 
    (lfinite obs'  $\implies$  ( $\exists$  a. X (mrw-values P vs (list-of obs')) obs'' a)  $\vee$ 
      ta-seq-consist P (mrw-values P vs (list-of obs')) obs''))
(is -  $\vee$  ?step-concl vs obs)
proof(induct an arbitrary: vs obs rule: wf-induct[consumes 1, case-names wf])
case (wf a)
note IH = wf.hyps[rule-format]
from step[OF  $\langle$ X vs obs a $\rangle$ ]
show ?case
proof
  assume obs = LNil thus ?thesis ..
next
  assume ?step vs obs a
  then obtain obs' obs'' a'
  where obs: obs = lappend obs' obs''
  and sc-obs': ta-seq-consist P vs obs'
  and decr: obs' = LNil  $\implies$  (a', a)  $\in$  R
  and fin: lfinite obs'  $\implies$ 
    X (mrw-values P vs (list-of obs')) obs'' a'  $\vee$ 
    ta-seq-consist P (mrw-values P vs (list-of obs')) obs''
  by blast
show ?case
proof(cases obs' = LNil)
case True
hence lfinite obs' by simp
from fin[OF this] show ?thesis
proof
  assume X: X (mrw-values P vs (list-of obs')) obs'' a'
  from True have (a', a)  $\in$  R by(rule decr)
  from IH[OF this X] show ?thesis
proof
  assume obs'' = LNil
  with True obs have obs = LNil by simp
  thus ?thesis ..
next
  assume ?step-concl (mrw-values P vs (list-of obs')) obs''
  hence ?step-concl vs obs using True obs by simp
  thus ?thesis ..
qed
next
  assume ta-seq-consist P (mrw-values P vs (list-of obs')) obs''
  thus ?thesis using obs True
  by cases(auto cong: action.case-cong obs-event.case-cong intro: exI[where x=LCons x LNil
for x])
qed
next

```

```

      case False
      with obs sc-obs' fin show ?thesis by auto
    qed
  qed
  qed }
  note step' = this

```

```

from major show ?thesis
proof (coinduction arbitrary: vs obs a rule: ta-seq-consist-coinduct-append)
  case (ta-seq-consist vs obs a)
  thus ?case by simp(rule step')
  qed
qed

```

lemma *ta-seq-consist-nthI*:

```

( $\wedge i$  ad al v.  $\llbracket$  enat  $i < llength$  obs;  $lnth$  obs  $i = NormalAction$  (ReadMem ad al v);
  ta-seq-consist  $P$  vs (ltake (enat  $i$ ) obs)  $\rrbracket$ 
 $\implies \exists b$ . mrw-values  $P$  vs (list-of (ltake (enat  $i$ ) obs)) (ad, al) =  $\llbracket (v, b) \rrbracket$ )
 $\implies$  ta-seq-consist  $P$  vs obs

```

proof(coinduction arbitrary: vs obs)

case (ta-seq-consist vs obs)

hence nth:

```

 $\wedge i$  ad al v.  $\llbracket$  enat  $i < llength$  obs;  $lnth$  obs  $i = NormalAction$  (ReadMem ad al v);
  ta-seq-consist  $P$  vs (ltake (enat  $i$ ) obs)  $\rrbracket$ 
 $\implies \exists b$ . mrw-values  $P$  vs (list-of (ltake (enat  $i$ ) obs)) (ad, al) =  $\llbracket (v, b) \rrbracket$  by blast

```

show ?case

proof(cases obs)

case LNil thus ?thesis by simp

next

case (LCons ob obs')

{ fix ad al v

assume ob = NormalAction (ReadMem ad al v)

with nth[of 0 ad al v] LCons

have $\exists b$. vs (ad, al) = $\llbracket (v, b) \rrbracket$

by (simp add: zero-enat-def[symmetric]) }

note base = this

moreover {

fix i ad al v

assume enat $i < llength$ obs' $lnth$ obs' $i = NormalAction$ (ReadMem ad al v)

and ta-seq-consist P (mrw-value P vs ob) (ltake (enat i) obs')

with LCons nth[of Suc i ad al v] base

have $\exists b$. mrw-values P (mrw-value P vs ob) (list-of (ltake (enat i) obs')) (ad, al) = $\llbracket (v, b) \rrbracket$

by (clarsimp simp add: eSuc-enat[symmetric] split: obs-event.split action.split) }

ultimately have ?LCons using LCons by (simp split: action.split obs-event.split)

thus ?thesis ..

qed

qed

lemma *ta-seq-consist-into-non-speculative*:

```

 $\llbracket$  ta-seq-consist  $P$  vs obs;  $\forall adal$ . set-option (vs adal)  $\subseteq$  vs' adal  $\times UNIV$   $\rrbracket$ 
 $\implies$  non-speculative  $P$  vs' obs

```

proof(coinduction arbitrary: vs' obs vs)

case (non-speculative vs' obs vs)

thus ?case

```

apply cases
apply(auto split: action.split-asm obs-event.split-asm)
apply(rule exI, erule conjI, auto)+
done
qed

```

lemma *llist-of-list-of-append*:

lfinite xs \implies llist-of (list-of xs @ ys) = lappend xs (llist-of ys)

unfolding *lfinite-eq-range-llist-of* **by**(*clarsimp simp add: lappend-llist-of-llist-of*)

lemma *ta-seq-consist-most-recent-write-for*:

assumes *sc: ta-seq-consist P Map.empty (lmap snd E)*

and *read: r \in read-actions E*

and *new-actions-for-fun: $\bigwedge adal a a'. \llbracket a \in \text{new-actions-for } P E adal; a' \in \text{new-actions-for } P E adal \rrbracket \implies a = a'$*

shows $\exists i. P, E \vdash r \rightsquigarrow mrw i \wedge i < r$

proof –

from *read* **obtain** *t v ad al*

where *nth-r: lnth E r = (t, NormalAction (ReadMem ad al v))*

and *r: enat r < llength E*

by(*cases*)(*cases lnth E r, auto simp add: action-obs-def actions-def*)

from *nth-r r*

have *E-unfold: E = lappend (ltake (enat r) E) (LCons (t, NormalAction (ReadMem ad al v)) (ldropn (Suc r) E))*

by (*metis lappend-ltake-enat-ldropn ldropn-Suc-conv-ldropn*)

from *sc* **obtain** *b* **where** *sc': ta-seq-consist P Map.empty (ltake (enat r) (lmap snd E))*

and *mrw': mrw-values P Map.empty (map snd (list-of (ltake (enat r) E))) (ad, al) = $\llbracket (v, b) \rrbracket$*

by(*subst (asm) (3) E-unfold*)(*auto simp add: ta-seq-consist-lappend lmap-lappend-distrib*)

from *mrw-values-mrw*[*OF mrw', of t*] *r*

obtain *E' w'*

where *E': E' = llist-of (list-of (ltake (enat r) E) @ $\llbracket (t, NormalAction (ReadMem ad al v)) \rrbracket$)*

and *v: v = value-written P (ltake (enat r) E) w' (ad, al)*

and *mrw'': P, E' \vdash r \rightsquigarrow mrw w'*

and *w': w' < r* **by**(*fastforce simp add: length-list-of-conv-the-enat min-def split: if-split-asm*)

from *E' r* **have** *sim: ltake (enat (Suc r)) E' \approx ltake (enat (Suc r)) E*

by(*subst E-unfold*)(*simp add: ltake-lappend llist-of-list-of-append min-def, auto simp add: eSuc-enat[symmetric] zero-enat-def[symmetric] eq-into-sim-actions*)

from *nth-r* **have** *adal-r: (ad, al) \in action-loc P E r* **by**(*simp add: action-obs-def*)

from *E' r* **have** *nth-r': lnth E' r = (t, NormalAction (ReadMem ad al v))*

by(*auto simp add: nth-append length-list-of-conv-the-enat min-def*)

with *mrw'' w' r adal-r* **obtain** *E \vdash w' \leq a r w' \in write-actions E (ad, al) \in action-loc P E w'*

by *cases*(*fastforce simp add: action-obs-def action-loc-change-prefix*[*OF sim[symmetric]*], *simplified action-obs-def*] *intro: action-order-change-prefix*[*OF - sim*] *write-actions-change-prefix*[*OF - sim*])

with *read adal-r* **have** *P, E \vdash r \rightsquigarrow mrw w'*

proof(*rule most-recent-write-for.intros*)

fix *wa'*

assume *write': wa' \in write-actions E*

and *adal-wa': (ad, al) \in action-loc P E wa'*

show *E \vdash wa' \leq a w' \vee E \vdash r \leq a wa'*

proof(*cases r \leq wa'*)

assume *r \leq wa'*

```

show ?thesis
proof(cases is-new-action (action-obs E wa'))
  case False
  with ⟨r ≤ wa'⟩ have E ⊢ r ≤ a wa' using read write'
    by(auto simp add: action-order-def elim!: read-actions.cases)
  thus ?thesis ..
next
case True
with write' adal-wa' have wa' ∈ new-actions-for P E (ad, al)
  by(simp add: new-actions-for-def)
hence w' ∉ new-actions-for P E (ad, al) using r w' ⟨r ≤ wa'⟩
  by(auto dest: new-actions-for-fun)
with ⟨w' ∈ write-actions E⟩ ⟨(ad, al) ∈ action-loc P E w'⟩
have ¬ is-new-action (action-obs E w') by(simp add: new-actions-for-def)
with write' True ⟨w' ∈ write-actions E⟩ have E ⊢ wa' ≤ a w' by(simp add: action-order-def)
thus ?thesis ..
qed
next
assume ¬ r ≤ wa'
hence wa' < r by simp
with write' adal-wa'
have wa' ∈ write-actions E' (ad, al) ∈ action-loc P E' wa'
by(auto intro: write-actions-change-prefix[OF - sim[symmetric]]) simp add: action-loc-change-prefix[OF
sim])
from most-recent-write-recent[OF mrw'' - this] nth-r'
have E' ⊢ wa' ≤ a w' ∨ E' ⊢ r ≤ a wa' by(simp add: action-obs-def)
thus ?thesis using ⟨wa' < r⟩ w'
  by(auto 4 3 del: disjCI intro: disjI1 disjI2 action-order-change-prefix[OF - sim])
qed
qed
with w' show ?thesis by blast
qed

```

lemma ta-seq-consist-mrw-before:

```

assumes sc: ta-seq-consist P Map.empty (lmap snd E)
and new-actions-for-fun: ∧ adal a a'. [| a ∈ new-actions-for P E adal; a' ∈ new-actions-for P E adal
|] ⇒ a = a'
and mrw: P, E ⊢ r ∼ mrw w
shows w < r

```

proof –

```

from mrw have r ∈ read-actions E by cases
with sc new-actions-for-fun obtain w' where P, E ⊢ r ∼ mrw w' w' < r
  by(auto dest: ta-seq-consist-most-recent-write-for)
with mrw show ?thesis by(auto dest: most-recent-write-for-fun)

```

qed

lemma ta-seq-consist-imp-sequentially-consistent:

```

assumes tsa-ok: thread-start-actions-ok E
and new-actions-for-fun: ∧ adal a a'. [| a ∈ new-actions-for P E adal; a' ∈ new-actions-for P E adal
|] ⇒ a = a'
and seq: ta-seq-consist P Map.empty (lmap snd E)
shows ∃ ws. sequentially-consistent P (E, ws) ∧ P ⊢ (E, ws) ✓

```

proof(intro exI conjI)

```

define ws where ws i = (THE w. P, E ⊢ i ∼ mrw w) for i

```



```

from seq have ns: non-speculative P (λ-. {}) (lmap snd E)
  by(rule ta-seq-consist-into-non-speculative) simp
show sequentially-consistent P (E, ws) unfolding ws-def
proof(rule sequentially-consistentI)
  fix r
  assume r ∈ read-actions E
  with seq new-actions-for-fun
  obtain w where P,E ⊢ r ∼mrw w by(auto dest: ta-seq-consist-most-recent-write-for)
  thus P,E ⊢ r ∼mrw THE w. P,E ⊢ r ∼mrw w by(simp add: THE-most-recent-writeI)
qed

show P ⊢ (E, ws) √
proof(rule wf-execI)
  show is-write-seen P E ws
  proof(rule is-write-seenI)
    fix a ad al v
    assume a: a ∈ read-actions E
    and adal: action-obs E a = NormalAction (ReadMem ad al v)
  from ns have seq': non-speculative P (λ-. {}) (ltake (enat a) (lmap snd E)) by(rule non-speculative-ltake)
  from seq a seq new-actions-for-fun
  obtain w where mrw: P,E ⊢ a ∼mrw w
    and w < a by(auto dest: ta-seq-consist-most-recent-write-for)
  hence w: ws a = w by(simp add: ws-def THE-most-recent-writeI)
  with mrw adal

  show ws a ∈ write-actions E
    and (ad, al) ∈ action-loc P E (ws a)
    and ¬ P,E ⊢ a ≤hb ws a
    by(fastforce elim!: most-recent-write-for.cases dest: happens-before-into-action-order anti-
    symPD[OF antisym-action-order] read-actions-not-write-actions)+

  let ?between = ltake (enat (a - Suc w)) (ldropn (Suc w) E)
  let ?prefix = ltake (enat w) E
  let ?vs-prefix = mrw-values P Map.empty (map snd (list-of ?prefix))

  { fix v'
    assume new: is-new-action (action-obs E w)
      and vs': ?vs-prefix (ad, al) = [(v', True)]
    from mrw-values-eq-SomeD[OF vs']
    obtain obs' wa obs'' where split: map snd (list-of ?prefix) = obs' @ wa # obs''
      and wa: is-write-action wa
      and adal': (ad, al) ∈ action-loc-aux P wa
      and new-wa: ¬ is-new-action wa by blast
    from split have length (map snd (list-of ?prefix)) = Suc (length obs' + length obs'') by simp
    hence len-prefix: llength ?prefix = enat ... by(simp add: length-list-of-conv-the-enat min-enat1-conv-enat)
    with split have nth (map snd (list-of ?prefix)) (length obs') = wa
      and enat (length obs') < llength ?prefix by simp-all
    hence snd (lnth ?prefix (length obs')) = wa by(simp add: list-of-lmap[symmetric] del: list-of-lmap)
    hence wa': action-obs E (length obs') = wa and enat (length obs') < llength E
      using ⟨enat (length obs') < llength ?prefix⟩ by(auto simp add: action-obs-def lnth-ltake)
    with wa have length obs' ∈ write-actions E by(auto intro: write-actions.intros simp add:
    actions-def)
    from most-recent-write-recent[OF mrw - this, of (ad, al)] adal adal' wa'
    have E ⊢ length obs' ≤a w ∨ E ⊢ a ≤a length obs' by simp
  }

```

```

hence False using new-wa new wa' adal len-prefix  $\langle w < a \rangle$ 
  by(auto elim!: action-orderE simp add: min-enat1-conv-enat split: enat.split-asm)
}
hence mrw-value-w: mrw-value P ?vs-prefix (snd (lnth E w)) (ad, al) =
  [(value-written P E w (ad, al),  $\neg$  is-new-action (action-obs E w))]
using  $\langle ws a \in \text{write-actions } E \rangle \langle (ad, al) \in \text{action-loc } P E (ws a) \rangle w$ 
  by(cases snd (lnth E w) rule: mrw-value-cases)(fastforce elim: write-actions.cases simp add:
value-written-def action-obs-def)+
have mrw-values P (mrw-value P ?vs-prefix (snd (lnth E w))) (list-of (lmap snd ?between)) (ad,
al) = [(value-written P E w (ad, al),  $\neg$  is-new-action (action-obs E w))]
proof(subst mrw-values-no-write-unchanged)
  fix wa
  assume wa  $\in$  set (list-of (lmap snd ?between))
    and write-wa: is-write-action wa
    and adal-wa: (ad, al)  $\in$  action-loc-aux P wa
  hence wa: wa  $\in$  lset (lmap snd ?between) by simp
  from wa obtain i-wa where wa = lnth (lmap snd ?between) i-wa
    and i-wa: enat i-wa < llength (lmap snd ?between)
    unfolding lset-conv-lnth by blast
  moreover hence i-wa-len: enat (Suc (w + i-wa)) < llength E by(cases llength E) auto
  ultimately have wa': wa = action-obs E (Suc (w + i-wa))
    by(simp-all add: lnth-ltake action-obs-def ac-simps)
  with write-wa i-wa-len have Suc (w + i-wa)  $\in$  write-actions E
    by(auto intro: write-actions.intros simp add: actions-def)
  from most-recent-write-recent[OF mrw - this, of (ad, al)] adal adal-wa wa'
  have  $E \vdash \text{Suc } (w + i-wa) \leq a \vee E \vdash a \leq \text{Suc } (w + i-wa)$  by(simp)
  hence is-new-action wa  $\wedge$   $\neg$  is-new-action (action-obs E w)
    using adal i-wa wa' by(auto elim: action-orderE)
  thus case (mrw-value P ?vs-prefix (snd (lnth E w)) (ad, al)) of None  $\Rightarrow$  False | Some (v, b)  $\Rightarrow$ 
b  $\wedge$  is-new-action wa
    unfolding mrw-value-w by simp
  qed(simp add: mrw-value-w)

moreover

from a have a  $\in$  actions E by simp
hence enat a < llength E by(rule actionsE)
with  $\langle w < a \rangle$  have enat (a - Suc w) < llength E - enat (Suc w)
  by(cases llength E) simp-all
hence  $E = \text{lappend } (\text{lappend } ?\text{prefix } (\text{LCons } (\text{lnth } E \ w) \ ?\text{between})) (\text{LCons } (\text{lnth } (\text{ldropn } (\text{Suc } w) \ E) \ (a - \text{Suc } w)) (\text{ldropn } (\text{Suc } (a - \text{Suc } w)) (\text{ldropn } (\text{Suc } w) \ E)))$ 
  using  $\langle w < a \rangle \langle \text{enat } a < \text{llength } E \rangle$  unfolding lappend-assoc lappend-code
  apply(subst ldropn-Suc-conv-ldropn, simp)
  apply(subst lappend-ltake-enat-ldropn)
  apply(subst ldropn-Suc-conv-ldropn, simp add: less-trans[where y=enat a])
  by simp
hence  $E': E = \text{lappend } (\text{lappend } ?\text{prefix } (\text{LCons } (\text{lnth } E \ w) \ ?\text{between})) (\text{LCons } (\text{lnth } E \ a) (\text{ldropn } (\text{Suc } a) \ E))$ 
  using  $\langle w < a \rangle \langle \text{enat } a < \text{llength } E \rangle$  by simp

from seq have ta-seq-consist P (mrw-values P Map.empty (list-of (lappend (lmap snd ?prefix) (LCons (snd (lnth E w)) (lmap snd ?between)))) (lmap snd (LCons (lnth E a) (ldropn (Suc a) E)))
  by(subst (asm) E')(simp add: lmap-lappend-distrib ta-seq-consist-lappend)
ultimately show value-written P E (ws a) (ad, al) = v using adal w

```

```

by(clarsimp simp add: action-obs-def list-of-lappend list-of-LCons)

show  $\neg P, E \vdash a \leq_{so} ws a$  using  $\langle w < a \rangle w$  adal by(auto elim!: action-orderE sync-orderE)

fix a'
assume a':  $a' \in \text{write-actions } E$  (ad, al)  $\in \text{action-loc } P E a'$ 

{
  presume  $E \vdash ws a \leq a a' E \vdash a' \leq a a$ 
  with mrw adal a' have  $a' = ws a$  unfolding w
  by cases(fastforce dest: antisymPD[OF antisym-action-order] read-actions-not-write-actions
elim!: meta-allE[where x=a'])
  thus  $a' = ws a a' = ws a$  by -
next
  assume  $P, E \vdash ws a \leq_{hb} a' P, E \vdash a' \leq_{hb} a$ 
  thus  $E \vdash ws a \leq a a' E \vdash a' \leq a a$  using a' by(blast intro: happens-before-into-action-order)+
next
  assume is-volatile P al P, E  $\vdash ws a \leq_{so} a' P, E \vdash a' \leq_{so} a$ 
  thus  $E \vdash ws a \leq a a' E \vdash a' \leq a a$  by(auto elim: sync-orderE)
}
qed
qed(rule tsa-ok)
qed

```

8.8.3 Cut-and-update and sequentially consistent completion

```

inductive foldl-list-all2 ::
   $('b \Rightarrow 'c \Rightarrow 'a \Rightarrow 'a) \Rightarrow ('b \Rightarrow 'c \Rightarrow 'a \Rightarrow \text{bool}) \Rightarrow ('b \Rightarrow 'c \Rightarrow 'a \Rightarrow \text{bool}) \Rightarrow 'b \text{ list} \Rightarrow 'c \text{ list} \Rightarrow 'a$ 
 $\Rightarrow \text{bool}$ 
for f and P and Q
where
  foldl-list-all2 f P Q [] [] s
  |  $[[ Q x y s; P x y s \Longrightarrow \text{foldl-list-all2 } f P Q xs ys (f x y s) ]]$   $\Longrightarrow \text{foldl-list-all2 } f P Q (x \# xs) (y \# ys)$ 
s

```

```

inductive-simps foldl-list-all2-simps [simp]:
  foldl-list-all2 f P Q [] ys s
  foldl-list-all2 f P Q xs [] s
  foldl-list-all2 f P Q (x # xs) (y # ys) s

```

```

inductive-simps foldl-list-all2-Cons1:
  foldl-list-all2 f P Q (x # xs) ys s

```

```

inductive-simps foldl-list-all2-Cons2:
  foldl-list-all2 f P Q xs (y # ys) s

```

definition *eq-upto-seq-inconsist* ::

```

'm prog  $\Rightarrow ('addr, 'thread-id) \text{ obs-event action list} \Rightarrow ('addr, 'thread-id) \text{ obs-event action list}$ 
 $\Rightarrow ('addr \times \text{addr-loc} \rightarrow 'addr \text{ val} \times \text{bool}) \Rightarrow \text{bool}$ 

```

where

```

eq-upto-seq-inconsist P =
  foldl-list-all2 ( $\lambda ob ob' \text{ vs. } \text{mrw-value } P \text{ vs } ob$ )
    ( $\lambda ob ob' \text{ vs. } \text{case } ob \text{ of } \text{NormalAction } (\text{ReadMem } ad \text{ al } v) \Rightarrow \exists b. \text{vs } (ad, al) = \text{Some}$ )

```


lemma *eq-upto-seq-inconsist-trans*:

```

[[ eq-upto-seq-inconsist P obs obs' vs; eq-upto-seq-inconsist P obs' obs'' vs ]]
  ==> eq-upto-seq-inconsist P obs obs'' vs
apply(induction obs arbitrary: obs' obs'' vs)
apply(clarsimp simp add: eq-upto-seq-inconsist-Cons1)+
apply(auto split!: action.split obs-event.split if-split-asm)
done

```

lemma *eq-upto-seq-inconsist-append2*:

```

[[ eq-upto-seq-inconsist P obs obs' vs; ¬ ta-seq-consist P vs (llist-of obs) ]]
  ==> eq-upto-seq-inconsist P obs (obs' @ obs'') vs
apply(induction obs arbitrary: obs' vs)
apply(clarsimp simp add: eq-upto-seq-inconsist-Cons1)+
apply(auto split!: action.split obs-event.split if-split-asm)
done

```

end

context *executions-sc-hb* **begin**

lemma *ta-seq-consist-mrwI*:

```

assumes E: E ∈ ℰ
and wf: P ⊢ (E, ws) ✓
and mrw: ∧ a. [[ enat a < r; a ∈ read-actions E ]] ==> P, E ⊢ a ~>mrw ws a
shows ta-seq-consist P Map.empty (lmap snd (ltake r E))

```

proof(*rule ta-seq-consist-nthI*)

```

fix i ad al v
assume i-len: enat i < llength (lmap snd (ltake r E))
  and E-i: lnth (lmap snd (ltake r E)) i = NormalAction (ReadMem ad al v)
  and sc: ta-seq-consist P Map.empty (ltake (enat i) (lmap snd (ltake r E)))
from i-len have enat i < r by simp
with sc have ta-seq-consist P Map.empty (ltake (enat i) (lmap snd E))
  by(simp add: min-def split: if-split-asm)
hence ns: non-speculative P (λ-. { }) (ltake (enat i) (lmap snd E))
  by(rule ta-seq-consist-into-non-speculative) simp
from i-len have i ∈ actions E by(simp add: actions-def)
moreover from E-i i-len have obs-i: action-obs E i = NormalAction (ReadMem ad al v)
  by(simp add: action-obs-def lnth-ltake)
ultimately have read: i ∈ read-actions E ..
with i-len have mrw-i: P, E ⊢ i ~>mrw ws i by(auto intro: mrw)
with E have ws i < i using ns by(rule mrw-before)

```

```

from mrw-i obs-i obtain adal-w: (ad, al) ∈ action-loc P E (ws i)
  and adal-r: (ad, al) ∈ action-loc P E i
  and write: ws i ∈ write-actions E by cases auto

```

```

from wf have is-write-seen P E ws by(rule wf-exec-is-write-seenD)
from is-write-seenD[OF this read obs-i]
have vw-v: value-written P E (ws i) (ad, al) = v by simp

```

```

let ?vs = mrw-values P Map.empty (map snd (list-of (ltake (enat (ws i)) E)))

```

from $\langle ws\ i < i \rangle$ *i-len* **have** $enat\ (ws\ i) < llength\ (ltake\ (enat\ i)\ E)$
by(*simp add: less-trans*[**where** $y=enat\ i$])
hence $ltake\ (enat\ i)\ E = lappend\ (ltake\ (enat\ (ws\ i))\ (ltake\ (enat\ i)\ E))\ (LCons\ (lnth\ (ltake\ (enat\ i)\ E)\ (ws\ i))\ (ldropn\ (Suc\ (ws\ i))\ (ltake\ (enat\ i)\ E)))$
by(*simp only: ldropn-Suc-conv-ldropn lappend-ltake-enat-ldropn*)
also have $\dots = lappend\ (ltake\ (enat\ (ws\ i))\ E)\ (LCons\ (lnth\ E\ (ws\ i))\ (ldropn\ (Suc\ (ws\ i))\ (ltake\ (enat\ i)\ E)))$
using $\langle ws\ i < i \rangle$ *i-len* $\langle enat\ (ws\ i) < llength\ (ltake\ (enat\ i)\ E) \rangle$
by (*simp add: lnth-ltake*)
finally have $r-E: ltake\ (enat\ i)\ E = \dots$.

have $mrw-values\ P\ Map.empty\ (list-of\ (ltake\ (enat\ i)\ (lmap\ snd\ (ltake\ r\ E))))\ (ad,\ al)$
 $= mrw-values\ P\ Map.empty\ (map\ snd\ (list-of\ (ltake\ (enat\ i)\ E)))\ (ad,\ al)$
using $\langle enat\ i < r \rangle$ **by**(*auto simp add: min-def*)
also have $\dots = mrw-values\ P\ (mrw-value\ P\ ?vs\ (snd\ (lnth\ E\ (ws\ i))))\ (map\ snd\ (list-of\ (ldropn\ (Suc\ (ws\ i))\ (ltake\ (enat\ i)\ E))))\ (ad,\ al)$
by(*subst r-E*)(*simp add: list-of-lappend*)
also have $\dots = mrw-value\ P\ ?vs\ (snd\ (lnth\ E\ (ws\ i)))\ (ad,\ al)$
proof(*rule mrw-values-no-write-unchanged*)
fix wa
assume $wa: wa \in set\ (map\ snd\ (list-of\ (ldropn\ (Suc\ (ws\ i))\ (ltake\ (enat\ i)\ E))))$
and $is-write-action\ wa\ (ad,\ al) \in action-loc-aux\ P\ wa$

from wa **obtain** w **where** $w < length\ (map\ snd\ (list-of\ (ldropn\ (Suc\ (ws\ i))\ (ltake\ (enat\ i)\ E))))$
and $map\ snd\ (list-of\ (ldropn\ (Suc\ (ws\ i))\ (ltake\ (enat\ i)\ E)))\ !w = wa$
unfolding *in-set-conv-nth* **by** *blast*
moreover hence $Suc\ (ws\ i + w) < i$ (**is** $?w < -$) **using** *i-len*
by(*cases llength E*)(*simp-all add: length-list-of-conv-the-enat*)
ultimately have $obs-w': action-obs\ E\ ?w = wa$ **using** *i-len*
by(*simp add: action-obs-def lnth-ltake less-trans*[**where** $y=enat\ i$]) *ac-simps*)
from $\langle ?w < i \rangle$ *i-len* **have** $?w \in actions\ E$
by(*simp add: actions-def less-trans*[**where** $y=enat\ i$])
with $\langle is-write-action\ wa \rangle$ $obs-w'\ \langle (ad,\ al) \in action-loc-aux\ P\ wa \rangle$
have $write': ?w \in write-actions\ E$
and $adal': (ad,\ al) \in action-loc\ P\ E\ ?w$
by(*auto intro: write-actions.intros*)

from $\langle ?w < i \rangle$ $\langle i \in read-actions\ E \rangle$ $\langle ?w \in actions\ E \rangle$
have $E \vdash ?w \leq a\ i$ **by**(*auto simp add: action-order-def elim: read-actions.cases*)

from $mrw-i\ adal-r\ write'\ adal'$
have $E \vdash ?w \leq a\ ws\ i \vee E \vdash i \leq a\ ?w$ **by**(*rule most-recent-write-recent*)
hence $E \vdash ?w \leq a\ ws\ i$
proof
assume $E \vdash i \leq a\ ?w$
with $\langle E \vdash ?w \leq a\ i \rangle$ **have** $?w = i$ **by**(*rule antisymPD*[*OF antisym-action-order*])
with $write'\ read$ **have** *False* **by**(*auto dest: read-actions-not-write-actions*)
thus *?thesis ..*
qed

from $adal-w\ write$ **have** $mrw-value\ P\ ?vs\ (snd\ (lnth\ E\ (ws\ i)))\ (ad,\ al) \neq None$
by(*cases snd (lnth E (ws i)) rule: mrw-value-cases*)
(auto simp add: action-obs-def split: if-split-asm elim: write-actions.cases)
then obtain $b\ v$ **where** $vb: mrw-value\ P\ ?vs\ (snd\ (lnth\ E\ (ws\ i)))\ (ad,\ al) = Some\ (v,\ b)$ **by** *auto*

```

moreover
from  $\langle E \vdash ?w \leq_a ws \ i \rangle \text{ obs-}w'$ 
have  $is\text{-new-action } wa \neg is\text{-new-action } (action\text{-obs } E \ (ws \ i))$  by(auto elim!: action-orderE)
from  $\langle \neg is\text{-new-action } (action\text{-obs } E \ (ws \ i)) \rangle \text{ write adal-}w$ 
obtain  $v'$  where  $action\text{-obs } E \ (ws \ i) = NormalAction \ (WriteMem \ ad \ al \ v')$ 
  by(auto elim!: write-actions.cases is-write-action.cases)
with  $vb$  have  $b$  by(simp add: action-obs-def)
with  $\langle is\text{-new-action } wa \rangle vb$ 
  show  $case \ mrw\text{-value } P \ ?vs \ (snd \ (lth \ E \ (ws \ i))) \ (ad, \ al) \ of \ None \Rightarrow \ False \ | \ [(v, \ b)] \Rightarrow \ b \wedge$ 
is-new-action wa by simp
qed
also {
  fix  $v$ 
  assume  $?vs \ (ad, \ al) = Some \ (v, \ True)$ 
  and  $is\text{-new-action } (action\text{-obs } E \ (ws \ i))$ 

  from mrw-values-eq-SomeD[OF this(1)]
  obtain  $wa$  where  $wa \in set \ (map \ snd \ (list\text{-of} \ (ltake \ (enat \ (ws \ i)) \ E)))$ 
  and  $is\text{-write-action } wa$ 
  and  $(ad, \ al) \in action\text{-loc-aux } P \ wa$ 
  and  $\neg is\text{-new-action } wa$  by(fastforce simp del: set-map)
  moreover then obtain  $w$  where  $w: w < ws \ i$  and  $wa: wa = snd \ (lth \ E \ w)$ 
  unfolding in-set-conv-nth by(cases llength E)(auto simp add: lth-ltake length-list-of-conv-the-enat)
  ultimately have  $w \in write\text{-actions } E$   $action\text{-obs } E \ w = wa$   $(ad, \ al) \in action\text{-loc } P \ E \ w$ 
  using  $\langle ws \ i \in write\text{-actions } E \rangle$ 
  by(auto intro!: write-actions.intros simp add: actions-def less-trans[where  $y=enat \ (ws \ i)$ ] action-obs-def elim!: write-actions.cases)
  with mrw-i adal-r have  $E \vdash w \leq_a ws \ i \vee E \vdash i \leq_a w$  by  $\neg(rule \ most\text{-recent-write-recent})$ 
  hence False
  proof
    assume  $E \vdash w \leq_a ws \ i$ 
    moreover from  $\langle \neg is\text{-new-action } wa \rangle \langle is\text{-new-action } (action\text{-obs } E \ (ws \ i)) \rangle \text{ write } w \ wa \langle w \in$ 
write-actions E
    have  $E \vdash ws \ i \leq_a w$  by(auto simp add: action-order-def action-obs-def)
    ultimately have  $w = ws \ i$  by(rule antisymPD[OF antisym-action-order])
    with  $\langle w < ws \ i \rangle$  show False by simp
  next
    assume  $E \vdash i \leq_a w$ 
    moreover from  $\langle w \in write\text{-actions } E \rangle \langle w < ws \ i \rangle \langle ws \ i < i \rangle \text{ read}$ 
    have  $E \vdash w \leq_a i$  by(auto simp add: action-order-def elim: read-actions.cases)
    ultimately have  $i = w$  by(rule antisymPD[OF antisym-action-order])
    with  $\langle w < ws \ i \rangle \langle ws \ i < i \rangle$  show False by simp
  qed }
then obtain  $b$  where  $\dots = Some \ (v, \ b)$  using vw-v write adal-w
  apply(atomize-elim)
  apply(auto simp add: action-obs-def value-written-def write-actions-iff)
  apply(erule is-write-action.cases)
  apply auto
  done
  finally show  $\exists b. mrw\text{-values } P \ Map.empty \ (list\text{-of} \ (ltake \ (enat \ i) \ (lmap \ snd \ (ltake \ r \ E)))) \ (ad, \ al)$ 
   $= [(v, \ b)]$ 
  by blast
qed

```

end

context *jmm-multithreaded* begin

definition *complete-sc* :: ('l, 'thread-id, 'x, 'm, 'w) state \Rightarrow ('addr \times addr-loc \rightarrow 'addr val \times bool) \Rightarrow
('thread-id \times ('l, 'thread-id, 'x, 'm, 'w, ('addr, 'thread-id) obs-event action) thread-action) llist

where

complete-sc *s vs* = *unfold-llist*

($\lambda(s, vs). \forall t \ ta \ s'. \neg s -t>ta \rightarrow s'$)

($\lambda(s, vs). \text{fst} (SOME ((t, ta), s'). s -t>ta \rightarrow s' \wedge ta\text{-seq-consist } P \text{ vs } (llist\text{-of } \{ta\}_o))$)

($\lambda(s, vs). \text{let } ((t, ta), s') = SOME ((t, ta), s'). s -t>ta \rightarrow s' \wedge ta\text{-seq-consist } P \text{ vs } (llist\text{-of } \{ta\}_o)$
in (*s'*, *mrw-values* *P vs* $\{ta\}_o$))

(*s, vs*)

definition *sc-completion* :: ('l, 'thread-id, 'x, 'm, 'w) state \Rightarrow ('addr \times addr-loc \rightarrow 'addr val \times bool)
 \Rightarrow bool

where

sc-completion *s vs* \longleftrightarrow

($\forall ttas \ s' \ t \ x \ ta \ x' \ m'$.

$s -\triangleright ttas \rightarrow^* s' \longrightarrow ta\text{-seq-consist } P \text{ vs } (llist\text{-of } (concat (map (\lambda(t, ta). \{ta\}_o) ttas))) \longrightarrow$

$thr \ s' \ t = \lfloor (x, no\text{-wait-locks}) \rfloor \longrightarrow t \vdash (x, shr \ s') -ta \rightarrow (x', m') \longrightarrow actions\text{-ok } s' \ t \ ta \longrightarrow$

($\exists ta' \ x'' \ m''. t \vdash (x, shr \ s') -ta' \rightarrow (x'', m'') \wedge actions\text{-ok } s' \ t \ ta' \wedge$

$ta\text{-seq-consist } P \text{ (mrw-values } P \text{ vs } (concat (map (\lambda(t, ta). \{ta\}_o) ttas))) (llist\text{-of}$

$\{ta'\}_o)$)

lemma *sc-completionD*:

$\llbracket sc\text{-completion } s \text{ vs}; s -\triangleright ttas \rightarrow^* s'; ta\text{-seq-consist } P \text{ vs } (llist\text{-of } (concat (map (\lambda(t, ta). \{ta\}_o) ttas)))$;

$thr \ s' \ t = \lfloor (x, no\text{-wait-locks}) \rfloor; t \vdash (x, shr \ s') -ta \rightarrow (x', m'); actions\text{-ok } s' \ t \ ta \rrbracket$

$\implies \exists ta' \ x'' \ m''. t \vdash (x, shr \ s') -ta' \rightarrow (x'', m'') \wedge actions\text{-ok } s' \ t \ ta' \wedge$

$ta\text{-seq-consist } P \text{ (mrw-values } P \text{ vs } (concat (map (\lambda(t, ta). \{ta\}_o) ttas))) (llist\text{-of } \{ta'\}_o)$

unfolding *sc-completion-def* by *blast*

lemma *sc-completionI*:

($\bigwedge ttas \ s' \ t \ x \ ta \ x' \ m'$.

$\llbracket s -\triangleright ttas \rightarrow^* s'; ta\text{-seq-consist } P \text{ vs } (llist\text{-of } (concat (map (\lambda(t, ta). \{ta\}_o) ttas)))$;

$thr \ s' \ t = \lfloor (x, no\text{-wait-locks}) \rfloor; t \vdash (x, shr \ s') -ta \rightarrow (x', m'); actions\text{-ok } s' \ t \ ta \rrbracket$

$\implies \exists ta' \ x'' \ m''. t \vdash (x, shr \ s') -ta' \rightarrow (x'', m'') \wedge actions\text{-ok } s' \ t \ ta' \wedge$

$ta\text{-seq-consist } P \text{ (mrw-values } P \text{ vs } (concat (map (\lambda(t, ta). \{ta\}_o) ttas))) (llist\text{-of } \{ta'\}_o)$

$\implies sc\text{-completion } s \text{ vs}$

unfolding *sc-completion-def* by *blast*

lemma *sc-completion-shift*:

assumes *sc-c*: *sc-completion* *s vs*

and τRed : $s -\triangleright ttas \rightarrow^* s'$

and *sc*: $ta\text{-seq-consist } P \text{ vs } (lconcat (lmap (\lambda(t, ta). llist\text{-of } \{ta\}_o) (llist\text{-of } ttas)))$

shows *sc-completion* *s'* (*mrw-values* *P vs* ($concat (map (\lambda(t, ta). \{ta\}_o) ttas$)))

proof(rule *sc-completionI*)

fix *ttas'* *s''* *t* *x* *ta* *x'* *m'*

assume $\tau Red'$: $s' -\triangleright ttas' \rightarrow^* s''$

and *sc'*: $ta\text{-seq-consist } P \text{ (mrw-values } P \text{ vs } (concat (map (\lambda(t, ta). \{ta\}_o) ttas))) (llist\text{-of } (concat (map (\lambda(t, ta). \{ta\}_o) ttas')))$

and *red*: $thr \ s'' \ t = \lfloor (x, no\text{-wait-locks}) \rfloor \ t \vdash (x, shr \ s'') -ta \rightarrow (x', m') \ actions\text{-ok } s'' \ t \ ta$

from $\tau Red \ \tau Red'$ **have** $s -\triangleright ttas @ ttas' \rightarrow^* s''$ **unfolding** *RedT-def* by (rule *rtrancl3p-trans*)

moreover from $sc\ sc'$ **have** $ta\text{-seq-consist}\ P\ vs\ (l\text{list-of}\ (concat\ (map\ (\lambda(t, ta). \{ta\}_o)\ (ttas\ @\ ttas'))))$
apply ($simp\ add: lappend\text{-l\text{list-of}\ \text{-l\text{list-of}\ [symmetric]}\ ta\text{-seq-consist}\ \text{-lappend}\ del: lappend\text{-l\text{list-of}\ \text{-l\text{list-of}\ }$)
apply ($simp\ add: lconcat\text{-l\text{list-of}\ [symmetric]}\ lmap\text{-l\text{list-of}\ [symmetric]}\ l\text{list.map-comp}\ o\text{-def}\ split\text{-def}\ del: lmap\text{-l\text{list-of}\ }$)
done
ultimately
show $\exists ta'\ x''\ m''.\ t \vdash \langle x, shr\ s' \rangle -ta' \rightarrow \langle x'', m'' \rangle \wedge actions\text{-ok}\ s''\ t\ ta' \wedge$
 $ta\text{-seq-consist}\ P\ (mrw\text{-values}\ P\ (mrw\text{-values}\ P\ vs\ (concat\ (map\ (\lambda(t, ta). \{ta\}_o)\ ttas)))\ (concat\ (map\ (\lambda(t, ta). \{ta\}_o)\ ttas'))\ (l\text{list-of}\ \{ta'\}_o)$
using $red\ unfolding\ foldl\text{-append}\ [symmetric]\ concat\text{-append}\ [symmetric]\ map\text{-append}\ [symmetric]$
by ($rule\ sc\text{-completionD}\ [OF\ sc\text{-c}]$)
qed

lemma $complete\text{-sc}\text{-in}\text{-Runs}$:

assumes $cau: sc\text{-completion}\ s\ vs$

and $ta\text{-seq-consist}\text{-convert}\text{-RA}: \bigwedge vs\ ln.\ ta\text{-seq-consist}\ P\ vs\ (l\text{list-of}\ (convert\text{-RA}\ ln))$

shows $mthr.Runs\ s\ (complete\text{-sc}\ s\ vs)$

proof –

let $?ttas' = \lambda ttas :: ('thread\text{-id} \times ('l, 'thread\text{-id}, 'x, 'm, 'w, ('addr, 'thread\text{-id})\ obs\text{-event}\ action)\ thread\text{-action})\ list.$

$concat\ (map\ (\lambda(t, ta). \{ta\}_o)\ ttas')$

let $?vs\ ttas' = mrw\text{-values}\ P\ vs\ (?ttas'\ ttas')$

define $s'\ vs'$

and $ttas :: ('thread\text{-id} \times ('l, 'thread\text{-id}, 'x, 'm, 'w, ('addr, 'thread\text{-id})\ obs\text{-event}\ action)\ thread\text{-action})\ list$

$list$

where $s' = s$ **and** $vs' = vs$ **and** $ttas = []$

hence $s \rightarrow ttas \rightarrow * s'$ $ta\text{-seq-consist}\ P\ vs\ (l\text{list-of}\ (?ttas'\ ttas))$ **by** $auto$

hence $mthr.Runs\ s'\ (complete\text{-sc}\ s'\ (?vs\ ttas'))$

proof ($coinduction\ arbitrary: s'\ ttas\ rule: mthr.Runs.coinduct$)

case ($Runs\ s'\ ttas'$)

note $Red = \langle s \rightarrow ttas' \rightarrow * s' \rangle$

and $sc = \langle ta\text{-seq-consist}\ P\ vs\ (l\text{list-of}\ (?ttas'\ ttas')) \rangle$

show $?case$

proof ($cases\ \exists t'\ ta'\ s''.\ s' -t' \triangleright ta' \rightarrow s''$)

case $False$

hence $?Stuck$ **by** ($simp\ add: complete\text{-sc}\text{-def}$)

thus $?thesis\ ..$

next

case $True$

let $?proceed = \lambda((t', ta'), s'').\ s' -t' \triangleright ta' \rightarrow s'' \wedge ta\text{-seq-consist}\ P\ (?vs\ ttas')\ (l\text{list-of}\ \{ta'\}_o)$

from $True$ **obtain** $t'\ ta'\ s''$ **where** $red: s' -t' \triangleright ta' \rightarrow s''$ **by** ($auto$)

then obtain $ta''\ s'''$ **where** $s' -t' \triangleright ta'' \rightarrow s'''$

and $ta\text{-seq-consist}\ P\ (?vs\ ttas')\ (l\text{list-of}\ \{ta''\}_o)$

proof ($cases$)

case ($redT\text{-normal}\ x\ x'\ m'$)

note $red = \langle t' \vdash \langle x, shr\ s' \rangle -ta' \rightarrow \langle x', m' \rangle \rangle$

and $ts''t' = \langle thr\ s'\ t' = [(x, no\text{-wait}\text{-locks})] \rangle$

and $aok = \langle actions\text{-ok}\ s'\ t'\ ta' \rangle$

and $s'' = \langle redT\text{-upd}\ s'\ t'\ ta'\ x'\ m'\ s'' \rangle$

from $sc\text{-completionD}\ [OF\ cau\ Red\ sc\ ts''t'\ red\ aok]$

obtain $ta''\ x''\ m''$ **where** $red': t' \vdash \langle x, shr\ s' \rangle -ta'' \rightarrow \langle x'', m'' \rangle$

and $aok': actions\text{-ok}\ s'\ t'\ ta''$

```

    and sc': ta-seq-consist P (?vs ttas') (llist-of {ta''}_o) by blast
  from redT-updWs-total obtain ws' where redT-updWs t' (wset s') {ta''}_w ws' ..
  then obtain s''' where redT-upd s' t' ta'' x'' m'' s''' by fastforce
  with red' ts''t' aok' have s' -t▷ta''→ s''' ..
  thus thesis using sc' by(rule that)
next
  case redT-acquire
  thus thesis by(simp add: that[OF red] ta-seq-consist-convert-RA)
qed
hence ?proceed ((t', ta''), s''') using Red by(auto)
hence *: ?proceed (Eps ?proceed) by(rule someI)
moreover from Red * have s -▷ttas' @ [fst (Eps ?proceed)]→* snd (Eps ?proceed)
  by(auto simp add: split-beta RedT-def intro: rtrancl3p-step)
moreover from True
  have complete-sc s' (?vs ttas') = LCons (fst (Eps ?proceed)) (complete-sc (snd (Eps ?proceed))
(?vs (ttas' @ [fst (Eps ?proceed)])))
  unfolding complete-sc-def by(simp add: split-def)
  moreover from sc <?proceed (Eps ?proceed)>
  have ta-seq-consist P vs (llist-of (?ttas' (ttas' @ [fst (Eps ?proceed)])))
  unfolding map-append concat-append lappend-llist-of-llist-of[symmetric]
  by(subst ta-seq-consist-lappend)(auto simp add: split-def)
ultimately have ?Step
  by(fastforce intro: exI[where x=ttas' @ [fst (Eps ?proceed)]] simp del: split-paired-Ex)
  thus ?thesis by simp
qed
qed
thus ?thesis by(simp add: s'-def ttas-def)
qed

lemma complete-sc-ta-seq-consist:
  assumes cau: sc-completion s vs
  and ta-seq-consist-convert-RA: ∧vs ln. ta-seq-consist P vs (llist-of (convert-RA ln))
  shows ta-seq-consist P vs (lconcat (lmap (λ(t, ta). llist-of {ta}_o) (complete-sc s vs)))
proof -
  define vs' where vs' = vs
  let ?obs = λttas. lconcat (lmap (λ(t, ta). llist-of {ta}_o) ttas)
  define obs where obs = ?obs (complete-sc s vs)
  define a where a = complete-sc s vs'
  let ?ttas' = λttas :: ('l, 'thread-id × ('l, 'thread-id, 'x, 'm, 'w, ('addr, 'thread-id) obs-event action) thread-action)
list.
    concat (map (λ(t, ta). {ta}_o) ttas')
  let ?vs = λttas'. mrw-values P vs (?ttas' ttas')
  from vs'-def obs-def
  have s -▷[]→* s ta-seq-consist P vs (llist-of (?ttas' [])) vs' = ?vs [] by(auto)
  hence ∃ s' ttas'. obs = ?obs (complete-sc s' vs') ∧ s -▷ttas'→* s' ∧
    ta-seq-consist P vs (llist-of (?ttas' ttas')) ∧ vs' = ?vs ttas' ∧
    a = complete-sc s' vs'
  unfolding obs-def vs'-def a-def by metis
  moreover have wf (inv-image {(m, n). m < n} (llength ∘ ltakeWhile (λ(t, ta). {ta}_o = [])))
  (is wf ?R) by(rule wf-inv-image)(rule wellorder-class.wf)
  ultimately show ta-seq-consist P vs' obs
proof(coinduct vs' obs a rule: ta-seq-consist-coinduct-append-wf)
  case (ta-seq-consist vs' obs a)
  then obtain s' ttas' where obs-def: obs = ?obs (complete-sc s' (?vs ttas'))

```

and Red: $s \rightarrow^* ttas' \rightarrow^* s'$
and sc: $ta\text{-seq-consist } P \text{ vs } (\text{llist-of } (?ttas' \text{ } ttas'))$
and vs'-def: $vs' = ?vs \text{ } ttas'$
and a-def: $a = \text{complete-sc } s' \text{ vs' by blast}$

show $?case$

proof(cases $\exists t' ta' s''. s' -t' \triangleright ta' \rightarrow s''$)

case *False*

hence $obs = LNil$ **unfolding** $obs\text{-def}$ $complete\text{-sc-def}$ **by** *simp*

hence $?LNil$ **unfolding** $obs\text{-def}$ **by** *auto*

thus $?thesis \dots$

next

case *True*

let $?proceed = \lambda((t', ta'), s''). s' -t' \triangleright ta' \rightarrow s'' \wedge ta\text{-seq-consist } P (?vs \text{ } ttas') (\text{llist-of } \{ta'\}_o)$

let $?tta = \text{fst } (Eps \text{ } ?proceed)$

let $?s' = \text{snd } (Eps \text{ } ?proceed)$

from *True* **obtain** $t' ta' s''$ **where** $red: s' -t' \triangleright ta' \rightarrow s''$ **by** *blast*

then obtain $ta'' s'''$ **where** $s' -t' \triangleright ta'' \rightarrow s'''$

and $ta\text{-seq-consist } P (?vs \text{ } ttas') (\text{llist-of } \{ta''\}_o)$

proof(cases)

case ($redT\text{-normal } x \ x' \ m'$)

note $red = \langle t' \vdash \langle x, shr \ s' \rangle -ta' \rightarrow \langle x', m' \rangle \rangle$

and $ts''t' = \langle thr \ s' \ t' = \lfloor (x, no\text{-wait-locks}) \rfloor \rangle$

and $aok = \langle actions\text{-ok } s' \ t' \ ta' \rangle$

and $s''' = \langle redT\text{-upd } s' \ t' \ ta' \ x' \ m' \ s'' \rangle$

from $sc\text{-completionD}[OF \ cau \ Red \ sc \ ts''t' \ red \ aok]$

obtain $ta'' \ x'' \ m''$ **where** $red': t' \vdash \langle x, shr \ s' \rangle -ta'' \rightarrow \langle x'', m'' \rangle$

and $aok': actions\text{-ok } s' \ t' \ ta''$

and $sc': ta\text{-seq-consist } P (?vs \text{ } ttas') (\text{llist-of } \{ta''\}_o)$ **by** *blast*

from $redT\text{-updWs-total}$ **obtain** ws' **where** $redT\text{-updWs } t' (wset \ s') \ \{ta''\}_w \ ws' \dots$

then obtain s''' **where** $redT\text{-upd } s' \ t' \ ta'' \ x'' \ m'' \ s'''$ **by** *fastforce*

with $red' \ ts''t' \ aok'$ **have** $s' -t' \triangleright ta'' \rightarrow s''' \dots$

thus $thesis$ **using** sc' **by**(*rule that*)

next

case $redT\text{-acquire}$

thus $thesis$ **by**(*simp add: that[OF red] ta-seq-consist-convert-RA*)

qed

hence $?proceed ((t', ta''), s''')$ **by** *auto*

hence $?proceed (Eps \text{ } ?proceed)$ **by**(*rule someI*)

show $?thesis$

proof(cases $obs = LNil$)

case *True* **thus** $?thesis \dots$

next

case *False*

from *True*

have $csc\text{-unfold: complete-sc } s' (?vs \text{ } ttas') = LCons \ ?tta (complete\text{-sc } ?s' (?vs (ttas' @ [?tta])))$

unfolding $complete\text{-sc-def}$ **by**(*simp add: split-def*)

hence $obs = \text{lappend } (\text{llist-of } \{snd \ ?tta\}_o) (?obs (complete\text{-sc } ?s' (?vs (ttas' @ [?tta])))$

using $obs\text{-def}$ **by**(*simp add: split-beta*)

moreover have $ta\text{-seq-consist } P \text{ vs' } (\text{llist-of } \{snd \ ?tta\}_o)$

using $\langle ?proceed (Eps \text{ } ?proceed) \rangle vs'\text{-def}$ **by**(*clarsimp simp add: split-beta*)

moreover {

assume $\text{llist-of } \{snd \ ?tta\}_o = LNil$

moreover from *obs-def* $\langle \text{obs} \neq \text{LNil} \rangle$
have *lfinite* (*ltakeWhile* $(\lambda(t, ta). \{ta\}_o = [])$ (*complete-sc* $s' (?vs \text{ttas}')$))
unfolding *lfinite-ltakeWhile* **by** (*fastforce simp add: split-def lconcat-eq-LNil*)
ultimately have (*complete-sc* $?s' (?vs (\text{ttas}' @ [?tta]))$, $a \in ?R$)
unfolding *a-def vs'-def csc-unfold*
by (*clarsimp simp add: split-def llist-of-eq-LNil-conv*)(*auto simp add: lfinite-eq-range-llist-of*)
}
moreover have $?obs$ (*complete-sc* $?s' (?vs (\text{ttas}' @ [?tta]))$) = $?obs$ (*complete-sc* $?s'$ (*mrw-values* $P \text{ vs}'$ (*list-of* (*llist-of* ($\{snd \text{?tta}\}_o$))))))
unfolding *vs'-def* **by** (*simp add: split-def*)
moreover from $\langle ?proceed$ (*Eps* $?proceed$) \rangle *Red*
have $s \rightarrow \text{ttas}' @ [?tta] \rightarrow * ?s'$ **by** (*auto simp add: RedT-def split-def intro: rtrancl3p-step*)
moreover from *sc* $\langle ?proceed$ (*Eps* $?proceed$) \rangle
have *ta-seq-consist* $P \text{ vs}$ (*llist-of* ($?ttas'$ ($\text{ttas}' @ [?tta]$)))
by (*clarsimp simp add: split-def ta-seq-consist-lappend lappend-llist-of-llist-of[symmetric] simp del: lappend-llist-of-llist-of*)
moreover have *mrw-values* $P \text{ vs}'$ (*list-of* (*llist-of* ($\{snd \text{?tta}\}_o$))) = $?vs$ ($\text{ttas}' @ [?tta]$)
unfolding *vs'-def* **by** (*simp add: split-def*)
moreover have *complete-sc* $?s' (?vs (\text{ttas}' @ [?tta]))$ = *complete-sc* $?s'$ (*mrw-values* $P \text{ vs}'$ (*list-of* (*llist-of* ($\{snd \text{?tta}\}_o$))))
unfolding *vs'-def* **by** (*simp add: split-def*)
ultimately have $?lappend$ **by** *blast*
thus $?thesis$..
qed
qed
qed
qed

lemma *sequential-completion-Runs*:

assumes *sc-completion* $s \text{ vs}$
and $\bigwedge \text{vs} \text{ ln. } \text{ta-seq-consist } P \text{ vs}$ (*llist-of* (*convert-RA* ln))
shows $\exists \text{ttas. } \text{mthr.Runs } s \text{ ttas} \wedge \text{ta-seq-consist } P \text{ vs}$ (*lconcat* (*lmap* $(\lambda(t, ta). \{ta\}_o \text{ ttas})$))
using *complete-sc-ta-seq-consist[OF assms] complete-sc-in-Runs[OF assms]*
by *blast*

definition *cut-and-update* :: $(l, \text{'thread-id}, \text{'x}, \text{'m}, \text{'w}) \text{ state} \Rightarrow (\text{'addr} \times \text{addr-loc} \rightarrow \text{'addr val} \times \text{bool}) \Rightarrow \text{bool}$

where

cut-and-update $s \text{ vs} \iff$
 $(\forall \text{ttas } s' t x \text{ ta } x' m'. \quad$
 $s \rightarrow \text{ttas} \rightarrow * s' \longrightarrow \text{ta-seq-consist } P \text{ vs}$ (*llist-of* (*concat* (*map* $(\lambda(t, ta). \{ta\}_o \text{ ttas})$))) \longrightarrow
 $\text{thr } s' t = [(x, \text{no-wait-locks})] \longrightarrow t \vdash (x, \text{shr } s') \text{ -ta} \rightarrow (x', m') \longrightarrow \text{actions-ok } s' t \text{ ta} \longrightarrow$
 $(\exists \text{ta}' x'' m''. t \vdash (x, \text{shr } s') \text{ -ta}' \rightarrow (x'', m'') \wedge \text{actions-ok } s' t \text{ ta}' \wedge$
 $\text{ta-seq-consist } P$ (*mrw-values* $P \text{ vs}$ (*concat* (*map* $(\lambda(t, ta). \{ta\}_o \text{ ttas})$))) (*llist-of* $\{ta'\}_o$)

\wedge

$\text{eq-upto-seq-inconsist } P \{ta\}_o \{ta'\}_o$ (*mrw-values* $P \text{ vs}$ (*concat* (*map* $(\lambda(t, ta). \{ta\}_o \text{ ttas})$)))

lemma *cut-and-updateI*[*intro?*]:

$(\bigwedge \text{ttas } s' t x \text{ ta } x' m'. \quad$
 $\llbracket s \rightarrow \text{ttas} \rightarrow * s'; \text{ta-seq-consist } P \text{ vs}$ (*llist-of* (*concat* (*map* $(\lambda(t, ta). \{ta\}_o \text{ ttas})$)))
 $\text{thr } s' t = [(x, \text{no-wait-locks})]; t \vdash (x, \text{shr } s') \text{ -ta} \rightarrow (x', m'); \text{actions-ok } s' t \text{ ta} \rrbracket$
 $\implies \exists \text{ta}' x'' m''. t \vdash (x, \text{shr } s') \text{ -ta}' \rightarrow (x'', m'') \wedge \text{actions-ok } s' t \text{ ta}' \wedge$

$\{\!|ta'\!\}_o \wedge$
 $ta\text{-seq-consist } P \text{ (mrw-values } P \text{ vs (concat (map } (\lambda(t, ta). \{\!|ta\!\}_o) \text{ ttas))) (llist-of$
 $\{\!|ta\!\}_o \text{ ttas)))}$
 $eq\text{-upto-seq-inconsist } P \{\!|ta\!\}_o \{\!|ta'\!\}_o \text{ (mrw-values } P \text{ vs (concat (map } (\lambda(t, ta).$
 $\{\!|ta\!\}_o) \text{ ttas)))}$
 $\implies \text{cut-and-update } s \text{ vs}$
unfolding *cut-and-update-def* **by** *blast*

lemma *cut-and-updateD*:

$\llbracket \text{cut-and-update } s \text{ vs}; s \text{ } \rightarrow^* \text{ttas} \rightarrow^* s'; ta\text{-seq-consist } P \text{ vs (llist-of (concat (map } (\lambda(t, ta). \{\!|ta\!\}_o)$
 $\text{ttas}))];$
 $\text{thr } s' \text{ } t = \llbracket (x, \text{no-wait-locks}) \rrbracket; t \vdash (x, \text{shr } s') \text{ } \text{--} ta \rightarrow (x', m'); \text{actions-ok } s' \text{ } t \text{ } ta \rrbracket$
 $\implies \exists ta' x'' m''. t \vdash (x, \text{shr } s') \text{ } \text{--} ta' \rightarrow (x'', m'') \wedge \text{actions-ok } s' \text{ } t \text{ } ta' \wedge$
 $ta\text{-seq-consist } P \text{ (mrw-values } P \text{ vs (concat (map } (\lambda(t, ta). \{\!|ta\!\}_o) \text{ ttas))) (llist-of } \{\!|ta'\!\}_o$
 \wedge
 $eq\text{-upto-seq-inconsist } P \{\!|ta\!\}_o \{\!|ta'\!\}_o \text{ (mrw-values } P \text{ vs (concat (map } (\lambda(t, ta). \{\!|ta\!\}_o)$
 $\text{ttas}))]$
unfolding *cut-and-update-def* **by** *blast*

lemma *cut-and-update-imp-sc-completion*:

$\text{cut-and-update } s \text{ vs} \implies \text{sc-completion } s \text{ vs}$
apply(*rule sc-completionI*)
apply(*drule (5) cut-and-updateD*)
apply *blast*
done

lemma *sequential-completion*:

assumes *cut-and-update*: $\text{cut-and-update } s \text{ vs}$
and *ta-seq-consist-convert-RA*: $\bigwedge vs \ln. ta\text{-seq-consist } P \text{ vs (llist-of (convert-RA } \ln))$
and *Red*: $s \rightarrow^* \text{ttas} \rightarrow^* s'$
and *sc*: $ta\text{-seq-consist } P \text{ vs (llist-of (concat (map } (\lambda(t, ta). \{\!|ta\!\}_o) \text{ ttas)))}$
and *red*: $s' \text{ } \text{--} ta \rightarrow s''$
shows
 $\exists ta' \text{ttas}'. \text{mthr.Runs } s' \text{ (LCons (t, ta') \text{ttas}') } \wedge$
 $ta\text{-seq-consist } P \text{ vs (lconcat (lmap } (\lambda(t, ta). \text{lappend (lappend (llist-of } \text{ttas) (LCons (t, ta')$
 $\text{ttas}')))) \wedge$
 $eq\text{-upto-seq-inconsist } P \{\!|ta\!\}_o \{\!|ta'\!\}_o \text{ (mrw-values } P \text{ vs (concat (map } (\lambda(t, ta). \{\!|ta\!\}_o) \text{ ttas)))}$

proof –

from *red* **obtain** $ta' s'''$
where $\text{red}' : \text{redT } s' \text{ (t, ta') } s'''$
and $\text{sc}' : ta\text{-seq-consist } P \text{ vs (lconcat (lmap } (\lambda(t, ta). \text{lappend (lappend (llist-of } \text{ttas) (LCons$
 $(t, ta') \text{LNil})))$
and $\text{eq} : eq\text{-upto-seq-inconsist } P \{\!|ta\!\}_o \{\!|ta'\!\}_o \text{ (mrw-values } P \text{ vs (concat (map } (\lambda(t, ta). \{\!|ta\!\}_o)$
 $\text{ttas}))]$

proof *cases*

case (*redT-normal* $x x' m'$)
note $\text{ts}'t = \langle \text{thr } s' \text{ } t = \llbracket (x, \text{no-wait-locks}) \rrbracket \rangle$
and $\text{red} = \langle t \vdash \langle x, \text{shr } s' \rangle \text{ } \text{--} ta \rightarrow \langle x', m' \rangle \rangle$
and $\text{aok} = \langle \text{actions-ok } s' \text{ } t \text{ } ta \rangle$
and $s'' = \langle \text{redT-upd } s' \text{ } t \text{ } x' \text{ } m' \text{ } s'' \rangle$
from *cut-and-updateD*[*OF cut-and-update, OF Red sc ts't red aok*]
obtain $ta' x'' m''$ **where** $\text{red} : t \vdash \langle x, \text{shr } s' \rangle \text{ } \text{--} ta' \rightarrow \langle x'', m'' \rangle$
and $\text{sc}' : ta\text{-seq-consist } P \text{ (mrw-values } P \text{ vs (concat (map } (\lambda(t, y). \{\!|y\!\}_o) \text{ ttas))) (llist-of } \{\!|ta'\!\}_o$
and $\text{eq} : eq\text{-upto-seq-inconsist } P \{\!|ta\!\}_o \{\!|ta'\!\}_o \text{ (mrw-values } P \text{ vs (concat (map } (\lambda(t, ta). \{\!|ta\!\}_o)$
 $\text{ttas}))]$

```

and aok: actions-ok  $s' t ta'$  by blast
obtain  $ws'''$  where redT-updWs  $t (wset\ s')$   $\{ta'\}_w\ ws'''$ 
using redT-updWs-total ..
then obtain  $s'''$  where  $s''': redT-upd\ s' t ta' x'' m'' s'''$  by fastforce
with red  $\langle thr\ s' t = [(x, no-wait-locks)] \rangle$  aok have  $s' \dashv ta' \rightarrow s'''$  by(rule redT.redT-normal)
moreover from sc sc'
have ta-seq-consist  $P vs (lconcat (lmap (\lambda(t, ta). llist-of \{ta\}_o) (lappend (llist-of\ ttas) (LCons (t, ta') LNil))))$ 
by(auto simp add: lmap-lappend-distrib ta-seq-consist-lappend split-def lconcat-llist-of[symmetric] o-def list-of-lconcat)
ultimately show thesis using eq by(rule that)
next
case (redT-acquire  $x\ ln\ n$ )
hence ta-seq-consist  $P vs (lconcat (lmap (\lambda(t, ta). llist-of \{ta\}_o) (lappend (llist-of\ ttas) (LCons (t, ta) LNil))))$ 
and eq-upto-seq-inconsist  $P \{ta\}_o \{ta\}_o (mrw-values\ P\ vs\ (concat (map (\lambda(t, ta). \{ta\}_o) ttas))$ 
using sc
by(simp-all add: lmap-lappend-distrib ta-seq-consist-lappend split-def lconcat-llist-of[symmetric] o-def list-of-lconcat ta-seq-consist-convert-RA ta-seq-consist-imp-eq-upto-seq-inconsist-refl)
with red show thesis by(rule that)
qed

```

Now, find a sequentially consistent completion from s''' onwards.

```

from Red red' have  $Red': s \dashv ttas @ [(t, ta')] \rightarrow^* s'''$ 
unfolding RedT-def by(auto intro: rtrancl3p-step)

from sc sc'
have ta-seq-consist  $P vs (lconcat (lmap (\lambda(t, ta). llist-of \{ta\}_o) (llist-of (ttas @ [(t, ta')]))))$ 
by(simp add: o-def split-def lappend-llist-of-llist-of[symmetric])
with cut-and-update-imp-sc-completion[OF cut-and-update] Red'
have sc-completion  $s''' (mrw-values\ P\ vs\ (concat (map (\lambda(t, ta). \{ta\}_o) (ttas @ [(t, ta')]))))$ 
by(rule sc-completion-shift)
from sequential-completion-Runs[OF this ta-seq-consist-convert-RA]
obtain  $ttas'$  where  $\tauRuns: mthr.Runs\ s''' ttas'$ 
and  $sc'': ta-seq-consist\ P (mrw-values\ P\ vs\ (concat (map (\lambda(t, ta). \{ta\}_o) (ttas @ [(t, ta')]))))$ 
 $(lconcat (lmap (\lambda(t, ta). llist-of \{ta\}_o) ttas'))$ 

by blast
from  $red' \tauRuns$  have  $mthr.Runs\ s' (LCons (t, ta') ttas')$  ..
moreover from sc sc' sc''
have ta-seq-consist  $P vs (lconcat (lmap (\lambda(t, ta). llist-of \{ta\}_o) (lappend (llist-of\ ttas) (LCons (t, ta') ttas'))))$ 
unfolding lmap-lappend-distrib lconcat-lappend by(simp add: o-def ta-seq-consist-lappend split-def list-of-lconcat)
ultimately show ?thesis using eq by blast
qed

end

end

```

8.9 Happens-before consistent completion of executions in the JMM

theory *HB-Completion* **imports**

Non-Speculative

begin

coinductive *ta-hb-consistent* :: 'm prog \Rightarrow ('thread-id \times ('addr, 'thread-id) obs-event action) list \Rightarrow ('thread-id \times ('addr, 'thread-id) obs-event action) llist \Rightarrow bool

for *P* :: 'm prog

where

LNil: *ta-hb-consistent* *P* *obs* *LNil*

| *LCons*:

\llbracket *ta-hb-consistent* *P* (*obs* @ [*ob*]) *obs'*;

case *ob* of (*t*, *NormalAction* (*ReadMem* *ad* *al* *v*))

\Rightarrow ($\exists w. w \in$ write-actions (*llist-of* (*obs* @ [*ob*])) \wedge (*ad*, *al*) \in action-loc *P* (*llist-of* (*obs* @ [*ob*]))

w \wedge

value-written *P* (*llist-of* (*obs* @ [*ob*])) *w* (*ad*, *al*) = *v* \wedge

P, *llist-of* (*obs* @ [*ob*]) \vdash *w* \leq hb length *obs* \wedge

($\forall w' \in$ write-actions (*llist-of* (*obs* @ [*ob*])). (*ad*, *al*) \in action-loc *P* (*llist-of* (*obs* @ [*ob*])) *w'*

\longrightarrow

(*P*, *llist-of* (*obs* @ [*ob*]) \vdash *w* \leq hb *w'* \wedge *P*, *llist-of* (*obs* @ [*ob*]) \vdash *w'* \leq hb length *obs* \vee

is-volatile *P* *al* \wedge *P*, *llist-of* (*obs* @ [*ob*]) \vdash *w* \leq so *w'* \wedge *P*, *llist-of* (*obs* @ [*ob*]) \vdash *w'* \leq so

length *obs*) \longrightarrow

w' = *w*)

| - \Rightarrow *True* \rrbracket

\Longrightarrow *ta-hb-consistent* *P* *obs* (*LCons* *ob* *obs'*)

inductive-simps *ta-hb-consistent-LNil* [*simp*]:

ta-hb-consistent *P* *obs* *LNil*

inductive-simps *ta-hb-consistent-LCons*:

ta-hb-consistent *P* *obs* (*LCons* *ob* *obs'*)

lemma *ta-hb-consistent-into-non-speculative*:

ta-hb-consistent *P* *obs0* *obs*

\Longrightarrow non-speculative *P* (*w-values* *P* ($\lambda-. \{\}$) (*map* *snd* *obs0*)) (*lmap* *snd* *obs*)

proof(*coinduction* arbitrary: *obs0* *obs*)

case (*non-speculative* *obs0* *obs*)

let *?vs* = *w-values* *P* ($\lambda-. \{\}$) (*map* *snd* *obs0*)

let *?CH* = $\lambda vs\ obs'. \exists obs0\ obs. vs = w-values\ P\ (\lambda-. \{\})\ (map\ snd\ obs0) \wedge obs' = lmap\ snd\ obs \wedge ta-hb-consistent\ P\ obs0\ obs$

from *non-speculative* **show** *?case*

proof(*cases*)

case *LNil* hence *?LNil* by *simp*

thus *?thesis* ..

next

case (*LCons* *tob* *obs''*)

note *obs* = $\langle obs = LCons\ tob\ obs'' \rangle$

obtain *t* *ob* **where** *tob*: *tob* = (*t*, *ob*) **by**(*cases* *tob*)

from $\langle ta-hb-consistent\ P\ (obs0\ @\ [tob])\ obs'' \rangle\ tob\ obs$

have *?CH* (*w-value* *P* *?vs* *ob*) (*lmap* *snd* *obs''*) **by**(*auto* *intro!*: *exI*)

moreover {

fix *ad* *al* *v*

```

assume ob: ob = NormalAction (ReadMem ad al v)
with LCons tob obtain w where w: w ∈ write-actions (llist-of (obs0 @ [tob]))
  and adal: (ad, al) ∈ action-loc P (llist-of (obs0 @ [tob])) w
  and v: value-written P (llist-of (obs0 @ [tob])) w (ad, al) = v by auto
from w obtain is-write-action (action-obs (llist-of (obs0 @ [tob])) w)
  and w-actions: w ∈ actions (llist-of (obs0 @ [tob])) by cases
hence v ∈ ?vs (ad, al)
proof(cases)
  case (WriteMem ad' al' v')
  hence NormalAction (WriteMem ad al v) ∈ set (map snd obs0)
    using adal ob tob v w-actions unfolding in-set-conv-nth
    by(auto simp add: action-obs-def nth-append value-written.simps actions-def cong: conj-cong)
split: if-split-asm
  thus ?thesis by(rule w-values-WriteMemD)
next
  case (NewHeapElem ad' hT)
  hence NormalAction (NewHeapElem ad hT) ∈ set (map snd obs0)
    using adal ob tob v w-actions unfolding in-set-conv-nth
    by(auto simp add: action-obs-def nth-append value-written.simps actions-def cong: conj-cong)
split: if-split-asm
  thus ?thesis using NewHeapElem adal unfolding v[symmetric]
    by(fastforce simp add: value-written.simps intro!: w-values-new-actionD intro: rev-image-eqI)
  qed }
hence case ob of NormalAction (ReadMem ad al v) ⇒ v ∈ ?vs (ad, al) | - ⇒ True
  by(simp split: action.split obs-event.split)
ultimately have ?LCons using obs tob by simp
thus ?thesis ..
qed

```

```

lemma ta-hb-consistent-lappendI:
  assumes hb1: ta-hb-consistent P E E'
  and hb2: ta-hb-consistent P (E @ list-of E') E''
  and fin: lfinite E'
  shows ta-hb-consistent P E (lappend E' E'')
using fin hb1 hb2
proof(induction arbitrary: E)
  case lfinite-LNil thus ?case by simp
next
  case (lfinite-LConsI E' tob)
  from  $\langle ta-hb-consistent P E (LCons tob E') \rangle$ 
  have ta-hb-consistent P (E @ [tob]) E' by cases
  moreover from  $\langle ta-hb-consistent P (E @ list-of (LCons tob E')) E'' \rangle$ ,  $\langle lfinite E' \rangle$ 
  have ta-hb-consistent P ((E @ [tob]) @ list-of E') E'' by simp
  ultimately have ta-hb-consistent P (E @ [tob]) (lappend E' E'') by(rule lfinite-LConsI.IH)
  thus ?case unfolding lappend-code apply(rule ta-hb-consistent.LCons)
    using  $\langle ta-hb-consistent P E (LCons tob E') \rangle$ 
    by cases (simp split: prod.split-asm action.split-asm obs-event.split-asm)
qed

```

```

lemma ta-hb-consistent-coinduct-append
  [consumes 1, case-names ta-hb-consistent, case-conclusion ta-hb-consistent LNil lappend]:
  assumes major: X E tobs
  and step:  $\bigwedge E tobs. X E tobs$ 

```


$\implies \text{tobs} = LNil \vee$
 $(\exists \text{tobs}' \text{tobs}''. \text{tobs} = \text{lappend } \text{tobs}' \text{tobs}'' \wedge \text{tobs}' \neq LNil \wedge \text{ta-hb-consistent } P \ E \ \text{tobs}' \wedge$
 $(\text{lfinite } \text{tobs}' \longrightarrow (X (E @ \text{list-of } \text{tobs}') \text{tobs}'' \vee$
 $\text{ta-hb-consistent } P (E @ \text{list-of } \text{tobs}') \text{tobs}''))$

(is $\wedge E \ \text{tobs}. - \implies - \vee ?\text{step } E \ \text{tobs}$)

shows *ta-hb-consistent* $P \ E \ \text{tobs}$

proof –

from *major*

have $\exists \text{tobs}' \text{tobs}''. \text{tobs} = \text{lappend } (\text{llist-of } \text{tobs}') \ \text{tobs}'' \wedge \text{ta-hb-consistent } P \ E \ (\text{llist-of } \text{tobs}') \wedge$
 $X (E @ \text{tobs}') \ \text{tobs}''$

by(*auto intro: exI*[**where** $x=[]$])

thus *?thesis*

proof(*coinduct*)

case (*ta-hb-consistent* $E \ \text{tobs}$)

then obtain $\text{tobs}' \ \text{tobs}''$

where $\text{tobs}: \text{tobs} = \text{lappend } (\text{llist-of } \text{tobs}') \ \text{tobs}''$

and $\text{hb-tobs}': \text{ta-hb-consistent } P \ E \ (\text{llist-of } \text{tobs}')$

and $X: X (E @ \text{tobs}') \ \text{tobs}''$ by *blast*

show *?case*

proof(*cases* tobs')

case *Nil*

with X have $X \ E \ \text{tobs}''$ by *simp*

from *step*[*OF this*] show *?thesis*

proof

assume $\text{tobs}'' = LNil$

with *Nil* tobs show *?thesis* by *simp*

next

assume *?step* $E \ \text{tobs}''$

then obtain $\text{tobs}''' \ \text{tobs}''''$

where $\text{tobs}''': \text{tobs}'' = \text{lappend } \text{tobs}''' \ \text{tobs}''''$ and $\text{tobs}'''' \neq LNil$

and $\text{sc-obs}''': \text{ta-hb-consistent } P \ E \ \text{tobs}'''$

and $\text{fin}: \text{lfinite } \text{tobs}''' \implies X (E @ \text{list-of } \text{tobs}''') \ \text{tobs}'''' \vee$

$\text{ta-hb-consistent } P (E @ \text{list-of } \text{tobs}''') \ \text{tobs}''''$

by *blast*

from $\langle \text{tobs}'''' \neq LNil \rangle$ obtain $t \ ob \ \text{tobs}''''''$ where $\text{tobs}''''': \text{tobs}'''' = LCons (t, ob) \ \text{tobs}''''''$

unfolding *neq-LNil-conv* by *auto*

with *Nil* $\text{tobs}'' \ \text{tobs}$ have *concl1*: $\text{tobs} = LCons (t, ob) (\text{lappend } \text{tobs}'''''' \ \text{tobs}''''''')$ by *simp*

have *?LCons*

proof(*cases* *lfinite* tobs'''')

case *False*

hence $\text{lappend } \text{tobs}'''''' \ \text{tobs}'''''' = \text{tobs}''''''$ using tobs'''' by(*simp add: lappend-inf*)

hence $\text{ta-hb-consistent } P (E @ [(t, ob)]) (\text{lappend } \text{tobs}'''''' \ \text{tobs}''''''')$

using $\text{sc-obs}'''' \ \text{tobs}''''$ by(*simp add: ta-hb-consistent-LCons*)

with *concl1* show *?LCons* apply(*simp*)

using $\text{sc-obs}''''$ [*unfolded* tobs''''] by *cases simp*

next

case *True*

with tobs'''' obtain tobs'''''''' where $\text{tobs}''''''': \text{tobs}'''''' = \text{llist-of } \text{tobs}''''''''$

by *simp*(*auto simp add: lfinite-eq-range-llist-of*)

from *fn*[*OF True*]

have $\text{ta-hb-consistent } P (E @ [(t, ob)]) (\text{llist-of } \text{tobs}''''''''') \wedge X (E @ (t, ob) \# \text{tobs}''''''''') \ \text{tobs}''''''''$

∨

$ta\text{-}hb\text{-}consistent\ P\ (E\ @\ [(t,\ ob)])\ (lappend\ (l\text{-}list\text{-}of\ tobs''''''')\ tobs''''')$

proof
assume $X\ (E\ @\ list\text{-}of\ tobs''')\ tobs''''$
hence $X\ (E\ @\ (t,\ ob)\ \#\ tobs''''''')\ tobs''''$ **using** $tobs''''''\ tobs''''$ **by** *simp*
moreover have $ta\text{-}hb\text{-}consistent\ P\ (E\ @\ [(t,\ ob)])\ (l\text{-}list\text{-}of\ tobs''''''')$
using $sc\text{-}obs''''\ tobs''''\ tobs''''''$ **by**(*simp add: ta-hb-consistent-LCons*)
ultimately show *?thesis* **by** *simp*

next
assume $ta\text{-}hb\text{-}consistent\ P\ (E\ @\ list\text{-}of\ tobs''')\ tobs''''$
with $sc\text{-}obs''''\ tobs''''''\ tobs''''$
have $ta\text{-}hb\text{-}consistent\ P\ (E\ @\ [(t,\ ob)])\ (lappend\ (l\text{-}list\text{-}of\ tobs''''''')\ tobs''''')$
by(*simp add: ta-hb-consistent-LCons ta-hb-consistent-lappendI*)
thus *?thesis ..*

qed
hence $((\exists\ tobs'\ tobs''.\ lappend\ (l\text{-}list\text{-}of\ tobs''''''')\ tobs'''' = lappend\ (l\text{-}list\text{-}of\ tobs')\ tobs'' \wedge$
 $ta\text{-}hb\text{-}consistent\ P\ (E\ @\ [(t,\ ob)])\ (l\text{-}list\text{-}of\ tobs') \wedge X\ (E\ @\ (t,\ ob)\ \#\ tobs')$
 $tobs'') \vee$
 $ta\text{-}hb\text{-}consistent\ P\ (E\ @\ [(t,\ ob)])\ (lappend\ (l\text{-}list\text{-}of\ tobs''''''')\ tobs''''')$
by *auto*
thus *?LCons* **using** *concl1 tobs''''''* **apply**(*simp*)
using $sc\text{-}obs''''[un\text{-}folded\ tobs''']$ **by** *cases simp*

qed
thus *?thesis ..*

qed
next
case (*Cons tob tobs''')*
with $X\ tobs\ hb\text{-}tobs'$ **show** *?thesis* **by**(*auto simp add: ta-hb-consistent-LCons*)

qed
qed
qed

lemma *ta-hb-consistent-coinduct-append-wf*

[*consumes 2, case-names ta-hb-consistent, case-conclusion ta-hb-consistent LNil lappend*]:

assumes *major: X E obs a*

and *wf: wf R*

and *step: $\bigwedge E\ obs\ a.\ X\ E\ obs\ a$*

$\implies\ obs = LNil \vee$

$(\exists\ obs'\ obs''\ a'.\ obs = lappend\ obs'\ obs'' \wedge ta\text{-}hb\text{-}consistent\ P\ E\ obs' \wedge (obs' = LNil \longrightarrow (a', a) \in R) \wedge$

$(l\text{-}finite\ obs' \longrightarrow X\ (E\ @\ list\text{-}of\ obs')\ obs''\ a' \vee$

$ta\text{-}hb\text{-}consistent\ P\ (E\ @\ list\text{-}of\ obs')\ obs''))$

(**is** $\bigwedge E\ obs\ a.\ - \implies - \vee\ ?step\ E\ obs\ a$)

shows $ta\text{-}hb\text{-}consistent\ P\ E\ obs$

proof –

{ **fix** $E\ obs\ a$

assume $X\ E\ obs\ a$

with wf

have $obs = LNil \vee (\exists\ obs'\ obs''.\ obs = lappend\ obs'\ obs'' \wedge obs' \neq LNil \wedge ta\text{-}hb\text{-}consistent\ P\ E\ obs' \wedge$

$(l\text{-}finite\ obs' \longrightarrow (\exists\ a.\ X\ (E\ @\ list\text{-}of\ obs')\ obs''\ a) \vee$

$ta\text{-}hb\text{-}consistent\ P\ (E\ @\ list\text{-}of\ obs')\ obs''))$

(**is** $- \vee\ ?step\ \text{-}concl\ E\ obs$)

proof(*induction a arbitrary: E obs rule: wf-induct[consumes 1, case-names wf]*)

case ($wf\ a$)

```

note IH = wf.IH[rule-format]
from step[OF ‹X E obs a›]
show ?case
proof
  assume obs = LNil thus ?thesis ..
next
assume ?step E obs a
then obtain obs' obs'' a'
  where obs: obs = lappend obs' obs''
  and sc-obs': ta-hb-consistent P E obs'
  and decr: obs' = LNil  $\implies$  (a', a) ∈ R
  and fin: lfinite obs'  $\implies$ 
    X (E @ list-of obs') obs'' a' ∨
    ta-hb-consistent P (E @ list-of obs') obs''
  by blast
show ?case
proof(cases obs' = LNil)
  case True
  hence lfinite obs' by simp
  from fin[OF this] show ?thesis
  proof
    assume X: X (E @ list-of obs') obs'' a'
    from True have (a', a) ∈ R by(rule decr)
    from IH[OF this X] show ?thesis
    proof
      assume obs'' = LNil
      with True obs have obs = LNil by simp
      thus ?thesis ..
    next
      assume ?step-concl (E @ list-of obs') obs''
      hence ?step-concl E obs using True obs by simp
      thus ?thesis ..
    qed
  next
    assume ta-hb-consistent P (E @ list-of obs') obs''
    thus ?thesis using obs True
      by cases (auto 4 3 cong: action.case-cong obs-event.case-cong intro: exI[where x=LCons
x LNil for x] simp add: ta-hb-consistent-LCons)
    qed
  next
    case False
    with obs sc-obs' fin show ?thesis by auto
  qed
qed
qed }
note step' = this

from major show ?thesis
proof(coinduction arbitrary: E obs a rule: ta-hb-consistent-coinduct-append)
  case (ta-hb-consistent E obs)
  thus ?case by simp(rule step')
qed
qed

```

lemma *ta-hb-consistent-lappendD2*:

assumes *hb*: *ta-hb-consistent P E (lappend E' E'')*

and *fin*: *lfinite E'*

shows *ta-hb-consistent P (E @ list-of E') E''*

using *fin hb*

by(*induct arbitrary: E*)(*fastforce simp add: ta-hb-consistent-LCons*)+

lemma *ta-hb-consistent-Read-hb*:

fixes *E E'* **defines** $E'' \equiv \text{lappend } (\text{llist-of } E') E$

assumes *hb*: *ta-hb-consistent P E' E*

and *tsa*: *thread-start-actions-ok E''*

and *E''*: *is-write-seen P (llist-of E') ws'*

and *new-actions-for-fun*:

$\bigwedge w w' \text{ adal. } \llbracket w \in \text{new-actions-for } P E'' \text{ adal};$
 $w' \in \text{new-actions-for } P E'' \text{ adal} \rrbracket \implies w = w'$

shows $\exists ws. P \vdash (E'', ws) \checkmark \wedge (\forall n. n \in \text{read-actions } E'' \longrightarrow \text{length } E' \leq n \longrightarrow P, E'' \vdash ws\ n \leq_{hb} n) \wedge$

$(\forall n. n < \text{length } E' \longrightarrow ws\ n = ws'\ n)$

proof(*intro exI conjI strip*)

let $?P =$

$\lambda n w. \text{case } \text{lth } E''\ n \text{ of}$

$(t, \text{NormalAction } (\text{ReadMem } ad\ al\ v)) \implies$

$(w \in \text{write-actions } E'' \wedge (ad, al) \in \text{action-loc } P E''\ w \wedge \text{value-written } P E''\ w\ (ad, al) = v \wedge$
 $P, E'' \vdash w \leq_{hb} n \wedge$

$(\forall w' \in \text{write-actions } E''. (ad, al) \in \text{action-loc } P E''\ w' \longrightarrow$

$(P, E'' \vdash w \leq_{hb} w' \wedge P, E'' \vdash w' \leq_{hb} n \vee$

$\text{is-volatile } P\ al \wedge P, E'' \vdash w \leq_{so} w' \wedge P, E'' \vdash w' \leq_{so} n) \longrightarrow$

$w' = w)$

let $?ws = \lambda n. \text{if } n < \text{length } E' \text{ then } ws'\ n \text{ else } Eps\ (?P\ n)$

have $\bigwedge n. n < \text{length } E' \implies ?ws\ n = ws'\ n$ **by** *simp*

moreover

have $P \vdash (E'', ?ws) \checkmark \wedge$

$(\forall n\ ad\ al\ v. n \in \text{read-actions } E'' \longrightarrow \text{length } E' \leq n \longrightarrow \text{action-obs } E''\ n = \text{NormalAction}$
 $(\text{ReadMem } ad\ al\ v) \longrightarrow P, E'' \vdash ?ws\ n \leq_{hb} n)$

proof(*intro conjI wf-execI strip is-write-seenI*)

fix $a'\ ad\ al\ v$

assume *read*: $a' \in \text{read-actions } E''$

and *aobs*: $\text{action-obs } E''\ a' = \text{NormalAction } (\text{ReadMem } ad\ al\ v)$

then obtain t **where** a' : $\text{enat } a' < \text{llength } E''$

and lth'' : $\text{lth } E''\ a' = (t, \text{NormalAction } (\text{ReadMem } ad\ al\ v))$

by(*cases*)(*cases lth E'' a', clarsimp simp add: actions-def action-obs-def*)

have $?ws\ a' \in \text{write-actions } E'' \wedge$

$(ad, al) \in \text{action-loc } P E''\ (?ws\ a') \wedge$

$\text{value-written } P E''\ (?ws\ a')\ (ad, al) = v \wedge$

$(\text{length } E' \leq a' \longrightarrow P, E'' \vdash ?ws\ a' \leq_{hb} a') \wedge$

$\neg P, E'' \vdash a' \leq_{hb} ?ws\ a' \wedge$

$(\text{is-volatile } P\ al \longrightarrow \neg P, E'' \vdash a' \leq_{so} ?ws\ a') \wedge$

$(\forall a''. a'' \in \text{write-actions } E'' \longrightarrow (ad, al) \in \text{action-loc } P E''\ a'' \longrightarrow$

$(P, E'' \vdash ?ws\ a' \leq_{hb} a'' \wedge P, E'' \vdash a'' \leq_{hb} a' \vee \text{is-volatile } P\ al \wedge P, E'' \vdash ?ws\ a' \leq_{so} a'' \wedge$
 $P, E'' \vdash a'' \leq_{so} a')$

$\longrightarrow a'' = ?ws\ a')$

proof(*cases a' < length E', safe del: notI disjE conjE*)

```

assume  $a'-E'$ :  $a' < \text{length } E'$ 
with  $\text{read aobs}$  have  $a'$ :  $a' \in \text{read-actions (llist-of } E')$ 
  and  $\text{aobs}'$ :  $\text{action-obs (llist-of } E')$   $a' = \text{NormalAction (ReadMem ad al v)}$ 
  by( $\text{auto simp add: } E''\text{-def action-obs-def lnth-lappend1 actions-def elim: read-actions.cases intro:}$ 
 $\text{read-actions.intros}$ )
  have  $\text{sim}$ :  $\text{ltake (enat (length } E')) (l\text{list-of } E') [\approx] \text{ltake (enat (length } E')) (l\text{append (llist-of } E')$ 
 $E)$ 
    by( $\text{rule eq-into-sim-actions}(simp \text{ add: ltake-all ltake-lappend1})$ )
from  $\text{tsa}$  have  $\text{tsa}'$ :  $\text{thread-start-actions-ok (llist-of } E')$ 
  by( $\text{rule thread-start-actions-ok-prefix}(simp \text{ add: } E''\text{-def lprefix-lappend})$ )

from  $\text{is-write-seenD}[OF E'' a' \text{aobs}'] a'-E'$ 
show  $?ws a' \in \text{write-actions } E''$ 
  and  $(ad, al) \in \text{action-loc } P E'' (?ws a')$ 
  and  $\text{value-written } P E'' (?ws a') (ad, al) = v$ 
  and  $\neg P, E'' \vdash a' \leq_{hb} ?ws a'$ 
  and  $\text{is-volatile } P al \implies \neg P, E'' \vdash a' \leq_{so} ?ws a'$ 
  by( $\text{auto elim!: write-actions.cases intro!: write-actions.intros simp add: } E''\text{-def lnth-lappend1}$ 
 $\text{actions-def action-obs-def value-written-def enat-less-enat-plusI dest: happens-before-change-prefix}[OF$ 
 $\text{- tsal sim[symmetric]] sync-order-change-prefix}[OF \text{- sim[symmetric]]]$ )

{ assume  $\text{length } E' \leq a'$ 
  thus  $P, E'' \vdash ?ws a' \leq_{hb} a'$  using  $a'-E'$  by  $\text{simp} \}$ 

{ fix  $w$ 
  assume  $w$ :  $w \in \text{write-actions } E'' (ad, al) \in \text{action-loc } P E'' w$ 
  and  $\text{hbso}$ :  $P, E'' \vdash ?ws a' \leq_{hb} w \wedge P, E'' \vdash w \leq_{hb} a' \vee \text{is-volatile } P al \wedge P, E'' \vdash ?ws a' \leq_{so}$ 
 $w \wedge P, E'' \vdash w \leq_{so} a'$ 
  show  $w = ?ws a'$ 
  proof( $\text{cases } w < \text{length } E'$ )
    case  $\text{True}$ 
    with  $\text{is-write-seenD}[OF E'' a' \text{aobs}'] a'-E' w \text{hbso}$  show  $?thesis$ 
    by( $\text{auto } 4 \ 3 \text{ elim!: write-actions.cases intro!: write-actions.intros simp add: } E''\text{-def}$ 
 $\text{lnth-lappend1 actions-def action-obs-def value-written-def enat-less-enat-plusI dest: happens-before-change-prefix}[OF$ 
 $\text{- tsal[unfolded } E''\text{-def] sim] happens-before-change-prefix}[OF \text{- tsal sim[symmetric]] sync-order-change-prefix}[OF$ 
 $\text{- sim, simplified] sync-order-change-prefix}[OF \text{- sim[symmetric], simplified] bspec[where } x=w]$ )
    next
    case  $\text{False}$ 
    from  $\text{hbso}$  have  $E'' \vdash w \leq a'$  by( $\text{auto intro: happens-before-into-action-order elim:}$ 
 $\text{sync-orderE}$ )
    moreover from  $w(1)$  read have  $w \neq a'$  by( $\text{auto dest: read-actions-not-write-actions}$ )
    ultimately have  $\text{new-w}$ :  $\text{is-new-action (action-obs } E'' w)$  using  $\text{False aobs } a'-E'$ 
    by( $\text{cases rule: action-orderE}$ )  $\text{auto}$ 
    moreover from  $\text{hbso } a'-E'$  have  $E'' \vdash ws' a' \leq a w$ 
    by( $\text{auto intro: happens-before-into-action-order elim: sync-orderE}$ )
    hence  $\text{new-a}'$ :  $\text{is-new-action (action-obs } E'' (?ws a'))$  using  $\text{new-w } a'-E'$ 
    by( $\text{cases rule: action-orderE}$ )  $\text{auto}$ 
    ultimately have  $w \in \text{new-actions-for } P E'' (ad, al) ?ws a' \in \text{new-actions-for } P E'' (ad, al)$ 
    using  $w \text{is-write-seenD}[OF E'' a' \text{aobs}'] a'-E'$ 
    by( $\text{auto simp add: new-actions-for-def actions-def action-obs-def lnth-lappend1 } E''\text{-def}$ 
 $\text{enat-less-enat-plusI elim!: write-actions.cases}$ )
    thus  $?thesis$  by( $\text{rule new-actions-for-fun}$ )
  } qed }
next

```

```

assume  $\neg a' < \text{length } E'$ 
hence  $a'-E'$ :  $\text{length } E' \leq a'$  by simp
define  $a$  where  $a = a' - \text{length } E'$ 
with  $a'$   $a'-E'$  have  $a$ :  $\text{enat } a < \text{length } E$ 
  by(simp add: E''-def) (metis enat-add-mono le-add-diff-inverse plus-enat-simps(1))

from  $a$ -def  $a$  obs  $\text{lnt}'' a'-E'$ 
have  $a$  obs:  $\text{action-obs } E a = \text{NormalAction } (\text{ReadMem } ad \ al \ v)$ 
  and  $\text{lnt}$ :  $\text{lnt} E a = (t, \text{NormalAction } (\text{ReadMem } ad \ al \ v))$ 
  by(simp-all add: E''-def lnt-lappend2 action-obs-def)

define  $E'''$  where  $E''' = \text{lappend } (\text{lnt-of } E') (\text{ltake } (\text{enat } a) E)$ 
let  $?E'' = \text{lappend } E''' (\text{LCons } (t, \text{NormalAction } (\text{ReadMem } ad \ al \ v)) \text{ LNil})$ 

note  $hb$  also
have  $E$ -unfold1:  $E = \text{lappend } (\text{ltake } (\text{enat } a) E) (\text{ldropn } a E)$  by simp
also have  $E$ -unfold2:  $\text{ldropn } a E = \text{LCons } (t, \text{NormalAction } (\text{ReadMem } ad \ al \ v)) (\text{ldropn } (\text{Suc } a) E)$ 
  using  $a$   $\text{lnt}$  by (metis ldropn-Suc-conv-ldropn)
finally
have  $ta$ -hb-consistent  $P (E' @ \text{list-of } (\text{ltake } (\text{enat } a) E))$ 
  ( $\text{LCons } (t, \text{NormalAction } (\text{ReadMem } ad \ al \ v)) (\text{ldropn } (\text{Suc } a) E)$ )
  by(rule ta-hb-consistent-lappendD2) simp
with  $a$   $a'-E'$   $a$ -def obtain  $w$  where  $w \in \text{write-actions } ?E''$ 
  and  $adal$ - $w$ :  $(ad, al) \in \text{action-loc } P ?E'' w$ 
  and  $w$  written:  $\text{value-written } P ?E'' w (ad, al) = v$ 
  and  $hb$ :  $P, ?E'' \vdash w \leq hb \ a'$ 
  and  $in$ -between-so:
   $\bigwedge w'. \llbracket w' \in \text{write-actions } ?E''; (ad, al) \in \text{action-loc } P ?E'' w';$ 
     $\text{is-volatile } P \ al; P, ?E'' \vdash w \leq so \ w'; P, ?E'' \vdash w' \leq so \ a' \rrbracket$ 
   $\implies w' = w$ 
  and  $in$ -between-hb:
   $\bigwedge w'. \llbracket w' \in \text{write-actions } ?E''; (ad, al) \in \text{action-loc } P ?E'' w';$ 
     $P, ?E'' \vdash w \leq hb \ w'; P, ?E'' \vdash w' \leq hb \ a' \rrbracket$ 
   $\implies w' = w$ 
  by(auto simp add: ta-hb-consistent-LCons length-list-of-conv-the-enat min-def lnt-ltake lappend-llist-of-llist-of[symmetric] E'''-def lappend-assoc simp del: lappend-llist-of-llist-of nth-list-of split: if-splits)

from  $a'$   $a'-E'$   $a$ 
have  $eq$ :  $\text{ltake } (\text{enat } (\text{Suc } a')) ?E'' = \text{ltake } (\text{enat } (\text{Suc } a')) E''$  (is  $?lhs = ?rhs$ )
  unfolding  $E''$ -def  $E'''$ -def  $\text{lappend-assoc}$ 
  apply(subst (2)  $E$ -unfold1)
  apply(subst  $E$ -unfold2)
  apply(subst (1 2)  $\text{ltake-lappend2}$ )
  apply(simp)
  apply(rule arg-cong) back
  apply(subst (1 2)  $\text{ltake-lappend2}$ )
  apply(simp add: min-def)
  apply (metis Suc-diff-le a-def le-Suc-eq order-le-less)
  apply(rule arg-cong) back
  apply(auto simp add: min-def a-def)
  apply(auto simp add: eSuc-enat[symmetric] zero-enat-def[symmetric])
done

```

hence *sim*: ?lhs [≈] ?rhs **by**(rule eq-into-sim-actions)
from *tsa* **have** *tsa'*: thread-start-actions-ok ?E'' **unfolding** E''-def E'''-def lappend-assoc
by(rule thread-start-actions-ok-prefix)(subst (2) E-unfold1, simp add: E-unfold2)

from *w a a'* a-def a'-E' **have** *w-a'*: $w < Suc\ a'$
by cases(simp add: actions-def E'''-def min-def zero-enat-def eSuc-enat split: if-split-asm)

from *w sim* **have** $w \in write\ actions\ E''$ **by**(rule write-actions-change-prefix)(simp add: w-a')
moreover
from *adal-w* action-loc-change-prefix[OF *sim*, of *w P*] *w-a'*
have $(ad, al) \in action\ loc\ P\ E''\ w$ **by** *simp*
moreover
from *written* value-written-change-prefix[OF *eq*, of *w P*] *w-a'*
have *value-written* $P\ E''\ w\ (ad, al) = v$ **by** *simp*
moreover
from *hb tsa sim* **have** $P, E'' \vdash w \leq hb\ a'$ **by**(rule happens-before-change-prefix)(simp-all add:

w-a')

moreover {
fix *w'*
assume *w'*: $w' \in write\ actions\ E''$
and *adal*: $(ad, al) \in action\ loc\ P\ E''\ w'$
and *hbso*: $P, E'' \vdash w \leq hb\ w' \wedge P, E'' \vdash w' \leq hb\ a' \vee is\ volatile\ P\ al \wedge P, E'' \vdash w \leq so\ w' \wedge$
 $P, E'' \vdash w' \leq so\ a'$
(is ?hbso E'')
from *hbso* **have** *ao*: $E'' \vdash w \leq a\ w'\ E'' \vdash w' \leq a\ a'$
by(auto dest: happens-before-into-action-order elim: sync-orderE)
have $w' = w$
proof(cases is-new-action (action-obs E'' w'))
case *True*
hence $w' \in new\ actions\ for\ P\ E''\ (ad, al)$ **using** *w' adal* **by**(simp add: new-actions-for-def)
moreover from *ao True* **have** is-new-action (action-obs E'' w) **by**(cases rule: action-orderE)

simp-all

with $\langle w \in write\ actions\ E'' \rangle \langle (ad, al) \in action\ loc\ P\ E''\ w \rangle$
have $w \in new\ actions\ for\ P\ E''\ (ad, al)$ **by**(simp add: new-actions-for-def)
ultimately show $w' = w$ **by**(rule new-actions-for-fun)

next
case *False*
with *ao* **have** $w' \leq a'$ **by**(auto elim: action-orderE)
hence *w'-a*: *enat* $w' < enat\ (Suc\ a')$ **by** *simp*
with *hbso w-a'* **have** ?hbso ?E''
by(auto 4 3 elim: happens-before-change-prefix[OF - *tsa' sim*[symmetric]] sync-order-change-prefix[OF

- *sim*[symmetric]] del: disjCI intro: disjI1 disjI2)

moreover from $w' \langle w' \leq a' \rangle a' a\ lnth\ a'-E'$ **have** $w' \in write\ actions\ ?E''$
by(cases)(cases $w' < a'$, auto intro!: write-actions.intros simp add: E'''-def actions-def
action-obs-def lnth-lappend min-def zero-enat-def eSuc-enat lnth-ltake a-def E''-def not-le not-less)

moreover from *adal* $\langle w' \leq a' \rangle a\ a'\ lnth\ w'\ a'-E'$ **have** $(ad, al) \in action\ loc\ P\ ?E''\ w'$
by(cases $w' < a'$)(cases $w' < length\ E'$, auto simp add: E'''-def action-obs-def lnth-lappend
lappend-assoc[symmetric] min-def lnth-ltake less-trans[where $y=enat\ a$] a-def E''-def lnth-ltake elim:

write-actions.cases)

ultimately show $w' = w$ **by**(blast dest: in-between-so in-between-hb)

qed }

ultimately have $?P\ a'\ w$ **using** *a'-E' lnth* **unfolding** E''-def a-def **by**(simp add: lnth-lappend)
hence P : $?P\ a'\ (Eps\ (?P\ a'))$ **by**(rule someI[where $P=?P\ a'$])

from $P \text{ lnh}'' a' - E'$
show $?ws a' \in \text{write-actions } E''$
and $(ad, al) \in \text{action-loc } P E'' (?ws a')$
and $\text{value-written } P E'' (?ws a') (ad, al) = v$
and $P, E'' \vdash ?ws a' \leq_{hb} a'$ **by** *simp-all*

show $\neg P, E'' \vdash a' \leq_{hb} ?ws a'$
proof
assume $P, E'' \vdash a' \leq_{hb} ?ws a'$
with $\langle P, E'' \vdash ?ws a' \leq_{hb} a' \rangle$ **have** $a' = ?ws a'$
by (*blast dest: antisymPD[OF antisym-action-order] happens-before-into-action-order*)
with $\text{read } \langle ?ws a' \in \text{write-actions } E'' \rangle$ **show** *False*
by (*auto dest: read-actions-not-write-actions*)
qed

show $\neg P, E'' \vdash a' \leq_{so} ?ws a'$
proof
assume $P, E'' \vdash a' \leq_{so} ?ws a'$
hence $E'' \vdash a' \leq_a ?ws a'$ **by** (*blast elim: sync-orderE*)
with $\langle P, E'' \vdash ?ws a' \leq_{hb} a' \rangle$ **have** $a' = ?ws a'$
by (*blast dest: antisymPD[OF antisym-action-order] happens-before-into-action-order*)
with $\text{read } \langle ?ws a' \in \text{write-actions } E'' \rangle$ **show** *False*
by (*auto dest: read-actions-not-write-actions*)
qed

fix a''
assume $a'' \in \text{write-actions } E'' (ad, al) \in \text{action-loc } P E'' a''$
and $P, E'' \vdash ?ws a' \leq_{hb} a'' \wedge P, E'' \vdash a'' \leq_{hb} a' \vee$
 $\text{is-volatile } P al \wedge P, E'' \vdash ?ws a' \leq_{so} a'' \wedge P, E'' \vdash a'' \leq_{so} a'$
thus $a'' = ?ws a'$ **using** $\text{lnh}'' P a' - E'$ **by** $\neg(\text{erule disjE, clarsimp+})$
qed

thus $?ws a' \in \text{write-actions } E''$
and $(ad, al) \in \text{action-loc } P E'' (?ws a')$
and $\text{value-written } P E'' (?ws a') (ad, al) = v$
and $\text{length } E' \leq a' \implies P, E'' \vdash ?ws a' \leq_{hb} a'$
and $\neg P, E'' \vdash a' \leq_{hb} ?ws a'$
and $\text{is-volatile } P al \implies \neg P, E'' \vdash a' \leq_{so} ?ws a'$
and $\bigwedge a''. \llbracket a'' \in \text{write-actions } E''; (ad, al) \in \text{action-loc } P E'' a''; P, E'' \vdash ?ws a' \leq_{hb} a''; P, E'' \vdash a'' \leq_{hb} a' \rrbracket \implies a'' = ?ws a'$
and $\bigwedge a''. \llbracket a'' \in \text{write-actions } E''; (ad, al) \in \text{action-loc } P E'' a''; \text{is-volatile } P al; P, E'' \vdash ?ws a' \leq_{so} a''; P, E'' \vdash a'' \leq_{so} a' \rrbracket \implies a'' = ?ws a'$
by *blast+*
qed(*assumption|rule tsa*)
thus $P \vdash (E'', ?ws) \checkmark$
and $\bigwedge n. \llbracket n \in \text{read-actions } E''; \text{length } E' \leq n \rrbracket \implies P, E'' \vdash ?ws n \leq_{hb} n$
by (*blast elim: read-actions.cases intro: read-actions.intros*)

fix n
assume $n < \text{length } E'$
thus $?ws n = ws' n$ **by** *simp*
qed

lemma *ta-hb-consistent-not-ReadI*:

$(\bigwedge t ad al v. (t, \text{NormalAction } (\text{ReadMem } ad al v)) \notin \text{lset } E) \implies \text{ta-hb-consistent } P E' E$

proof(*coinduction arbitrary*: $E' E$)
case (*ta-hb-consistent* $E' E$)
thus ?*case by*(*cases* E)(*auto split*: *action.split obs-event.split, blast*)
qed

context *jmm-multithreaded begin*

definition *complete-hb* :: ($'l, 'thread-id, 'x, 'm, 'w$) *state* \Rightarrow ($'thread-id \times ('addr, 'thread-id)$ *obs-event action*) *list*

\Rightarrow ($'thread-id \times ('l, 'thread-id, 'x, 'm, 'w, ('addr, 'thread-id)$ *obs-event action*) *thread-action*) *llist*

where

complete-hb $s E = \text{unfold-llist}$
 $(\lambda(s, E). \forall t \text{ ta } s'. \neg s -t \triangleright \text{ta} \rightarrow s')$
 $(\lambda(s, E). \text{fst} (\text{SOME} ((t, \text{ta}), s'). s -t \triangleright \text{ta} \rightarrow s' \wedge \text{ta-hb-consistent } P E (\text{llist-of} (\text{map} (\text{Pair } t) \{\!\! \{ \text{ta} \!\!\}_o))))))$
 $(\lambda(s, E). \text{let} ((t, \text{ta}), s') = \text{SOME} ((t, \text{ta}), s'). s -t \triangleright \text{ta} \rightarrow s' \wedge \text{ta-hb-consistent } P E (\text{llist-of} (\text{map} (\text{Pair } t) \{\!\! \{ \text{ta} \!\!\}_o))))$
 $\text{in } (s', E @ \text{map} (\text{Pair } t) \{\!\! \{ \text{ta} \!\!\}_o))$
 (s, E)

definition *hb-completion* ::

($'l, 'thread-id, 'x, 'm, 'w$) *state* \Rightarrow ($'thread-id \times ('addr, 'thread-id)$ *obs-event action*) *list* \Rightarrow *bool*

where

hb-completion $s E \longleftrightarrow$
 $(\forall \text{ttas } s' t x \text{ ta } x' m' i.$
 $s \rightarrow \triangleright \text{ttas} \rightarrow * s' \longrightarrow$
 $\text{non-speculative } P (\text{w-values } P (\lambda-. \{\!\! \{ \}\!\!\}) (\text{map } \text{snd } E)) (\text{llist-of} (\text{concat} (\text{map} (\lambda(t, \text{ta}). \{\!\! \{ \text{ta} \!\!\}_o) \text{ttas}))) \longrightarrow$
 $\text{thr } s' t = \lfloor (x, \text{no-wait-locks}) \rfloor \longrightarrow t \vdash (x, \text{shr } s') -\text{ta} \rightarrow (x', m') \longrightarrow \text{actions-ok } s' t \text{ ta} \longrightarrow$
 $\text{non-speculative } P (\text{w-values } P (\text{w-values } P (\lambda-. \{\!\! \{ \}\!\!\}) (\text{map } \text{snd } E)) (\text{concat} (\text{map} (\lambda(t, \text{ta}). \{\!\! \{ \text{ta} \!\!\}_o) \text{ttas}))) (\text{llist-of} (\text{take } i \{\!\! \{ \text{ta} \!\!\}_o)) \longrightarrow$
 $(\exists \text{ta}' x'' m''. t \vdash (x, \text{shr } s') -\text{ta}' \rightarrow (x'', m'') \wedge \text{actions-ok } s' t \text{ ta}' \wedge$
 $\text{take } i \{\!\! \{ \text{ta}' \!\!\}_o = \text{take } i \{\!\! \{ \text{ta} \!\!\}_o \wedge$
 $\text{ta-hb-consistent } P$
 $(E @ \text{concat} (\text{map} (\lambda(t, \text{ta}). \text{map} (\text{Pair } t) \{\!\! \{ \text{ta} \!\!\}_o) \text{ttas}) @ \text{map} (\text{Pair } t) (\text{take } i$
 $\{\!\! \{ \text{ta} \!\!\}_o))$
 $(\text{llist-of} (\text{map} (\text{Pair } t) (\text{drop } i \{\!\! \{ \text{ta}' \!\!\}_o))) \wedge$
 $(i < \text{length } \{\!\! \{ \text{ta} \!\!\}_o \longrightarrow i < \text{length } \{\!\! \{ \text{ta}' \!\!\}_o) \wedge$
 $(\text{if } \exists \text{ad } al \text{ v. } \{\!\! \{ \text{ta} \!\!\}_o ! i = \text{NormalAction } (\text{ReadMem } ad \text{ al } v) \text{ then sim-action else}$
 $(=)) (\{\!\! \{ \text{ta} \!\!\}_o ! i) (\{\!\! \{ \text{ta}' \!\!\}_o ! i))$

lemma *hb-completionD*:

$\llbracket \text{hb-completion } s E; s \rightarrow \triangleright \text{ttas} \rightarrow * s';$
 $\text{non-speculative } P (\text{w-values } P (\lambda-. \{\!\! \{ \}\!\!\}) (\text{map } \text{snd } E)) (\text{llist-of} (\text{concat} (\text{map} (\lambda(t, \text{ta}). \{\!\! \{ \text{ta} \!\!\}_o) \text{ttas})))$
 $\text{thr } s' t = \lfloor (x, \text{no-wait-locks}) \rfloor; t \vdash (x, \text{shr } s') -\text{ta} \rightarrow (x', m'); \text{actions-ok } s' t \text{ ta};$
 $\text{non-speculative } P (\text{w-values } P (\text{w-values } P (\lambda-. \{\!\! \{ \}\!\!\}) (\text{map } \text{snd } E)) (\text{concat} (\text{map} (\lambda(t, \text{ta}). \{\!\! \{ \text{ta} \!\!\}_o) \text{ttas}))) (\text{llist-of} (\text{take } i \{\!\! \{ \text{ta} \!\!\}_o)) \rrbracket$
 $\implies \exists \text{ta}' x'' m''. t \vdash (x, \text{shr } s') -\text{ta}' \rightarrow (x'', m'') \wedge \text{actions-ok } s' t \text{ ta}' \wedge$
 $\text{take } i \{\!\! \{ \text{ta}' \!\!\}_o = \text{take } i \{\!\! \{ \text{ta} \!\!\}_o \wedge$
 $\text{ta-hb-consistent } P (E @ \text{concat} (\text{map} (\lambda(t, \text{ta}). \text{map} (\text{Pair } t) \{\!\! \{ \text{ta} \!\!\}_o) \text{ttas}) @ \text{map} (\text{Pair } t) (\text{take } i$
 $\{\!\! \{ \text{ta} \!\!\}_o))$
 $(\text{llist-of} (\text{map} (\text{Pair } t) (\text{drop } i \{\!\! \{ \text{ta}' \!\!\}_o))) \wedge$
 $(i < \text{length } \{\!\! \{ \text{ta} \!\!\}_o \longrightarrow i < \text{length } \{\!\! \{ \text{ta}' \!\!\}_o) \wedge$

(if $\exists ad\ al\ v.\ \{\!\{ta\}\!\}_o ! i = NormalAction\ (ReadMem\ ad\ al\ v)$ then *sim-action* else (=))
 $(\{\!\{ta\}\!\}_o ! i)\ (\{\!\{ta'\}\!\}_o ! i)$

unfolding *hb-completion-def by blast*

lemma *hb-completionI* [intro?]:

($\wedge ttas\ s'\ t\ x\ ta\ x'\ m'\ i.$
 $\llbracket s \multimap ttas \rightarrow^* s';\ non\ speculative\ P\ (w\ values\ P\ (\lambda.\ \{\!\{\}\!\})\ (map\ snd\ E))\ (l\ list\ of\ (concat\ (map\ (\lambda(t,\ ta).\ \{\!\{ta\}\!\}_o)\ ttas))\rrbracket;$
 $thr\ s'\ t = \llbracket (x,\ no\ wait\ locks)\rrbracket; t \vdash (x,\ shr\ s') -ta \rightarrow (x',\ m');\ actions\ ok\ s'\ t\ ta;$
 $non\ speculative\ P\ (w\ values\ P\ (w\ values\ P\ (\lambda.\ \{\!\{\}\!\})\ (map\ snd\ E))\ (concat\ (map\ (\lambda(t,\ ta).\ \{\!\{ta\}\!\}_o)\ ttas))\ (l\ list\ of\ (take\ i\ \{\!\{ta\}\!\}_o))\ \rrbracket$
 $\implies \exists ta'\ x''\ m''.\ t \vdash (x,\ shr\ s') -ta' \rightarrow (x'',\ m'') \wedge actions\ ok\ s'\ t\ ta' \wedge take\ i\ \{\!\{ta'\}\!\}_o = take\ i\ \{\!\{ta\}\!\}_o \wedge$
 $ta\text{-}hb\text{-}consistent\ P\ (E\ @\ concat\ (map\ (\lambda(t,\ ta).\ map\ (Pair\ t)\ \{\!\{ta\}\!\}_o)\ ttas)\ @\ map\ (Pair\ t)\ (take\ i\ \{\!\{ta\}\!\}_o))\ (l\ list\ of\ (map\ (Pair\ t)\ (drop\ i\ \{\!\{ta'\}\!\}_o))) \wedge$
 $(i < length\ \{\!\{ta\}\!\}_o \longrightarrow i < length\ \{\!\{ta'\}\!\}_o) \wedge$
 (if $\exists ad\ al\ v.\ \{\!\{ta\}\!\}_o ! i = NormalAction\ (ReadMem\ ad\ al\ v)$ then *sim-action* else (=))
 $(\{\!\{ta\}\!\}_o ! i)\ (\{\!\{ta'\}\!\}_o ! i)$
 $\implies hb\text{-}completion\ s\ E$

unfolding *hb-completion-def by blast*

lemma *hb-completion-shift*:

assumes *hb-c*: *hb-completion* *s E*

and τRed : $s \multimap ttas \rightarrow^* s'$

and *sc*: *non-speculative* $P\ (w\ values\ P\ (\lambda.\ \{\!\{\}\!\})\ (map\ snd\ E))\ (l\ list\ of\ (concat\ (map\ (\lambda(t,\ ta).\ \{\!\{ta\}\!\}_o)\ ttas))$

(*is non-speculative - ?vs -*)

shows *hb-completion* $s'\ (E\ @\ (concat\ (map\ (\lambda(t,\ ta).\ map\ (Pair\ t)\ \{\!\{ta\}\!\}_o)\ ttas))$

(*is hb-completion - ?E*)

proof(*rule hb-completionI*)

fix $ttas'\ s''\ t\ x\ ta\ x'\ m'\ i$

assume $\tau Red'$: $s' \multimap ttas' \rightarrow^* s''$

and *sc'*: *non-speculative* $P\ (w\ values\ P\ (\lambda.\ \{\!\{\}\!\})\ (map\ snd\ ?E))\ (l\ list\ of\ (concat\ (map\ (\lambda(t,\ ta).\ \{\!\{ta\}\!\}_o)\ ttas'))$

and *red*: $thr\ s''\ t = \llbracket (x,\ no\ wait\ locks)\rrbracket\ t \vdash \langle x,\ shr\ s'' \rangle -ta \rightarrow \langle x',\ m' \rangle\ actions\ ok\ s''\ t\ ta$

and *ns*: *non-speculative* $P\ (w\ values\ P\ (w\ values\ P\ (\lambda.\ \{\!\{\}\!\})\ (map\ snd\ ?E))\ (concat\ (map\ (\lambda(t,\ ta).\ \{\!\{ta\}\!\}_o)\ ttas'))\ (l\ list\ of\ (take\ i\ \{\!\{ta\}\!\}_o))$

from $\tau Red\ \tau Red'$ **have** $s \multimap ttas\ @\ ttas' \rightarrow^* s''$ **unfolding** *RedT-def by*(*rule rtrancl3p-trans*)

moreover from *sc sc'* **have** *non-speculative* $P\ ?vs\ (l\ list\ of\ (concat\ (map\ (\lambda(t,\ ta).\ \{\!\{ta\}\!\}_o)\ ttas\ @\ ttas'))$

unfolding *map-append concat-append lappend-l\ list\ of\ l\ list\ of*[*symmetric*] *map-concat*

by(*simp add*: *non-speculative-lappend o-def split-def del*: *lappend-l\ list\ of\ l\ list\ of*)

ultimately

show $\exists ta'\ x''\ m''.\ t \vdash \langle x,\ shr\ s'' \rangle -ta' \rightarrow \langle x'',\ m'' \rangle \wedge actions\ ok\ s''\ t\ ta' \wedge take\ i\ \{\!\{ta'\}\!\}_o = take\ i\ \{\!\{ta\}\!\}_o \wedge$

$ta\text{-}hb\text{-}consistent\ P\ (?E\ @\ concat\ (map\ (\lambda(t,\ ta).\ map\ (Pair\ t)\ \{\!\{ta\}\!\}_o)\ ttas')\ @\ map\ (Pair\ t)\ (take\ i\ \{\!\{ta\}\!\}_o))$

$(l\ list\ of\ (map\ (Pair\ t)\ (drop\ i\ \{\!\{ta'\}\!\}_o))) \wedge$

$(i < length\ \{\!\{ta\}\!\}_o \longrightarrow i < length\ \{\!\{ta'\}\!\}_o) \wedge$

(if $\exists ad\ al\ v.\ \{\!\{ta\}\!\}_o ! i = NormalAction\ (ReadMem\ ad\ al\ v)$ then *sim-action* else (=)) $(\{\!\{ta\}\!\}_o !$

$i)\ (\{\!\{ta'\}\!\}_o ! i)$

using *red ns* **unfolding** *append-assoc*

apply(*subst* (2) *append-assoc*[*symmetric*])

unfolding *concat-append*[*symmetric*] *map-append*[*symmetric*] *foldr-append*[*symmetric*]

by(rule hb-completionD[OF hb-c])(simp-all add: map-concat o-def split-def)
qed

lemma hb-completion-shift1:
assumes hb-c: hb-completion s E
and Red: s -t>ta→ s'
and sc: non-speculative P (w-values P (λ-. {})) (map snd E)) (llist-of {ta}_o)
shows hb-completion s' (E @ map (Pair t) {ta}_o)
using hb-completion-shift[OF hb-c, of [(t, ta)] s'] Red sc
by(simp add: RedT-def rtrancl3p-Cons rtrancl3p-Nil del: split-paired-Ex)

lemma complete-hb-in-Runs:
assumes hb-c: hb-completion s E
and ta-hb-consistent-convert-RA: $\bigwedge t E ln. ta-hb-consistent P E (llist-of (map (Pair t) (convert-RA ln)))$
shows mthr.Runs s (complete-hb s E)
using hb-c
proof(coinduction arbitrary: s E)
case (Runs s E)
let ?P = $\lambda((t, ta), s'). s -t>ta \rightarrow s' \wedge ta-hb-consistent P E (llist-of (map (Pair t) \{ta\}_o))$
show ?case
proof(cases $\exists t ta s'. s -t>ta \rightarrow s'$)
case False
then have ?Stuck **by**(simp add: complete-hb-def)
thus ?thesis ..
next
case True
let ?t = fst (fst (Eps ?P)) **and** ?ta = snd (fst (Eps ?P)) **and** ?s' = snd (Eps ?P)
from True **obtain** t ta s' **where** red: s -t>ta→ s' **by** blast
hence $\exists x. ?P x$
proof(cases)
case (redT-normal x x' m')
from hb-completionD[OF Runs - - $\langle thr s t = [(x, no-wait-locks)] \rangle \langle t \vdash \langle x, shr s \rangle -ta \rightarrow \langle x', m' \rangle$
 $\langle actions-ok s t ta \rangle$, of [] 0]
obtain ta' x'' m'' **where** $t \vdash \langle x, shr s \rangle -ta' \rightarrow \langle x'', m'' \rangle$
and actions-ok s t ta' ta-hb-consistent P E (llist-of (map (Pair t) {ta'}_o))
by fastforce
moreover obtain ws' **where** redT-updWs t (wset s) {ta'}_w ws' **by** (metis redT-updWs-total)
ultimately show ?thesis **using** $\langle thr s t = [(x, no-wait-locks)] \rangle$
by(cases ta')(auto intro!: exI redT.redT-normal)
next
case (redT-acquire x n ln)
thus ?thesis **using** ta-hb-consistent-convert-RA[of E t ln]
by(auto intro!: exI redT.redT-acquire)
qed
hence ?P (Eps ?P) **by**(rule someI-ex)
hence red: s -?t>?ta→ ?s'
and hb: ta-hb-consistent P E (llist-of (map (Pair ?t) {?ta}_o))
by(simp-all add: split-beta)
moreover
from ta-hb-consistent-into-non-speculative[OF hb]
have non-speculative P (w-values P (λ-. {})) (map snd E)) (llist-of {?ta}_o) **by**(simp add: o-def)
with Runs red **have** hb-completion ?s' (E @ map (Pair ?t) {?ta}_o) **by**(rule hb-completion-shift1)
ultimately have ?Step **using** True

unfolding *complete-hb-def* **by**(*fastforce simp del: split-paired-Ex simp add: split-def*)
thus *?thesis ..*
qed
qed

lemma *complete-hb-ta-hb-consistent:*
assumes *hb-completion s E*
and *ta-hb-consistent-convert-RA: $\bigwedge E t ln. ta-hb-consistent P E (l\text{list-of } (map (Pair t) (convert-RA ln)))$*
shows *ta-hb-consistent P E (lconcat (lmap ($\lambda(t, ta). l\text{list-of } (map (Pair t) \{ta\}_o)$) (complete-hb s E)))*
(is *ta-hb-consistent - - (?obs (complete-hb s E))*
proof –
define *obs a* **where** *obs = ?obs (complete-hb s E)* **and** *a = complete-hb s E*
with *\langle hb-completion s E \rangle* **have** $\exists s. hb-completion s E \wedge obs = ?obs (complete-hb s E) \wedge a = complete-hb s E$ **by** *blast*
moreover **have** *wf (inv-image $\{(m, n). m < n\}$ (llength \circ ltakeWhile ($\lambda(t, ta). \{ta\}_o = []$)))*
(is *wf ?R)* **by**(*rule wf-inv-image*)(*rule wellorder-class.wf*)
ultimately **show** *ta-hb-consistent P E obs*
proof(*coinduct E obs a rule: ta-hb-consistent-coinduct-append-wf*)
case (*ta-hb-consistent E obs a*)
then **obtain** *s* **where** *hb-c: hb-completion s E*
and *obs: obs = lconcat (lmap ($\lambda(t, ta). l\text{list-of } (map (Pair t) \{ta\}_o)$) (complete-hb s E))*
and *a: a = complete-hb s E*
by *blast*
let $?P = \lambda((t, ta), s'). s -t>ta \rightarrow s' \wedge ta-hb-consistent P E (l\text{list-of } (map (Pair t) \{ta\}_o))$
show *?case*
proof(*cases $\exists t ta s'. s -t>ta \rightarrow s'$*)
case *False*
with *obs* **have** *?LNil* **by**(*simp add: complete-hb-def*)
thus *?thesis ..*
next
case *True*
let $?t = fst (fst (Eps ?P))$ **and** $?ta = snd (fst (Eps ?P))$ **and** $?s' = snd (Eps ?P)$
from *True* **obtain** *t ta s'* **where** *red: s -t>ta \rightarrow s'* **by** *blast*
hence $\exists x. ?P x$
proof(*cases*)
case (*redT-normal x x' m'*)
from *hb-completionD[OF hb-c - - \langle thr s t = [(x, no-wait-locks)] \rangle \langle t \vdash \langle x, shr s \rangle -ta \rightarrow \langle x', m' \rangle \langle actions-ok s t ta \rangle, of [] 0]*
obtain *ta' x'' m''* **where** $t \vdash \langle x, shr s \rangle -ta' \rightarrow \langle x'', m'' \rangle$
and *actions-ok s t ta' ta-hb-consistent P E (l\text{list-of } (map (Pair t) \{ta'\}_o))*
by *fastforce*
moreover **obtain** *ws'* **where** *redT-updWs t (uset s) \{ta'\}_w ws'* **by** (*metis redT-updWs-total*)
ultimately **show** *?thesis* **using** $\langle thr s t = [(x, no-wait-locks)] \rangle$
by(*cases ta'*)(*auto intro!: exI redT.redT-normal*)
next
case (*redT-acquire x n ln*)
thus *?thesis* **using** *ta-hb-consistent-convert-RA[of E t ln]*
by(*auto intro!: exI redT.redT-acquire*)
qed
hence $?P (Eps ?P)$ **by**(*rule someI-ex*)
hence *red': s -?t>?ta \rightarrow ?s'*
and *hb: ta-hb-consistent P E (l\text{list-of } (map (Pair ?t) \{?ta\}_o))*

```

    by(simp-all add: split-beta)
  moreover
  from ta-hb-consistent-into-non-speculative[OF hb]
  have non-speculative P (w-values P (λ-. {})) (map snd E) (llist-of {?ta}_o) by(simp add: o-def)
  with hb-c red' have hb-c': hb-completion ?s' (E @ map (Pair ?t) {?ta}_o)
    by(rule hb-completion-shift1)
  show ?thesis
  proof(cases lnull obs)
    case True thus ?thesis unfolding lnull-def by simp
  next
    case False
    have eq: (∀ t ta s'. ¬ s -> ta → s') = False using True by auto
    { assume {?ta}_o = []
      moreover from obs False
      have lfinite (ltakeWhile (λ(t, ta). {?ta}_o = [])) (complete-hb s E)
        unfolding lfinite-ltakeWhile by(fastforce simp add: split-def lconcat-eq-LNil)
      ultimately have (complete-hb ?s' (E @ map (Pair ?t) {?ta}_o), a) ∈ ?R
        using red unfolding a complete-hb-def
        apply(subst (2) unfold-llist.code)
        apply(subst (asm) unfold-llist.code)
        apply(auto simp add: split-beta simp del: split-paired-Ex split-paired-All split: if-split-asm)
        apply(auto simp add: lfinite-eq-range-llist-of)
        done }
    hence ?lappend using red hb hb-c' unfolding obs complete-hb-def
      apply(subst unfold-llist.code)
      apply(simp add: split-beta eq del: split-paired-Ex split-paired-All split del: if-split)
      apply(intro exI conjI impI refl disjI1|rule refl|assumption|simp-all add: llist-of-eq-LNil-conv)+
      done
    thus ?thesis ..
  qed
qed
qed
qed

```

```

lemma hb-completion-Runs:
  assumes hb-completion s E
  and ∧E t ln. ta-hb-consistent P E (llist-of (map (Pair t) (convert-RA ln)))
  shows ∃ ttas. mthr.Runs s ttas ∧ ta-hb-consistent P E (lconcat (lmap (λ(t, ta). llist-of (map (Pair
t) {?ta}_o)) ttas))
  using complete-hb-in-Runs[OF assms] complete-hb-ta-hb-consistent[OF assms]
  by blast
end
end
end

```

8.10 Locales for heap operations with set of allocated addresses

```

theory JMM-Heap
imports
  ../Common/WellForm
  SC-Completion
  HB-Completion

```

begin

definition $w\text{-addrs} :: ('addr \times addr\text{-loc} \Rightarrow 'addr\ val\ set) \Rightarrow 'addr\ set$
where $w\text{-addrs}\ vs = \{a. \exists adal. Addr\ a \in vs\ adal\}$

lemma $w\text{-addrs}\text{-empty}$ [simp]: $w\text{-addrs}\ (\lambda_. \{\}) = \{\}$
by(simp add: $w\text{-addrs}\text{-def}$)

locale $allocated\text{-heap}\text{-base} = heap\text{-base} +$
constrains $addr2thread\text{-id} :: ('addr :: addr) \Rightarrow 'thread\text{-id}$
and $thread\text{-id}2addr :: 'thread\text{-id} \Rightarrow 'addr$
and $spurious\text{-wakeups} :: bool$
and $empty\text{-heap} :: 'heap$
and $allocate :: 'heap \Rightarrow htype \Rightarrow ('heap \times 'addr)\ set$
and $typeof\text{-addr} :: 'heap \Rightarrow 'addr \rightarrow htype$
and $heap\text{-read} :: 'heap \Rightarrow 'addr \Rightarrow addr\text{-loc} \Rightarrow 'addr\ val \Rightarrow bool$
and $heap\text{-write} :: 'heap \Rightarrow 'addr \Rightarrow addr\text{-loc} \Rightarrow 'addr\ val \Rightarrow 'heap \Rightarrow bool$
fixes $allocated :: 'heap \Rightarrow 'addr\ set$

locale $allocated\text{-heap} =$
 $allocated\text{-heap}\text{-base} +$
 $heap +$
constrains $addr2thread\text{-id} :: ('addr :: addr) \Rightarrow 'thread\text{-id}$
and $thread\text{-id}2addr :: 'thread\text{-id} \Rightarrow 'addr$
and $spurious\text{-wakeups} :: bool$
and $empty\text{-heap} :: 'heap$
and $allocate :: 'heap \Rightarrow htype \Rightarrow ('heap \times 'addr)\ set$
and $typeof\text{-addr} :: 'heap \Rightarrow 'addr \rightarrow htype$
and $heap\text{-read} :: 'heap \Rightarrow 'addr \Rightarrow addr\text{-loc} \Rightarrow 'addr\ val \Rightarrow bool$
and $heap\text{-write} :: 'heap \Rightarrow 'addr \Rightarrow addr\text{-loc} \Rightarrow 'addr\ val \Rightarrow 'heap \Rightarrow bool$
and $allocated :: 'heap \Rightarrow 'addr\ set$
and $P :: 'm\ prog$

assumes $allocated\text{-empty}$: $allocated\ empty\text{-heap} = \{\}$
and $allocate\text{-allocatedD}$:
 $(h', a) \in allocate\ h\ hT \Longrightarrow allocated\ h' = insert\ a\ (allocated\ h) \wedge a \notin allocated\ h$
and $heap\text{-write}\text{-allocated}\text{-same}$:
 $heap\text{-write}\ h\ a\ al\ v\ h' \Longrightarrow allocated\ h' = allocated\ h$

begin

lemma $allocate\text{-allocated}\text{-mono}$: $(h', a) \in allocate\ h\ C \Longrightarrow allocated\ h \subseteq allocated\ h'$
by(simp-all add: $allocate\text{-allocatedD}$)

lemma

shows $start\text{-addrs}\text{-allocated}$: $allocated\ start\text{-heap} = set\ start\text{-addrs}$
and $distinct\text{-start}\text{-addrs}'$: $distinct\ start\text{-addrs}$

proof –

{ **fix** $h\ ads\ b$ **and** $xs :: cname\ list$
let $?start\text{-addrs}\ h\ ads\ b\ xs = fst\ (snd\ (foldl\ create\text{-initial}\text{-object}\ (h,\ ads,\ b)\ xs))$
let $?start\text{-heap}\ h\ ads\ b\ xs = fst\ (foldl\ create\text{-initial}\text{-object}\ (h,\ ads,\ b)\ xs)$
assume $allocated\ h = set\ ads$
hence $allocated\ (?start\text{-heap}\ h\ ads\ b\ xs) = set\ (?start\text{-addrs}\ h\ ads\ b\ xs) \wedge$
 $(distinct\ ads \rightarrow distinct\ (?start\text{-addrs}\ h\ ads\ b\ xs))$
(is $?concl\ xs\ h\ ads\ b)$

```

proof(induct xs arbitrary: h ads b)
  case Nil thus ?case by auto
next
  case (Cons x xs)
  note ads = ⟨allocated h = set ads⟩
  show ?case
  proof(cases b ∧ allocate h (Class-type x) ≠ {})
    case False thus ?thesis using ads
      by(simp add: create-initial-object-simps zip-append1)
  next
  case [simp]: True
  then obtain h' a'
    where h'a': (SOME ha. ha ∈ allocate h (Class-type x)) = (h', a')
    and new-obj: (h', a') ∈ allocate h (Class-type x)
    by(cases (SOME ha. ha ∈ allocate h (Class-type x))(auto simp del: True dest: allocate-Eps))

  from new-obj have allocated h' = insert a' (allocated h) a' ∉ allocated h
    by(auto dest: allocate-allocatedD)
  with ads have allocated h' = set (ads @ [a']) by auto
  hence ?concl xs h' (ads @ [a']) True by(rule Cons)
  moreover have a' ∉ set ads using ⟨a' ∉ allocated h⟩ ads by blast
  ultimately show ?thesis by(simp add: create-initial-object-simps new-obj h'a')
  qed
qed }
from this[of empty-heap [] True initialization-list]
show allocated start-heap = set start-addr
  and distinct-start-addr: distinct start-addr
  unfolding start-heap-def start-addr-def start-heap-data-def
  by(auto simp add: allocated-empty)
qed

lemma w-addr-start-heap-obs: w-addr (w-values P vs (map NormalAction start-heap-obs)) ⊆ w-addr
vs
proof –
  { fix xs
    let ?NewObj = λa C. NewHeapElem a (Class-type C) :: ('addr, 'thread-id) obs-event
    let ?start-heap-obs xs = map (λ(C, a). ?NewObj a C) xs
    have w-addr (w-values P vs (map NormalAction (?start-heap-obs xs))) ⊆ w-addr vs
      (is ?concl xs)
    proof(induct xs arbitrary: vs)
      case Nil thus ?case by simp
    next
      case (Cons x xs)
      have w-addr (w-values P vs (map NormalAction (map (λ(C, a). ?NewObj a C) (x # xs))))
        = w-addr (w-values P (w-value P vs (NormalAction (?NewObj (snd x) (fst x)))) (map Nor-
malAction (map (λ(C, a). ?NewObj a C) xs)))
      by(simp add: split-beta)
      also have ... ⊆ w-addr (w-value P vs (NormalAction (?NewObj (snd x) (fst x)))) by(rule Cons)
      also have ... ⊆ w-addr vs
      by(auto simp add: w-addr-def default-val-not-Addr Addr-not-default-val)
      finally show ?case .
    qed }
  }
  thus ?thesis by(simp add: start-heap-obs-def)
qed

```

end

context *heap-base* begin

lemma *addr-loc-default-conf*:

$P \vdash \text{class-type-of } CTn \text{ has } F:T \text{ (fm) in } C$
 $\implies P, h \vdash \text{addr-loc-default } P \ CTn \ (CField \ C \ F) : \leq T$

apply(*cases CTn*)

apply *simp*

apply(*frule has-field-decl-above*)

apply *simp*

done

definition *vs-conf* :: $'m \text{ prog} \Rightarrow 'heap \Rightarrow ('addr \times \text{addr-loc} \Rightarrow 'addr \text{ val set}) \Rightarrow \text{bool}$

where $\text{vs-conf } P \ h \ \text{vs} \longleftrightarrow (\forall \text{ ad al v. } v \in \text{vs} \ (\text{ad}, \text{al}) \longrightarrow (\exists T. P, h \vdash \text{ad@al} : T \wedge P, h \vdash v : \leq T))$

lemma *vs-confI*:

$(\bigwedge \text{ ad al v. } v \in \text{vs} \ (\text{ad}, \text{al}) \implies \exists T. P, h \vdash \text{ad@al} : T \wedge P, h \vdash v : \leq T) \implies \text{vs-conf } P \ h \ \text{vs}$

unfolding *vs-conf-def* by *blast*

lemma *vs-confD*:

$\llbracket \text{vs-conf } P \ h \ \text{vs}; v \in \text{vs} \ (\text{ad}, \text{al}) \rrbracket \implies \exists T. P, h \vdash \text{ad@al} : T \wedge P, h \vdash v : \leq T$

unfolding *vs-conf-def* by *blast*

lemma *vs-conf-insert-iff*:

$\text{vs-conf } P \ h \ (\text{vs}((\text{ad}, \text{al}) := \text{insert } v \ (\text{vs} \ (\text{ad}, \text{al}))))$
 $\longleftrightarrow \text{vs-conf } P \ h \ \text{vs} \wedge (\exists T. P, h \vdash \text{ad@al} : T \wedge P, h \vdash v : \leq T)$

by(*auto 4 3 elim: vs-confD intro: vs-confI split: if-split-asm*)

end

context *heap* begin

lemma *vs-conf-hext*: $\llbracket \text{vs-conf } P \ h \ \text{vs}; h \trianglelefteq h' \rrbracket \implies \text{vs-conf } P \ h' \ \text{vs}$

by(*blast intro!: vs-confI intro: conf-hext addr-loc-type-hext-mono dest: vs-confD*)

lemma *vs-conf-allocate*:

$\llbracket \text{vs-conf } P \ h \ \text{vs}; (h', a) \in \text{allocate } h \ hT; \text{is-htype } P \ hT \rrbracket$
 $\implies \text{vs-conf } P \ h' \ (\text{w-value } P \ \text{vs} \ (\text{NormalAction} \ (\text{NewHeapElem} \ a \ hT)))$

apply(*drule vs-conf-hext*)

apply(*erule hext-allocate*)

apply(*auto intro!: vs-confI simp add: addr-locs-def split: if-split-asm htype.split-asm*)

apply(*auto 3 3 intro: addr-loc-type.intros defval-conf dest: allocate-SomeD elim: has-field-is-class vs-confD*)

apply(*rule exI conjI addr-loc-type.intros|drule allocate-SomeD|erule has-field-is-class|simp*)+

done

end

heap-read-typeable must not be defined in *heap-conf-base* (where it should be) because this would lead to duplicate definitions of *heap-read-typeable* in contexts where *heap-conf-base* is imported twice with different parameters, e.g., P and $J2JVM \ P$ in $J\text{-JVM-heap-conf-read}$.

context *heap-base* begin

definition *heap-read-typeable* :: ('heap \Rightarrow bool) \Rightarrow 'm prog \Rightarrow bool
where *heap-read-typeable* hconf P \longleftrightarrow (\forall h ad al v T. hconf h \longrightarrow P,h \vdash ad@al : T \longrightarrow P,h \vdash v : \leq T \longrightarrow heap-read h ad al v)

lemma *heap-read-typeableI*:

(\bigwedge h ad al v T. \llbracket P,h \vdash ad@al : T; P,h \vdash v : \leq T; hconf h \rrbracket \Longrightarrow heap-read h ad al v) \Longrightarrow heap-read-typeable hconf P

unfolding *heap-read-typeable-def* **by** blast

lemma *heap-read-typeableD*:

\llbracket heap-read-typeable hconf P; P,h \vdash ad@al : T; P,h \vdash v : \leq T; hconf h \rrbracket \Longrightarrow heap-read h ad al v

unfolding *heap-read-typeable-def* **by** blast

end

context *heap-base* **begin**

definition *heap-read-typed* :: 'm prog \Rightarrow 'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr val \Rightarrow bool

where *heap-read-typed* P h ad al v \longleftrightarrow heap-read h ad al v \wedge (\forall T. P,h \vdash ad@al : T \longrightarrow P,h \vdash v : \leq T)

lemma *heap-read-typedI*:

\llbracket heap-read h ad al v; \bigwedge T. P,h \vdash ad@al : T \Longrightarrow P,h \vdash v : \leq T \rrbracket \Longrightarrow heap-read-typed P h ad al v

unfolding *heap-read-typed-def* **by** blast

lemma *heap-read-typed-into-heap-read*:

heap-read-typed P h ad al v \Longrightarrow heap-read h ad al v

unfolding *heap-read-typed-def* **by** blast

lemma *heap-read-typed-typed*:

\llbracket heap-read-typed P h ad al v; P,h \vdash ad@al : T \rrbracket \Longrightarrow P,h \vdash v : \leq T

unfolding *heap-read-typed-def* **by** blast

end

context *heap-conf* **begin**

lemma *heap-conf-read-heap-read-typed*:

heap-conf-read addr2thread-id thread-id2addr empty-heap allocate typeof-addr (heap-read-typed P) heap-write hconf P

proof

fix h a al v T

assume heap-read-typed P h a al v P,h \vdash a@al : T

thus P,h \vdash v : \leq T **by**(rule heap-read-typed-typed)

qed

end

context *heap* **begin**

lemma *start-addr-dom-w-values*:

assumes wf: wf-syscls P

and a: a \in set start-addr

and adal: P,start-heap \vdash a@al : T

```

shows w-values P (λ-. {}) (map NormalAction start-heap-obs) (a, al) ≠ {}
proof –
  from a obtain CTn where CTn: NewHeapElem a CTn ∈ set start-heap-obs
    unfolding in-set-start-addr-conv-NewHeapElem ..
  then obtain obs obs' where obs: start-heap-obs = obs @ NewHeapElem a CTn # obs' by(auto dest:
split-list)
  have w-value P (w-values P (λ-. {}) (map NormalAction obs)) (NormalAction (NewHeapElem a
CTn)) (a, al) ≠ {}
  proof(cases CTn)
    case [simp]: (Class-type C)
    with wf CTn have typeof-addr start-heap a = [Class-type C]
      by(auto intro: NewHeapElem-start-heap-obsD)
    with adal show ?thesis by cases auto
  next
    case [simp]: (Array-type T n)
    with wf CTn have typeof-addr start-heap a = [Array-type T n]
      by(auto dest: NewHeapElem-start-heap-obsD)
    with adal show ?thesis by cases(auto dest: has-field-decl-above)
  qed
  moreover have w-value P (w-values P (λ-. {}) (map NormalAction obs)) (NormalAction (NewHeapElem
a CTn :: ('addr, 'thread-id) obs-event))
    (a, al) ⊆ w-values P (λ-. {}) (map NormalAction start-heap-obs) (a, al)
    by(simp add: obs del: w-value.simps)(rule w-values-mono)
  ultimately show ?thesis by blast
qed

end

end

end

```

8.11 Combination of locales for heap operations and interleaving

```

theory JMM-Framework

```

```

imports

```

```

  JMM-Heap

```

```

  ../Framework/FWInitFinLift

```

```

  ../Common/WellForm

```

```

begin

```

```

lemma enat-plus-eq-enat-conv: — Move to Extended_Nat

```

```

  enat m + n = enat k ⟷ k ≥ m ∧ n = enat (k - m)

```

```

by(cases n) auto

```

```

declare convert-new-thread-action-id [simp]

```

```

context heap begin

```

```

lemma init-fin-lift-state-start-state:

```

```

  init-fin-lift-state s (start-state f P C M vs) = start-state (λC M Ts T meth vs. (s, f C M Ts T meth
vs)) P C M vs

```

```

by(simp add: start-state-def init-fin-lift-state-def split-beta fun-eq-iff)

```

lemma *non-speculative-start-heap-obs*:

non-speculative P vs (llist-of (map snd (lift-start-obs start-tid start-heap-obs)))
apply(rule *non-speculative-nthI*)
using *start-heap-obs-not-Read*
by(*clarsimp simp add: lift-start-obs-def lnth-LCons o-def eSuc-enat[symmetric] in-set-conv-nth split: nat.split-asm*)

lemma *ta-seq-consist-start-heap-obs*:

ta-seq-consist P Map.empty (llist-of (map snd (lift-start-obs start-tid start-heap-obs)))
using *start-heap-obs-not-Read*
by(*auto intro: ta-seq-consist-nthI simp add: lift-start-obs-def o-def lnth-LCons in-set-conv-nth split: nat.split-asm*)

end

context *allocated-heap* **begin**

lemma *w-addr-lift-start-heap-obs*:

w-addr (w-values P vs (map snd (lift-start-obs start-tid start-heap-obs))) \subseteq w-addr vs
by(*simp add: lift-start-obs-def o-def w-addr-start-heap-obs*)

end

context *heap* **begin**

lemma *w-values-start-heap-obs-typeable*:

assumes *wf: wf-syscls P*
and *mrws: v \in w-values P (λ -. { }) (map snd (lift-start-obs start-tid start-heap-obs)) (ad, al)*
shows $\exists T. P, \text{start-heap} \vdash \text{ad}@al : T \wedge P, \text{start-heap} \vdash v : \leq T$

proof –

from *in-w-valuesD[OF mrws]*

obtain *obs' wa obs''*

where *eq: map snd (lift-start-obs start-tid start-heap-obs) = obs' @ wa # obs''*

and *is-write-action wa*

and *adal: (ad, al) \in action-loc-aux P wa*

and *vwa: value-written-aux P wa al = v*

by *blast*

from $\langle \text{is-write-action } wa \rangle$ **show** *?thesis*

proof *cases*

case (*WriteMem ad' al' v'*)

with *vwa adal eq* **have** *WriteMem ad al v \in set start-heap-obs*

by(*auto simp add: map-eq-append-conv Cons-eq-append-conv lift-start-obs-def*)

thus *?thesis* **by**(*rule start-heap-write-typeable*)

next

case (*NewHeapElem ad' hT*)

with *vwa adal eq* **have** *NewHeapElem ad hT \in set start-heap-obs*

by(*auto simp add: map-eq-append-conv Cons-eq-append-conv lift-start-obs-def*)

hence *typeof-addr start-heap ad = [hT]*

by(*rule NewHeapElem-start-heap-obsD[OF wf]*)

thus *?thesis* **using** *adal vwa NewHeapElem*

apply(*cases hT*)

apply(*auto intro!: addr-loc-type.intros dest: has-field-decl-above*)

apply(*frule has-field-decl-above*)

apply(*auto intro!: addr-loc-type.intros dest: has-field-decl-above*)

done
qed
qed

lemma *start-state-vs-conf*:

$wf\text{-syscls } P \implies vs\text{-conf } P \text{ start-heap } (w\text{-values } P \ (\lambda\text{-}\{\}) \ (map \ snd \ (lift\text{-start-obs} \ start\text{-tid} \ start\text{-heap-obs})))$
by(*rule vs-confI*)(*rule w-values-start-heap-obs-typeable*)

end

8.11.1 JMM traces for Jinja semantics

context *multithreaded-base* **begin**

inductive-set $\mathcal{E} :: ('l, 't, 'x, 'm, 'w) \text{ state} \Rightarrow ('t \times 'o) \text{ llist set}$
for $\sigma :: ('l, 't, 'x, 'm, 'w) \text{ state}$

where

$mthr.Runs \ \sigma \ E'$
 $\implies lconcat \ (lmap \ (\lambda(t, ta). \ llist\text{-of} \ (map \ (Pair \ t) \ \{\!|ta|\!\}_o)) \ E') \in \mathcal{E} \ \sigma$

lemma *actions- $\mathcal{E}E$ -aux*:

fixes $\sigma \ E'$

defines $E == lconcat \ (lmap \ (\lambda(t, ta). \ llist\text{-of} \ (map \ (Pair \ t) \ \{\!|ta|\!\}_o)) \ E')$

assumes $mthr: mthr.Runs \ \sigma \ E'$

and $a: enat \ a < llength \ E$

obtains $m \ n \ t \ ta$

where $lnth \ E \ a = (t, \{\!|ta|\!\}_o \ ! \ n)$

and $n < length \ \{\!|ta|\!\}_o$ **and** $enat \ m < llength \ E'$

and $a = (\sum i < m. length \ \{\!|snd \ (lnth \ E' \ i)\!\}_o) + n$

and $lnth \ E' \ m = (t, ta)$

proof –

from $lnth\text{-}lconcat\text{-}conv[OF \ a[unfolded \ E\text{-}def], \ folded \ E\text{-}def]$

obtain $m \ n$

where $lnth \ E \ a = lnth \ (lnth \ (lmap \ (\lambda(t, ta). \ llist\text{-of} \ (map \ (Pair \ t) \ \{\!|ta|\!\}_o)) \ E') \ m) \ n$

and $enat \ n < llength \ (lnth \ (lmap \ (\lambda(t, ta). \ llist\text{-of} \ (map \ (Pair \ t) \ \{\!|ta|\!\}_o)) \ E') \ m)$

and $enat \ m < llength \ (lmap \ (\lambda(t, ta). \ llist\text{-of} \ (map \ (Pair \ t) \ \{\!|ta|\!\}_o)) \ E')$

and $enat \ a = (\sum i < m. llength \ (lnth \ (lmap \ (\lambda(t, ta). \ llist\text{-of} \ (map \ (Pair \ t) \ \{\!|ta|\!\}_o)) \ E') \ i)) + enat$

n

by *blast*

moreover

obtain $t \ ta$ **where** $lnth \ E' \ m = (t, ta)$ **by**(*cases lnth E' m*)

ultimately have $E\text{-}a: lnth \ E \ a = (t, \{\!|ta|\!\}_o \ ! \ n)$

and $n: n < length \ \{\!|ta|\!\}_o$

and $m: enat \ m < llength \ E'$

and $a: enat \ a = (\sum i < m. llength \ (lnth \ (lmap \ (\lambda(t, ta). \ llist\text{-of} \ (map \ (Pair \ t) \ \{\!|ta|\!\}_o)) \ E') \ i)) +$

$enat \ n$

by(*simp-all*)

note a

also have $(\sum i < m. llength \ (lnth \ (lmap \ (\lambda(t, ta). \ llist\text{-of} \ (map \ (Pair \ t) \ \{\!|ta|\!\}_o)) \ E') \ i)) =$
 $sum \ (enat \circ \ (\lambda i. length \ \{\!|snd \ (lnth \ E' \ i)\!\}_o)) \ \{.. < m\}$

using m **by**(*simp add: less-trans[where y=enat m] split-beta*)

also have $\dots = enat \ (\sum i < m. length \ \{\!|snd \ (lnth \ E' \ i)\!\}_o)$

by(*subst sum-comp-morphism*)(*simp-all add: zero-enat-def*)

finally have $a: a = (\sum i < m. length \ \{\!|snd \ (lnth \ E' \ i)\!\}_o) + n$ **by** *simp*

with E -a n m show thesis using $\langle \text{lnth } E' \ m = (t, ta) \rangle$ by(rule that)
qed

lemma actions- $\mathcal{E}E$:

assumes $E: E \in \mathcal{E} \ \sigma$

and $a: \text{enat } a < \text{llength } E$

obtains $E' \ m \ n \ t \ ta$

where $E = \text{lconcat } (\text{lmap } (\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \ \{\!\!\{ta\}\!\!\}_o)) \ E')$

and $\text{mthr.Runs } \sigma \ E'$

and $\text{lnth } E \ a = (t, \ \{\!\!\{ta\}\!\!\}_o \ ! \ n)$

and $n < \text{length } \{\!\!\{ta\}\!\!\}_o$ and $\text{enat } m < \text{llength } E'$

and $a = (\sum_{i < m}. \text{length } \{\!\!\{\text{snd } (\text{lnth } E' \ i)\}\!\!\}_o) + n$

and $\text{lnth } E' \ m = (t, ta)$

proof –

from E obtain $E' \ ws$

where $E: E = \text{lconcat } (\text{lmap } (\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \ \{\!\!\{ta\}\!\!\}_o)) \ E')$

and $\text{mthr.Runs } \sigma \ E'$ by(rule $\mathcal{E}.\text{cases}$) blast

from $\langle \text{mthr.Runs } \sigma \ E' \rangle$ a[unfolded E]

show ?thesis

by(rule actions- $\mathcal{E}E$ -aux)(fold E , rule that[OF $E \ \langle \text{mthr.Runs } \sigma \ E' \rangle$])

qed

end

context τ multithreaded-wf begin

Alternative characterisation for \mathcal{E}

lemma \mathcal{E} -conv-Runs:

$\mathcal{E} \ \sigma = \text{lconcat } \text{'lmap } (\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \ \{\!\!\{ta\}\!\!\}_o)) \ \text{'llist-of-tllist } \text{' } \{E. \text{mthr.}\tau\text{Runs } \sigma \ E\}$
(is ?lhs = ?rhs)

proof(intro equalityI subsetI)

fix E

assume $E \in ?rhs$

then obtain E' where $E: E = \text{lconcat } (\text{lmap } (\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \ \{\!\!\{ta\}\!\!\}_o)) \ (\text{llist-of-tllist } E'))$

and $\tau\text{Runs}: \text{mthr.}\tau\text{Runs } \sigma \ E'$ by(blast)

obtain E'' where $E': E' = \text{tmap } (\lambda(\text{tls}, s', \text{tl}, s''). \text{tl}) \ (\text{case-sum } (\lambda(\text{tls}, s'). \ \lfloor s' \rfloor) \ \text{Map.empty}) \ E''$

and $\tau\text{Runs}' : \text{mthr.}\tau\text{Runs-table2 } \sigma \ E''$

using τRuns by(rule $\text{mthr.}\tau\text{Runs-into-}\tau\text{Runs-table2}$)

have $\text{mthr.Runs } \sigma \ (\text{lconcat } (\text{lappend } (\text{lmap } (\lambda(\text{tls}, s, \text{tl}, s'). \text{llist-of } (\text{tls} \ @ \ \lfloor \text{tl} \rfloor)) \ (\text{llist-of-tllist } E'')) \ (\text{LCons } (\text{case terminal } E'' \ \text{of } \text{Inl } (\text{tls}, s') \Rightarrow \text{llist-of } \text{tls} \ | \ \text{Inr } \text{tls} \Rightarrow \text{tls})$

$\text{LNil}))$

(is $\text{mthr.Runs} - ?E''$)

using $\tau\text{Runs}'$ by(rule $\text{mthr.}\tau\text{Runs-table2-into-Runs}$)

moreover

let $?tail = \lambda E''. \text{case terminal } E'' \ \text{of } \text{Inl } (\text{tls}, s') \Rightarrow \text{llist-of } \text{tls} \ | \ \text{Inr } \text{tls} \Rightarrow \text{tls}$

{

have $E = \text{lconcat } (\text{lfilter } (\lambda xs. \neg \text{lnull } xs) \ (\text{lmap } (\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \ \{\!\!\{ta\}\!\!\}_o)) \ (\text{llist-of-tllist } E'))$

unfolding E by(simp add: $\text{lconcat-lfilter-neq-LNil}$)

also have $\dots = \text{lconcat } (\text{lmap } (\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \ \{\!\!\{ta\}\!\!\}_o)) \ (\text{lmap } (\lambda(\text{tls}, s', \text{tta}, s''). \text{tta}) \ (\text{lfilter } (\lambda(\text{tls}, s', (t, ta), s''). \ \{\!\!\{ta\}\!\!\}_o \neq \ \square)) \ (\text{llist-of-tllist } E''))$

by(simp add: E' lfilter-lmap llist.map-comp o-def split-def)

also

```

from ⟨mthr.τRuns-table2 σ E''⟩
have lmap (λ(tls, s', tta, s''). tta) (lfilter (λ(tls, s', (t, ta), s''). {ta}o ≠ []) (llist-of-tllist E'')) =
  lfilter (λ(t, ta). {ta}o ≠ []) (lconcat (lappend (lmap (λ(tls, s, tl, s'). llist-of (tls @ [tl]))
(llist-of-tllist E'')) (LCons (?tail E'') LNil)))
  (is ?lhs σ E'' = ?rhs σ E'')
proof(coinduction arbitrary: σ E'' rule: llist.coinduct-strong)
  case (Eq-llist σ E'')
  have ?null
  by(cases lfinite (llist-of-tllist E''))(fastforce split: sum.split-asm simp add: split-beta lset-lconcat-lfinite
lappend-inf mthr.silent-move2-def dest: mthr.τRuns-table2-silentsD[OF Eq-llist] mthr.τRuns-table2-terminal-silent.
Eq-llist] mthr.τRuns-table2-terminal-inf-stepD[OF Eq-llist] mτmove-silentD inf-step-silentD silent-moves2-silentD
split: sum.split-asm)+
  moreover
  have ?LCons
  proof(intro impI conjI)
    assume lhs': ¬ null (lmap (λ(tls, s', tta, s''). tta) (lfilter (λ(tls, s', (t, ta), s''). {ta}o ≠ [])
(llist-of-tllist E'')))
    (is ¬ null ?lhs')
    and ¬ null (lfilter (λ(t, ta). {ta}o ≠ []) (lconcat (lappend (lmap (λ(tls, s, tl, s'). llist-of (tls @
[tl])) (llist-of-tllist E'')) (LCons (case terminal E'' of Inl (tls, s') ⇒ llist-of tls | Inr tls ⇒ tls) LNil)))
    (is ¬ null ?rhs')

  note τRuns' = ⟨mthr.τRuns-table2 σ E''⟩
  from lhs' obtain tl tls' where ?lhs σ E'' = LCons tl tls'
  by(auto simp only: not-null-conv)
  then obtain tls s' s'' tlsstlss'
  where tls': tls' = lmap (λ(tls, s', tta, s''). tta) tlsstlss'
  and filter: lfilter (λ(tls, s', (t, ta), s''). obs-a ta ≠ []) (llist-of-tllist E'') = LCons (tls, s', tl,
s'') tlsstlss'
  using lhs' by(fastforce simp add: lmap-eq-LCons-conv)
  from lfilter-eq-LConsD[OF filter]
  obtain us vs where eq: llist-of-tllist E'' = lappend us (LCons (tls, s', tl, s'') vs)
  and fin: lfinite us
  and empty: ∀ (tls, s', (t, ta), s'') ∈ lset us. obs-a ta = []
  and neq-empty: obs-a (snd tl) ≠ []
  and tlsstlss': tlsstlss' = lfilter (λ(tls, s', (t, ta), s''). obs-a ta ≠ []) vs
  by(auto simp add: split-beta)
  from eq obtain E''' where E'': E'' = lappendt us E'''
  and eq': llist-of-tllist E''' = LCons (tls, s', tl, s'') vs
  and terminal: terminal E''' = terminal E''
  unfolding llist-of-tllist-eq-lappend-conv by auto
  from τRuns' fin E'' obtain σ' where τRuns'': mthr.τRuns-table2 σ' E'''
  by(auto dest: mthr.τRuns-table2-lappendtD)
  then obtain σ'' E'''' where mthr.τRuns-table2 σ'' E'''' E''' = TCons (tls, s', tl, s'') E''''
  using eq' by cases auto
  moreover from τRuns' E'' fin
  have ∀ (tls, s, tl, s') ∈ lset us. ∀ (t, ta) ∈ set tls. ta = ε
  by(fastforce dest: mthr.τRuns-table2-silentsD mτmove-silentD simp add: mthr.silent-move2-def)
  hence lfilter (λ(t, ta). obs-a ta ≠ []) (lconcat (lmap (λ(tls, s, tl, s'). llist-of (tls @ [tl])) us)) =
LNil
  using empty by(auto simp add: lfilter-empty-conv lset-lconcat-lfinite split-beta)
  moreover from τRuns'' eq' have snd ' set tls ⊆ {ε}
  by(cases)(fastforce dest: silent-moves2-silentD)+
  hence [(t, ta) ← tls . obs-a ta ≠ []] = []

```

```

    by(auto simp add: filter-empty-conv split-beta)
  ultimately
  show lhd ?lhs' = lhd ?rhs'
    and ( $\exists \sigma E''$ . ltl ?lhs' = lmap ( $\lambda(tls, s', tta, s'')$ ). tta) (lfilter ( $\lambda(tls, s', (t, ta), s'')$ ).  $\{ta\}_o \neq []$ )
  (llist-of-tllist E'')  $\wedge$ 
    ltl ?rhs' = lfilter ( $\lambda(t, ta)$ .  $\{ta\}_o \neq []$ ) (lconcat (lappend (lmap ( $\lambda(tls, s, tl, s')$ ). llist-of (tls @
  [tl])) (llist-of-tllist E'')) (LCons (case terminal E'' of Inl (tls, s')  $\Rightarrow$  llist-of tls | Inr tls  $\Rightarrow$  tls) LNil)))
   $\wedge$ 
     $\tau trsys$ . $\tau$ Runs-table2 redT m $\tau$ move  $\sigma E''$ )  $\vee$ 
    ltl ?lhs' = ltl ?rhs'
  using lhs' E'' fin tls' tlstlss' filter eq' neq-empty
  by(auto simp add: lmap-lappend-distrib lappend-assoc split-beta filter-empty-conv simp del:
  split-paired-Ex)
  qed
  ultimately show ?case ..
  qed
  also have lmap ( $\lambda(t, ta)$ . llist-of (map (Pair t) (obs-a ta))) ... = lfilter ( $\lambda obs$ .  $\neg$  lnull obs) (lmap
  ( $\lambda(t, ta)$ . llist-of (map (Pair t) (obs-a ta))) (lconcat (lappend (lmap ( $\lambda(tls, s, tl, s')$ ). llist-of (tls @
  [tl])) (llist-of-tllist E'')) (LCons (?tail E'') LNil))))
  unfolding lfilter-lmap by(simp add: o-def split-def llist-of-eq-LNil-conv)
  finally have E = lconcat (lmap ( $\lambda(t, ta)$ . llist-of (map (Pair t)  $\{ta\}_o$ )) ?E''')
  by(simp add: lconcat-lfilter-neq-LNil) }
  ultimately show E  $\in$  ?lhs by(blast intro:  $\mathcal{E}$ .intros)
next
fix E
assume E  $\in$  ?lhs
then obtain E' where E: E = lconcat (lmap ( $\lambda(t, ta)$ . llist-of (map (Pair t) (obs-a ta))) E')
  and Runs: mthr.Runs  $\sigma E'$  by(blast elim:  $\mathcal{E}$ .cases)
from Runs obtain E'' where E': E' = lmap ( $\lambda(s, tl, s')$ . tl) E''
  and Runs': mthr.Runs-table  $\sigma E''$  by(rule mthr.Runs-into-Runs-table)
have mthr. $\tau$ Runs  $\sigma$  (tmap ( $\lambda(s, tl, s')$ . tl) id (tfilter None ( $\lambda(s, tl, s')$ .  $\neg$  m $\tau$ move s tl s') (tlstl-of-llist
  (Some (llast (LCons  $\sigma$  (lmap ( $\lambda(s, tl, s')$ . s') E''))))) E''))
  (is mthr. $\tau$ Runs - ?E''')
  using Runs' by(rule mthr.Runs-table-into- $\tau$ Runs)
moreover
have ( $\lambda(s, (t, ta), s')$ . obs-a ta  $\neq []$ ) = ( $\lambda(s, (t, ta), s')$ . obs-a ta  $\neq []$   $\wedge$   $\neg$  m $\tau$ move s (t, ta) s')
  by(rule ext)(auto dest: m $\tau$ move-silentD)
hence E = lconcat (lmap ( $\lambda(t, ta)$ . llist-of (map (Pair t) (obs-a ta))) (llist-of-tllist ?E'''))
  unfolding E E'
  by(subst (1 2) lconcat-lfilter-neq-LNil[symmetric])(simp add: lfilter-lmap lfilter-lfilter o-def split-def)
ultimately show E  $\in$  ?rhs by(blast)
qed
end

```

Running threads have been started before

definition Status-no-wait-locks :: ('l,'t,status \times 'x) thread-info \Rightarrow bool

where

Status-no-wait-locks ts \longleftrightarrow

($\forall t$ status x ln. ts t = \llbracket (status, x), ln \rrbracket \longrightarrow status \neq Running \longrightarrow ln = no-wait-locks)

lemma Status-no-wait-locks-PreStartD:

\wedge ln. \llbracket Status-no-wait-locks ts; ts t = \llbracket (PreStart, x), ln \rrbracket \rrbracket \Longrightarrow ln = no-wait-locks

unfolding Status-no-wait-locks-def **by** blast

lemma *Status-no-wait-locks-FinishedD*:

$\bigwedge ln. \llbracket \text{Status-no-wait-locks } ts; ts \ t = \llbracket ((\text{Finished}, x), ln) \rrbracket \rrbracket \implies ln = \text{no-wait-locks}$
unfolding *Status-no-wait-locks-def* **by** *blast*

lemma *Status-no-wait-locksI*:

$(\bigwedge t \ status \ x \ ln. \llbracket ts \ t = \llbracket ((\text{status}, x), ln) \rrbracket; \text{status} = \text{PreStart} \vee \text{status} = \text{Finished} \rrbracket \implies ln = \text{no-wait-locks})$

$\implies \text{Status-no-wait-locks } ts$

unfolding *Status-no-wait-locks-def*

apply *clarify*

apply(*case-tac status*)

apply *auto*

done

context *heap-base* **begin**

lemma *Status-no-wait-locks-start-state*:

Status-no-wait-locks (thr (init-fin-lift-state status (start-state f P C M vs)))

by(*clarsimp simp add: Status-no-wait-locks-def init-fin-lift-state-def start-state-def split-beta*)

end

context *multithreaded-base* **begin**

lemma *init-fin-preserve-Status-no-wait-locks*:

assumes *ok: Status-no-wait-locks (thr s)*

and *redT: multithreaded-base.redT init-fin-final init-fin (map NormalAction \circ convert-RA) s tta s'*

shows *Status-no-wait-locks (thr s')*

using *redT*

proof(*cases rule: multithreaded-base.redT.cases[consumes 1, case-names redT-normal redT-acquire]*)

case *redT-acquire*

with *ok* **show** *?thesis*

by(*auto intro!: Status-no-wait-locksI dest: Status-no-wait-locks-PreStartD Status-no-wait-locks-FinishedD*

split: if-split-asm)

next

case *redT-normal*

show *?thesis*

proof(*rule Status-no-wait-locksI*)

fix *t' status' x' ln'*

assume *tst': thr s' t' = $\llbracket ((\text{status}', x'), ln') \rrbracket$*

and *status: status' = PreStart \vee status' = Finished*

show *ln' = no-wait-locks*

proof(*cases thr s t'*)

case *None*

with *redT-normal tst'* **show** *?thesis*

by(*fastforce elim!: init-fin.cases dest: redT-updTs-new-thread simp add: final-thread.actions-ok-iff*

split: if-split-asm)

next

case (*Some s'ln*)

obtain *status'' x'' ln''*

where [*simp*]: *s'ln = ((status'', x''), ln'')* **by**(*cases s'ln*) *auto*

show *?thesis*

proof(*cases fst tta = t'*)


```

    case True
    with redT-normal tst' status show ?thesis by(auto simp add: expand-finfun-eq fun-eq-iff)
next
case False
with tst' redT-normal Some status have status'' = status' ln'' = ln'
  by(force dest: redT-updTs-Some simp add: final-thread.actions-ok-iff)+
with ok Some status show ?thesis
  by(auto dest: Status-no-wait-locks-PreStartD Status-no-wait-locks-FinishedD)
qed
qed
qed
qed

lemma init-fin-Running-InitialThreadAction:
  assumes redT: multithreaded-base.redT init-fin-final init-fin (map NormalAction ◦ convert-RA) s tta
  s'
  and not-running:  $\bigwedge x \ln. \text{thr } s \ t \neq [((\text{Running}, x), \ln)]$ 
  and running:  $\text{thr } s' \ t = [((\text{Running}, x'), \ln')]$ 
  shows tta = (t, {InitialThreadAction})
using redT
proof(cases rule: multithreaded-base.redT.cases[consumes 1, case-names redT-normal redT-acquire])
  case redT-acquire
  with running not-running show ?thesis by(auto split: if-split-asm)
next
case redT-normal
show ?thesis
proof(cases thr s t)
  case None
  with redT-normal running not-running show ?thesis
  by(fastforce simp add: final-thread.actions-ok-iff elim: init-fin.cases dest: redT-updTs-new-thread
split: if-split-asm)
  next
  case (Some a)
  with redT-normal running not-running show ?thesis
  apply(cases a)
  apply(auto simp add: final-thread.actions-ok-iff split: if-split-asm elim: init-fin.cases)
  apply((drule (1) redT-updTs-Some)?, fastforce)+
  done
qed
qed

end

context if-multithreaded begin

lemma init-fin-Trsys-preserve-Status-no-wait-locks:
  assumes ok: Status-no-wait-locks (thr s)
  and Trsys: if.mthr.Trsys s ttas s'
  shows Status-no-wait-locks (thr s')
using Trsys ok
by(induct)(blast dest: init-fin-preserve-Status-no-wait-locks)+

lemma init-fin-Trsys-Running-InitialThreadAction:
  assumes redT: if.mthr.Trsys s ttas s'

```

```

and not-running:  $\bigwedge x \ln. \text{thr } s \ t \neq [((\text{Running}, x), \ln)]$ 
and running:  $\text{thr } s' \ t = [((\text{Running}, x'), \ln')]$ 
shows  $(t, \{\text{InitialThreadAction}\}) \in \text{set } ttas$ 
using redT not-running running
proof(induct arbitrary: x' ln')
  case rtrancl3p-refl thus ?case by(fastforce)
next
  case (rtrancl3p-step s ttas s' tta s') thus ?case
  by(cases  $\exists x \ln. \text{thr } s' \ t = [((\text{Running}, x), \ln)]$ )(fastforce dest: init-fin-Running-InitialThreadAction)+
qed

```

end

```

locale heap-multithreaded-base =
  heap-base
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  +
  mthr: multithreaded-base final r convert-RA
for addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype
and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
and final :: 'x  $\Rightarrow$  bool
and r :: ('addr, 'thread-id, 'x, 'heap, 'addr, ('addr, 'thread-id) obs-event) semantics ( $\langle - \vdash - \dashrightarrow - \rangle$ )
  [50,0,0,50] 80)
and convert-RA :: 'addr released-locks  $\Rightarrow$  ('addr, 'thread-id) obs-event list

```

sublocale *heap-multithreaded-base* < *mthr: if-multithreaded-base final r convert-RA*

.

context *heap-multithreaded-base* **begin**

abbreviation *\mathcal{E} -start* ::

```

(cname  $\Rightarrow$  mname  $\Rightarrow$  ty list  $\Rightarrow$  ty  $\Rightarrow$  'md  $\Rightarrow$  'addr val list  $\Rightarrow$  'x)
 $\Rightarrow$  'md prog  $\Rightarrow$  cname  $\Rightarrow$  mname  $\Rightarrow$  'addr val list  $\Rightarrow$  status
 $\Rightarrow$  ('thread-id  $\times$  ('addr, 'thread-id) obs-event action) llist set

```

where

```

 $\mathcal{E}$ -start f P C M vs status  $\equiv$ 
  lappend (llist-of (lift-start-obs start-tid start-heap-obs)) '
  mthr.if. $\mathcal{E}$  (init-fin-lift-state status (start-state f P C M vs))

```

end

```

locale heap-multithreaded =
  heap-multithreaded-base
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write

```

```

  final r convert-RA
+
heap
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  P
+
mthr: multithreaded final r convert-RA

for addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and final :: 'x ⇒ bool
and r :: ('addr, 'thread-id, 'x, 'heap, 'addr, ('addr, 'thread-id) obs-event) semantics (⟨- ⊢ - ---> →)
[50,0,0,50] 80
and convert-RA :: 'addr released-locks ⇒ ('addr, 'thread-id) obs-event list
and P :: 'md prog

sublocale heap-multithreaded < mthr: if-multithreaded final r convert-RA
by(unfold-locales)

sublocale heap-multithreaded < if: jmm-multithreaded
  mthr.init-fin-final mthr.init-fin map NormalAction ◦ convert-RA P
.

context heap-multithreaded begin

lemma thread-start-actions-ok-init-fin-RedT:
  assumes Red: mthr.if.RedT (init-fin-lift-state status (start-state f P C M vs)) ttas s'
    (is mthr.if.RedT ?start-state - -)
  shows thread-start-actions-ok (llist-of (lift-start-obs start-tid start-heap-obs @ concat (map (λ(t, ta).
map (Pair t) {ta}_o) ttas)))
    (is thread-start-actions-ok (llist-of (?obs-prefix @ ?E')))
proof(rule thread-start-actions-okI)
  let ?E = llist-of (?obs-prefix @ ?E')
  fix a
  assume a: a ∈ actions ?E
  and new: ¬ is-new-action (action-obs ?E a)
  show ∃ i ≤ a. action-obs ?E i = InitialThreadAction ∧ action-tid ?E i = action-tid ?E a
proof(cases action-tid ?E a = start-tid)
  case True thus ?thesis
  by(auto simp add: lift-start-obs-def action-tid-def action-obs-def)
next
  case False
  let ?a = a - length ?obs-prefix

  from False have a-len: a ≥ length ?obs-prefix
  by(rule contrapos-np)(auto simp add: lift-start-obs-def action-tid-def lnth-LCons nth-append split:

```

nat.split)

hence [*simp*]: *action-tid* ?*E* *a* = *action-tid* (*l*list-of ?*E*') ?*a* *action-obs* ?*E* *a* = *action-obs* (*l*list-of ?*E*') ?*a*

by(*simp-all add: action-tid-def nth-append action-obs-def*)

from *False* **have** *not-running*: $\bigwedge x \ln. \text{thr } ?\text{start-state } (\text{action-tid } (\text{l}list\text{-of } ?E') ?a) \neq [((\text{Running}, x), \ln)]$

by(*auto simp add: start-state-def split-beta init-fin-lift-state-def split: if-split-asm*)

from *a* *a-len* **have** ?*a* < *length* ?*E*' **by**(*simp add: actions-def*)

from *nth-concat-conv*[*OF this*]

obtain *m n* **where** *E'-a*: ?*E*' ! ?*a* = ($\lambda(t, ta). (t, \{\!|ta|\!\}_o ! n)$) (*ttas* ! *m*)

and *n*: *n* < *length* $\{\!|snd (ttas ! m)|\!\}_o$

and *m*: *m* < *length* *ttas*

and *a-conv*: ?*a* = ($\sum i < m. \text{length } (\text{map } (\lambda(t, ta). \text{map } (\text{Pair } t) \{\!|ta|\!\}_o) \text{ttas } ! i)$) + *n*

by(*clarsimp simp add: split-def*)

from *Red* **obtain** *s'' s'''* **where** *Red1*: *mthr.if.RedT* ?*start-state* (*take m ttas*) *s''*

and *red*: *mthr.if.redT* *s''* (*ttas* ! *m*) *s'''*

and *Red2*: *mthr.if.RedT* *s'''* (*drop (Suc m) ttas*) *s'*

unfolding *mthr.if.RedT-def*

by(*subst (asm) (4) id-take-nth-drop[OF m]*)(*blast elim: rtrancl3p-appendE rtrancl3p-converseE*)

from *E'-a m n* **have** [*simp*]: *action-tid* (*l*list-of ?*E*') ?*a* = *fst* (*ttas* ! *m*)

by(*simp add: action-tid-def split-def*)

from *red* **obtain** *status x ln* **where** *tst*: *thr s''* (*fst (ttas ! m)*) = [*(status, x), ln*] **by** *cases auto show ?thesis*

proof(*cases status = PreStart \vee status = Finished*)

case *True*

from *Red1* **have** *Status-no-wait-locks* (*thr s''*)

unfolding *mthr.if.RedT-def*

by(*rule mthr.init-fin-Trsys-preserve-Status-no-wait-locks[OF Status-no-wait-locks-start-state]*)

with *True tst* **have** *ln = no-wait-locks*

by(*auto dest: Status-no-wait-locks-PreStartD Status-no-wait-locks-FinishedD*)

with *red tst True* **have** $\{\!|snd (ttas ! m)|\!\}_o = [\text{InitialThreadAction}]$ **by**(*cases auto*)

hence *action-obs* ?*E* *a* = *InitialThreadAction* **using** *a-conv n a-len E'-a*

by(*simp add: action-obs-def nth-append split-beta*)

thus ?*thesis* **by**(*auto*)

next

case *False*

hence *status = Running* **by**(*cases status auto*)

with *tst mthr.init-fin-Trsys-Running-InitialThreadAction*[*OF Red1*][*unfolded mthr.if.RedT-def*]

not-running]

have (*fst (ttas ! m)*, $\{\!|\text{InitialThreadAction}|\!\}$) \in *set (take m ttas)*

using *E'-a* **by**(*auto simp add: action-tid-def split-beta*)

then obtain *i* **where** *i*: *i* < *m*

and *nth-i*: *ttas* ! *i* = (*fst (ttas ! m)*, $\{\!|\text{InitialThreadAction}|\!\}$)

unfolding *in-set-conv-nth* **by** *auto*

let ?*i*' = *length (concat (map ($\lambda(t, ta). \text{map } (\text{Pair } t) \{\!|ta|\!\}_o$) (take i ttas)))*

let ?*i* = *length ?obs-prefix* + ?*i*'

from *i m nth-i*

```

have ?i' < length (concat (map (λ(t, ta). map (Pair t) {ta}_o) (take m ttas)))
  apply(simp add: length-concat o-def split-beta)
  apply(subst (6) id-take-nth-drop[where i=i])
  apply(simp-all add: take-map[symmetric] min-def)
done
also from m have ... ≤ ?a unfolding a-conv
  by(simp add: length-concat sum-list-sum-nth min-def split-def atLeast0LessThan)
finally have ?i < a using a-len by simp
moreover
from i m nth-i have ?i' < length ?E'
  apply(simp add: length-concat o-def split-def)
  apply(subst (7) id-take-nth-drop[where i=i])
  apply(simp-all add: take-map[symmetric])
done
from nth-i i E'-a a-conv m
have lnth ?E ?i = (fst (ttas ! m), InitialThreadAction)
by(simp add: lift-start-obs-def nth-append length-concat o-def split-def)(rule nth-concat-eqI[where
k=0 and i=i], simp-all add: take-map o-def split-def)
ultimately show ?thesis using E'-a
  by(cases ttas ! m)(auto simp add: action-obs-def action-tid-def nth-append intro!: exI[where
x=?i])
qed
qed
qed

```

lemma *thread-start-actions-ok-init-fin*:

```

assumes E: E ∈ mthr.if.ℰ (init-fin-lift-state status (start-state f P C M vs))
shows thread-start-actions-ok (lappend (llist-of (lift-start-obs start-tid start-heap-obs)) E)
(is thread-start-actions-ok ?E)
proof(rule thread-start-actions-okI)
let ?start-heap-obs = lift-start-obs start-tid start-heap-obs
let ?start-state = init-fin-lift-state status (start-state f P C M vs)
fix a
assume a: a ∈ actions ?E
and a-new: ¬ is-new-action (action-obs ?E a)
show ∃ i. i ≤ a ∧ action-obs ?E i = InitialThreadAction ∧ action-tid ?E i = action-tid ?E a
proof(cases action-tid ?E a = start-tid)
case True thus ?thesis
  by(auto simp add: lift-start-obs-def action-tid-def action-obs-def)
next
case False

let ?a = a - length ?start-heap-obs

from False have a ≥ length ?start-heap-obs
  by(rule contrapos-np)(auto simp add: lift-start-obs-def action-tid-def lnth-LCons lnth-lappend1
split: nat.split)
hence [simp]: action-tid ?E a = action-tid E ?a action-obs ?E a = action-obs E ?a
  by(simp-all add: action-tid-def lnth-lappend2 action-obs-def)

from False have not-running: ∧ x ln. thr ?start-state (action-tid E ?a) ≠ [((Running, x), ln)]
  by(auto simp add: start-state-def split-beta init-fin-lift-state-def split: if-split-asm)

```

```

from  $E$  obtain  $E'$  where  $E'$ :  $E = \text{lconcat} (\text{lmap} (\lambda(t, ta). \text{llist-of} (\text{map} (\text{Pair } t) \{\{ta\}_o\})) E')$ 
  and  $\tau\text{Runs}$ :  $\text{mthr.if.mthr.Runs } ?\text{start-state } E'$  by(rule  $\text{mthr.if.}\mathcal{E}.\text{cases}$ )
from  $a$   $E' \langle a \geq \text{length } ?\text{start-heap-obs} \rangle$ 
have  $\text{enat-a}$ :  $\text{enat } ?a < \text{llength} (\text{lconcat} (\text{lmap} (\lambda(t, ta). \text{llist-of} (\text{map} (\text{Pair } t) \{\{ta\}_o\})) E')$ 
  by(cases  $\text{llength} (\text{lconcat} (\text{lmap} (\lambda(t, ta). \text{llist-of} (\text{map} (\text{Pair } t) \{\{ta\}_o\})) E')$ )(auto simp add:
actions-def)
with  $\tau\text{Runs}$  obtain  $m$   $n$   $t$   $ta$ 
  where  $a\text{-obs}$ :  $\text{lnth} (\text{lconcat} (\text{lmap} (\lambda(t, ta). \text{llist-of} (\text{map} (\text{Pair } t) \{\{ta\}_o\})) E')$  ( $a - \text{length}$ 
 $?\text{start-heap-obs}$ ) = ( $t, \{\{ta\}_o ! n$ )
  and  $n$ :  $n < \text{length } \{\{ta\}_o$ 
  and  $m$ :  $\text{enat } m < \text{llength } E'$ 
  and  $a\text{-conv}$ :  $?a = (\sum i < m. \text{length } \{\{\text{snd} (\text{lnth } E' i)\}_o\}) + n$ 
  and  $E'\text{-m}$ :  $\text{lnth } E' m = (t, ta)$ 
  by(rule  $\text{mthr.if.actions-}\mathcal{E}E\text{-aux}$ )
from  $a\text{-obs}$  have [ $\text{simp}$ ]:  $\text{action-tid } E ?a = t$   $\text{action-obs } E ?a = \{\{ta\}_o ! n$ 
  by(simp-all add:  $E'$   $\text{action-tid-def}$   $\text{action-obs-def}$ )

let  $?E' = \text{ldropn} (\text{Suc } m) E'$ 
let  $?m\text{-}E' = \text{ltake} (\text{enat } m) E'$ 
have  $E'\text{-unfold}$ :  $E' = \text{lappend} (\text{ltake} (\text{enat } m) E') (\text{LCons} (\text{lnth } E' m) ?E')$ 
  unfolding  $\text{ldropn-Suc-conv-ldropn}[OF m]$  by simp
hence  $\text{mthr.if.mthr.Runs } ?\text{start-state} (\text{lappend } ?m\text{-}E' (\text{LCons} (\text{lnth } E' m) ?E'))$ 
  using  $\tau\text{Runs}$  by simp
then obtain  $\sigma'$  where  $\sigma\text{-}\sigma'$ :  $\text{mthr.if.mthr.Trsys } ?\text{start-state} (\text{list-of } ?m\text{-}E') \sigma'$ 
  and  $\tau\text{Runs}'$ :  $\text{mthr.if.mthr.Runs } \sigma' (\text{LCons} (\text{lnth } E' m) ?E')$ 
  by(rule  $\text{mthr.if.mthr.Runs-lappendE}$ ) simp
from  $\tau\text{Runs}'$  obtain  $\sigma'''$  where  $\text{red-a}$ :  $\text{mthr.if.redT } \sigma' (t, ta) \sigma'''$ 
  and  $\tau\text{Runs}''$ :  $\text{mthr.if.mthr.Runs } \sigma''' ?E'$ 
  unfolding  $E'\text{-m}$  by cases
from  $\text{red-a}$  obtain  $\text{status } x$   $ln$  where  $\text{tst}$ :  $\text{thr } \sigma' t = [((\text{status}, x), ln)]$  by cases auto
show  $?thesis$ 
proof(cases  $\text{status} = \text{PreStart} \vee \text{status} = \text{Finished}$ )
  case  $\text{True}$ 
  have  $\text{Status-no-wait-locks} (\text{thr } \sigma')$ 
  by(rule  $\text{mthr.init-fin-Trsys-preserve-Status-no-wait-locks}[OF - \sigma\text{-}\sigma']$ )(rule  $\text{Status-no-wait-locks-start-state}$ )
  with  $\text{True}$   $\text{tst}$  have  $ln = \text{no-wait-locks}$ 
  by(auto dest:  $\text{Status-no-wait-locks-PreStartD}$   $\text{Status-no-wait-locks-FinishedD}$ )
  with  $\text{red-a}$   $\text{tst}$   $\text{True}$  have  $\{\{ta\}_o = [\text{InitialThreadAction}]$  by(cases) auto
  hence  $\text{action-obs } E ?a = \text{InitialThreadAction}$  using  $a\text{-obs } n$  unfolding  $E'$ 
  by(simp add:  $\text{action-obs-def}$ )
  thus  $?thesis$  by(auto)
next
  case  $\text{False}$ 
  hence  $\text{status} = \text{Running}$  by(cases  $\text{status}$ ) auto
  with  $\text{tst}$   $\text{mthr.init-fin-Trsys-Running-InitialThreadAction}[OF \sigma\text{-}\sigma' \text{not-running}]$ 
  have ( $\text{action-tid } E ?a, \{\{\text{InitialThreadAction}\}\} \in \text{set} (\text{list-of} (\text{ltake} (\text{enat } m) E'))$ )
  using  $a\text{-obs } E'$  by(auto simp add:  $\text{action-tid-def}$ )
  then obtain  $i$  where  $i < m$   $\text{enat } i < \text{llength } E'$ 
  and  $n\text{th-}i$ :  $\text{lnth } E' i = (\text{action-tid } E ?a, \{\{\text{InitialThreadAction}\}\})$ 
  unfolding  $\text{in-set-conv-nth}$ 
  by(cases  $\text{llength } E'$ )(auto simp add:  $\text{length-list-of-conv-the-enat}$   $\text{lnth-ltake}$ )

let  $?i' = \sum i < i. \text{length } \{\{\text{snd} (\text{lnth } E' i)\}_o$ 

```

let $?i = \text{length } ?\text{start-heap-obs} + ?i'$

from $\langle i < m \rangle$ have $(\sum i < m. \text{length } \{\text{snd } (\text{lnth } E' i)\}_o) = ?i' + (\sum i = i..< m. \text{length } \{\text{snd } (\text{lnth } E' i)\}_o)$

unfolding *atLeast0LessThan[symmetric]* by *(subst sum.atLeastLessThan-concat) simp-all*

hence $?i' \leq ?a$ unfolding *a-conv* by *simp*

hence $?i \leq a$ using $\langle a \geq \text{length } ?\text{start-heap-obs} \rangle$ by *arith*

from $\langle ?i' \leq ?a \rangle$ have *enat* $?i' < \text{llength } E$ using *enat-a E'*

by *(simp add: le-less-trans[where y=enat ?a])*

from *lnth-lconcat-conv[OF this[unfolded E'], folded E']*

obtain $k l$

where *nth-i'*: $\text{lnth } E ?i' = \text{lnth } (\text{lnth } (\text{lmap } (\lambda(t, ta). \text{lList-of } (\text{map } (\text{Pair } t) \{\text{ta}\}_o)) E') k) l$

and $l: l < \text{length } \{\text{snd } (\text{lnth } E' k)\}_o$

and $k: \text{enat } k < \text{llength } E'$

and *i-conv*: $\text{enat } ?i' = (\sum i < k. \text{llength } (\text{lnth } (\text{lmap } (\lambda(t, ta). \text{lList-of } (\text{map } (\text{Pair } t) \{\text{ta}\}_o)) E') i)) + \text{enat } l$

by *(fastforce simp add: split-beta)*

have $(\sum i < k. \text{llength } (\text{lnth } (\text{lmap } (\lambda(t, ta). \text{lList-of } (\text{map } (\text{Pair } t) \{\text{ta}\}_o)) E') i)) =$
 $(\sum i < k. (\text{enat } \circ (\lambda i. \text{length } \{\text{snd } (\text{lnth } E' i)\}_o)) i)$

by *(rule sum.cong)(simp-all add: less-trans[where y=enat k] split-beta k)*

also have $\dots = \text{enat } (\sum i < k. \text{length } \{\text{snd } (\text{lnth } E' i)\}_o)$

by *(rule sum-comp-morphism)(simp-all add: zero-enat-def)*

finally have *i-conv*: $?i' = (\sum i < k. \text{length } \{\text{snd } (\text{lnth } E' i)\}_o) + l$ using *i-conv* by *simp*

have *[simp]*: $i = k$

proof *(rule ccontr)*

assume $i \neq k$

thus *False* unfolding *neq-iff*

proof

assume $i < k$

hence $(\sum i < k. \text{length } \{\text{snd } (\text{lnth } E' i)\}_o) =$

$(\sum i < i. \text{length } \{\text{snd } (\text{lnth } E' i)\}_o) + (\sum i = i..< k. \text{length } \{\text{snd } (\text{lnth } E' i)\}_o)$

unfolding *atLeast0LessThan[symmetric]* by *(subst sum.atLeastLessThan-concat) simp-all*

with *i-conv* have $(\sum i = i..< k. \text{length } \{\text{snd } (\text{lnth } E' i)\}_o) = l$ by *simp-all*

moreover have $(\sum i = i..< k. \text{length } \{\text{snd } (\text{lnth } E' i)\}_o) \geq \text{length } \{\text{snd } (\text{lnth } E' i)\}_o$

by *(subst sum.atLeast-Suc-lessThan[OF <i < k>]) simp*

ultimately show *False* using *nth-i* by *simp*

next

assume $k < i$

hence $?i' = (\sum i < k. \text{length } \{\text{snd } (\text{lnth } E' i)\}_o) + (\sum i = k..< i. \text{length } \{\text{snd } (\text{lnth } E' i)\}_o)$

unfolding *atLeast0LessThan[symmetric]* by *(subst sum.atLeastLessThan-concat) simp-all*

with *i-conv* have $(\sum i = k..< i. \text{length } \{\text{snd } (\text{lnth } E' i)\}_o) = l$ by *simp*

moreover have $(\sum i = k..< i. \text{length } \{\text{snd } (\text{lnth } E' i)\}_o) \geq \text{length } \{\text{snd } (\text{lnth } E' k)\}_o$

by *(subst sum.atLeast-Suc-lessThan[OF <k < i>]) simp*

ultimately show *False* using l by *simp*

qed

qed

with l *nth-i* have *[simp]*: $l = 0$ by *simp*

hence $\text{lnth } E ?i' = (\text{action-tid } E ?a, \text{InitialThreadAction})$

using *nth-i nth-i' k* by *simp*

```

    with ⟨?i ≤ a⟩ show ?thesis
    by(auto simp add: action-tid-def action-obs-def lnth-lappend2)
  qed
  qed
  qed

```

end

In the subsequent locales, *convert-RA* refers to *convert-RA* and is no longer a parameter!

lemma *convert-RA-not-write*:

```

  ∧ln. ob ∈ set (convert-RA ln) ⇒ ¬ is-write-action (NormalAction ob)
by(auto simp add: convert-RA-def)

```

lemma *ta-seq-consist-convert-RA*:

```

  fixes ln shows
  ta-seq-consist P vs (llist-of ((map NormalAction ∘ convert-RA) ln))
proof(rule ta-seq-consist-nthI)
  fix i ad al v
  assume enat i < llength (llist-of ((map NormalAction ∘ convert-RA) ln :: ('b, 'c) obs-event action list))
  and lnth (llist-of ((map NormalAction ∘ convert-RA) ln :: ('b, 'c) obs-event action list)) i =
  NormalAction (ReadMem ad al v)
  hence ReadMem ad al v ∈ set (convert-RA ln :: ('b, 'c) obs-event list)
  by(auto simp add: in-set-conv-nth)
  hence False by(auto simp add: convert-RA-def)
  thus ∃ b. mrw-values P vs (list-of (ltake (enat i) (llist-of ((map NormalAction ∘ convert-RA) ln))))
  (ad, al) = [(v, b)] ..
qed

```

lemma *ta-hb-consistent-convert-RA*:

```

  ∧ln. ta-hb-consistent P E (llist-of (map (Pair t) ((map NormalAction ∘ convert-RA) ln)))
by(rule ta-hb-consistent-not-ReadI)(auto simp add: convert-RA-def)

```

locale *allocated-multithreaded* =

```

  allocated-heap
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  allocated
  P
  +
  mthr: multithreaded final r convert-RA

```

```

for addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and allocated :: 'heap ⇒ 'addr set

```


and *final* :: 'x ⇒ bool
and *r* :: ('addr, 'thread-id, 'x, 'heap, 'addr, ('addr, 'thread-id) obs-event) semantics (⟨ - ⊢ - - - -> ->
[50,0,0,50] 80)
and *P* :: 'md prog
+
assumes *red-allocated-mono*: $t \vdash (x, m) -ta \rightarrow (x', m') \implies \text{allocated } m \subseteq \text{allocated } m'$
and *red-New-allocatedD*:
 $\llbracket t \vdash (x, m) -ta \rightarrow (x', m'); \text{NewHeapElem } ad \ C\text{Tn} \in \text{set } \{ta\}_o \rrbracket$
 $\implies ad \in \text{allocated } m' \wedge ad \notin \text{allocated } m$
and *red-allocated-NewD*:
 $\llbracket t \vdash (x, m) -ta \rightarrow (x', m'); ad \in \text{allocated } m'; ad \notin \text{allocated } m \rrbracket$
 $\implies \exists C\text{Tn}. \text{NewHeapElem } ad \ C\text{Tn} \in \text{set } \{ta\}_o$
and *red-New-same-addr-same*:
 $\llbracket t \vdash (x, m) -ta \rightarrow (x', m');$
 $\{ta\}_o ! i = \text{NewHeapElem } a \ C\text{Tn}; i < \text{length } \{ta\}_o;$
 $\{ta\}_o ! j = \text{NewHeapElem } a \ C\text{Tn}'; j < \text{length } \{ta\}_o \rrbracket$
 $\implies i = j$

sublocale *allocated-multithreaded* < *heap-multithreaded*
addr2thread-id thread-id2addr
spurious-wakeups
empty-heap allocate typeof-addr heap-read heap-write
final r convert-RA P
by(*unfold-locales*)

context *allocated-multithreaded* **begin**

lemma *redT-allocated-mono*:
assumes *mthr.redT* $\sigma (t, ta) \sigma'$
shows $\text{allocated } (shr \ \sigma) \subseteq \text{allocated } (shr \ \sigma')$
using *assms*
by *cases*(*auto dest: red-allocated-mono del: subsetI*)

lemma *RedT-allocated-mono*:
assumes *mthr.RedT* $\sigma \ ttas \ \sigma'$
shows $\text{allocated } (shr \ \sigma) \subseteq \text{allocated } (shr \ \sigma')$
using *assms unfolding mthr.RedT-def*
by *induct*(*auto dest!: redT-allocated-mono intro: subset-trans del: subsetI*)

lemma *init-fin-allocated-mono*:
 $t \vdash (x, m) -ta \rightarrow i (x', m') \implies \text{allocated } m \subseteq \text{allocated } m'$
by(*cases rule: mthr.init-fin.cases*)(*auto dest: red-allocated-mono*)

lemma *init-fin-redT-allocated-mono*:
assumes *mthr.if.redT* $\sigma (t, ta) \sigma'$
shows $\text{allocated } (shr \ \sigma) \subseteq \text{allocated } (shr \ \sigma')$
using *assms*
by *cases*(*auto dest: init-fin-allocated-mono del: subsetI*)

lemma *init-fin-RedT-allocated-mono*:
assumes *mthr.if.RedT* $\sigma \ ttas \ \sigma'$
shows $\text{allocated } (shr \ \sigma) \subseteq \text{allocated } (shr \ \sigma')$
using *assms unfolding mthr.if.RedT-def*

by *induct(auto dest!: init-fin-redT-allocated-mono intro: subset-trans del: subsetI)*

lemma *init-fin-red-New-allocatedD:*

assumes $t \vdash (x, m) -ta \rightarrow i (x', m')$ *NormalAction* (*NewHeapElem* *ad CTn*) \in *set* $\{ta\}_o$
shows $ad \in$ *allocated* $m' \wedge ad \notin$ *allocated* m

using *assms*

by *cases(auto dest: red-New-allocatedD)*

lemma *init-fin-red-allocated-NewD:*

assumes $t \vdash (x, m) -ta \rightarrow i (x', m')$ $ad \in$ *allocated* $m' \wedge ad \notin$ *allocated* m
shows $\exists CTn.$ *NormalAction* (*NewHeapElem* *ad CTn*) \in *set* $\{ta\}_o$

using *assms*

by(*cases*)(*auto dest!: red-allocated-NewD*)

lemma *init-fin-red-New-same-addr-same:*

assumes $t \vdash (x, m) -ta \rightarrow i (x', m')$
and $\{ta\}_o ! i =$ *NormalAction* (*NewHeapElem* *a CTn*) $i <$ *length* $\{ta\}_o$
and $\{ta\}_o ! j =$ *NormalAction* (*NewHeapElem* *a CTn'*) $j <$ *length* $\{ta\}_o$
shows $i = j$

using *assms*

by *cases(auto dest: red-New-same-addr-same)*

lemma *init-fin-redT-allocated-NewHeapElemD:*

assumes *mthr.if.redT* $s (t, ta) s'$
and $ad \in$ *allocated* (*shr* s')
and $ad \notin$ *allocated* (*shr* s)
shows $\exists CTn.$ *NormalAction* (*NewHeapElem* *ad CTn*) \in *set* $\{ta\}_o$

using *assms*

by(*cases*)(*auto dest: init-fin-red-allocated-NewD*)

lemma *init-fin-RedT-allocated-NewHeapElemD:*

assumes *mthr.if.RedT* $s ttas s'$
and $ad \in$ *allocated* (*shr* s')
and $ad \notin$ *allocated* (*shr* s)
shows $\exists t ta CTn.$ $(t, ta) \in$ *set* $ttas \wedge$ *NormalAction* (*NewHeapElem* *ad CTn*) \in *set* $\{ta\}_o$

using *assms*

proof(*induct rule: mthr.if.RedT-induct'*)

case refl **thus** *?case* **by** *simp*

next

case (*step* $ttas s' t ta s'$) **thus** *?case*

by(*cases* $ad \in$ *allocated* (*shr* s'))(*fastforce* *simp del: split-paired-Ex dest: init-fin-redT-allocated-NewHeapElemD*)

qed

lemma *\mathcal{E} -new-actions-for-unique:*

assumes $E: E \in$ *\mathcal{E} -start* $f P C M$ *vs status*
and $a: a \in$ *new-actions-for* $P E$ *adal*
and $a': a' \in$ *new-actions-for* $P E$ *adal*
shows $a = a'$

using $a a'$

proof(*induct* $a a'$ *rule: wlog-linorder-le*)

case symmetry **thus** *?case* **by** *simp*

next

case (*le* $a a'$)

note $a = \langle a \in$ *new-actions-for* $P E$ *adal* \rangle

```

and  $a' = \langle a' \in \text{new-actions-for } P \ E \ \text{adal} \rangle$ 
and  $a - a' = \langle a \leq a' \rangle$ 
obtain  $ad \ al \ \text{where } \text{adal}: \text{adal} = (ad, al) \ \text{by}(\text{cases } \text{adal})$ 

let  $?init\text{-obs} = \text{lift-start-obs } \text{start-tid } \text{start-heap-obs}$ 
let  $?start\text{-state} = \text{init-fin-lift-state } \text{status } (\text{start-state } f \ P \ C \ M \ vs)$ 

have  $\text{distinct}: \text{distinct } (\text{filter } (\lambda \text{obs}. \exists a \ CTn. \text{obs} = \text{NormalAction } (\text{NewHeapElem } a \ CTn)) \ (\text{map } \text{snd } ?init\text{-obs}))$ 
unfolding  $\text{start-heap-obs-def}$ 
by( $\text{fastforce } \text{intro}: \text{inj-onI } \text{intro}!: \text{distinct-filter } \text{simp } \text{add}: \text{distinct-map } \text{distinct-zipI1 } \text{distinct-initialization-list}$ )

from  $\text{start-addr-allocated}$ 
have  $\text{dom-start-state}: \{a. \exists CTn. \text{NormalAction } (\text{NewHeapElem } a \ CTn) \in \text{snd } \text{'set } ?init\text{-obs}\} \subseteq$ 
 $\text{allocated } (\text{shr } ?start\text{-state})$ 
by( $\text{fastforce } \text{simp } \text{add}: \text{init-fin-lift-state-conv-simps } \text{shr-start-state } \text{dest}: \text{NewHeapElem-start-heap-obs-start-addrD } \text{subsetD}$ )

show  $?case$ 
proof( $\text{cases } a' < \text{length } ?init\text{-obs}$ )
case  $\text{True}$ 
with  $a' \ \text{adal } E \ \text{obtain } t\text{-}a' \ CTn\text{-}a'$ 
where  $CTn\text{-}a': ?init\text{-obs} ! a' = (t\text{-}a', \text{NormalAction } (\text{NewHeapElem } ad \ CTn\text{-}a'))$ 
by( $\text{cases } ?init\text{-obs} ! a'$ )( $\text{fastforce } \text{elim}!: \text{is-new-action.cases } \text{action-loc-aux-cases } \text{simp } \text{add}: \text{action-obs-def } \text{lnth-lappend1 } \text{new-actions-for-def } \text{+}$ )
from  $\text{True } a\text{-}a' \ \text{have } \text{len}\text{-}a: a < \text{length } ?init\text{-obs} \ \text{by } \text{simp}$ 
with  $a \ \text{adal } E \ \text{obtain } t\text{-}a \ CTn\text{-}a$ 
where  $CTn\text{-}a: ?init\text{-obs} ! a = (t\text{-}a, \text{NormalAction } (\text{NewHeapElem } ad \ CTn\text{-}a))$ 
by( $\text{cases } ?init\text{-obs} ! a$ )( $\text{fastforce } \text{elim}!: \text{is-new-action.cases } \text{action-loc-aux-cases } \text{simp } \text{add}: \text{action-obs-def } \text{lnth-lappend1 } \text{new-actions-for-def } \text{+}$ )
from  $CTn\text{-}a \ CTn\text{-}a' \ \text{True } \text{len}\text{-}a$ 
have  $\text{NormalAction } (\text{NewHeapElem } ad \ CTn\text{-}a') \in \text{snd } \text{'set } ?init\text{-obs}$ 
and  $\text{NormalAction } (\text{NewHeapElem } ad \ CTn\text{-}a) \in \text{snd } \text{'set } ?init\text{-obs}$  unfolding  $\text{set-conv-nth}$ 
by( $\text{fastforce } \text{intro}: \text{rev-image-eqI}$ )+
hence [ $\text{simp}$ ]:  $CTn\text{-}a' = CTn\text{-}a$  using  $\text{distinct-start-addr}'$ 
by( $\text{auto } \text{simp } \text{add}: \text{in-set-conv-nth } \text{distinct-conv-nth } \text{start-heap-obs-def } \text{start-addr-def}$ ) blast
from  $\text{distinct-filterD}[OF \ \text{distinct}, \ \text{of } a' \ a \ \text{NormalAction } (\text{NewHeapElem } ad \ CTn\text{-}a)] \ \text{len}\text{-}a \ \text{True}$ 
 $CTn\text{-}a \ CTn\text{-}a'$ 
show  $a = a' \ \text{by } \text{simp}$ 
next
case  $\text{False}$ 
obtain  $n \ \text{where } n: \text{length } ?init\text{-obs} = n \ \text{by } \text{blast}$ 
with  $\text{False} \ \text{have } n \leq a' \ \text{by } \text{simp}$ 

from  $E \ \text{obtain } E'' \ \text{where } E: E = \text{lappend } (\text{llist-of } ?init\text{-obs}) \ E''$ 
and  $E'': E'' \in \text{mthr.if.}\mathcal{E} \ ?start\text{-state} \ \text{by } \text{auto}$ 
from  $E'' \ \text{obtain } E' \ \text{where } E': E'' = \text{lconcat } (\text{lmap } (\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \ \{\{ta\}_o\})) \ E')$ 
and  $\tau\text{Runs}: \text{mthr.if.mthr.Runs } ?start\text{-state } E' \ \text{by}(\text{rule } \text{mthr.if.}\mathcal{E}.\text{cases})$ 

from  $E \ E'' \ a' \ n \ \langle n \leq a' \rangle \ \text{adal} \ \text{have } a': a' - n \in \text{new-actions-for } P \ E'' \ \text{adal}$ 
by( $\text{auto } \text{simp } \text{add}: \text{new-actions-for-def } \text{lnth-lappend2 } \text{action-obs-def } \text{actions-lappend } \text{elim}: \text{actionsE}$ )

from  $a' \ \text{have } a' - n \in \text{actions } E'' \ \text{by}(\text{auto } \text{elim}: \text{new-actionsE})$ 
hence  $\text{enat } (a' - n) < \text{llength } E'' \ \text{by}(\text{rule } \text{actionsE})$ 

```

with \tauRuns **obtain** $a'-m$ $a'-n$ $t-a'$ $ta-a'$
where $E-a'$: $lnth E'' (a' - n) = (t-a', \{\!\{ta-a'\}\!\}_o ! a'-n)$
and $a'-n$: $a'-n < length \{\!\{ta-a'\}\!\}_o$ **and** $a'-m$: $enat a'-m < llength E'$
and $a'-conv$: $a' - n = (\sum i < a'-m. length \{\!\{snd (lnth E' i)\}\!\}_o) + a'-n$
and $E'-a'-m$: $lnth E' a'-m = (t-a', ta-a')$
unfolding E' **by** $(rule\ mthr.if.actions-\mathcal{E}Eaux)$

from a' **have** $is-new-action$ $(action-obs E'' (a' - n))$
and $(ad, al) \in action-loc P E'' (a' - n)$
unfolding $adal$ **by** $(auto\ elim: new-actionsE)$
then obtain CTn'
where $action-obs E'' (a' - n) = NormalAction (NewHeapElem ad CTn')$
by $cases(fastforce)+$
hence $New-ta-a'$: $\{\!\{ta-a'\}\!\}_o ! a'-n = NormalAction (NewHeapElem ad CTn')$
using $E-a' a'-n$ **unfolding** $action-obs-def$ **by** $simp$

show $?thesis$
proof $(cases\ a < n)$
case $True$
with $a\ adal\ E\ n$ **obtain** $t-a\ CTn-a$ **where** $?init-obs ! a = (t-a, NormalAction (NewHeapElem ad CTn-a))$
by $(cases\ ?init-obs ! a)(fastforce\ elim!: is-new-action.cases\ simp\ add: action-obs-def\ lnth-lappend1\ new-actions-for-def)+$

with $subsetD[OF\ dom-start-state, of\ ad]\ n\ True$
have $a-shr-\sigma$: $ad \in allocated (shr\ ?start-state)$
by $(fastforce\ simp\ add: set-conv-nth\ intro: rev-image-eqI)$

have $E'-unfold'$: $E' = lappend (ltake (enat a'-m) E') (LCons (lnth E' a'-m) (ldropn (Suc a'-m) E'))$
unfolding $ldropn-Suc-conv-ldropn[OF\ a'-m]$ **by** $simp$
hence $mthr.if.mthr.Runs\ ?start-state (lappend (ltake (enat a'-m) E') (LCons (lnth E' a'-m) (ldropn (Suc a'-m) E')))$
using \tauRuns **by** $simp$

then obtain σ'
where $\sigma-\sigma'$: $mthr.if.mthr.Trsys\ ?start-state (list-of (ltake (enat a'-m) E'))\ \sigma'$
and \tauRuns' : $mthr.if.mthr.Runs\ \sigma' (LCons (lnth E' a'-m) (ldropn (Suc a'-m) E'))$
by $(rule\ mthr.if.mthr.Runs-lappendE)\ simp$
from \tauRuns' **obtain** σ''
where $red-a'$: $mthr.if.redT\ \sigma' (t-a', ta-a')\ \sigma''$
and \tauRuns'' : $mthr.if.mthr.Runs\ \sigma'' (ldropn (Suc a'-m) E')$
unfolding $E'-a'-m$ **by** $cases$
from $New-ta-a' a'-n$ **have** $NormalAction (NewHeapElem ad CTn') \in set \{\!\{ta-a'\}\!\}_o$
unfolding $in-set-conv-nth$ **by** $blast$
with $red-a'$ **obtain** $x-a'\ x'-a'\ m'-a'$
where $red'-a'$: $mthr.init-fin\ t-a' (x-a', shr\ \sigma')\ ta-a' (x'-a', m'-a')$
and σ''' : $redT-upd\ \sigma'\ t-a'\ ta-a'\ x'-a'\ m'-a'\ \sigma''$
and $ts-t-a'$: $thr\ \sigma'\ t-a' = \lfloor(x-a', no-wait-locks)\rfloor$
by $cases\ auto$
from $red'-a' \langle NormalAction (NewHeapElem ad CTn') \in set \{\!\{ta-a'\}\!\}_o \rangle$
obtain $ta'-a'\ X-a'\ X'-a'$
where $x-a'$: $x-a' = (Running, X-a')$
and $x'-a'$: $x'-a' = (Running, X'-a')$

and $ta-a'$: $ta-a' = \text{convert-TA-initial } (\text{convert-obs-initial } ta'-a')$
 and $red''-a'$: $t-a' \vdash \langle X-a', \text{shr } \sigma' \rangle - ta'-a' \rightarrow \langle X'-a', m'-a' \rangle$
 by *cases fastforce+*

from $ta-a'$ $New-ta-a'$ $a'-n$ **have** $New-ta'-a'$: $\{ta'-a'\}_o ! a'-n = \text{NewHeapElem ad } CTn'$
 and $a'-n'$: $a'-n < \text{length } \{ta'-a'\}_o$ **by** *auto*
 hence $NewHeapElem ad CTn' \in \text{set } \{ta'-a'\}_o$ **unfolding** *in-set-conv-nth* **by** *blast*
 with $red''-a'$ **have** $allocated-ad'$: $ad \notin \text{allocated } (\text{shr } \sigma')$
 by(*auto dest: red-New-allocatedD*)

have $allocated (\text{shr } ?\text{start-state}) \subseteq \text{allocated } (\text{shr } \sigma')$
 using $\sigma-\sigma'$ **unfolding** *mthr.if.RedT-def[symmetric]* **by**(*rule init-fin-RedT-allocated-mono*)
 hence *False* **using** $allocated-ad'$ $a\text{-shr-}\sigma$ **by** *blast*
 thus *?thesis ..*

next

case *False*
 hence $n \leq a$ **by** *simp*

from $E E'' a n \langle n \leq a \rangle$ **adal** **have** $a: a - n \in \text{new-actions-for } P E''$ **adal**
 by(*auto simp add: new-actions-for-def lnth-lappend2 action-obs-def actions-lappend elim: actionsE*)

from a **have** $a - n \in \text{actions } E''$ **by**(*auto elim: new-actionsE*)
 hence $\text{enat } (a - n) < \text{llength } E''$ **by**(*rule actionsE*)

with τRuns **obtain** $a-m$ $a-n$ $t-a$ $ta-a$
 where $E-a$: $\text{lnth } E'' (a - n) = (t-a, \{ta-a\}_o ! a-n)$
 and $a-n$: $a-n < \text{length } \{ta-a\}_o$ **and** $a-m$: $\text{enat } a-m < \text{llength } E'$
 and $a\text{-conv}$: $a - n = (\sum i < a-m. \text{length } \{snd (\text{lnth } E' i)\}_o) + a-n$
 and $E'-a-m$: $\text{lnth } E' a-m = (t-a, ta-a)$
unfolding E' **by**(*rule mthr.if.actions-E-aux*)

from a **have** $\text{is-new-action } (\text{action-obs } E'' (a - n))$
 and $(ad, al) \in \text{action-loc } P E'' (a - n)$
unfolding $adal$ **by**(*auto elim: new-actionsE*)
then obtain CTn **where** $\text{action-obs } E'' (a - n) = \text{NormalAction } (\text{NewHeapElem ad } CTn)$
 by *cases(fastforce)+*
 hence $New-ta-a$: $\{ta-a\}_o ! a-n = \text{NormalAction } (\text{NewHeapElem ad } CTn)$
 using $E-a$ $a-n$ **unfolding** *action-obs-def* **by** *simp*

let $?E' = \text{ldropn } (\text{Suc } a-m) E'$

have $E'\text{-unfold}$: $E' = \text{lappend } (\text{ltake } (\text{enat } a-m) E') (\text{LCons } (\text{lnth } E' a-m) ?E')$
unfolding *ldropn-Suc-conv-ldropn[OF a-m]* **by** *simp*
hence *mthr.if.mthr.Runs ?start-state* $(\text{lappend } (\text{ltake } (\text{enat } a-m) E') (\text{LCons } (\text{lnth } E' a-m) ?E'))$
 using τRuns **by** *simp*
then obtain σ' **where** $\sigma-\sigma'$: $\text{mthr.if.mthr.Trsys } ?\text{start-state } (\text{list-of } (\text{ltake } (\text{enat } a-m) E')) \sigma'$
 and $\tau\text{Runs}'$: $\text{mthr.if.mthr.Runs } \sigma' (\text{LCons } (\text{lnth } E' a-m) ?E')$
 by(*rule mthr.if.mthr.Runs-lappendE*) *simp*
 from $\tau\text{Runs}'$ **obtain** σ''
 where $red-a$: $\text{mthr.if.redT } \sigma' (t-a, ta-a) \sigma''$
 and $\tau\text{Runs}''$: $\text{mthr.if.mthr.Runs } \sigma'' ?E'$
unfolding $E'-a-m$ **by** *cases*
 from $New-ta-a$ $a-n$ **have** $\text{NormalAction } (\text{NewHeapElem ad } CTn) \in \text{set } \{ta-a\}_o$

unfolding *in-set-conv-nth* **by** *blast*
with *red-a* **obtain** $x\text{-}a\ x'\text{-}a\ m'\text{-}a$
where *red'-a*: $mthr.\text{init}\text{-}fin\ t\text{-}a\ (x\text{-}a,\ shr\ \sigma')\ ta\text{-}a\ (x'\text{-}a,\ m'\text{-}a)$
and σ'' : $redT\text{-}upd\ \sigma'\ t\text{-}a\ ta\text{-}a\ x'\text{-}a\ m'\text{-}a\ \sigma''$
and *ts-t-a*: $thr\ \sigma'\ t\text{-}a = [(x\text{-}a,\ no\text{-}wait\text{-}locks)]$
by *cases auto*
from *red'-a* $\langle NormalAction\ (NewHeapElem\ ad\ CTn) \in set\ \{ta\text{-}a\}_o \rangle$
obtain $ta'\text{-}a\ X\text{-}a\ X'\text{-}a$
where $x\text{-}a$: $x\text{-}a = (Running,\ X\text{-}a)$
and $x'\text{-}a$: $x'\text{-}a = (Running,\ X'\text{-}a)$
and $ta\text{-}a$: $ta\text{-}a = convert\text{-}TA\text{-}initial\ (convert\text{-}obs\text{-}initial\ ta'\text{-}a)$
and $red''\text{-}a$: $t\text{-}a \vdash (X\text{-}a,\ shr\ \sigma') - ta'\text{-}a \rightarrow (X'\text{-}a,\ m'\text{-}a)$
by *cases fastforce+*
from $ta\text{-}a\ New\text{-}ta\text{-}a\ a\text{-}n$ **have** $New\text{-}ta'\text{-}a$: $\{ta'\text{-}a\}_o ! a\text{-}n = NewHeapElem\ ad\ CTn$
and $a\text{-}n'$: $a\text{-}n < length\ \{ta'\text{-}a\}_o$ **by** *auto*
hence $NewHeapElem\ ad\ CTn \in set\ \{ta'\text{-}a\}_o$ **unfolding** *in-set-conv-nth* **by** *blast*
with $red''\text{-}a$ **have** $allocated\text{-}m'\text{-}a\text{-}ad$: $ad \in allocated\ m'\text{-}a$
by (*auto dest: red-New-allocatedD*)

have $a\text{-}m \leq a'\text{-}m$
proof(*rule ccontr*)
assume $\neg ?thesis$
hence $a'\text{-}m < a\text{-}m$ **by** *simp*
hence $(\sum i < a\text{-}m.\ length\ \{snd\ (lnth\ E'\ i)\}_o) = (\sum i < a'\text{-}m.\ length\ \{snd\ (lnth\ E'\ i)\}_o) + (\sum i$
 $= a'\text{-}m..<a\text{-}m.\ length\ \{snd\ (lnth\ E'\ i)\}_o)$
by (*simp add: sum-upto-add-nat*)
hence $a' - n < a - n$ **using** $\langle a'\text{-}m < a\text{-}m \rangle$ $a'\text{-}n\ E'\text{-}a'\text{-}m$ **unfolding** *a-conv a'-conv*
by (*subst (asm) sum.atLeast-Suc-lessThan*) *simp-all*
with $a\text{-}a'$ **show** *False* **by** *simp*
qed

have $a'\text{-}less$: $a' - n < (a - n) - a\text{-}n + length\ \{ta\text{-}a\}_o$
proof(*rule ccontr*)
assume $\neg ?thesis$
hence $a'\text{-}greater$: $(a - n) - a\text{-}n + length\ \{ta\text{-}a\}_o \leq a' - n$ **by** *simp*

have $a\text{-}m < a'\text{-}m$
proof(*rule ccontr*)
assume $\neg ?thesis$
with $\langle a\text{-}m \leq a'\text{-}m \rangle$ **have** $a\text{-}m = a'\text{-}m$ **by** *simp*
with $a'\text{-}greater\ a\text{-}n\ a'\text{-}n\ E'\text{-}a'\text{-}m\ E'\text{-}a\text{-}m$ **show** *False*
unfolding *a-conv a'-conv* **by** *simp*
qed

hence $a'\text{-}m\text{-}a\text{-}m$: $enat\ (a'\text{-}m - Suc\ a\text{-}m) < llength\ ?E'$ **using** $a'\text{-}m$
by (*cases llength E'*) *simp-all*
from $\langle a\text{-}m < a'\text{-}m \rangle$ $a'\text{-}m\ E'\text{-}a'\text{-}m$
have $E'\text{-}a'\text{-}m'$: $lnth\ ?E'\ (a'\text{-}m - Suc\ a\text{-}m) = (t\text{-}a',\ ta\text{-}a')$ **by** *simp*

have $E'\text{-}unfold'$: $?E' = lappend\ (ltake\ (enat\ (a'\text{-}m - Suc\ a\text{-}m))\ ?E')\ (LCons\ (lnth\ ?E'\ (a'\text{-}m - Suc\ a\text{-}m))\ (ldropn\ (Suc\ (a'\text{-}m - Suc\ a\text{-}m))\ ?E'))$
unfolding *ldropn-Suc-conv-ldropn[OF a'-m-a-m] lappend-ltake-enat-ldropn ..*
hence $mthr.\text{if}.\text{mthr}.\text{Runs}\ \sigma''\ (lappend\ (ltake\ (enat\ (a'\text{-}m - Suc\ a\text{-}m))\ ?E')\ (LCons\ (lnth\ ?E'\ (a'\text{-}m - Suc\ a\text{-}m))\ (ldropn\ (Suc\ (a'\text{-}m - Suc\ a\text{-}m))\ ?E')))$
using $\tau\text{Runs}''$ **by** *simp*

then obtain σ'''
where $\sigma''\text{-}\sigma''':$ *mthr.if.mthr.Trsys* σ'' (*list-of* (*ltake* (*enat* ($a'-m - \text{Suc } a-m$)) $?E'$)) σ'''
and $\tau\text{Runs}''':$ *mthr.if.mthr.Runs* σ''' (*LCons* (*lnth* $?E'$ ($a'-m - \text{Suc } a-m$)) (*ldropn* (*Suc* ($a'-m - \text{Suc } a-m$)) $?E'$))
by(*rule mthr.if.mthr.Runs-lappendE*) *simp*
from $\tau\text{Runs}'''$ **obtain** σ''''
where $\text{red-}a':$ *mthr.if.redT* σ'''' ($t-a'$, $ta-a'$) σ''''
and $\tau\text{Runs}'''':$ *mthr.if.mthr.Runs* σ'''' (*ldropn* (*Suc* ($a'-m - \text{Suc } a-m$)) $?E'$)
unfolding $E'-a'-m'$ **by** *cases*
from *New-ta-a'* $a'-n$ **have** *NormalAction* (*NewHeapElem ad CTn'*) \in *set* $\{\{ta-a'\}_o\}$
unfolding *in-set-conv-nth* **by** *blast*
with $\text{red-}a'$ **obtain** $x-a'$ $x'-a'$ $m'-a'$
where $\text{red}'-a':$ *mthr.init-fin* $t-a'$ ($x-a'$, *shr* σ'''') $ta-a'$ ($x'-a'$, $m'-a'$)
and $\sigma'''''':$ *redT-upd* σ'''' $t-a'$ $ta-a'$ $x'-a'$ $m'-a'$ σ''''''
and $ts-t-a':$ *thr* σ'''' $t-a' = \lfloor(x-a', \text{no-wait-locks})\rfloor$
by *cases auto*
from $\text{red}'-a' \langle \text{NormalAction} (\text{NewHeapElem ad CTn}') \in \text{set } \{\{ta-a'\}_o \rangle$
obtain $ta'-a'$ $X-a'$ $X'-a'$
where $x-a':$ $x-a' = (\text{Running}, X-a')$
and $x'-a':$ $x'-a' = (\text{Running}, X'-a')$
and $ta-a':$ $ta-a' = \text{convert-TA-initial} (\text{convert-obs-initial } ta'-a')$
and $\text{red}''-a':$ $t-a' \vdash (X-a', \text{shr } \sigma''''') -ta'-a' \rightarrow (X'-a', m'-a')$
by *cases fastforce+*
from $ta-a'$ *New-ta-a'* $a'-n$ **have** *New-ta'-a':* $\{\{ta'-a'\}_o\} ! a'-n = \text{NewHeapElem ad CTn}'$
and $a'-n':$ $a'-n < \text{length } \{\{ta'-a'\}_o\}$ **by** *auto*
hence *NewHeapElem ad CTn'* \in *set* $\{\{ta'-a'\}_o\}$ **unfolding** *in-set-conv-nth* **by** *blast*
with $\text{red}''-a'$ **have** *allocated-ad':* $ad \notin \text{allocated} (\text{shr } \sigma''''')$
by(*auto dest: red-New-allocatedD*)

have *allocated* $m'-a = \text{allocated} (\text{shr } \sigma''')$ **using** σ'''' **by** *auto*
also have $\dots \subseteq \text{allocated} (\text{shr } \sigma''''')$
using $\sigma''\text{-}\sigma''''$ **unfolding** *mthr.if.RedT-def[symmetric]* **by**(*rule init-fin-RedT-allocated-mono*)
finally have $ad \in \text{allocated} (\text{shr } \sigma''''')$ **using** *allocated-m'-a-ad* **by** *blast*
with *allocated-ad'* **show** *False* **by** *contradiction*
qed

from $\langle a-m \leq a'-m \rangle$ **have** [*simp*]: $a-m = a'-m$
proof(*rule le-antisym*)
show $a'-m \leq a-m$
proof(*rule ccontr*)
assume $\neg ?thesis$
hence $a-m < a'-m$ **by** *simp*
hence $(\sum i < a'-m. \text{length } \{\{\text{snd} (\text{lnth } E' i)\}_o\}) = (\sum i < a-m. \text{length } \{\{\text{snd} (\text{lnth } E' i)\}_o\}) + (\sum i = a-m.. < a'-m. \text{length } \{\{\text{snd} (\text{lnth } E' i)\}_o\})$
by(*simp add: sum-upto-add-nat*)
with $a'-\text{less } \langle a-m < a'-m \rangle$ $E'-a-m$ $a-n$ $a'-n$ **show** *False*
unfolding $a'-\text{conv}$ $a-\text{conv}$ **by**(*subst (asm) sum.atLeast-Suc-lessThan*) *simp-all*
qed
qed
with $E'-a-m$ $E'-a'-m$ **have** [*simp*]: $t-a' = t-a$ $ta-a' = ta-a$ **by** *simp-all*
from *New-ta-a'* $a'-n$ $ta-a$ **have** $a'-n':$ $a'-n < \text{length } \{\{ta'-a'\}_o\}$
and *New-ta'-a':* $\{\{ta'-a'\}_o\} ! a'-n = \text{NewHeapElem ad CTn}'$ **by** *auto*
with $\text{red}''-a$ *New-ta'-a* $a-n'$ **have** $a'-n = a-n$
by(*auto dest: red-New-same-addr-same*)

```

    with ⟨a-m = a'-m⟩ have a - n = a' - n unfolding a-conv a'-conv by simp
    thus ?thesis using ⟨n ≤ a⟩ ⟨n ≤ a'⟩ by simp
  qed
  qed
  qed
end

```

Knowledge of addresses of a multithreaded state

```

fun ka-Val :: 'addr val ⇒ 'addr set
where
  ka-Val (Addr a) = {a}
| ka-Val - = {}

```

```

fun new-obs-addr :: ('addr, 'thread-id) obs-event ⇒ 'addr set
where
  new-obs-addr (ReadMem ad al (Addr ad')) = {ad'}
| new-obs-addr (NewHeapElem ad hT) = {ad}
| new-obs-addr - = {}

```

```

lemma new-obs-addr-cases[consumes 1, case-names ReadMem NewHeapElem, cases set]:
  assumes ad ∈ new-obs-addr ob
  obtains ad' al where ob = ReadMem ad' al (Addr ad)
  | CTn where ob = NewHeapElem ad CTn
using assms
by(cases ob rule: new-obs-addr.cases) auto

```

```

definition new-obs-addr :: ('addr, 'thread-id) obs-event list ⇒ 'addr set
where
  new-obs-addr obs = ⋃(new-obs-addr ' set obs)

```

```

fun new-obs-addr-if :: ('addr, 'thread-id) obs-event action ⇒ 'addr set
where
  new-obs-addr-if (NormalAction a) = new-obs-addr a
| new-obs-addr-if - = {}

```

```

definition new-obs-addr-if :: ('addr, 'thread-id) obs-event action list ⇒ 'addr set
where
  new-obs-addr-if obs = ⋃(new-obs-addr-if ' set obs)

```

```

lemma ka-Val-subset-new-obs-Addr-ReadMem:
  ka-Val v ⊆ new-obs-addr (ReadMem ad al v)
by(cases v) simp-all

```

```

lemma typeof-ka: typeof v ≠ None ⇒ ka-Val v = {}
by(cases v) simp-all

```

```

lemma ka-Val-undefined-value [simp]:
  ka-Val undefined-value = {}
apply(cases undefined-value :: 'a val)
apply(bestsimp simp add: undefined-value-not-Addr dest: subst)+
done

```

```

locale known-addr-base =

```



```

fixes known-addr :: 't ⇒ 'x ⇒ 'addr set
begin

definition known-addr-thr :: ('l, 't, 'x) thread-info ⇒ 'addr set
where known-addr-thr ts = (∪ t ∈ dom ts. known-addr t (fst (the (ts t))))

definition known-addr-state :: ('l, 't, 'x, 'm, 'w) state ⇒ 'addr set
where known-addr-state s = known-addr-thr (thr s)

lemma known-addr-state-simps [simp]:
  known-addr-state (ls, (ts, m), ws) = known-addr-thr ts
by(simp add: known-addr-state-def)

lemma known-addr-thr-cases[consumes 1, case-names known-addr, cases set: known-addr-thr]:
  assumes ad ∈ known-addr-thr ts
  and ∧ t x ln. [ ts t = [(x, ln)]; ad ∈ known-addr t x ] ⇒ thesis
  shows thesis
using assms
by(auto simp add: known-addr-thr-def ran-def)

lemma known-addr-stateI:
  ∧ ln. [ ad ∈ known-addr t x; thr s t = [(x, ln)] ] ⇒ ad ∈ known-addr-state s
by(fastforce simp add: known-addr-state-def known-addr-thr-def intro: rev-bxI)

fun known-addr-if :: 't ⇒ status × 'x ⇒ 'addr set
where known-addr-if t (s, x) = known-addr t x

end

locale if-known-addr-base =
  known-addr-base known-addr
  +
  multithreaded-base final r convert-RA
  for known-addr :: 't ⇒ 'x ⇒ 'addr set
  and final :: 'x ⇒ bool
  and r :: ('addr, 't, 'x, 'heap, 'addr, 'obs) semantics (⟨-| - - -> -⟩ [50,0,0,50] 80)
  and convert-RA :: 'addr released-locks ⇒ 'obs list

sublocale if-known-addr-base < if: known-addr-base known-addr-if .

locale known-addr =
  allocated-multithreaded
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  allocated
  final r
  P
  +
  if-known-addr-base known-addr final r convert-RA

for addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool

```

```

and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr ⇒ htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and allocated :: 'heap ⇒ 'addr set
and known-addrs :: 'thread-id ⇒ 'x ⇒ 'addr set
and final :: 'x ⇒ bool
and r :: ('addr, 'thread-id, 'x, 'heap, 'addr, ('addr, 'thread-id) obs-event) semantics (⟦ - ⟧ - - - -> -)
[50,0,0,50] 80)
and P :: 'md prog
+
assumes red-known-addrs-new:
  t ⊢ ⟨x, m⟩ -ta→ ⟨x', m'⟩
  ⇒ known-addrs t x' ⊆ known-addrs t x ∪ new-obs-addrs {ta}_o
and red-known-addrs-new-thread:
  [ t ⊢ ⟨x, m⟩ -ta→ ⟨x', m'⟩; NewThread t' x'' m'' ∈ set {ta}_t ]
  ⇒ known-addrs t' x'' ⊆ known-addrs t x
and red-read-knows-addr:
  [ t ⊢ ⟨x, m⟩ -ta→ ⟨x', m'⟩; ReadMem ad al v ∈ set {ta}_o ]
  ⇒ ad ∈ known-addrs t x
and red-write-knows-addr:
  [ t ⊢ ⟨x, m⟩ -ta→ ⟨x', m'⟩; {ta}_o ! n = WriteMem ad al (Addr ad'); n < length {ta}_o ]
  ⇒ ad' ∈ known-addrs t x ∨ ad' ∈ new-obs-addrs (take n {ta}_o)
  — second possibility necessary for heap-clone
begin

notation mthr.redT-syntax1 (⟦ - ⟧ - - - -> -) [50,0,0,50] 80)

lemma if-red-known-addrs-new:
  assumes t ⊢ (x, m) -ta→i (x', m')
  shows known-addrs-if t x' ⊆ known-addrs-if t x ∪ new-obs-addrs-if {ta}_o
using assms
by cases(auto dest!: red-known-addrs-new simp add: new-obs-addrs-if-def new-obs-addrs-def)

lemma if-red-known-addrs-new-thread:
  assumes t ⊢ (x, m) -ta→i (x', m') NewThread t' x'' m'' ∈ set {ta}_t
  shows known-addrs-if t' x'' ⊆ known-addrs-if t x
using assms
by cases(fastforce dest: red-known-addrs-new-thread)+

lemma if-red-read-knows-addr:
  assumes t ⊢ (x, m) -ta→i (x', m') NormalAction (ReadMem ad al v) ∈ set {ta}_o
  shows ad ∈ known-addrs-if t x
using assms
by cases(fastforce dest: red-read-knows-addr)+

lemma if-red-write-knows-addr:
  assumes t ⊢ (x, m) -ta→i (x', m')
  and {ta}_o ! n = NormalAction (WriteMem ad al (Addr ad')) n < length {ta}_o
  shows ad' ∈ known-addrs-if t x ∨ ad' ∈ new-obs-addrs-if (take n {ta}_o)
using assms
by cases(auto dest: red-write-knows-addr simp add: new-obs-addrs-if-def new-obs-addrs-def take-map)

```

```

lemma if-redT-known-addr-new:
  assumes redT: mthr.if.redT s (t, ta) s'
  shows if.known-addr-state s'  $\subseteq$  if.known-addr-state s  $\cup$  new-obs-addr-if  $\{ta\}_o$ 
using redT
proof(cases)
  case redT-acquire thus ?thesis
    by(cases s)(fastforce simp add: if.known-addr-thr-def split: if-split-asm intro: rev-bexI)
next
  case (redT-normal x x' m)
  note red =  $\langle t \vdash (x, shr\ s) -ta \rightarrow i(x', m) \rangle$ 
  show ?thesis
  proof
    fix ad
    assume ad  $\in$  if.known-addr-state s'
    hence ad  $\in$  if.known-addr-thr (thr s') by(simp add: if.known-addr-state-def)
    then obtain t' x'' ln'' where ts't': thr s' t' =  $\lfloor(x'', ln'')\rfloor$ 
      and ad: ad  $\in$  known-addr-if t' x''
      by(rule if.known-addr-thr-cases)
    show ad  $\in$  if.known-addr-state s  $\cup$  new-obs-addr-if  $\{ta\}_o$ 
    proof(cases thr s t')
      case None
      with redT-normal  $\langle thr\ s'\ t' = \lfloor(x'', ln'')\rfloor \rangle$ 
      obtain m'' where NewThread t' x'' m''  $\in$  set  $\{ta\}_t$ 
        by(fastforce dest: redT-updTs-new-thread split: if-split-asm)
      with red have known-addr-if t' x''  $\subseteq$  known-addr-if t x by(rule if-red-known-addr-new-thread)
      also have ...  $\subseteq$  known-addr-if t x  $\cup$  new-obs-addr-if  $\{ta\}_o$  by simp
      finally have ad  $\in$  known-addr-if t x  $\cup$  new-obs-addr-if  $\{ta\}_o$  using ad by blast
      thus ?thesis using  $\langle thr\ s\ t = \lfloor(x, no-wait-locks)\rfloor \rangle$  by(blast intro: if.known-addr-stateI)
    next
      case (Some xln)
      show ?thesis
      proof(cases t = t')
        case True
        with redT-normal ts't' if-red-known-addr-new[OF red] ad
        have ad  $\in$  known-addr-if t x  $\cup$  new-obs-addr-if  $\{ta\}_o$  by auto
        thus ?thesis using  $\langle thr\ s\ t = \lfloor(x, no-wait-locks)\rfloor \rangle$  by(blast intro: if.known-addr-stateI)
      next
        case False
        with ts't' redT-normal ad Some show ?thesis
        by(fastforce dest: redT-updTs-Some[where ts=thr s and t=t'] intro: if.known-addr-stateI)
      qed
    qed
  qed
qed

```

```

lemma if-redT-read-knows-addr:
  assumes redT: mthr.if.redT s (t, ta) s'
  and read: NormalAction (ReadMem ad al v)  $\in$  set  $\{ta\}_o$ 
  shows ad  $\in$  if.known-addr-state s
using redT
proof(cases)
  case redT-acquire thus ?thesis using read by auto
next
  case (redT-normal x x' m')

```

with *if-red-read-knows-addr*[$OF \langle t \vdash (x, shr\ s) -ta \rightarrow i(x', m') \rangle read$]
show *?thesis*
by(*auto simp add: if-known-addr-state-def if-known-addr-thr-def intro: beXI[where x=t]*)
qed

lemma *init-fin-redT-known-addr-subset:*

assumes *mthr.if.redT s (t, ta) s'*
shows *if-known-addr-state s' \subseteq if-known-addr-state s \cup known-addr-if t (fst (the (thr s' t)))*
using *assms*
apply(*cases*)
apply(*rule subsetI*)
apply(*clarsimp simp add: if-known-addr-thr-def split: if-split-asm*)
apply(*rename-tac status x status' x' m' a ws' t'' status'' x'' ln''*)
apply(*case-tac thr s t''*)
apply(*drule (2) redT-updTs-new-thread*)
apply *clarsimp*
apply(*drule (1) if-red-known-addr-new-thread*)
apply *simp*
apply(*drule (1) subsetD*)
apply(*rule-tac x=(status, x) in if-known-addr-stateI*)
apply(*simp*)
apply *simp*
apply(*frule-tac t=t'' in redT-updTs-Some, assumption*)
apply *clarsimp*
apply(*rule-tac x=(status'', x'') in if-known-addr-stateI*)
apply *simp*
apply *simp*
apply(*auto simp add: if-known-addr-state-def if-known-addr-thr-def split: if-split-asm*)
done

lemma *w-values-no-write-unchanged:*

assumes *no-write: $\bigwedge w. \llbracket w \in set\ obs; is-write-action\ w; adal \in action-loc-aux\ P\ w \rrbracket \implies False$*
shows *w-values P vs obs adal = vs adal*
using *assms*
proof(*induct obs arbitrary: vs*)
case *Nil show ?case by simp*
next
case (*Cons ob obs*)
from *Cons.prem[ob]*
have *w-value P vs ob adal = vs adal*
by(*cases adal*)(*cases ob rule: w-value-cases, auto simp add: addr-locs-def split: htype.split-asm, blast+*)
moreover
have *w-values P (w-value P vs ob) obs adal = w-value P vs ob adal*
proof(*rule Cons.hyps*)
fix *w*
assume *w \in set obs is-write-action w adal \in action-loc-aux P w*
with *Cons.prem[ob w] $\langle w-value\ P\ vs\ ob\ adal = vs\ adal \rangle$*
show *False by simp*
qed
ultimately show ?case by simp
qed

lemma *redT-non-speculative-known-addr-allocated:*

assumes *red*: $mthr.if.redT\ s\ (t, ta)\ s'$
and *tasc*: $non\text{-}speculative\ P\ vs\ (l\!l\!i\!s\text{-}of\ \{\!|ta\!\}_o)$
and *ka*: $if.known\text{-}addrs\text{-}state\ s\ \subseteq\ allocated\ (shr\ s)$
and *vs*: $w\text{-}addrs\ vs\ \subseteq\ allocated\ (shr\ s)$
shows $if.known\text{-}addrs\text{-}state\ s' \subseteq allocated\ (shr\ s')$ (**is** *?thesis1*)
and $w\text{-}addrs\ (w\text{-}values\ P\ vs\ \{\!|ta\!\}_o) \subseteq allocated\ (shr\ s')$ (**is** *?thesis2*)
proof –
have *?thesis1* \wedge *?thesis2* **using** *red*
proof(*cases*)
case (*redT-acquire* $x\ ln\ n$)
hence $if.known\text{-}addrs\text{-}state\ s' = if.known\text{-}addrs\text{-}state\ s$
by(*auto* 4 4 *simp* *add*: *if.known-addrs-state-def* *if.known-addrs-thr-def* *split*: *if-split-asm* *dest*:
bspec)
also **note** *ka*
also **from** *redT-acquire* **have** $shr\ s = shr\ s'$ **by** *simp*
finally **have** $if.known\text{-}addrs\text{-}state\ s' \subseteq allocated\ (shr\ s')$.
moreover **have** $w\text{-}values\ P\ vs\ \{\!|ta\!\}_o = vs$ **using** *redT-acquire*
by(*fastforce* *intro!*: *w-values-no-write-unchanged* *del*: *equalityI* *dest*: *convert-RA-not-write*)
ultimately **show** *?thesis* **using** *vs* **by**(*simp* *add*: $\langle shr\ s = shr\ s' \rangle$)
next
case (*redT-normal* $x\ x'\ m'$)
note $red = \langle t \vdash (x, shr\ s) \text{-}ta \rightarrow i(x', m') \rangle$
and $tst = \langle thr\ s\ t = [(x, no\text{-}wait\text{-}locks)] \rangle$
have $allocated\text{-}subset: allocated\ (shr\ s) \subseteq allocated\ (shr\ s')$
using $\langle mthr.if.redT\ s\ (t, ta)\ s' \rangle$ **by**(*rule* *init-fin-redT-allocated-mono*)
with *vs* **have** $vs': w\text{-}addrs\ vs \subseteq allocated\ (shr\ s')$ **by** *blast*
{ **fix** *obs* *obs'*
assume $\{\!|ta\!\}_o = obs\ @\ obs'$
moreover **with** *tasc* **have** $non\text{-}speculative\ P\ vs\ (l\!l\!i\!s\text{-}of\ obs)$
by(*simp* *add*: *lappend-llist-of-llist-of[symmetric]* *non-speculative-lappend* *del*: *lappend-llist-of-llist-of*)
ultimately **have** $w\text{-}addrs\ (w\text{-}values\ P\ vs\ obs) \cup new\text{-}obs\text{-}addrs\text{-}if\ obs \subseteq allocated\ (shr\ s')$
(is *?concl* *obs*)
proof(*induct* *obs* *arbitrary*: *obs'* *rule*: *rev-induct*)
case *Nil* **thus** *?case* **using** *vs'* **by**(*simp* *add*: *new-obs-addrs-if-def*)
next
case (*snoc* *ob* *obs*)
note $ta = \langle \{\!|ta\!\}_o = (obs\ @\ [ob])\ @\ obs' \rangle$
note $tasc = \langle non\text{-}speculative\ P\ vs\ (l\!l\!i\!s\text{-}of\ (obs\ @\ [ob])) \rangle$
from *snoc* **have** *IH*: *?concl* *obs*
by(*simp* *add*: *lappend-llist-of-llist-of[symmetric]* *non-speculative-lappend* *del*: *lappend-llist-of-llist-of*)
hence *?concl* $(obs\ @\ [ob])$
proof(*cases* *ob* *rule*: *mrw-value-cases*)
case (*1* *ad'* *al* *v*)
note $ob = \langle ob = NormalAction\ (WriteMem\ ad'\ al\ v) \rangle$
with *ta* **have** $Write: \{\!|ta\!\}_o ! length\ obs = NormalAction\ (WriteMem\ ad'\ al\ v)$ **by** *simp*
show *?thesis*
proof
fix *ad''*
assume $ad'' \in w\text{-}addrs\ (w\text{-}values\ P\ vs\ (obs\ @\ [ob])) \cup new\text{-}obs\text{-}addrs\text{-}if\ (obs\ @\ [ob])$
hence $ad'' \in w\text{-}addrs\ (w\text{-}values\ P\ vs\ obs) \cup new\text{-}obs\text{-}addrs\text{-}if\ obs \vee v = Addr\ ad''$
by(*auto* *simp* *add*: *ob* *w-addrs-def* *ran-def* *new-obs-addrs-if-def* *split*: *if-split-asm*)
thus $ad'' \in allocated\ (shr\ s')$
proof
assume $ad'' \in w\text{-}addrs\ (w\text{-}values\ P\ vs\ obs) \cup new\text{-}obs\text{-}addrs\text{-}if\ obs$

```

    also note IH finally show ?thesis .
  next
    assume v: v = Addr ad''
    with Write have  $\{ta\}_o ! \text{length } \text{obs} = \text{NormalAction } (\text{WriteMem } ad' \text{ al } (\text{Addr } ad''))$  by
simp
    with red have  $ad'' \in \text{known-addr-if } t \ x \vee ad'' \in \text{new-obs-addr-if } (\text{take } (\text{length } \text{obs})$ 
 $\{ta\}_o)$ 
    by(rule if-red-write-knows-addr)(simp add: ta)
    thus ?thesis
  proof
    assume  $ad'' \in \text{known-addr-if } t \ x$ 
    hence  $ad'' \in \text{if.known-addr-state } s$  using tst by(rule if.known-addr-stateI)
    with ka allocated-subset show ?thesis by blast
  next
    assume  $ad'' \in \text{new-obs-addr-if } (\text{take } (\text{length } \text{obs}) \{ta\}_o)$ 
    with ta have  $ad'' \in \text{new-obs-addr-if } \text{obs}$  by simp
    with IH show ?thesis by blast
  qed
  qed
  qed
next
  case (2 ad hT)

  hence ob: ob = NormalAction (NewHeapElem ad hT) by simp
  hence w-addr (w-values P vs (obs @ [ob]))  $\subseteq$  w-addr (w-values P vs obs)
  by(cases hT)(auto simp add: w-addr-def default-val-not-Addr Addr-not-default-val)
  moreover from ob ta have NormalAction (NewHeapElem ad hT)  $\in$  set  $\{ta\}_o$  by simp
  from init-fin-red-New-allocatedD[OF red this] have ad  $\in$  allocated m' ..
  with redT-normal have ad  $\in$  allocated (shr s') by auto
  ultimately show ?thesis using IH ob by(auto simp add: new-obs-addr-if-def)
next
  case (4 ad al v)
  note ob =  $\langle ob = \text{NormalAction } (\text{ReadMem } ad \text{ al } v) \rangle$ 
  { fix ad'
    assume v: v = Addr ad'
    with tasc ob have mrw: Addr ad'  $\in$  w-values P vs obs (ad, al)
      by(auto simp add: lappend-llist-of-llist-of[symmetric] non-speculative-lappend simp del:
lappend-llist-of-llist-of)
    hence  $ad' \in \text{w-addr} (\text{w-values } P \text{ vs } \text{obs})$ 
      by(auto simp add: w-addr-def)
    with IH have  $ad' \in \text{allocated } (\text{shr } s')$  by blast }
  with ob IH show ?thesis by(cases v)(simp-all add: new-obs-addr-if-def)
  qed(simp-all add: new-obs-addr-if-def)
  thus ?case by simp
  qed }
  note this[of  $\{ta\}_o$  []]
  moreover have if.known-addr-state s'  $\subseteq$  if.known-addr-state s  $\cup$  new-obs-addr-if {ta}_o
    using  $\langle \text{mthr.if.redT } s \ (t, ta) \ s' \rangle$  by(rule if-redT-known-addr-new)
  ultimately show ?thesis using ka allocated-subset by blast
  qed
  thus ?thesis1 ?thesis2 by simp-all
  qed

```

lemma *RedT-non-speculative-known-addr-allocated*:

assumes *red*: *mthr.if.RedT* *s ttas s'*
and *tasc*: *non-speculative P vs (llist-of (concat (map ($\lambda(t, ta).$ $\{ta\}_o$) ttas)))*
and *ka*: *if.known-addr-state s \subseteq allocated (shr s)*
and *vs*: *w-addr vs \subseteq allocated (shr s)*
shows *if.known-addr-state s' \subseteq allocated (shr s') (is ?thesis1 s')*
and *w-addr (w-values P vs (concat (map ($\lambda(t, ta).$ $\{ta\}_o$) ttas))) \subseteq allocated (shr s') (is ?thesis2 s' ttas)*

proof –

from *red tasc* **have** *?thesis1 s' \wedge ?thesis2 s' ttas*
proof(*induct rule: mthr.if.RedT-induct'*)
case *refl* **thus** *?case* **using** *ka vs* **by** *simp*
next
case (*step ttas s' t ta s''*)
hence *non-speculative P (w-values P vs (concat (map ($\lambda(t, ta).$ $\{ta\}_o$) ttas))) (llist-of $\{ta\}_o$)*
and *?thesis1 s' ?thesis2 s' ttas*
by(*simp-all add: lappend-llist-of-llist-of[symmetric] non-speculative-lappend del: lappend-llist-of-llist-of*)
from *redT-non-speculative-known-addr-allocated[OF \langle mthr.if.redT s' (t, ta) s'' \rangle this]*
show *?case* **by** *simp*
qed
thus *?thesis1 s' ?thesis2 s' ttas* **by** *simp-all*
qed

lemma *read-ex-NewHeapElem [consumes 5, case-names start Red]*:

assumes *RedT*: *mthr.if.RedT (init-fin-lift-state status (start-state f P C M vs)) ttas s*
and *red*: *mthr.if.redT s (t, ta) s'*
and *read*: *NormalAction (ReadMem ad al v) \in set $\{ta\}_o$*
and *sc*: *non-speculative P ($\lambda.$ $\{ \}$) (llist-of (map *snd* (lift-start-obs start-tid start-heap-obs) @ concat (map ($\lambda(t, ta).$ $\{ta\}_o$) ttas)))*
and *known*: *known-addr start-tid (f (fst (method P C M)) M (fst (snd (method P C M))) (fst (snd (snd (method P C M)))) (the (snd (snd (snd (method P C M)))))) vs) \subseteq allocated start-heap*
obtains (*start*) *CTn* **where** *NewHeapElem ad CTn \in set start-heap-obs*
 $|$ (*Red*) *ttas' s'' t' ta' s''' ttas'' CTn*
where *mthr.if.RedT (init-fin-lift-state status (start-state f P C M vs)) ttas' s''*
and *mthr.if.redT s'' (t', ta') s'''*
and *mthr.if.RedT s''' ttas'' s*
and *ttas = ttas' @ (t', ta') # ttas''*
and *NormalAction (NewHeapElem ad CTn) \in set $\{ta'\}_o$*

proof –

let *?start-state = init-fin-lift-state status (start-state f P C M vs)*
let *?obs-prefix = lift-start-obs start-tid start-heap-obs*
let *?vs-start = w-values P ($\lambda.$ $\{ \}$) (map *snd* ?obs-prefix)*
from *sc* **have** *non-speculative P (w-values P ($\lambda.$ $\{ \}$) (map *snd* (lift-start-obs start-tid start-heap-obs))) (llist-of (concat (map ($\lambda(t, ta).$ $\{ta\}_o$) ttas)))*
by(*simp add: non-speculative-lappend lappend-llist-of-llist-of[symmetric] del: lappend-llist-of-llist-of*)
with *RedT* **have** *if.known-addr-state s \subseteq allocated (shr s)*
proof(*rule RedT-non-speculative-known-addr-allocated*)
show *if.known-addr-state ?start-state \subseteq allocated (shr ?start-state)*
using *known*
by(*auto simp add: if.known-addr-state-def if.known-addr-thr-def start-state-def init-fin-lift-state-def split-beta split: if-split-asm*)

have *w-addr ?vs-start \subseteq w-addr ($\lambda.$ $\{ \}$)* **by**(*rule w-addr-lift-start-heap-obs*)

```

thus  $w\text{-addrs } ?vs\text{-start} \subseteq \text{allocated } (\text{shr } ?start\text{-state})$  by simp
qed
also from red read obtain  $x\text{-ra } x'\text{-ra } m'\text{-ra}$ 
  where  $\text{red}'\text{-ra}: t \vdash (x\text{-ra}, \text{shr } s) \text{---}ta \rightarrow i (x'\text{-ra}, m'\text{-ra})$ 
  and  $s': \text{redT}\text{-upd } s \ t \ ta \ x'\text{-ra } m'\text{-ra } s'$ 
  and  $ts\text{-t}: \text{thr } s \ t = \lfloor (x\text{-ra}, \text{no}\text{-wait}\text{-locks}) \rfloor$ 
  by cases auto
from red'-ra read
have  $ad \in \text{known}\text{-addrs}\text{-if } t \ x\text{-ra}$  by(rule if-red-read-knows-addr)
hence  $ad \in \text{if}\text{-known}\text{-addrs}\text{-state } s$  using  $ts\text{-t}$  by(rule if.known-addrs-stateI)
finally have  $ad \in \text{allocated } (\text{shr } s)$  .

show ?thesis
proof(cases ad ∈ allocated start-heap)
  case True
    then obtain  $CTn$  where  $\text{NewHeapElem } ad \ CTn \in \text{set } \text{start}\text{-heap}\text{-obs}$ 
    unfolding start-addrs-allocated by(blast dest: start-addrs-NewHeapElem-start-heap-obsD)
    thus ?thesis by(rule start)
  next
    case False
    hence  $ad \notin \text{allocated } (\text{shr } ?start\text{-state})$  by(simp add: start-state-def split-beta shr-init-fin-lift-state)
    with  $\text{RedT } \langle ad \in \text{allocated } (\text{shr } s) \rangle$  obtain  $t' \ ta' \ CTn$ 
    where  $tta: (t', ta') \in \text{set } ttas$ 
    and  $\text{new}: \text{NormalAction } (\text{NewHeapElem } ad \ CTn) \in \text{set } \{ta'\}_o$ 
    by(blast dest: init-fin-RedT-allocated-NewHeapElemD)
    from  $tta$  obtain  $ttas' \ ttas''$  where  $ttas: ttas = ttas' \ @ \ (t', ta') \ \# \ ttas''$  by(auto dest: split-list)
    with  $\text{RedT}$  obtain  $s'' \ s'''$ 
    where  $\text{mthr}\text{-if}\text{-RedT } ?start\text{-state } ttas' \ s''$ 
    and  $\text{mthr}\text{-if}\text{-redT } s'' \ (t', ta') \ s'''$ 
    and  $\text{mthr}\text{-if}\text{-RedT } s''' \ ttas'' \ s$ 
    unfolding mthr.if.RedT-def by(auto elim!: rtrancl3p-appendE dest!: converse-rtrancl3p-step)
    thus thesis using  $ttas \ \text{new}$  by(rule Red)
  qed
qed

end

locale known-addrs-typing =
  known-addrs
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  allocated known-addrs
  final r P
for addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype
and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
and allocated :: 'heap  $\Rightarrow$  'addr set
and known-addrs :: 'thread-id  $\Rightarrow$  'x  $\Rightarrow$  'addr set

```



```

and final :: 'x ⇒ bool
and r :: ('addr, 'thread-id, 'x, 'heap, 'addr, ('addr, 'thread-id) obs-event) semantics (⟦ - ⟧ - - -> -)
[50,0,0,50] 80)
and wfx :: 'thread-id ⇒ 'x ⇒ 'heap ⇒ bool
and P :: 'md prog
+
assumes wfs-non-speculative-invar:
  [ [ t ⊢ (x, m) -ta→ (x', m'); wfx t x m;
    vs-conf P m vs; non-speculative P vs (llist-of (map NormalAction {ta}_o)) ] ]
  ⇒ wfx t x' m'
and wfs-non-speculative-spawn:
  [ [ t ⊢ (x, m) -ta→ (x', m'); wfx t x m;
    vs-conf P m vs; non-speculative P vs (llist-of (map NormalAction {ta}_o));
    NewThread t'' x'' m'' ∈ set {ta}_t ] ]
  ⇒ wfx t'' x'' m''
and wfs-non-speculative-other:
  [ [ t ⊢ (x, m) -ta→ (x', m'); wfx t x m;
    vs-conf P m vs; non-speculative P vs (llist-of (map NormalAction {ta}_o));
    wfx t'' x'' m ] ]
  ⇒ wfx t'' x'' m'
and wfs-non-speculative-vs-conf:
  [ [ t ⊢ (x, m) -ta→ (x', m'); wfx t x m;
    vs-conf P m vs; non-speculative P vs (llist-of (take n (map NormalAction {ta}_o))) ] ]
  ⇒ vs-conf P m' (w-values P vs (take n (map NormalAction {ta}_o)))
and red-read-typeable:
  [ [ t ⊢ (x, m) -ta→ (x', m'); wfx t x m; ReadMem ad al v ∈ set {ta}_o ] ]
  ⇒ ∃ T. P, m ⊢ ad@al : T
and red-NewHeapElemD:
  [ [ t ⊢ (x, m) -ta→ (x', m'); wfx t x m; NewHeapElem ad hT ∈ set {ta}_o ] ]
  ⇒ typeof-addr m' ad = [hT]
and red-hext-incr:
  [ [ t ⊢ (x, m) -ta→ (x', m'); wfx t x m;
    vs-conf P m vs; non-speculative P vs (llist-of (map NormalAction {ta}_o)) ] ]
  ⇒ m ≤ m'
begin

lemma redT-wfs-non-speculative-invar:
  assumes redT: mthr.redT s (t, ta) s'
  and wfx: ts-ok wfx (thr s) (shr s)
  and vs: vs-conf P (shr s) vs
  and ns: non-speculative P vs (llist-of (map NormalAction {ta}_o))
  shows ts-ok wfx (thr s') (shr s')
using redT
proof(cases)
  case (redT-normal x x' m')
  with vs wfx ns show ?thesis
  apply(clarsimp intro!: ts-okI split: if-split-asm)
  apply(erule wfs-non-speculative-invar, auto dest: ts-okD)
  apply(rename-tac t' x' ln ws')
  apply(case-tac thr s t')
  apply(frule (2) redT-updTs-new-thread, clarify)
  apply(frule (1) mthr.new-thread-memory)
  apply(auto intro: wfs-non-speculative-other wfs-non-speculative-spawn dest: ts-okD simp add: redT-updTs-Some)
  done

```

next

case (*redT-acquire* x ln n)
thus *?thesis* **using** *wfx* **by**(*auto intro!*: *ts-okI* *dest*: *ts-okD* *split*: *if-split-asm*)

qed

lemma *redT-wfs-non-speculative-vs-conf*:

assumes *redT*: *mthr.redT* s (t , ta) s'
and *wfx*: *ts-ok* *wfx* (*thr* s) (*shr* s)
and *conf*: *vs-conf* P (*shr* s) vs
and *ns*: *non-speculative* P vs (*l**list-of* (*take* n (*map* *NormalAction* $\{ta\}_o$)))
shows *vs-conf* P (*shr* s') (*w-values* P vs (*take* n (*map* *NormalAction* $\{ta\}_o$)))

using *redT*

proof(*cases*)

case (*redT-normal* x x' m')
thus *?thesis* **using** *ns* *conf* *wfx* **by**(*auto dest*: *wfs-non-speculative-vs-conf* *ts-okD*)

next

case (*redT-acquire* x l ln)
have *w-values* P vs (*take* n (*map* *NormalAction* (*convert-RA* ln :: ('*addr*', '*thread-id*) *obs-event* *list*)))
= vs

by(*fastforce dest*: *in-set-takeD* *simp* *add*: *convert-RA-not-write* *intro!*: *w-values-no-write-unchanged* *del*: *equalityI*)

thus *?thesis* **using** *conf* *redT-acquire* **by**(*auto*)

qed

lemma *if-redT-non-speculative-invar*:

assumes *red*: *mthr.if.redT* s (t , ta) s'
and *ts-ok*: *ts-ok* (*init-fin-lift* *wfx*) (*thr* s) (*shr* s)
and *sc*: *non-speculative* P vs (*l**list-of* $\{ta\}_o$)
and *vs*: *vs-conf* P (*shr* s) vs
shows *ts-ok* (*init-fin-lift* *wfx*) (*thr* s') (*shr* s')

proof –

let $?s = \lambda s. (\text{locks } s, (\lambda t. \text{map-option } (\lambda((\text{status}, x), \text{ln}). (x, \text{ln})) (\text{thr } s \ t), \text{shr } s), \text{wset } s, \text{interrupts } s)$

from *ts-ok* **have** *ts-ok'*: *ts-ok* *wfx* (*thr* ($?s$ s)) (*shr* ($?s$ s)) **by**(*auto intro!*: *ts-okI* *dest*: *ts-okD*)

from vs **have** vs' : *vs-conf* P (*shr* ($?s$ s)) vs **by** *simp*

from *red* **show** *?thesis*

proof(*cases*)

case (*redT-normal* x x' m)
note $tst = \langle \text{thr } s \ t = \lfloor (x, \text{no-wait-locks}) \rfloor \rangle$
from $\langle t \vdash (x, \text{shr } s) -ta \rightarrow i (x', m) \rangle$
show *?thesis*

proof(*cases*)

case (*NormalAction* X TA X')
from $\langle ta = \text{convert-TA-initial } (\text{convert-obs-initial } TA) \rangle \langle \text{mthr.if.actions-ok } s \ t \ ta \rangle$
have *mthr.actions-ok* ($?s$ s) t TA
by(*auto elim*: *rev-iffD1*[*OF* - *thread-oks-ts-change*] *cond-action-oks-final-change*)

with tst *NormalAction* $\langle \text{redT-upd } s \ t \ ta \ x' \ m \ s' \rangle$ **have** *mthr.redT* ($?s$ s) (t , TA) ($?s$ s')

using *map-redT-updT*s[*of snd* *thr* s $\{ta\}_t$]

by(*auto intro!*: *mthr.redT.intros* *simp* *add*: *split-def* *map-prod-def* *o-def* *fun-eq-iff*)

moreover **note** *ts-ok'* vs'

moreover **from** $\langle ta = \text{convert-TA-initial } (\text{convert-obs-initial } TA) \rangle$ **have** $\{ta\}_o = \text{map } \text{NormalAc}$

```

tion  $\{TA\}_o$  by(auto)
  with sc have non-speculative P vs (llist-of (map NormalAction  $\{TA\}_o$ )) by simp
  ultimately have ts-ok wfx (thr (?s s')) (shr (?s s'))
    by(auto dest: redT-wfs-non-speculative-invar)
  thus ?thesis using  $\langle \{ta\}_o = \text{map NormalAction } \{TA\}_o \rangle$  by(auto intro!: ts-okI dest: ts-okD)
next
  case InitialThreadAction
  with redT-normal ts-ok' vs show ?thesis
    by(auto 4 3 intro!: ts-okI dest: ts-okD split: if-split-asm)
next
  case ThreadFinishAction
  with redT-normal ts-ok' vs show ?thesis
    by(auto 4 3 intro!: ts-okI dest: ts-okD split: if-split-asm)
qed
next
  case (redT-acquire x ln l)
  thus ?thesis using vs ts-ok by(auto 4 3 intro!: ts-okI dest: ts-okD split: if-split-asm)
qed
qed

lemma if-redT-non-speculative-vs-conf:
  assumes red: mthr.if.redT s (t, ta) s'
  and ts-ok: ts-ok (init-fin-lift wfx) (thr s) (shr s)
  and sc: non-speculative P vs (llist-of (take n  $\{ta\}_o$ ))
  and vs: vs-conf P (shr s) vs
  shows vs-conf P (shr s') (w-values P vs (take n  $\{ta\}_o$ ))
proof -
  let ?s =  $\lambda s. (\text{locks } s, (\lambda t. \text{map-option } (\lambda((\text{status}, x), \text{ln}). (x, \text{ln})) (\text{thr } s \ t), \text{shr } s), \text{wset } s, \text{interrupts } s)$ 
  from ts-ok have ts-ok': ts-ok wfx (thr (?s s)) (shr (?s s)) by(auto intro!: ts-okI dest: ts-okD)
  from vs have vs': vs-conf P (shr (?s s)) vs by simp

  from red show ?thesis
proof(cases)
  case (redT-normal x x' m)
  note tst =  $\langle \text{thr } s \ t = \lfloor (x, \text{no-wait-locks}) \rfloor \rangle$ 
  from  $\langle t \vdash (x, \text{shr } s) -ta \rightarrow i (x', m) \rangle$ 
  show ?thesis
proof(cases)
  case (NormalAction X TA X')
  from  $\langle ta = \text{convert-TA-initial } (\text{convert-obs-initial } TA) \rangle \langle \text{mthr.if.actions-ok } s \ t \ ta \rangle$ 
  have mthr.actions-ok (?s s) t TA
    by(auto elim: rev-iffD1[OF - thread-oks-ts-change] cond-action-oks-final-change)

  with tst NormalAction  $\langle \text{redT-upd } s \ t \ ta \ x' \ m \ s' \rangle$  have mthr.redT (?s s) (t, TA) (?s s')
    using map-redT-updTs[of snd thr s  $\{ta\}_t$ ]
    by(auto intro!: mthr.redT.intros simp add: split-def map-prod-def o-def fun-eq-iff)
  moreover note ts-ok' vs'
  moreover from  $\langle ta = \text{convert-TA-initial } (\text{convert-obs-initial } TA) \rangle$  have  $\{ta\}_o = \text{map NormalAction } \{TA\}_o$  by(auto)
  with sc have non-speculative P vs (llist-of (take n (map NormalAction  $\{TA\}_o$ ))) by simp
  ultimately have vs-conf P (shr (?s s')) (w-values P vs (take n (map NormalAction  $\{TA\}_o$ )))
    by(auto dest: redT-wfs-non-speculative-vs-conf)

```

```

    thus ?thesis using ‹{ta}o = map NormalAction {TA}o› by(auto)
  next
    case InitialThreadAction
    with redT-normal vs show ?thesis by(auto simp add: take-Cons')
  next
    case ThreadFinishAction
    with redT-normal vs show ?thesis by(auto simp add: take-Cons')
  qed
next
  case (redT-acquire x l ln)
  have w-values P vs (take n (map NormalAction (convert-RA ln :: ('addr, 'thread-id) obs-event
list))) = vs
  by(fastforce simp add: convert-RA-not-write take-Cons' dest: in-set-takeD intro!: w-values-no-write-unchanged
del: equalityI)
  thus ?thesis using vs redT-acquire by auto
  qed
qed

```

lemma *if-RedT-non-speculative-invar:*

```

  assumes red: mthr.if.RedT s ttas s'
  and tsok: ts-ok (init-fin-lift wfx) (thr s) (shr s)
  and sc: non-speculative P vs (llist-of (concat (map (λ(t, ta). {ta}o) ttas)))
  and vs: vs-conf P (shr s) vs
  shows ts-ok (init-fin-lift wfx) (thr s') (shr s') (is ?thesis1)
  and vs-conf P (shr s') (w-values P vs (concat (map (λ(t, ta). {ta}o) ttas))) (is ?thesis2)
using red tsok sc vs unfolding mthr.if.RedT-def
proof(induct arbitrary: vs rule: rtrancl3p-converse-induct')
  case refl
  case 1 thus ?case by –
  case 2 thus ?case by simp
next
  case (step s tta s' ttas)
  obtain t ta where tta: tta = (t, ta) by(cases tta)

  case 1
  hence sc1: non-speculative P vs (llist-of {ta}o)
  and sc2: non-speculative P (w-values P vs {ta}o) (llist-of (concat (map (λ(t, ta). {ta}o) ttas)))
  unfolding lconcat-llist-of[symmetric] lmap-llist-of[symmetric] llist.map-comp o-def llist-of.simps
  llist.map(2) lconcat-LCons tta
  by(simp-all add: non-speculative-lappend list-of-lconcat o-def)
  from if-redT-non-speculative-invar[OF step(2)[unfolded tta] - sc1] if-redT-non-speculative-vs-conf[OF
  step(2)[unfolded tta], where vs = vs and n=length {ta}o] 1 step.hyps(3)[of w-values P vs {ta}o] sc2
  sc1
  show ?case by simp

  case 2
  hence sc1: non-speculative P vs (llist-of {ta}o)
  and sc2: non-speculative P (w-values P vs {ta}o) (llist-of (concat (map (λ(t, ta). {ta}o) ttas)))
  unfolding lconcat-llist-of[symmetric] lmap-llist-of[symmetric] llist.map-comp o-def llist-of.simps
  llist.map(2) lconcat-LCons tta
  by(simp-all add: non-speculative-lappend list-of-lconcat o-def)
  from if-redT-non-speculative-invar[OF step(2)[unfolded tta] - sc1] if-redT-non-speculative-vs-conf[OF
  step(2)[unfolded tta], where vs = vs and n=length {ta}o] 2 step.hyps(4)[of w-values P vs {ta}o] sc2
  sc1

```

show ?case **by**(simp add: tta o-def)
qed

lemma *init-fin-heat-incr*:
assumes $t \vdash (x, m) -ta \rightarrow i (x', m')$
and *init-fin-lift wfx* $t x m$
and *non-speculative* $P vs (l\text{list-of } \{ta\}_o)$
and *vs-conf* $P m vs$
shows $m \trianglelefteq m'$
using *assms*
by(cases)(auto intro: red-heat-incr)

lemma *init-fin-redT-heat-incr*:
assumes *mthr.if.redT* $s (t, ta) s'$
and *ts-ok* (*init-fin-lift wfx*) (*thr* s) (*shr* s)
and *non-speculative* $P vs (l\text{list-of } \{ta\}_o)$
and *vs-conf* $P (shr s) vs$
shows $shr s \trianglelefteq shr s'$
using *assms*
by(cases)(auto dest: *init-fin-heat-incr ts-okD*)

lemma *init-fin-RedT-heat-incr*:
assumes *mthr.if.RedT* $s ttas s'$
and *ts-ok* (*init-fin-lift wfx*) (*thr* s) (*shr* s)
and *sc*: *non-speculative* $P vs (l\text{list-of } (\text{concat } (\text{map } (\lambda(t, ta). \{ta\}_o) ttas)))$
and *vs*: *vs-conf* $P (shr s) vs$
shows $shr s \trianglelefteq shr s'$

using *assms*

proof(*induction rule: mthr.if.RedT-induct'*)

case *refl* **thus** ?case **by** *simp*

next

case (*step* $ttas s' t ta s''$)

note $ts\text{-ok} = \langle ts\text{-ok } (init\text{-fin-lift } wfx) (thr s) (shr s) \rangle$

from $\langle non\text{-speculative } P vs (l\text{list-of } (\text{concat } (\text{map } (\lambda(t, ta). \{ta\}_o) (ttas @ [(t, ta)])))) \rangle$

have ns : *non-speculative* $P vs (l\text{list-of } (\text{concat } (\text{map } (\lambda(t, ta). \{ta\}_o) ttas)))$

and ns' : *non-speculative* $P (w\text{-values } P vs (\text{concat } (\text{map } (\lambda(t, ta). \{ta\}_o) ttas))) (l\text{list-of } \{ta\}_o)$

by(*simp-all add: lappend-llist-of-llist-of[symmetric] non-speculative-lappend del: lappend-llist-of-llist-of*)

from $ts\text{-ok } ns$ **have** $shr s \trianglelefteq shr s'$

using $\langle vs\text{-conf } P (shr s) vs \rangle$ **by**(*rule step.IH*)

also have $ts\text{-ok } (init\text{-fin-lift } wfx) (thr s') (shr s')$

using $\langle mthr.if.RedT s ttas s' \rangle ts\text{-ok } ns \langle vs\text{-conf } P (shr s) vs \rangle$

by(*rule if-RedT-non-speculative-invar*)

with $\langle mthr.if.redT s' (t, ta) s'' \rangle$

have $\dots \trianglelefteq shr s''$ **using** ns'

proof(*rule init-fin-redT-heat-incr*)

from $\langle mthr.if.RedT s ttas s' \rangle ts\text{-ok } ns \langle vs\text{-conf } P (shr s) vs \rangle$

show *vs-conf* $P (shr s') (w\text{-values } P vs (\text{concat } (\text{map } (\lambda(t, ta). \{ta\}_o) ttas)))$

by(*rule if-RedT-non-speculative-invar*)

qed

finally show ?case .

qed

lemma *init-fin-red-read-typeable*:
assumes $t \vdash (x, m) -ta \rightarrow i (x', m')$

and *init-fin-lift* $wf\ t\ x\ m\ NormalAction\ (ReadMem\ ad\ al\ v) \in set\ \{ta\}_o$
shows $\exists T. P, m \vdash ad@al : T$
using *assms*
by *cases(auto dest: red-read-typeable)*

lemma *Ex-new-action-for*:
assumes *wf: wf-syscls P*
and *wf-x-start: ts-ok wf (thr (start-state f P C M vs)) start-heap*
and *ka: known-addr start-tid (f (fst (method P C M)) M (fst (snd (method P C M))) (fst (snd (snd (method P C M)))) (the (snd (snd (snd (method P C M)))))) vs) \subseteq allocated start-heap*
and *E: E \in \mathcal{E} -start f P C M vs status*
and *read: ra \in read-actions E*
and *aloc: adal \in action-loc P E ra*
and *sc: non-speculative P ($\lambda\cdot$. $\{\}$) (ltake (enat ra) (lmap snd E))*
shows $\exists wa. wa \in new-actions-for\ P\ E\ adal \wedge wa < ra$

proof –
let *?obs-prefix = lift-start-obs start-tid start-heap-obs*
let *?start-state = init-fin-lift-state status (start-state f P C M vs)*

from *start-state-vs-conf[OF wf]*
have *vs-conf-start: vs-conf P start-heap (w-values P ($\lambda\cdot$. $\{\}$)) (map NormalAction start-heap-obs)*
by (*simp add: lift-start-obs-def o-def*)

obtain *ad al where adal: adal = (ad, al) by(cases adal)*
with *read aloc obtain v where ra: action-obs E ra = NormalAction (ReadMem ad al v)*
and *ra-len: enat ra < llength E*
by (*cases lnth E ra*)(*auto elim!: read-actions.cases actionsE*)

from *E obtain E'' where E: E = lappend (llist-of ?obs-prefix) E''*
and *E'': E'' \in mthr.if. \mathcal{E} ?start-state by(auto)*
from *E'' obtain E' where E': E'' = lconcat (lmap ($\lambda(t, ta). llist-of (map (Pair t) \{ta\}_o)) E')$*
and *τ Runs: mthr.if.mthr.Runs ?start-state E' by(rule mthr.if. \mathcal{E} .cases)*

have *ra-len': length ?obs-prefix \leq ra*
proof (*rule ccontr*)
assume \neg *?thesis*
hence *ra < length ?obs-prefix by simp*
moreover with *ra ra-len E obtain ra' ad al v*
where *start-heap-obs ! ra' = ReadMem ad al v ra' < length start-heap-obs*
by (*cases ra*)(*auto simp add: lnth-LCons lnth-lappend1 action-obs-def lift-start-obs-def*)
ultimately have *ReadMem ad al v \in set start-heap-obs unfolding in-set-conv-nth by blast*
thus *False by(simp add: start-heap-obs-not-Read)*

qed
let *?n = length ?obs-prefix*
from *ra ra-len ra-len' E have enat (ra - ?n) < llength E''*
and *ra-obs: action-obs E'' (ra - ?n) = NormalAction (ReadMem ad al v)*
by (*cases llength E'', auto simp add: action-obs-def lnth-lappend2*)

from *τ Runs $\langle enat (ra - ?n) < llength E'' \rangle$ obtain ra-m ra-n t-ra ta-ra*
where *E-ra: lnth E'' (ra - ?n) = (t-ra, \{ta-ra\}_o ! ra-n)*
and *ra-n: ra-n < length \{ta-ra\}_o* **and** *ra-m: enat ra-m < llength E'*
and *ra-conv: ra - ?n = ($\sum i < ra-m. length \{snd (lnth E' i)\}_o$) + ra-n*
and *E'-ra-m: lnth E' ra-m = (t-ra, ta-ra)*
unfolding *E' by(rule mthr.if.actions- \mathcal{E} E-aux)*

let $?E' = \text{ldropn } (Suc \text{ ra-m}) \ E'$

have E' -unfold: $E' = \text{lappend } (\text{ltake } (\text{enat } \text{ra-m}) \ E') \ (LCons \ (\text{lnth } E' \ \text{ra-m}) \ ?E')$
unfolding $\text{ldropn-Suc-conv-ldropn}[OF \ \text{ra-m}]$ **by** simp

hence $\text{mthr.if.mthr.Runs } ?\text{start-state } (\text{lappend } (\text{ltake } (\text{enat } \text{ra-m}) \ E') \ (LCons \ (\text{lnth } E' \ \text{ra-m}) \ ?E'))$
using τRuns **by** simp

then obtain σ' **where** $\sigma\text{-}\sigma'$: $\text{mthr.if.mthr.Trsys } ?\text{start-state } (\text{list-of } (\text{ltake } (\text{enat } \text{ra-m}) \ E')) \ \sigma'$
and $\tau\text{Runs}'$: $\text{mthr.if.mthr.Runs } \sigma' \ (LCons \ (\text{lnth } E' \ \text{ra-m}) \ ?E')$
by(rule $\text{mthr.if.mthr.Runs-lappendE}$) simp

from $\tau\text{Runs}'$ **obtain** σ'' **where** red-ra : $\text{mthr.if.redT } \sigma' \ (t\text{-ra}, \ \text{ta-ra}) \ \sigma''$
and $\tau\text{Runs}''$: $\text{mthr.if.mthr.Runs } \sigma'' \ ?E'$
unfolding E' -ra-m **by** cases

from E -ra ra-n ra-obs **have** $\text{NormalAction } (\text{ReadMem } \text{ad } \text{al } v) \in \text{set } \{\text{ta-ra}\}_o$
by(auto simp $\text{add: action-obs-def in-set-conv-nth}$)

with red-ra **obtain** $x\text{-ra } x'\text{-ra } m'\text{-ra}$
where $\text{red}'\text{-ra}$: $\text{mthr.init-fin } t\text{-ra } (x\text{-ra}, \ \text{shr } \sigma') \ \text{ta-ra } (x'\text{-ra}, \ m'\text{-ra})$
and σ'' : $\text{redT-upd } \sigma' \ t\text{-ra } \text{ta-ra } x'\text{-ra } m'\text{-ra } \sigma''$
and ts-t-a : $\text{thr } \sigma' \ t\text{-ra} = \lfloor (x\text{-ra}, \ \text{no-wait-locks}) \rfloor$
by cases auto

from $\text{red}'\text{-ra}$ $\langle \text{NormalAction } (\text{ReadMem } \text{ad } \text{al } v) \in \text{set } \{\text{ta-ra}\}_o \rangle$
obtain $\text{ta}'\text{-ra } X\text{-ra } X'\text{-ra}$
where $x\text{-ra}$: $x\text{-ra} = (\text{Running}, \ X\text{-ra})$
and $x'\text{-ra}$: $x'\text{-ra} = (\text{Running}, \ X'\text{-ra})$
and ta-ra : $\text{ta-ra} = \text{convert-TA-initial } (\text{convert-obs-initial } \text{ta}'\text{-ra})$
and $\text{red}''\text{-ra}$: $t\text{-ra} \vdash (X\text{-ra}, \ \text{shr } \sigma') \text{-ta}'\text{-ra} \rightarrow (X'\text{-ra}, \ m'\text{-ra})$
by cases fastforce+

from $\langle \text{NormalAction } (\text{ReadMem } \text{ad } \text{al } v) \in \text{set } \{\text{ta-ra}\}_o \rangle \ \text{ta-ra}$
have $\text{ReadMem } \text{ad } \text{al } v \in \text{set } \{\text{ta}'\text{-ra}\}_o$ **by** auto

from wfx-start **have** wfx-start : $\text{ts-ok } (\text{init-fin-lift } \text{wfx}) \ (\text{thr } ?\text{start-state}) \ (\text{shr } ?\text{start-state})$
by(simp $\text{add: start-state-def split-beta}$)

from $\text{sc ra-len}'$

have $\text{non-speculative } P \ (w\text{-values } P \ (\lambda\text{-} \ \{\}) \ (\text{map } \text{snd } ?\text{obs-prefix}))$
 $(\text{lmap } \text{snd } (\text{ltake } (\text{enat } (\text{ra} - ?n)) \ (\text{lconcat } (\text{lmap } (\lambda(t, \ \text{ta}). \ \text{llist-of } (\text{map } (\text{Pair } t) \ \{\text{ta}\}_o)) \ E'))))$
unfolding $E \ E'$ **by**(simp $\text{add: ltake-lappend2 lmap-lappend-distrib non-speculative-lappend}$)

also note ra-conv **also note** $\text{plus-enat-simps}(1)[\text{symmetric}]$
also have $\text{enat } (\sum i < \text{ra-m}. \ \text{length } \{\text{snd } (\text{lnth } E' \ i)\}_o) = (\sum i < \text{ra-m}. \ \text{enat } (\text{length } \{\text{snd } (\text{lnth } E' \ i)\}_o))$
by($\text{subst sum-comp-morphism}[\text{symmetric}]$)(simp-all $\text{add: zero-enat-def}$)

also have $\dots = (\sum i < \text{ra-m}. \ \text{llength } (\text{lnth } (\text{lmap } (\lambda(t, \ \text{ta}). \ \text{llist-of } (\text{map } (\text{Pair } t) \ \{\text{ta}\}_o)) \ E') \ i))$
using ra-m **by**-(rule $\text{sum.cong}[OF \ \text{refl}]$, simp $\text{add: le-less-trans}[\text{where } y = \text{enat } \text{ra-m}] \ \text{split-beta}$)

also note $\text{ltake-plus-conv-lappend}$ **also note** $\text{lconcat-ltake}[\text{symmetric}]$
also note $\text{lmap-lappend-distrib}$
also note $\text{non-speculative-lappend}$

finally have $\text{non-speculative } P \ (w\text{-values } P \ (\lambda\text{-} \ \{\}) \ (\text{map } \text{snd } ?\text{obs-prefix})) \ (\text{lmap } \text{snd } (\text{lconcat } (\text{lmap } (\lambda(t, \ \text{ta}). \ \text{llist-of } (\text{map } (\text{Pair } t) \ \{\text{ta}\}_o)) \ (\text{llist-of } (\text{list-of } (\text{ltake } (\text{enat } \text{ra-m}) \ E'))))))$
by(simp add: split-def)

hence sc' : $\text{non-speculative } P \ (w\text{-values } P \ (\lambda\text{-} \ \{\}) \ (\text{map } \text{snd } ?\text{obs-prefix})) \ (\text{llist-of } (\text{concat } (\text{map } (\lambda(t, \ \text{ta}). \ \{\text{ta}\}_o) \ (\text{list-of } (\text{ltake } (\text{enat } \text{ra-m}) \ E'))))))$
unfolding $\text{lmap-lconcat llist.map-comp o-def lconcat-llist-of}[\text{symmetric}] \ \text{lmap-llist-of}[\text{symmetric}]$

by(*simp add: split-beta o-def*)

from *vs-conf-start* **have** *vs-conf-start: vs-conf P (shr ?start-state) (w-values P (λ-. {})) (map snd ?obs-prefix)*

by(*simp add: init-fin-lift-state-conv-simps start-state-def split-beta lift-start-obs-def o-def*)

with $\sigma\text{-}\sigma'$ *wfx-start sc'* **have** *ts-ok (init-fin-lift wfx) (thr σ') (shr σ')*

unfolding *mthr.if.RedT-def[symmetric]* **by**(*rule if-RedT-non-speculative-invar*)

with *ts-t-a* **have** *wfx t-ra X-ra (shr σ')* **unfolding** *x-ra* **by**(*auto dest: ts-okD*)

with *red''-ra* $\langle \text{ReadMem ad al } v \in \text{set } \{ta'\}_o \rangle$

obtain *T'* **where** *type-adal: P, shr $\sigma' \vdash \text{ad}@al : T'$* **by**(*auto dest: red-read-typeable*)

from *sc ra-len'* **have** *non-speculative P (λ-. {})* (*l*list-of (*map snd ?obs-prefix*))

unfolding *E* **by**(*simp add: ltake-lappend2 lmap-lappend-distrib non-speculative-lappend*)

with *sc'* **have** *sc'': non-speculative P (λ-. {})* (*l*list-of (*map snd (lift-start-obs start-tid start-heap-obs)*

$\text{@ concat (map } (\lambda(t, ta). \{ta\}_o) (\text{list-of (ltake (enat ra-m) E'))$)))

by(*simp add: lappend-llist-of-llist-of[symmetric] non-speculative-lappend del: lappend-llist-of-llist-of*)

from $\sigma\text{-}\sigma'$ *red-ra* $\langle \text{NormalAction (ReadMem ad al } v) \in \text{set } \{ta\}_o \rangle$ *sc'' ka*

show $\exists wa. wa \in \text{new-actions-for } P E \text{ adal} \wedge wa < ra$

unfolding *mthr.if.RedT-def[symmetric]*

proof(*cases rule: read-ex-NewHeapElem*)

case (*start CTn*)

then obtain *n* **where** *n: start-heap-obs ! n = NewHeapElem ad CTn*

and *len: n < length start-heap-obs*

unfolding *in-set-conv-nth* **by** *blast*

from *len* **have** *Suc n \in actions E* **unfolding** *E* **by**(*simp add: actions-def enat-less-enat-plusI*)

moreover

from $\sigma\text{-}\sigma'$ **have** *hext: start-heap \trianglelefteq shr σ'* **unfolding** *mthr.if.RedT-def[symmetric]*

using *wfx-start sc' vs-conf-start*

by(*auto dest!: init-fin-RedT-hext-incr simp add: start-state-def split-beta init-fin-lift-state-conv-simps*)

from *start* **have** *typeof-addr start-heap ad = $\lfloor CTn \rfloor$*

by(*auto dest: NewHeapElem-start-heap-obsD[OF wf]*)

with *hext* **have** *typeof-addr (shr σ') ad = $\lfloor CTn \rfloor$* **by**(*rule typeof-addr-hext-mono*)

with *type-adal* **have** *adal \in action-loc P E (Suc n)* **using** *n len* **unfolding** *E adal*

by *cases(auto simp add: action-obs-def lnth-lappend1 lift-start-obs-def)*

moreover **have** *is-new-action (action-obs E (Suc n))* **using** *n len* **unfolding** *E*

by(*simp add: action-obs-def lnth-lappend1 lift-start-obs-def*)

ultimately **have** *Suc n \in new-actions-for P E adal* **by**(*rule new-actionsI*)

moreover **have** *Suc n < ra* **using** *ra-len' len* **by**(*simp*)

ultimately **show** *?thesis* **by** *blast*

next

case (*Red ttas' s'' t' ta' s''' ttas'' CTn*)

from $\langle \text{NormalAction (NewHeapElem ad CTn)} \in \text{set } \{ta'\}_o \rangle$

obtain *obs obs'* **where** *obs: $\{ta'\}_o = \text{obs} \text{ @ NormalAction (NewHeapElem ad CTn)} \# \text{obs}'$*

by(*auto dest: split-list*)

let *?wa = ?n + length (concat (map ($\lambda(t, ta). \{ta\}_o$) ttas')) + length obs*

have *enat (length (concat (map ($\lambda(t, ta). \{ta\}_o$) ttas')) + length obs) < enat (length (concat (map ($\lambda(t, ta). \{ta\}_o$) (ttas' @ [(t', ta')]))))*

using *obs* **by** *simp*

also **have** $\dots = \text{llength (lconcat (lmap llist-of (lmap } (\lambda(t, ta). \{ta\}_o) (\text{l}list-of (ttas' \text{ @ } [(t', ta')]))))$

by(*simp del: map-map map-append add: lconcat-llist-of*)
also have $\dots \leq \text{llength} (\text{lconcat} (\text{lmap} (\lambda(t, ta). \text{llist-of } \{ta\}_o) (\text{llist-of } (ttas' @ (t', ta') \# ttas''))))$
by(*auto simp add: o-def split-def intro: lprefix-llist-ofI intro!: lprefix-lconcatI lprefix-llength-le*)
also note *len-less = calculation*
have $\dots \leq (\sum i < ra-m. \text{llength} (\text{lnth} (\text{lmap} (\lambda(t, ta). \text{llist-of } \{ta\}_o) E') i))$
unfolding $\langle \text{list-of} (\text{ltake} (\text{enat } ra-m) E') = ttas' @ (t', ta') \# ttas'' \rangle$ [*symmetric*]
by(*simp add: ltake-lmap[symmetric] lconcat-ltake del: ltake-lmap*)
also have $\dots = \text{enat} (\sum i < ra-m. \text{length} \{snd (\text{lnth } E' i)\}_o)$ **using** *ra-m*
by(*subst sum-comp-morphism[symmetric, where h=enat]*)(*auto intro: sum.cong simp add: zero-enat-def less-trans[where y=enat ra-m] split-beta*)
also have $\dots \leq \text{enat} (ra - ?n)$ **unfolding** *ra-conv* **by** *simp*
finally have *enat-length: enat (length (concat (map ($\lambda(t, ta). \{ta\}_o$) ttas')) + length obs) < enat (ra - length (lift-start-obs start-tid start-heap-obs))* .
then have *wa-ra: ?wa < ra* **by** *simp*
with *ra-len* **have** *?wa ∈ actions E* **by**(*cases llength E*)(*simp-all add: actions-def*)
moreover
from $\langle \text{mthr.if.redT } s'' (t', ta') s''' \rangle, \langle \text{NormalAction} (\text{NewHeapElem ad } CTn) \in \text{set } \{ta'\}_o \rangle$
obtain *x-wa x-wa'* **where** *ts''t': thr s'' t' = [(x-wa, no-wait-locks)]*
and *red-wa: mthr.init-fin t' (x-wa, shr s'') ta' (x-wa', shr s''')*
by(*cases*) *fastforce+*

from *sc'*
have *ns: non-speculative P (w-values P ($\lambda-. \{ \}$) (map snd ?obs-prefix)) (llist-of (concat (map ($\lambda(t, ta). \{ta\}_o$) ttas')))*
and *ns': non-speculative P (w-values P (w-values P ($\lambda-. \{ \}$) (map snd ?obs-prefix)) (concat (map ($\lambda(t, ta). \{ta\}_o$) ttas')) (llist-of $\{ta'\}_o$))*
and *ns'': non-speculative P (w-values P (w-values P (w-values P ($\lambda-. \{ \}$) (map snd ?obs-prefix)) (concat (map ($\lambda(t, ta). \{ta\}_o$) ttas')) $\{ta'\}_o$ (llist-of (concat (map ($\lambda(t, ta). \{ta\}_o$) ttas')))*
unfolding $\langle \text{list-of} (\text{ltake} (\text{enat } ra-m) E') = ttas' @ (t', ta') \# ttas'' \rangle$
by(*simp-all add: lappend-llist-of-llist-of[symmetric] lmap-lappend-distrib non-speculative-lappend del: lappend-llist-of-llist-of*)
from $\langle \text{mthr.if.RedT } ?start\text{-state } ttas' s'' \rangle$ *wfx-start ns*
have *ts-ok'': ts-ok (init-fin-lift wfx) (thr s'') (shr s'')*
using *vs-conf-start* **by**(*rule if-RedT-non-speculative-invar*)
with *ts''t'* **have** *wfx t': wfx t' (snd x-wa) (shr s'')* **by**(*cases x-wa*)(*auto dest: ts-okD*)

{
have *action-obs E ?wa =*
 $\text{snd} (\text{lnth} (\text{lconcat} (\text{lmap} (\lambda(t, ta). \text{llist-of} (\text{map} (\text{Pair } t) \{ta\}_o) E')) (\text{length} (\text{concat} (\text{map} (\lambda(t, y). \{y\}_o) ttas')) + \text{length } \text{obs}))$
unfolding *E E'* **by**(*simp add: action-obs-def lnth-lappend2*)
also from *enat-length* $\langle \text{enat} (ra - ?n) < \text{llength } E' \rangle$
have $\dots = \text{lnth} (\text{lconcat} (\text{lmap} (\lambda(t, ta). \text{llist-of } \{ta\}_o) E')) (\text{length} (\text{concat} (\text{map} (\lambda(t, y). \{y\}_o) ttas')) + \text{length } \text{obs})$
unfolding *E'*
by(*subst lnth-lmap[symmetric, where f=snd]*)(*erule (1) less-trans, simp add: lmap-lconcat llist.map-comp split-def o-def*)
also from *len-less*
have $\text{enat} (\text{length} (\text{concat} (\text{map} (\lambda(t, ta). \{ta\}_o) ttas')) + \text{length } \text{obs}) < \text{llength} (\text{lconcat} (\text{ltake} (\text{enat } ra-m) (\text{lmap} (\lambda(t, ta). \text{llist-of } \{ta\}_o) E')))$
unfolding $\langle \text{list-of} (\text{ltake} (\text{enat } ra-m) E') = ttas' @ (t', ta') \# ttas'' \rangle$ [*symmetric*]
by(*simp add: ltake-lmap[symmetric] del: ltake-lmap*)
note *lnth-lconcat-ltake[OF this, symmetric]*
also note *ltake-lmap*

```

also have ltake (enat ra-m) E' = llist-of (list-of (ltake (enat ra-m) E')) by(simp)
also note ⟨list-of (ltake (enat ra-m) E') = ttas' @ (t', ta') # ttas''⟩
also note lmap-llist-of also have (λ(t, ta). llist-of {ta}_o) = llist-of ∘ (λ(t, ta). {ta}_o)
  by(simp add: o-def split-def)
also note map-map[symmetric] also note lconcat-llist-of
also note lnth-llist-of
also have concat (map (λ(t, ta). {ta}_o) (ttas' @ (t', ta') # ttas'')) ! (length (concat (map (λ(t,
ta). {ta}_o) ttas'))) + length obs = NormalAction (NewHeapElem ad CTn)
  by(simp add: nth-append obs)
finally have action-obs E ?wa = NormalAction (NewHeapElem ad CTn) .
}
note wa-obs = this

from ⟨mthr.if.RedT ?start-state ttas' s'⟩ wfx-start ns vs-conf-start
have vs'': vs-conf P (shr s') (w-values P (w-values P (λ-. { }) (map snd ?obs-prefix)) (concat (map
(λ(t, ta). {ta}_o) ttas')))
  by(rule if-RedT-non-speculative-invar)
from if-redT-non-speculative-vs-conf[OF ⟨mthr.if.redT s'' (t', ta') s'''⟩ ts-ok'' - vs'', of length
{ta'}_o] ns'
have vs''': vs-conf P (shr s''') (w-values P (w-values P (w-values P (λ-. { }) (map snd ?obs-prefix))
(concat (map (λ(t, ta). {ta}_o) ttas'))) {ta'}_o)
  by simp

from ⟨mthr.if.redT s'' (t', ta') s'''⟩ ts-ok'' ns' vs''
have ts-ok (init-fin-lift wfx) (thr s''') (shr s''')
  by(rule if-redT-non-speculative-invar)
with ⟨mthr.if.RedT s''' ttas'' σ'⟩
have hext: shr s''' ≤ shr σ' using ns'' vs'''
  by(rule init-fin-RedT-hext-incr)

from red-wa have typeof-addr (shr s''') ad = [CTn]
using wfx' ⟨NormalAction (NewHeapElem ad CTn) ∈ set {ta'}_o⟩ by cases(auto dest: red-NewHeapElemD)
with hext have typeof-addr (shr σ') ad = [CTn] by(rule typeof-addr-hext-mono)
with type-adal have adal ∈ action-loc P E ?wa using wa-obs unfolding E adal
  by cases (auto simp add: action-obs-def lnth-lappend1 lift-start-obs-def)
moreover have is-new-action (action-obs E ?wa) using wa-obs by simp
ultimately have ?wa ∈ new-actions-for P E adal by(rule new-actionsI)
thus ?thesis using wa-ra by blast
qed
qed

lemma executions-sc-hb:
  assumes wfxsyscls P
  and ts-ok wfx (thr (start-state f P C M vs)) start-heap
  and known-addr start-tid (f (fst (method P C M)) M (fst (snd (method P C M))) (fst (snd (snd
(method P C M)))) (the (snd (snd (snd (method P C M)))))) vs) ⊆ allocated start-heap
  shows
    executions-sc-hb (E-start f P C M vs status) P
    (is executions-sc-hb ?E P)
proof
  fix E a adal a'
  assume E ∈ ?E a ∈ new-actions-for P E adal a' ∈ new-actions-for P E adal
  thus a = a' by(rule E-new-actions-for-unique)
next

```

```

fix  $E$   $ra$  adal
assume  $E \in ?E$   $ra \in \text{read-actions } E$   $adal \in \text{action-loc } P E ra$ 
  and non-speculative  $P (\lambda-. \{ \}) (\text{ltake } (\text{enat } ra) (\text{lmap } \text{snd } E))$ 
with assms show  $\exists wa. wa \in \text{new-actions-for } P E adal \wedge wa < ra$ 
  by(rule Ex-new-action-for)
qed

```

lemma *executions-aux*:

```

assumes wf: wf-syscls  $P$ 
and wfx-start: ts-ok  $wfx$  (thr (start-state  $f P C M vs$ )) start-heap (is ts-ok  $wfx$  (thr ?start-state) -)
and ka: known-addrs start-tid ( $f$  (fst (method  $P C M$ ))  $M$  (fst (snd (method  $P C M$ ))) (fst (snd (snd (method  $P C M$ )))))) (the (snd (snd (snd (method  $P C M$ ))))))  $vs \subseteq \text{allocated start-heap}$ 
shows executions-aux ( $\mathcal{E}$ -start  $f P C M vs$  status)  $P$ 
  (is executions-aux  $?E P$ )

```

proof

```

fix  $E a$  adal  $a'$ 
assume  $E \in ?E$   $a \in \text{new-actions-for } P E adal$   $a' \in \text{new-actions-for } P E adal$ 
thus  $a = a'$  by(rule E-new-actions-for-unique)

```

next

```

fix  $E ws r$  adal
assume  $E: E \in ?E$ 
and wf-exec:  $P \vdash (E, ws) \checkmark$ 
and read:  $r \in \text{read-actions } E adal \in \text{action-loc } P E r$ 
and sc:  $\bigwedge a. [a < r; a \in \text{read-actions } E] \implies P, E \vdash a \rightsquigarrow_{mrw} ws a$ 

```

interpret *jmm*: *executions-sc-hb* $?E P$

using *wf wfx-start ka* **by**(*rule executions-sc-hb*)

from E *wf-exec* sc

have *ta-seq-consist* $P \text{Map.empty}$ (*ltake* (*enat* r) (*lmap* *snd* E))

unfolding *ltake-lmap* **by**(*rule jmm.ta-seq-consist-mrwI*) *simp*

hence *non-speculative* $P (\lambda-. \{ \}) (\text{ltake } (\text{enat } r) (\text{lmap } \text{snd } E))$

by(*rule ta-seq-consist-into-non-speculative*) *simp*

with *wf wfx-start ka E read*

have $\exists i. i \in \text{new-actions-for } P E adal \wedge i < r$

by(*rule Ex-new-action-for*)

thus $\exists i < r. i \in \text{new-actions-for } P E adal$ **by** *blast*

qed

lemma *drf*:

assumes *cut-and-update*:

if.cut-and-update

(*init-fin-lift-state* *status* (*start-state* $f P C M vs$))

(*mrw-values* $P \text{Map.empty}$ (*map* *snd* (*lift-start-obs* *start-tid* *start-heap-obs*)))

(**is** *if.cut-and-update* *?start-state* (*mrw-values* - - (*map* - *?start-heap-obs*)))

and *wf*: *wf-syscls* P

and *wfx-start*: *ts-ok* wfx (*thr* (*start-state* $f P C M vs$)) *start-heap*

and *ka*: *known-addr*s *start-tid* (f (*fst* (*method* $P C M$)) M (*fst* (*snd* (*method* $P C M$))) (*fst* (*snd* (*snd* (*method* $P C M$)))))) (*the* (*snd* (*snd* (*snd* (*method* $P C M$)))))) $vs \subseteq \text{allocated start-heap}$

shows *drf* (\mathcal{E} -*start* $f P C M vs$ *status*) P (**is** *drf* $?E -$)

proof -

interpret *jmm*: *executions-sc-hb* $?E P$

using *wf wfx-start ka* **by**(*rule executions-sc-hb*)

```

let ?n = length ?start-heap-obs
let ?E' = lappend (llist-of ?start-heap-obs) ' mthr.if.ℰ ?start-state

show ?thesis
proof
  fix E ws r
  assume E: E ∈ ?E'
  and wf: P ⊢ (E, ws) √
  and mrw:  $\bigwedge a. \llbracket a < r; a \in \text{read-actions } E \rrbracket \implies P, E \vdash a \rightsquigarrow_{\text{mrw}} ws a$ 
  show  $\exists E' \in ?E'. \exists ws'. P \vdash (E', ws') \sqrt \wedge \text{ltake } (\text{enat } r) E = \text{ltake } (\text{enat } r) E' \wedge$ 
     $\text{sequentially-consistent } P (E', ws') \wedge$ 
     $\text{action-tid } E r = \text{action-tid } E' r \wedge \text{action-obs } E r \approx \text{action-obs } E' r \wedge$ 
     $(r \in \text{actions } E \longrightarrow r \in \text{actions } E')$ 
  proof(cases  $\exists r'. r' \in \text{read-actions } E \wedge r \leq r'$ )
    case False
    have sequentially-consistent P (E, ws)
    proof(rule sequentially-consistentI)
      fix a
      assume a ∈ read-actions E
      with False have a < r by auto
      thus P, E ⊢ a  $\rightsquigarrow_{\text{mrw}}$  ws a using ⟨a ∈ read-actions E⟩ by(rule mrw)
    qed
    moreover have action-obs E r  $\approx$  action-obs E r by(rule sim-action-refl)
    ultimately show ?thesis using wf E by blast
  next
  case True
  let ?P =  $\lambda r'. r' \in \text{read-actions } E \wedge r \leq r'$ 
  let ?r = Least ?P
  from True obtain r' where r': ?P r' by blast
  hence r: ?P ?r by(rule LeastI)
  {
    fix a
    assume a < ?r a ∈ read-actions E
    have P, E ⊢ a  $\rightsquigarrow_{\text{mrw}}$  ws a
    proof(cases a < r)
      case True
      thus ?thesis using ⟨a ∈ read-actions E⟩ by(rule mrw)
    next
    case False
    with ⟨a ∈ read-actions E⟩ have ?P a by simp
    hence ?r ≤ a by(rule Least-le)
    with ⟨a < ?r⟩ have False by simp
    thus ?thesis ..
  }
  qed }
  note mrw' = this

from E obtain E'' where E: E = lappend (llist-of ?start-heap-obs) E''
  and E'': E'' ∈ mthr.if.ℰ ?start-state by auto

from E'' obtain E' where E': E'' = lconcat (lmap (λ(t, ta). llist-of (map (Pair t)  $\{\!|ta\!|_o\}$ )) E')
  and τRuns: mthr.if.mthr.Runs ?start-state E'
  by(rule mthr.if.ℰ.cases)

have r-len: length ?start-heap-obs ≤ ?r

```

proof(*rule ccontr*)
assume $\neg ?thesis$
hence $?r < \text{length } ?start\text{-heap}\text{-obs}$ **by** *simp*
moreover with $r \ E$ **obtain** $t \ ad \ al \ v$ **where** $?start\text{-heap}\text{-obs} ! ?r = (t, \text{NormalAction } (\text{ReadMem } ad \ al \ v))$
by(*cases ?start-heap-obs ! ?r*)(*fastforce elim!: read-actions.cases simp add: actions-def action-obs-def lnth-lappend1*)
ultimately have $(t, \text{NormalAction } (\text{ReadMem } ad \ al \ v)) \in \text{set } ?start\text{-heap}\text{-obs}$ **unfolding**
in-set-conv-nth **by** *blast*
thus *False* **by**(*auto simp add: start-heap-obs-not-Read*)
qed
let $?n = \text{length } ?start\text{-heap}\text{-obs}$
from $r \ r\text{-len } E$ **have** $r: ?r - ?n \in \text{read}\text{-actions } E''$
by(*fastforce elim!: read-actions.cases simp add: actions-lappend action-obs-def lnth-lappend2*
elim: actionsE intro: read-actions.intros)

from r **have** $?r - ?n \in \text{actions } E''$ **by**(*auto*)
hence $\text{enat } (?r - ?n) < \text{llength } E''$ **by**(*rule actionsE*)
with τRuns **obtain** $r\text{-m } r\text{-n } t\text{-r } ta\text{-r}$
where $E\text{-r}: \text{lnth } E'' (?r - ?n) = (t\text{-r}, \{\!|ta\text{-r}\!\}_o ! r\text{-n})$
and $r\text{-n}: r\text{-n} < \text{length } \{\!|ta\text{-r}\!\}_o$ **and** $r\text{-m}: \text{enat } r\text{-m} < \text{llength } E'$
and $r\text{-conv}: ?r - ?n = (\sum i < r\text{-m}. \text{length } \{\!|snd (\text{lnth } E' i)\!\}_o) + r\text{-n}$
and $E'\text{-r-m}: \text{lnth } E' r\text{-m} = (t\text{-r}, ta\text{-r})$
unfolding E' **by**(*rule mthr.if.actions-E-aux*)

let $?E' = \text{ldropn } (\text{Suc } r\text{-m}) E'$
let $?r\text{-m}\text{-}E' = \text{ltake } (\text{enat } r\text{-m}) E'$
have $E'\text{-unfold}: E' = \text{lappend } (\text{ltake } (\text{enat } r\text{-m}) E') (\text{LCons } (\text{lnth } E' r\text{-m}) ?E')$
unfolding *ldropn-Suc-conv-ldropn[OF r-m]* **by** *simp*
hence $\text{mthr.if.mthr.Runs } ?start\text{-state } (\text{lappend } ?r\text{-m}\text{-}E' (\text{LCons } (\text{lnth } E' r\text{-m}) ?E'))$
using τRuns **by** *simp*
then obtain σ' **where** $\sigma\text{-}\sigma': \text{mthr.if.mthr.Trsys } ?start\text{-state } (\text{list-of } ?r\text{-m}\text{-}E') \sigma'$
and $\tau\text{Runs}' : \text{mthr.if.mthr.Runs } \sigma' (\text{LCons } (\text{lnth } E' r\text{-m}) ?E')$
by(*rule mthr.if.mthr.Runs-lappendE*) *simp*
from $\tau\text{Runs}'$ **obtain** σ''' **where** $\text{red-ra}: \text{mthr.if.redT } \sigma' (t\text{-r}, ta\text{-r}) \sigma'''$
and $\tau\text{Runs}'' : \text{mthr.if.mthr.Runs } \sigma''' ?E'$
unfolding $E'\text{-r-m}$ **by** *cases*

let $?vs = \text{mrw-values } P \ \text{Map.empty } (\text{map } \text{snd } ?start\text{-heap}\text{-obs})$
{ **fix** a
assume $\text{enat } a < \text{enat } ?r$
and $a \in \text{read}\text{-actions } E$
have $a < r$
proof(*rule ccontr*)
assume $\neg a < r$
with $\langle a \in \text{read}\text{-actions } E \rangle$ **have** $?P \ a$ **by** *simp*
hence $?r \leq a$ **by**(*rule Least-le*)
with $\langle \text{enat } a < \text{enat } ?r \rangle$ **show** *False* **by** *simp*
qed
hence $P, E \vdash a \rightsquigarrow \text{mrw } vs \ a$ **using** $\langle a \in \text{read}\text{-actions } E \rangle$ **by**(*rule mrw*) **}**
with $\langle E \in ?\mathcal{E}' \rangle \ \text{wf}$ **have** $\text{ta-seq-consist } P \ \text{Map.empty } (\text{lmap } \text{snd } (\text{ltake } (\text{enat } ?r) E))$
by(*rule jmm.ta-seq-consist-mrwI*)

hence $\text{start-sc}: \text{ta-seq-consist } P \ \text{Map.empty } (\text{lmap } \text{snd } ?start\text{-heap}\text{-obs})$

and $ta\text{-seq-consist } P \text{ ?vs } (lmap \text{ snd } (ltake \text{ (enat } (?r - ?n)) E'))$
using $\langle ?n \leq ?r \rangle$ **unfolding** E $ltake\text{-lappend } lmap\text{-lappend-distrib}$
by ($simp\text{-all add: } ta\text{-seq-consist-lappend } o\text{-def}$)

note $this(2)$ **also from** $r\text{-m}$
have $r\text{-m-sum-len-eq: } (\sum i < r\text{-m. } llength \text{ (lnth } (lmap \text{ (}\lambda(t, ta). \text{ llist-of } (map \text{ (Pair } t) \text{ \{ta\}}_o)) E')$
 $i)) = \text{enat } (\sum i < r\text{-m. } length \text{ \{snd } (lnth \text{ } E' \text{ } i)\}_o)$
by ($subst \text{ sum-comp-morphism[symmetric, where } h = \text{enat}])$ ($auto \text{ simp add: zero-enat-def split-def}$
 $less\text{-trans[where } y = \text{enat } r\text{-m] intro: sum.cong}$)
hence $ltake \text{ (enat } (?r - ?n)) E'' =$
 $lappend \text{ (lconcat } (lmap \text{ (}\lambda(t, ta). \text{ llist-of } (map \text{ (Pair } t) \text{ \{ta\}}_o)) ?r\text{-m-E'})$
 $(ltake \text{ (enat } r\text{-n) (ldrop \text{ (enat } (\sum i < r\text{-m. } length \text{ \{snd } (lnth \text{ } E' \text{ } i)\}_o)) E'))$
unfolding $ltake\text{-lmap[symmetric] lconcat-ltake } r\text{-conv plus-enat-simps(1)[symmetric] ltake-plus-conv-lappend}$
unfolding E' **by** $simp$
finally have $ta\text{-seq-consist } P \text{ ?vs } (lmap \text{ snd } (lconcat \text{ (lmap } (\lambda(t, ta). \text{ llist-of } (map \text{ (Pair } t) \text{ \{ta\}}_o))$
 $?r\text{-m-E'))$)

and $sc\text{-ta-r: } ta\text{-seq-consist } P \text{ (mrw-values } P \text{ ?vs } (map \text{ snd } (list\text{-of } (lconcat \text{ (lmap } (\lambda(t, ta). \text{ llist-of } (map \text{ (Pair } t) \text{ \{ta\}}_o)) ?r\text{-m-E'))$
 $lmap \text{ snd } (ltake \text{ (enat } r\text{-n) (ldropn } (\sum i < r\text{-m. } length \text{ \{snd } (lnth \text{ } E' \text{ } i)\}_o) E'))$)

unfolding $lmap\text{-lappend-distrib by}(simp\text{-all add: } ta\text{-seq-consist-lappend split-def ldrop-enat}$
note $this(1)$ **also**
have $lmap \text{ snd } (lconcat \text{ (lmap } (\lambda(t, ta). \text{ llist-of } (map \text{ (Pair } t) \text{ \{ta\}}_o)) (ltake \text{ (enat } r\text{-m) } E'))$
 $= \text{ llist-of } (concat \text{ (map } (\lambda(t, ta). \text{ \{ta\}}_o) (list\text{-of } ?r\text{-m-E'))$)
unfolding $lmap\text{-lconcat llist.map-comp } o\text{-def split-def lconcat-llist-of[symmetric] map-map}$
 $lmap\text{-llist-of[symmetric]}$
by $simp$
finally have $ta\text{-seq-consist } P \text{ ?vs } (llist\text{-of } (concat \text{ (map } (\lambda(t, ta). \text{ \{ta\}}_o) (list\text{-of } ?r\text{-m-E'))$) .

from $if.\text{sequential-completion[OF cut-and-update } ta\text{-seq-consist-convert-RA } \sigma\text{-}\sigma'[\text{folded } mthr.if.RedT\text{-def}]$
 $this \text{ red-ra}]$

obtain $ta' \text{ ttas'}$
where $mthr.if.mthr.Runs \sigma' (LCons \text{ (}t\text{-r, } ta') \text{ ttas'})$
and $sc: ta\text{-seq-consist } P \text{ (mrw-values } P \text{ Map.empty } (map \text{ snd } ?start\text{-heap-obs})$
 $(lconcat \text{ (lmap } (\lambda(t, ta). \text{ llist-of } \{ta\}_o) (lappend \text{ (llist-of } (list\text{-of } ?r\text{-m-E')) (LCons$
 $(t\text{-r, } ta') \text{ ttas'))$)

and $eq\text{-ta: } eq\text{-upto-seq-inconsist } P \text{ \{ta-r\}_o \{ta'\}_o \text{ (mrw-values } P \text{ ?vs } (concat \text{ (map } (\lambda(t, ta). \text{ \{ta\}}_o) (list\text{-of } ?r\text{-m-E'))$)

by $blast$

let $?E\text{-sc}' = lappend \text{ (llist-of } (list\text{-of } ?r\text{-m-E')) (LCons \text{ (}t\text{-r, } ta') \text{ ttas'})$
let $?E\text{-sc}'' = lconcat \text{ (lmap } (\lambda(t, ta). \text{ llist-of } (map \text{ (Pair } t) \text{ \{ta\}}_o)) ?E\text{-sc}'$)
let $?E\text{-sc} = lappend \text{ (llist-of } ?start\text{-heap-obs}) ?E\text{-sc}''$

from $\sigma\text{-}\sigma' \langle mthr.if.mthr.Runs \sigma' (LCons \text{ (}t\text{-r, } ta') \text{ ttas'}) \rangle$
have $mthr.if.mthr.Runs ?start\text{-state } ?E\text{-sc}'$ **by** ($rule \text{ mthr.if.mthr.Trsys-into-Runs}$)
hence $?E\text{-sc}'' \in mthr.if.\mathcal{E} ?start\text{-state}$ **by** ($rule \text{ mthr.if.\mathcal{E}.intros}$)
hence $?E\text{-sc} \in ?\mathcal{E}$ **by** ($rule \text{ imageI}$)
moreover from $\langle ?E\text{-sc}'' \in mthr.if.\mathcal{E} ?start\text{-state} \rangle$
have $tsa\text{-ok: } thread\text{-start-actions-ok } ?E\text{-sc}$ **by** ($rule \text{ thread-start-actions-ok-init-fin}$)

from sc **have** $ta\text{-seq-consist } P \text{ Map.empty } (lmap \text{ snd } ?E\text{-sc})$
by ($simp \text{ add: } lmap\text{-lappend-distrib } o\text{-def } lmap\text{-lconcat llist.map-comp split-def } ta\text{-seq-consist-lappend}$
 $start\text{-sc}$)

from $ta\text{-seq-consist-imp-sequentially-consistent[OF tsa-ok } jmm.\mathcal{E}\text{-new-actions-for-fun[OF } \langle ?E\text{-sc} \in ?\mathcal{E} \rangle]$ $this]$

obtain *ws-sc* **where** *sequentially-consistent P* (*?E-sc*, *ws-sc*)
and $P \vdash (?E-sc, ws-sc) \checkmark$ **unfolding** *start-heap-obs-def[symmetric]* **by** *iprover*
moreover {
have *enat-sum-r-m-eq*: $enat (\sum i < r-m. length \{snd (lnth E' i)\}_o) = llength (lconcat (lmap (\lambda(t, ta). llist-of (map (Pair t) \{ta\}_o)) ?r-m-E'))$
by(*auto intro: sum.cong simp add: less-trans[OF - r-m] lnth-ltake llength-lconcat-lfinite-conv-sum sum-comp-morphism[symmetric, where h=enat] zero-enat-def[symmetric] split-beta*)
also have $\dots \leq llength E''$ **unfolding** *E'*
by(*blast intro: lprefix-llength-le lprefix-lconcatI lmap-lprefix*)
finally have *r-m-E*: $ltake (enat (?n + (\sum i < r-m. length \{snd (lnth E' i)\}_o))) E = ltake (enat (?n + (\sum i < r-m. length \{snd (lnth E' i)\}_o))) ?E-sc$
by(*simp add: ltake-lappend lappend-eq-lappend-conv lmap-lappend-distrib r-m-sum-len-eq ltake-lmap[symmetric] min-def zero-enat-def[symmetric] E E' lconcat-ltake ltake-all del: ltake-lmap*)

have *drop-r-m-E*: $ldropn (?n + (\sum i < r-m. length \{snd (lnth E' i)\}_o)) E = lappend (llist-of (map (Pair t-r) \{ta-r\}_o)) (lconcat (lmap (\lambda(t, ta). llist-of (map (Pair t) \{ta\}_o)) (ldropn (Suc r-m) E')))$
(is - = ?drop-r-m-E) using *E'-r-m unfolding E E'*
by(*subst (2) E'-unfold*)(*simp add: ldropn-lappend2 lmap-lappend-distrib enat-sum-r-m-eq[symmetric]*)

have *drop-r-m-E-sc*: $ldropn (?n + (\sum i < r-m. length \{snd (lnth E' i)\}_o)) ?E-sc = lappend (llist-of (map (Pair t-r) \{ta\}_o)) (lconcat (lmap (\lambda(t, ta). llist-of (map (Pair t) \{ta\}_o)) ttas^'))$
by(*simp add: ldropn-lappend2 lmap-lappend-distrib enat-sum-r-m-eq[symmetric]*)

let *?vs-r-m* = *mrw-values P ?vs (map snd (list-of (lconcat (lmap (\lambda(t, ta). llist-of (map (Pair t) \{ta\}_o)) ?r-m-E'))))*
note *sc-ta-r* **also**
from *drop-r-m-E* **have** $ldropn (\sum i < r-m. length \{snd (lnth E' i)\}_o) E'' = ?drop-r-m-E$
unfolding *E* **by**(*simp add: ldropn-lappend2*)
also have $lmap snd (ltake (enat r-n) \dots) = llist-of (take r-n \{ta-r\}_o)$ **using** *r-n*
by(*simp add: ltake-lappend lmap-lappend-distrib ltake-lmap[symmetric] take-map o-def zero-enat-def[symmetric] del: ltake-lmap*)
finally have *sc-ta-r*: $ta-seq-consist P ?vs-r-m (llist-of (take r-n \{ta-r\}_o))$.
note *eq-ta*
also have $\{ta-r\}_o = take r-n \{ta-r\}_o @ drop r-n \{ta-r\}_o$ **by** *simp*
finally have *eq-upto-seq-inconsist P* $(take r-n \{ta-r\}_o @ drop r-n \{ta-r\}_o) \{ta\}'_o ?vs-r-m$
by(*simp add: list-of-lconcat split-def o-def map-concat*)
from *eq-upto-seq-inconsist-appendD[OF this sc-ta-r]*
have *r-n'*: $r-n \leq length \{ta\}'_o$
and *take-r-n-eq*: $take r-n \{ta\}'_o = take r-n \{ta-r\}_o$
and *eq-r-n*: $eq-upto-seq-inconsist P (drop r-n \{ta-r\}_o) (drop r-n \{ta\}'_o) (mrw-values P ?vs-r-m (take r-n \{ta-r\}_o))$
using *r-n* **by**(*simp-all add: min-def*)
from *r-conv* $\langle ?n \leq ?r \rangle$ **have** *r-conv'*: $?r = (?n + (\sum i < r-m. length \{snd (lnth E' i)\}_o)) + r-n$
by *simp*
from *r-n' r-n take-r-n-eq r-m-E drop-r-m-E drop-r-m-E-sc*
have *take-r'-eq*: $ltake (enat ?r) E = ltake (enat ?r) ?E-sc$ **unfolding** *r-conv'*
apply(*subst (1 2) plus-enat-simps(1)[symmetric]*)
apply(*subst (1 2) ltake-plus-conv-lappend*)
apply(*simp add: lappend-eq-lappend-conv ltake-lappend1 ldrop-enat take-map*)
done
hence *take-r-eq*: $ltake (enat r) E = ltake (enat r) ?E-sc$
by(*rule ltake-eq-ltake-antimono*)(*simp add: \langle ?P ?r \rangle*)

```

from eq-r-n Cons-nth-drop-Suc[OF r-n, symmetric]
have drop r-n  $\{ta'\}_o \neq []$  by(auto simp add: eq-upto-seq-inconsist-simps)
hence r-n': r-n < length  $\{ta'\}_o$  by simp
hence eq-r-n:  $\{ta-r\}_o ! r-n \approx \{ta'\}_o ! r-n$ 
  using eq-r-n Cons-nth-drop-Suc[OF r-n, symmetric] Cons-nth-drop-Suc[OF r-n', symmetric]
by(simp add: eq-upto-seq-inconsist-simps split: action.split-asm obs-event.split-asm if-split-asm)
obtain tid-eq: action-tid E r = action-tid ?E-sc r
  and obs-eq: action-obs E r  $\approx$  action-obs ?E-sc r
proof(cases r < ?r)
  case True
  { from True have action-tid E r = action-tid (ltake (enat ?r) E) r
    by(simp add: action-tid-def lnth-ltake)
    also note take-r'-eq
    also have action-tid (ltake (enat ?r) ?E-sc) r = action-tid ?E-sc r
      using True by(simp add: action-tid-def lnth-ltake)
    finally have action-tid E r = action-tid ?E-sc r . }
  moreover
  { from True have action-obs E r = action-obs (ltake (enat ?r) E) r
    by(simp add: action-obs-def lnth-ltake)
    also note take-r'-eq
    also have action-obs (ltake (enat ?r) ?E-sc) r = action-obs ?E-sc r
      using True by(simp add: action-obs-def lnth-ltake)
    finally have action-obs E r  $\approx$  action-obs ?E-sc r by simp }
  ultimately show thesis by(rule that)
next
  case False
  with  $\langle ?P ?r \rangle$  have r-eq: r = ?r by simp
  hence lnth E r = (t-r,  $\{ta-r\}_o ! r-n$ ) using E-r r-conv' E by(simp add: lnth-lappend2)
  moreover have lnth ?E-sc r = (t-r,  $\{ta'\}_o ! r-n$ ) using  $\langle ?n \leq ?r \rangle$  r-n'
  by(subst r-eq)(simp add: r-conv lnth-lappend2 lmap-lappend-distrib enat-sum-r-m-eq[symmetric]
lnth-lappend1 del: length-lift-start-obs)
  ultimately have action-tid E r = action-tid ?E-sc r action-obs E r  $\approx$  action-obs ?E-sc r
    using eq-r-n by(simp-all add: action-tid-def action-obs-def)
  thus thesis by(rule that)
qed

  have enat r < enat ?n + llength (lconcat (lmap ( $\lambda(t, ta).$  llist-of (map (Pair t)  $\{ta\}_o$ )) (lappend
  ?r-m-E' (LCons (t-r, ta') LNil))))
    using  $\langle ?P ?r \rangle$  r-n' unfolding lmap-lappend-distrib
    by(simp add: enat-sum-r-m-eq[symmetric] r-conv')
  also have llength (lconcat (lmap ( $\lambda(t, ta).$  llist-of (map (Pair t)  $\{ta\}_o$ )) (lappend ?r-m-E'
  (LCons (t-r, ta') LNil))))  $\leq$  llength ?E-sc''
    by(rule lprefix-llength-le[OF lprefix-lconcatI])(simp add: lmap-lprefix)
  finally have r  $\in$  actions ?E-sc by(simp add: actions-def add-left-mono)
  note this tid-eq obs-eq take-r-eq }
  ultimately show ?thesis by blast
qed
qed(rule  $\mathcal{E}$ -new-actions-for-unique)
qed

```

lemma sc-legal:

assumes hb-completion:

if.hb-completion (init-fin-lift-state status (start-state f P C M vs)) (lift-start-obs start-tid start-heap-obs)


```

(is if.hb-completion ?start-state ?start-heap-obs)
and wf: wf-syscls P
and wfx-start: ts-ok wfx (thr (start-state f P C M vs)) start-heap
and ka: known-addr start-tid (f (fst (method P C M)) M (fst (snd (method P C M))) (fst (snd
(snd (method P C M)))) (the (snd (snd (snd (method P C M)))))) vs)  $\subseteq$  allocated start-heap
shows sc-legal ( $\mathcal{E}$ -start f P C M vs status) P
(is sc-legal ? $\mathcal{E}$  P)
proof -
interpret jmm: executions-sc-hb ? $\mathcal{E}$  P
using wf wfx-start ka by(rule executions-sc-hb)

interpret jmm: executions-aux ? $\mathcal{E}$  P
using wf wfx-start ka by(rule executions-aux)

show ?thesis
proof
fix E ws r
assume E: E  $\in$  ? $\mathcal{E}$  and wf-exec: P  $\vdash$  (E, ws)  $\checkmark$ 
and mrw:  $\bigwedge a. [a < r; a \in \text{read-actions } E] \implies P, E \vdash a \rightsquigarrow_{\text{mrw}} ws a$ 

from E obtain E'' where E: E = lappend (llist-of ?start-heap-obs) E''
and E'': E''  $\in$  mthr.if. $\mathcal{E}$  ?start-state by auto

from E'' obtain E' where E': E'' = lconcat (lmap ( $\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \{\{ta\}_o\}) E'$ ) E')
and  $\tau$ Runs: mthr.if.mthr.Runs ?start-state E'
by(rule mthr.if. $\mathcal{E}$ .cases)

show  $\exists E' \in ?\mathcal{E}. \exists ws'. P \vdash (E', ws') \checkmark \wedge \text{ltake } (\text{enat } r) E = \text{ltake } (\text{enat } r) E' \wedge$ 
 $(\forall a \in \text{read-actions } E'. \text{if } a < r \text{ then } ws' a = ws a \text{ else } P, E' \vdash ws' a \leq_{\text{hb}} a) \wedge$ 
 $\text{action-tid } E' r = \text{action-tid } E r \wedge$ 
 $(\text{if } r \in \text{read-actions } E \text{ then sim-action else } (=)) (\text{action-obs } E' r) (\text{action-obs } E$ 
r)  $\wedge$ 
 $(r \in \text{actions } E \longrightarrow r \in \text{actions } E')$ 
(is  $\exists E' \in ?\mathcal{E}. \exists ws'. - \wedge ?\text{same } E' \wedge ?\text{read } E' ws' \wedge ?\text{tid } E' \wedge ?\text{obs } E' \wedge ?\text{actions } E'$ )
proof(cases r < length ?start-heap-obs)
case True

from if.hb-completion-Runs[OF hb-completion ta-hb-consistent-convert-RA]
obtain ttas where Runs: mthr.if.mthr.Runs ?start-state ttas
and hb: ta-hb-consistent P ?start-heap-obs (lconcat (lmap ( $\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \{\{ta\}_o\})$ 
ttas))
by blast

from Runs have  $\mathcal{E}$ : lconcat (lmap ( $\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \{\{ta\}_o\})$  ttas)  $\in$  mthr.if. $\mathcal{E}$ 
?start-state
by(rule mthr.if. $\mathcal{E}$ .intros)

let ?E = lappend (llist-of ?start-heap-obs) (lconcat (lmap ( $\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \{\{ta\}_o\})$ 
ttas))
from  $\mathcal{E}$  have E': ?E  $\in$  ? $\mathcal{E}$  by blast

from  $\mathcal{E}$  have tsa: thread-start-actions-ok ?E by(rule thread-start-actions-ok-init-fin)

```

from *start-heap-obs-not-Read*
have *ws*: *is-write-seen* *P* (*l*list-of (*lift-start-obs* *start-tid* *start-heap-obs*)) *ws*
by(*unfold in-set-conv-nth*)(*rule is-write-seenI*, *auto simp add: action-obs-def actions-def lift-start-obs-def lnth-LCons elim!*: *read-actions.cases split: nat.split-asm*)

with *hb tsa*
have $\exists ws'. P \vdash (?E, ws') \checkmark \wedge$
 $(\forall n. n \in \text{read-actions } ?E \longrightarrow \text{length } ?\text{start-heap-obs} \leq n \longrightarrow P, ?E \vdash ws' n \leq_{hb} n) \wedge$
 $(\forall n < \text{length } ?\text{start-heap-obs}. ws' n = ws n)$
by(*rule ta-hb-consistent-Read-hb*)(*rule jmm.E-new-actions-for-fun*[*OF E'*])

then obtain *ws'* **where** *wf-exec'*: $P \vdash (?E, ws') \checkmark$
and *read-hb*: $\bigwedge n. \llbracket n \in \text{read-actions } ?E; \text{length } ?\text{start-heap-obs} \leq n \rrbracket \Longrightarrow P, ?E \vdash ws' n \leq_{hb} n$
and *same*: $\bigwedge n. n < \text{length } ?\text{start-heap-obs} \Longrightarrow ws' n = ws n$ **by** *blast*

from *True* **have** *?same* *?E* **unfolding** *E* **by**(*simp add: ltake-lappend1*)
moreover {
fix *a*
assume *a*: $a \in \text{read-actions } ?E$
have *if* $a < r$ **then** $ws' a = ws a$ **else** $P, ?E \vdash ws' a \leq_{hb} a$
proof(*cases a < length ?start-heap-obs*)
case *True*
with *a* **have** *False* **using** *start-heap-obs-not-Read*
by *cases*(*auto simp add: action-obs-def actions-def lnth-lappend1 lift-start-obs-def lnth-LCons in-set-conv-nth split: nat.split-asm*)
thus *?thesis ..*
next
case *False*
with *read-hb*[*of a*] *True a* **show** *?thesis* **by** *auto*
qed }
hence *?read* *?E* *ws'* **by** *blast*
moreover from *True E* **have** *?tid* *?E* **by**(*simp add: action-tid-def lnth-lappend1*)
moreover from *True E* **have** *?obs* *?E* **by**(*simp add: action-obs-def lnth-lappend1*)
moreover from *True* **have** *?actions* *?E* **by**(*simp add: actions-def enat-less-enat-plusI*)
ultimately show *?thesis* **using** *E'* *wf-exec'* **by** *blast*

next
case *False*
hence *r*: $\text{length } ?\text{start-heap-obs} \leq r$ **by** *simp*

show *?thesis*
proof(*cases enat r < llength E*)
case *False*
then obtain *?same* *E* *?read* *E* *ws* *?tid* *E* *?obs* *E* *?actions* *E*
by(*cases llength E*)(*fastforce elim!*: *read-actions.cases simp add: actions-def split: if-split-asm*)
with *wf-exec* $\langle E \in ?\mathcal{E} \rangle$ **show** *?thesis* **by** *blast*

next
case *True*
note $r' = \text{this}$

let $?r = r - \text{length } ?\text{start-heap-obs}$
from *E r r'* **have** *enat* $?r < \text{llength } E''$ **by**(*cases llength E''*)(*auto*)
with τRuns **obtain** *r-m* *r-n* *t-r* *ta-r*
where *E-r*: $\text{lnth } E'' ?r = (t-r, \{\!\!| \text{ta-r} \!\!\}_o ! r-n)$
and *r-n*: $r-n < \text{length } \{\!\!| \text{ta-r} \!\!\}_o$ **and** *r-m*: $\text{enat } r-m < \text{llength } E'$
and *r-conv*: $?r = (\sum_{i < r-m}. \text{length } \{\!\!| \text{snd } (\text{lnth } E' i) \!\!\}_o) + r-n$

and $E'-r-m$: $\text{lnth } E' \ r-m = (t-r, ta-r)$
unfolding E' **by**(rule $\text{mthr.if.actions-}\mathcal{E}E\text{-aux}$)

let $?E' = \text{ldropn } (\text{Suc } r-m) \ E'$
let $?r-m-E' = \text{ltake } (\text{enat } r-m) \ E'$
have $E'\text{-unfold}$: $E' = \text{lappend } (\text{ltake } (\text{enat } r-m) \ E') \ (\text{LCons } (\text{lnth } E' \ r-m) \ ?E')$
unfolding $\text{ldropn-Suc-conv-ldropn}[OF \ r-m]$ **by** simp
hence $\text{mthr.if.mthr.Runs } ?\text{start-state } (\text{lappend } ?r-m-E' \ (\text{LCons } (\text{lnth } E' \ r-m) \ ?E'))$
using τRuns **by** simp
then obtain σ' **where** $\sigma\text{-}\sigma'$: $\text{mthr.if.mthr.Trsys } ?\text{start-state } (\text{list-of } ?r-m-E') \ \sigma'$
and $\tau\text{Runs}'$: $\text{mthr.if.mthr.Runs } \sigma' \ (\text{LCons } (\text{lnth } E' \ r-m) \ ?E')$
by(rule $\text{mthr.if.mthr.Runs-lappendE}$) simp
from $\tau\text{Runs}'$ **obtain** σ''' **where** red-ra : $\text{mthr.if.redT } \sigma' \ (t-r, ta-r) \ \sigma'''$
and $\tau\text{Runs}''$: $\text{mthr.if.mthr.Runs } \sigma''' \ ?E'$
unfolding $E'-r-m$ **by** cases

let $?vs = \text{mrw-values } P \ \text{Map.empty } (\text{map } \text{snd } ?\text{start-heap-obs})$
from $\langle E \in ?\mathcal{E} \rangle \ \text{wf-exec}$ **have** $\text{ta-seq-consist } P \ \text{Map.empty } (\text{lmap } \text{snd } (\text{ltake } (\text{enat } r) \ E))$
by(rule $\text{jmm.ta-seq-consist-mrwI}$)(simp add: mrw)
hence ns : $\text{non-speculative } P \ (\lambda-. \ \{\}) \ (\text{lmap } \text{snd } (\text{ltake } (\text{enat } r) \ E))$
by(rule $\text{ta-seq-consist-into-non-speculative}$) simp
also note E **also note** ltake-lappend2 **also note** E'
also note $E'\text{-unfold}$ **also note** $\text{lmap-lappend-distrib}$ **also note** $\text{lmap-lappend-distrib}$
also note lconcat-lappend **also note** $\text{lmap.map}(2)$ **also note** $E'-r-m$ **also note** $\text{prod.simps}(2)$
also note ltake-lappend2 **also note** lconcat-LCons **also note** ltake-lappend1
also note $\text{non-speculative-lappend}$ **also note** $\text{lmap-lappend-distrib}$ **also note** $\text{non-speculative-lappend}$
also have $\text{lconcat } (\text{lmap } (\lambda(t, ta). \ \text{lmap.map}(2) \ (\text{map } (\text{Pair } t) \ \{\!\!\{ta\}\!\!\}_o)) \ (\text{ltake } (\text{enat } r-m) \ E')) =$
 $\text{lmap.map}(2) \ (\text{lconcat } (\text{map } (\lambda(t, ta). \ \text{map } (\text{Pair } t) \ \{\!\!\{ta\}\!\!\}_o) \ (\text{list-of } (\text{ltake } (\text{enat } r-m) \ E'))))$
by($\text{simp add: lconcat-lmap.map}(2)$) simp
 $\text{del: lmap-lmap.map}(2)$

ultimately

have $\text{non-speculative } P \ (\lambda-. \ \{\}) \ (\text{lmap } \text{snd } (\text{lmap.map}(2) \ ?\text{start-heap-obs}))$
and $\text{non-speculative } P \ (\text{w-values } P \ (\lambda-. \ \{\}) \ (\text{map } \text{snd } ?\text{start-heap-obs}))$
 $(\text{lmap } \text{snd } (\text{lconcat } (\text{lmap } (\lambda(t, ta). \ \text{lmap.map}(2) \ (\text{map } (\text{Pair } t) \ \{\!\!\{ta\}\!\!\}_o)) \ (\text{ltake } (\text{enat } r-m) \ E'))))$
and ns' : $\text{non-speculative } P \ (\text{w-values } P \ (\text{w-values } P \ (\lambda-. \ \{\}) \ (\text{map } \text{snd } ?\text{start-heap-obs})) \ (\text{map } \text{snd } (\text{concat } (\text{map } (\lambda(t, ta). \ \text{map } (\text{Pair } t) \ \{\!\!\{ta\}\!\!\}_o) \ (\text{list-of } (\text{ltake } (\text{enat } r-m) \ E')))))$
 $(\text{lmap } \text{snd } (\text{ltake } (\text{enat } r-n) \ (\text{lmap.map}(2) \ (\text{map } (\text{Pair } t-r) \ \{\!\!\{ta-r\}\!\!\}_o))))$
using $r \ r\text{-conv } r-m \ r-n$
by($\text{simp-all add: length-concat o-def split-def sum-list-sum-nth length-list-of-conv-the-enat less-min-eq1 atLeast0LessThan lnth-ltake split: if-split-asm cong: sum.cong-simp}$)
hence ns : $\text{non-speculative } P \ (\text{w-values } P \ (\lambda-. \ \{\}) \ (\text{map } \text{snd } ?\text{start-heap-obs}))$
 $(\text{lmap.map}(2) \ (\text{lconcat } (\text{map } (\lambda(t, ta). \ \{\!\!\{ta\}\!\!\}_o) \ (\text{list-of } (\text{ltake } (\text{enat } r-m) \ E')))))$
unfolding $\text{lconcat-lmap.map}(2)$ simp lmap-lconcat $\text{lmap-lmap.map}(2)$ lmap.map-comp
 o-def split-def
by(simp)

from ns'

have ns' : $\text{non-speculative } P \ (\text{w-values } P \ (\text{w-values } P \ (\lambda-. \ \{\}) \ (\text{map } \text{snd } ?\text{start-heap-obs})) \ (\text{concat } (\text{map } (\lambda(t, ta). \ \{\!\!\{ta\}\!\!\}_o) \ (\text{list-of } (\text{ltake } (\text{enat } r-m) \ E')))) \ (\text{lmap.map}(2) \ (\text{take } r-n \ \{\!\!\{ta-r\}\!\!\}_o))$
unfolding map-concat map-map **by**($\text{simp add: take-map}[symmetric]$) o-def split-def

let $?hb = \lambda\text{ta}'\text{-}r \ :: \ ('addr, 'thread\text{-}id, \text{status} \times 'x, 'heap, 'addr, ('addr, 'thread\text{-}id) \ \text{obs-event action}) \ \text{thread-action}$.

$\text{ta-hb-consistent } P \ (?start\text{-}heap\text{-}obs \ @ \ \text{concat } (\text{map } (\lambda(t, ta). \ \text{map } (\text{Pair } t) \ \{\!\!\{ta\}\!\!\}_o) \ (\text{list-of } (\text{ltake$

(*enat* *r-m*) *E'*)) @ *map* (*Pair* *t-r*) (*take* *r-n* $\{\{ta-r\}_o\}$) (*l*list-of (*map* (*Pair* *t-r*) (*drop* *r-n* $\{\{ta'-r\}_o\}$)))
let *?sim* = $\lambda ta'-r.$ (*if* $\exists ad$ *al* *v.* $\{\{ta-r\}_o\} ! r-n = \text{NormalAction}$ (*ReadMem* *ad* *al* *v*) *then*
sim-action **else** (=)) ($\{\{ta-r\}_o\} ! r-n$) ($\{\{ta'-r\}_o\} ! r-n$)

from *red-ra* **obtain** *ta'-r* σ''''

where *red-ra'*: *mthr.if.redT* σ' (*t-r*, *ta'-r*) σ''''

and *eq*: *take* *r-n* $\{\{ta'-r\}_o\} = \text{take } r-n \{\{ta-r\}_o\}$

and *hb*: *?hb* *ta'-r*

and *r-n'*: *r-n* < *length* $\{\{ta'-r\}_o\}$

and *sim*: *?sim* *ta'-r*

proof(*cases*)

case (*redT-normal* *x* *x'* *m'*)

note *tst* = $\langle \text{thr } \sigma' \text{ } t-r = \lfloor (x, \text{no-wait-locks}) \rfloor \rangle$

and *red* = $\langle t-r \vdash (x, \text{shr } \sigma') -ta-r \rightarrow i (x', m') \rangle$

and *aok* = $\langle \text{mthr.if.actions-ok } \sigma' \text{ } t-r \text{ } ta-r \rangle$

and $\sigma'''' = \langle \text{redT-upd } \sigma' \text{ } t-r \text{ } ta-r \text{ } x' \text{ } m' \text{ } \sigma'''' \rangle$

from *if.hb-completionD*[*OF* *hb-completion* σ - σ' [*folded* *mthr.if.RedT-def*] *ns* *tst* *red* *aok* *ns'*]

r-n

obtain *ta'-r* *x''* *m''*

where *red'*: *t-r* $\vdash (x, \text{shr } \sigma') -ta'-r \rightarrow i (x'', m'')$

and *aok'*: *mthr.if.actions-ok* $\sigma' \text{ } t-r \text{ } ta'-r$

and *eq'*: *take* *r-n* $\{\{ta'-r\}_o\} = \text{take } r-n \{\{ta-r\}_o\}$

and *hb*: *?hb* *ta'-r*

and *r-n'*: *r-n* < *length* $\{\{ta'-r\}_o\}$

and *sim*: *?sim* *ta'-r* **by** *blast*

from *redT-updWs-total*[*of* *t-r* *wset* σ' $\{\{ta'-r\}_w\}$]

obtain σ'''' **where** *redT-upd* $\sigma' \text{ } t-r \text{ } ta'-r \text{ } x'' \text{ } m'' \text{ } \sigma''''$ **by** *fastforce*

with *red'* *tst* *aok'* **have** *mthr.if.redT* σ' (*t-r*, *ta'-r*) σ'''' ..

thus *thesis* **using** *eq'* *hb* *r-n'* *sim* **by**(*rule* *that*)

next

case (*redT-acquire* *x* *n* *ln*)

hence *?hb* *ta-r* **using** *set-convert-RA-not-Read*[**where** *ln=ln*]

by $-(\text{rule } ta-hb-consistent-not-ReadI, \text{fastforce } simp \text{ del: } set-convert-RA-not-Read \text{ dest!:$

in-set-dropD)

with *red-ra* *r-n* **show** *?thesis* **by**(*auto* *intro: that*)

qed

from *hb*

have *non-speculative* *P* (*w-values* *P* ($\lambda.$ $\{\}$) (*map* *snd* (*?start-heap-obs* @ *concat* (*map* ($\lambda(t, ta).$ *map* (*Pair* *t*) $\{\{ta\}_o\}$) (*list-of* (*l*take (*enat* *r-m*) *E'*))) @ *map* (*Pair* *t-r*) (*take* *r-n* $\{\{ta-r\}_o\}$)))) (*l*map *snd* (*l*list-of (*map* (*Pair* *t-r*) (*drop* *r-n* $\{\{ta'-r\}_o\}$))))

by(*rule* *ta-hb-consistent-into-non-speculative*)

with *ns'* *eq*[*symmetric*] **have** *non-speculative* *P* (*w-values* *P* ($\lambda.$ $\{\}$) (*map* *snd* (*?start-heap-obs* @ *concat* (*map* ($\lambda(t, ta).$ *map* (*Pair* *t*) $\{\{ta\}_o\}$) (*list-of* (*l*take (*enat* *r-m*) *E'*)))))) (*l*list-of (*map* *snd* (*map* (*Pair* *t-r*) $\{\{ta'-r\}_o\}$))))

by(*subst* *append-take-drop-id*[**where** *xs*= $\{\{ta'-r\}_o\}$ **and** *n*=*r-n*, *symmetric*])(*simp* *add: o-def* *map-concat* *split-def* *lappend-l*list-of-*l*list-of[*symmetric*] *non-speculative-lappend* *del: append-take-drop-id* *lappend-l*list-of-*l*list-of)

with *ns* **have** *ns''*: *non-speculative* *P* (*w-values* *P* ($\lambda.$ $\{\}$) (*map* *snd* *?start-heap-obs*) (*l*list-of (*concat* (*map* ($\lambda(t, ta).$ $\{\{ta\}_o\}$) (*list-of* (*l*take (*enat* *r-m*) *E'*) @ [(*t-r*, *ta'-r*)]))))

unfolding *lconcat-l*list-of[*symmetric*] *map-append* *lappend-l*list-of-*l*list-of[*symmetric*] *lmap-l*list-of[*symmetric*] *l*list.*map-comp*

by(*simp* *add: o-def* *split-def* *non-speculative-lappend* *list-of-lconcat* *map-concat*)

from σ - σ' *red-ra'* **have** *mthr.if.RedT* *?start-state* (*list-of* *?r-m-E'* @ [(*t-r*, *ta'-r*)] σ'''')

unfolding *mthr.if.RedT-def* ..

with *hb-completion*
have *hb-completion'*: *if.hb-completion* σ'''' (*?start-heap-obs* @ *concat* (*map* ($\lambda(t, ta)$). *map* (*Pair* t) $\{\!\{ta\}\!\}_o$) (*list-of* (*ltake* (*enat* $r-m$) E') @ $[(t-r, ta'-r)]$))
using *ns''* **by** (*rule if.hb-completion-shift*)
from *if.hb-completion-Runs*[*OF hb-completion' ta-hb-consistent-convert-RA*]
obtain *ttas'* **where** *Runs'*: *mthr.if.mthr.Runs* σ'''' *ttas'*
and *hb'*: *ta-hb-consistent* P (*?start-heap-obs* @ *concat* (*map* ($\lambda(t, ta)$). *map* (*Pair* t) $\{\!\{ta\}\!\}_o$) (*list-of* (*ltake* (*enat* $r-m$) E') @ $[(t-r, ta'-r)]$)) (*lconcat* (*lmap* ($\lambda(t, ta)$). *llist-of* (*map* (*Pair* t) $\{\!\{ta\}\!\}_o$) *ttas'*))
by *blast*

let $?E = \text{lappend} (\text{llist-of } ?\text{start-heap-obs}) (\text{lconcat} (\text{lmap} (\lambda(t, ta)$. *llist-of* (*map* (*Pair* t) $\{\!\{ta\}\!\}_o$) (*lappend* (*ltake* (*enat* $r-m$) E') (*LCons* ($t-r, ta'-r$) *ttas'*))))

have \mathcal{E} : *lconcat* (*lmap* ($\lambda(t, ta)$. *llist-of* (*map* (*Pair* t) $\{\!\{ta\}\!\}_o$) (*lappend* (*ltake* (*enat* $r-m$) E') (*LCons* ($t-r, ta'-r$) *ttas'*))) \in *mthr.if.* \mathcal{E} *?start-state*
by (*subst* (λ) *llist-of-list-of*[*symmetric*])(*simp*, *blast* *intro*: *mthr.if.* \mathcal{E} .*intros* *mthr.if.mthr.Trsys-into-Runs* σ - σ' *mthr.if.mthr.Runs.Step* *red-ra'* *Runs'*)
hence \mathcal{E}' : $?E \in ?\mathcal{E}$ **by** *blast*

from \mathcal{E} **have** *tsa*: *thread-start-actions-ok* $?E$ **by** (*rule thread-start-actions-ok-init-fin*)
also let $?E' = \text{lappend} (\text{llist-of} (\text{lift-start-obs } \text{start-tid } \text{start-heap-obs} @ \text{concat} (\text{map} (\lambda(t, ta)$. *map* (*Pair* t) $\{\!\{ta\}\!\}_o$) (*list-of* (*ltake* (*enat* $r-m$) E'))) @ *map* (*Pair* $t-r$) (*take* $r-n$ $\{\!\{ta-r\}\!\}_o$)) (*lappend* (*llist-of* (*map* (*Pair* $t-r$) (*drop* $r-n$ $\{\!\{ta'-r\}\!\}_o$)) (*lconcat* (*lmap* ($\lambda(t, ta)$. *llist-of* (*map* (*Pair* t) $\{\!\{ta\}\!\}_o$) *ttas'*)))
have $?E = ?E'$
using *eq*[*symmetric*]
by (*simp* *add*: *lmap-lappend-distrib* *lappend-assoc* *lappend-llist-of-llist-of*[*symmetric*] *lconcat-llist-of*[*symmetric*] *lmap-llist-of*[*symmetric*] *llist.map-comp* *o-def* *split-def* *del*: *lmap-llist-of*)(*simp* *add*: *lappend-assoc*[*symmetric*] *lmap-lappend-distrib*[*symmetric*] *map-append*[*symmetric*] *lappend-llist-of-llist-of* *del*: *map-append*)
finally have *tsa'*: *thread-start-actions-ok* $?E'$.

from *hb* *hb'* *eq*[*symmetric*]
have *HB*: *ta-hb-consistent* P (*?start-heap-obs* @ *concat* (*map* ($\lambda(t, ta)$). *map* (*Pair* t) $\{\!\{ta\}\!\}_o$) (*list-of* (*ltake* (*enat* $r-m$) E')) @ *map* (*Pair* $t-r$) (*take* $r-n$ $\{\!\{ta-r\}\!\}_o$) (*lappend* (*llist-of* (*map* (*Pair* $t-r$) (*drop* $r-n$ $\{\!\{ta'-r\}\!\}_o$)) (*lconcat* (*lmap* ($\lambda(t, ta)$. *llist-of* (*map* (*Pair* t) $\{\!\{ta\}\!\}_o$) *ttas'*)))
by $-(\text{rule } \text{ta-hb-consistent-lappendI}, \text{simp-all } \text{add: } \text{take-map}[\text{symmetric}] \text{ drop-map}[\text{symmetric}])$

define *EE* **where** $EE = \text{llist-of} (?\text{start-heap-obs} @ \text{concat} (\text{map} (\lambda(t, ta)$. *map* (*Pair* t) $\{\!\{ta\}\!\}_o$) (*list-of* (*ltake* (*enat* $r-m$) E')) @ *map* (*Pair* $t-r$) (*take* $r-n$ $\{\!\{ta-r\}\!\}_o$))

from r *r-conv* **have** *r-conv'*: $r = (\sum_{i < r-m} \text{length } \{\!\{snd (\text{lnth } E' i)\}\!\}_o) + r-n + \text{length } ?\text{start-heap-obs}$ **by** *auto*
hence *len-EE*: *llength* *EE* = *enat* r **using** $r-m$ $r-n$
by (*auto* *simp* *add*: *EE-def* *length-concat* *sum-list-sum-nth* *atLeast0LessThan* *lnth-ltake* *less-min-eq1* *split-def* *min-def* *length-list-of-conv-the-enat* *cong*: *sum.cong-simp*)

from r -*conv* $r-m$
have *r-conv3*: *llength* (*lconcat* (*lmap* (λx . *llist-of* (*map* (*Pair* (*fst* x)) $\{\!\{snd\} x\}\!\}_o$) (*ltake* (*enat* $r-m$) E'))) = *enat* ($r - \text{Suc} (\text{length } \text{start-heap-obs}) - r-n$)
apply (*simp* *add*: *llength-lconcat-lfinite-conv-sum* *lnth-ltake* *cong*: *sum.cong-simp* *conj-cong*)
apply (*auto* *simp* *add*: *sum-comp-morphism*[**where** $h = \text{enat}$, *symmetric*] *zero-enat-def* *less-trans*[**where** $y = \text{enat } r-m$] *intro*: *sum.cong*)

```

done

have is-ws: is-write-seen P EE ws
proof(rule is-write-seenI)
  fix a ad al v
  assume a: a ∈ read-actions EE
  and a-obs: action-obs EE a = NormalAction (ReadMem ad al v)
  from a have a-r: a < r by cases(simp add: len-EE actions-def)

  from r E'-r-m r-m r-n r-conv3
  have eq: ltake (enat r) EE = ltake (enat r) E
  unfolding E E' EE-def
  apply(subst (2) E'-unfold)
  apply(simp add: ltake-lappend2 lappend-llist-of-llist-of[symmetric] lappend-eq-lappend-conv
lmap-lappend-distrib lconcat-llist-of[symmetric] o-def split-def lmap-llist-of[symmetric] del: lappend-llist-of-llist-of
lmap-llist-of)
  apply(subst ltake-lappend1)
  defer
  apply(simp add: ltake-lmap[symmetric] take-map[symmetric] ltake-llist-of[symmetric] del:
ltake-lmap ltake-llist-of)
  apply(auto simp add: min-def)
  done
  hence sim: ltake (enat r) EE [≈] ltake (enat r) E by(rule eq-into-sim-actions)

  from a sim have a': a ∈ read-actions E
  by(rule read-actions-change-prefix)(simp add: a-r)
  from action-obs-change-prefix-eq[OF eq, of a] a-r a-obs
  have a-obs': action-obs E a = NormalAction (ReadMem ad al v) by simp

  have a-mrw: P,E ⊢ a ~>mrw ws a using a-r a' by(rule mrw)
  with ⟨E ∈ ?E⟩ wf-exec have ws-a-a: ws a < a
  by(rule jmm.mrw-before)(auto intro: a-r less-trans mrw)
  hence [simp]: ws a < r using a-r by simp

  from wf-exec have ws: is-write-seen P E ws by(rule wf-exec-is-write-seenD)
  from is-write-seenD[OF this a' a-obs']
  have ws a ∈ write-actions E
  and (ad, al) ∈ action-loc P E (ws a)
  and value-written P E (ws a) (ad, al) = v
  and ¬ P,E ⊢ a ≤hb ws a
  and is-volatile P al ⇒ ¬ P,E ⊢ a ≤so ws a
  and between: ∧ a'. [ a' ∈ write-actions E; (ad, al) ∈ action-loc P E a';
P,E ⊢ ws a ≤hb a' ∧ P,E ⊢ a' ≤hb a ∨ is-volatile P al ∧ P,E ⊢ ws a ≤so a' ∧
P,E ⊢ a' ≤so a ]
  ⇒ a' = ws a by simp-all

  from ⟨ws a ∈ write-actions E⟩ sim[symmetric]
  show ws a ∈ write-actions EE by(rule write-actions-change-prefix) simp

  from action-loc-change-prefix[OF sim, of ws a P] ⟨(ad, al) ∈ action-loc P E (ws a)⟩
  show (ad, al) ∈ action-loc P EE (ws a) by(simp)

  from value-written-change-prefix[OF eq, of ws a P] ⟨value-written P E (ws a) (ad, al) = v⟩
  show value-written P EE (ws a) (ad, al) = v by simp

```

from $wf\text{-exec}$ **have** $tsa\text{-}E$: *thread-start-actions-ok* E
by(*rule wf-exec-thread-start-actions-okD*)

from $\langle \neg P, E \vdash a \leq_{hb} ws \ a \rangle$ **show** $\neg P, EE \vdash a \leq_{hb} ws \ a$
proof(*rule contrapos-nn*)
assume $P, EE \vdash a \leq_{hb} ws \ a$
thus $P, E \vdash a \leq_{hb} ws \ a$ **using** $tsa\text{-}E \text{ sim}$
by(*rule happens-before-change-prefix*)(*simp-all add: a-r*)
qed

{ **assume** *is-volatile* $P \ al$
hence $\neg P, E \vdash a \leq_{so} ws \ a$ **by** *fact*
thus $\neg P, EE \vdash a \leq_{so} ws \ a$
by(*rule contrapos-nn*)(*rule sync-order-change-prefix*[$OF - sim$], *simp-all add: a-r*) }

fix a'
assume $a' \in \text{write-actions } EE \ (ad, al) \in \text{action-loc } P \ EE \ a'$
moreover
hence [*simp*]: $a' < r$ **by** *cases*(*simp add: actions-def len-EE*)
ultimately have a' : $a' \in \text{write-actions } E \ (ad, al) \in \text{action-loc } P \ E \ a'$
using *sim action-loc-change-prefix*[$OF \ sim$, *of* $a' \ P$]
by(*auto intro: write-actions-change-prefix*)
{ **assume** $P, EE \vdash ws \ a \leq_{hb} a' \ P, EE \vdash a' \leq_{hb} a$
hence $P, E \vdash ws \ a \leq_{hb} a' \ P, E \vdash a' \leq_{hb} a$
using $tsa\text{-}E \text{ sim } a\text{-}r$ **by**(*auto elim!: happens-before-change-prefix*)
with *between*[$OF \ a'$] **show** $a' = ws \ a$ **by** *simp* }
{ **assume** *is-volatile* $P \ al \ P, EE \vdash ws \ a \leq_{so} a' \ P, EE \vdash a' \leq_{so} a$
with *sim a-r between*[$OF \ a'$] **show** $a' = ws \ a$
by(*fastforce elim: sync-order-change-prefix intro!: disjI2 del: disjCI*) }
qed

with $HB \ tsa'$
have $\exists ws'. P \vdash (?E', ws') \checkmark \wedge$
 $(\forall n. n \in \text{read-actions } ?E' \longrightarrow \text{length } (?start\text{-heap}\text{-obs} \ @ \ \text{concat } (\text{map } (\lambda(t, ta). \text{map } (Pair \ t) \ \{\{ta\}\}_o) \ (\text{list-of } (\text{ltake } (\text{enat } r\text{-}m) \ E')))) \ @ \ \text{map } (Pair \ t\text{-}r) \ (\text{take } r\text{-}n \ \{\{ta\}\}_o)) \leq n \longrightarrow P, ?E' \vdash ws' \ n \leq_{hb} n) \wedge$
 $(\forall n < \text{length } (\text{lift}\text{-start}\text{-obs } \text{start}\text{-tid } \text{start}\text{-heap}\text{-obs} \ @ \ \text{concat } (\text{map } (\lambda(t, ta). \text{map } (Pair \ t) \ \{\{ta\}\}_o) \ (\text{list-of } (\text{ltake } (\text{enat } r\text{-}m) \ E')))) \ @ \ \text{map } (Pair \ t\text{-}r) \ (\text{take } r\text{-}n \ \{\{ta\}\}_o)). ws' \ n = ws \ n)$
unfolding $EE\text{-def}$
by(*rule ta-hb-consistent-Read-hb*)(*rule jmm.E-new-actions-for-fun*[$OF \ \mathcal{E}'$ [*unfolded* $\langle ?E = ?E' \rangle$]])
also have $r\text{-conv}''$: $\text{length } (?start\text{-heap}\text{-obs} \ @ \ \text{concat } (\text{map } (\lambda(t, ta). \text{map } (Pair \ t) \ \{\{ta\}\}_o) \ (\text{list-of } (\text{ltake } (\text{enat } r\text{-}m) \ E')))) \ @ \ \text{map } (Pair \ t\text{-}r) \ (\text{take } r\text{-}n \ \{\{ta\}\}_o) = r$
using $r\text{-}n \ r\text{-}m$ **unfolding** $r\text{-conv}'$
by(*auto simp add: length-concat sum-list-sum-nth atLeast0LessThan lnth-ltake split-def o-def less-min-eq1 min-def length-list-of-conv-the-enat cong: sum.cong-simp*)
finally obtain ws' **where** $wf\text{-exec}'$: $P \vdash (?E', ws') \checkmark$
and $read\text{-hb}$: $\bigwedge n. \llbracket n \in \text{read-actions } ?E'; r \leq n \rrbracket \Longrightarrow P, ?E' \vdash ws' \ n \leq_{hb} n$
and $read\text{-same}$: $\bigwedge n. n < r \Longrightarrow ws' \ n = ws \ n$ **by** *blast*

have $?same \ ?E'$
apply(*subst ltake-lappend1, simp add: r-conv''[symmetric] length-list-of-conv-the-enat*)
unfolding $E \ E' \ \text{lappend}\text{-l}\text{list}\text{-of}\text{-l}\text{list}\text{-of}$ [*symmetric*]

```

apply(subst (1 2) ltake-lappend2, simp add: r[simplified])
apply(subst lappend-eq-lappend-conv, simp)
apply safe
apply(subst E'-unfold)
unfolding lmap-lappend-distrib
apply(subst lconcat-lappend, simp)
apply(subst lconcat-llist-of[symmetric])
apply(subst (3) lmap-llist-of[symmetric])
apply(subst (3) lmap-llist-of[symmetric])
apply(subst llist.map-comp)
apply(simp only: split-def o-def)
apply(subst llist-of-list-of, simp)
apply(subst (1 2) ltake-lappend2, simp add: r-conv3)
apply(subst lappend-eq-lappend-conv, simp)
apply safe
unfolding llist.map(2) lconcat-LCons E'-r-m snd-conv fst-conv take-map
apply(subst ltake-lappend1)
defer
apply(subst append-take-drop-id[where xs={ta-r}_o and n=r-n, symmetric])
unfolding map-append lappend-llist-of-llist-of[symmetric]
apply(subst ltake-lappend1)
using r-n
apply(simp add: min-def r-conv3)
apply(rule refl)
apply(simp add: r-conv3)
using r-n by arith

moreover {
  fix a
  assume a ∈ read-actions ?E'
  with read-hb[of a] read-same[of a]
  have if a < r then ws' a = ws a else P, ?E' ⊢ ws' a ≤hb a by simp }
hence ?read ?E' ws' by blast
moreover from r-m r-n r-n'
have E'-r: lnth ?E' r = (t-r, {ta'-r}_o ! r-n) unfolding r-conv'
  by(auto simp add: lnth-lappend nth-append length-concat sum-list-sum-nth atLeast0LessThan
split-beta lnth-ltake less-min-eq1 length-list-of-conv-the-enat cong: sum.cong-simp)
from E-r r have E-r: lnth E r = (t-r, {ta-r}_o ! r-n)
  unfolding E by(simp add: lnth-lappend)
  have r ∈ read-actions E ↔ (∃ ad al v. {ta-r}_o ! r-n = NormalAction (ReadMem ad al v))
using True
by(auto elim!: read-actions.cases simp add: action-obs-def E-r actions-def intro!: read-actions.intros)
with sim E'-r E-r have ?tid ?E' ?obs ?E'
  by(auto simp add: action-tid-def action-obs-def)
moreover have ?actions ?E' using r-n r-m r-n' unfolding r-conv'
  by(cases llength ?E')(auto simp add: actions-def less-min-eq2 length-concat sum-list-sum-nth
atLeast0LessThan split-beta lnth-ltake less-min-eq1 length-list-of-conv-the-enat enat-plus-eq-enat-conv
cong: sum.cong-simp)
ultimately show ?thesis using wf-exec' E'
  unfolding <?E = ?E'> by blast
qed
qed
qed
qed

```


end

lemma *w-value-mrw-value-conf*:

assumes *set-option* ($vs' \text{ adal} \subseteq vs \text{ adal} \times UNIV$)

shows *set-option* ($mrw\text{-value } P \text{ vs}' \text{ ob adal} \subseteq w\text{-value } P \text{ vs ob adal} \times UNIV$)

using *assms* **by**(*cases adal*)(*cases ob rule: w-value-cases, auto*)

lemma *w-values-mrw-values-conf*:

assumes *set-option* ($vs' \text{ adal} \subseteq vs \text{ adal} \times UNIV$)

shows *set-option* ($mrw\text{-values } P \text{ vs}' \text{ obs adal} \subseteq w\text{-values } P \text{ vs obs adal} \times UNIV$)

using *assms*

by(*induct obs arbitrary: vs' vs*)(*auto del: subsetI intro: w-value-mrw-value-conf*)

lemma *w-value-mrw-value-dom-eq-preserve*:

assumes $dom \text{ vs}' = \{\text{adal. vs adal} \neq \{\}\}$

shows $dom (mrw\text{-value } P \text{ vs}' \text{ ob}) = \{\text{adal. } w\text{-value } P \text{ vs ob adal} \neq \{\}\}$

using *assms*

apply(*cases ob rule: w-value-cases*)

apply(*simp-all add: dom-def split-beta del: not-None-eq*)

apply(*blast elim: equalityE dest: subsetD*)+

done

lemma *w-values-mrw-values-dom-eq-preserve*:

assumes $dom \text{ vs}' = \{\text{adal. vs adal} \neq \{\}\}$

shows $dom (mrw\text{-values } P \text{ vs}' \text{ obs}) = \{\text{adal. } w\text{-values } P \text{ vs obs adal} \neq \{\}\}$

using *assms*

by(*induct obs arbitrary: vs vs'*)(*auto del: equalityI intro: w-value-mrw-value-dom-eq-preserve*)

context *jmm-multithreaded* **begin**

definition *non-speculative-read* ::

$nat \Rightarrow ('l, 'thread\text{-id}, 'x, 'm, 'w) \text{ state} \Rightarrow ('addr \times addr\text{-loc} \Rightarrow 'addr \text{ val set}) \Rightarrow bool$

where

$non\text{-speculative-read } n \text{ s vs} \longleftrightarrow$

$(\forall ttas \text{ s}' \text{ t x ta x}' \text{ m}' \text{ i ad al v v'.$

$s \rightarrow ttas \rightarrow * \text{ s}' \longrightarrow non\text{-speculative } P \text{ vs } (l\text{list-of } (concat (map (\lambda(t, ta). \{\!|ta|\!\}_o) ttas))) \longrightarrow$

$thr \text{ s}' \text{ t} = \lfloor(x, no\text{-wait-locks})\rfloor \longrightarrow t \vdash (x, shr \text{ s}') -ta \rightarrow (x', m') \longrightarrow actions\text{-ok } \text{ s}' \text{ t ta} \longrightarrow$

$i < length \{\!|ta|\!\}_o \longrightarrow$

$non\text{-speculative } P (w\text{-values } P \text{ vs } (concat (map (\lambda(t, ta). \{\!|ta|\!\}_o) ttas))) (l\text{list-of } (take \text{ i } \{\!|ta|\!\}_o))$

\longrightarrow

$\{\!|ta|\!\}_o ! \text{ i} = NormalAction (ReadMem \text{ ad al v}) \longrightarrow$

$v' \in w\text{-values } P \text{ vs } (concat (map (\lambda(t, ta). \{\!|ta|\!\}_o) ttas) @ take \text{ i } \{\!|ta|\!\}_o) (\text{ad}, \text{al}) \longrightarrow$

$(\exists ta' x'' m''. t \vdash (x, shr \text{ s}') -ta' \rightarrow (x'', m'') \wedge actions\text{-ok } \text{ s}' \text{ t ta}' \wedge$

$i < length \{\!|ta'|\!\}_o \wedge take \text{ i } \{\!|ta'|\!\}_o = take \text{ i } \{\!|ta|\!\}_o \wedge \{\!|ta'|\!\}_o ! \text{ i} = NormalAction$

$(ReadMem \text{ ad al v}') \wedge$

$length \{\!|ta'|\!\}_o \leq max \text{ n } (length \{\!|ta|\!\}_o))$

lemma *non-speculative-readI* [*intro?*]:

$(\bigwedge ttas \text{ s}' \text{ t x ta x}' \text{ m}' \text{ i ad al v v'.$

$\lfloor s \rightarrow ttas \rightarrow * \text{ s}'; non\text{-speculative } P \text{ vs } (l\text{list-of } (concat (map (\lambda(t, ta). \{\!|ta|\!\}_o) ttas)))$;

$thr \text{ s}' \text{ t} = \lfloor(x, no\text{-wait-locks})\rfloor$; $t \vdash (x, shr \text{ s}') -ta \rightarrow (x', m')$; $actions\text{-ok } \text{ s}' \text{ t ta}$;

$i < length \{\!|ta|\!\}_o$; $non\text{-speculative } P (w\text{-values } P \text{ vs } (concat (map (\lambda(t, ta). \{\!|ta|\!\}_o) ttas))) (l\text{list-of}$

$(take \text{ i } \{\!|ta|\!\}_o))$;

$\{ta\}_o ! i = \text{NormalAction } (\text{ReadMem } ad \ al \ v);$
 $v' \in w\text{-values } P \text{ vs } (\text{concat } (\text{map } (\lambda(t, ta). \{ta\}_o) \ ttas) \ @ \ \text{take } i \ \{ta\}_o) \ (ad, al) \]$
 $\implies \exists ta' \ x'' \ m''. \ t \vdash (x, \text{shr } s') -ta' \rightarrow (x'', m'') \wedge \text{actions-ok } s' \ t \ ta' \wedge$
 $i < \text{length } \{ta'\}_o \wedge \text{take } i \ \{ta'\}_o = \text{take } i \ \{ta\}_o \wedge \{ta'\}_o ! i = \text{NormalAction}$
 $(\text{ReadMem } ad \ al \ v') \wedge$
 $\text{length } \{ta'\}_o \leq \max n \ (\text{length } \{ta\}_o)$
 $\implies \text{non-speculative-read } n \ s \ \text{vs}$
unfolding non-speculative-read-def by blast

lemma non-speculative-readD:

$\llbracket \text{non-speculative-read } n \ s \ \text{vs}; \ s \rightarrow \text{ttas} \rightarrow * \ s'; \ \text{non-speculative } P \ \text{vs } (\text{l-list-of } (\text{concat } (\text{map } (\lambda(t, ta). \{ta\}_o) \ ttas)))$
 $\{ta\}_o \ ttas));$
 $\text{thr } s' \ t = \llbracket (x, \text{no-wait-locks}); \ t \vdash (x, \text{shr } s') -ta \rightarrow (x', m'); \ \text{actions-ok } s' \ t \ ta;$
 $i < \text{length } \{ta\}_o; \ \text{non-speculative } P \ (w\text{-values } P \ \text{vs } (\text{concat } (\text{map } (\lambda(t, ta). \{ta\}_o) \ ttas))) \ (\text{l-list-of}$
 $(\text{take } i \ \{ta\}_o));$
 $\{ta\}_o ! i = \text{NormalAction } (\text{ReadMem } ad \ al \ v);$
 $v' \in w\text{-values } P \ \text{vs } (\text{concat } (\text{map } (\lambda(t, ta). \{ta\}_o) \ ttas) \ @ \ \text{take } i \ \{ta\}_o) \ (ad, al) \]$
 $\implies \exists ta' \ x'' \ m''. \ t \vdash (x, \text{shr } s') -ta' \rightarrow (x'', m'') \wedge \text{actions-ok } s' \ t \ ta' \wedge$
 $i < \text{length } \{ta'\}_o \wedge \text{take } i \ \{ta'\}_o = \text{take } i \ \{ta\}_o \wedge \{ta'\}_o ! i = \text{NormalAction}$
 $(\text{ReadMem } ad \ al \ v') \wedge$
 $\text{length } \{ta'\}_o \leq \max n \ (\text{length } \{ta\}_o)$
unfolding non-speculative-read-def by blast

end

8.11.2 non-speculative generalises cut-and-update and ta-hb-consistent

context known-addr-typing begin

lemma read-non-speculative-new-actions-for:

fixes $status \ f \ C \ M \ \text{params} \ E$
defines $E \equiv \text{lift-start-obs } \text{start-tid} \ \text{start-heap-obs}$
and $vs \equiv w\text{-values } P \ (\lambda-. \ \{ \}) \ (\text{map } \text{snd } E)$
and $s \equiv \text{init-fin-lift-state } \text{status} \ (\text{start-state } f \ P \ C \ M \ \text{params})$
assumes $wf: \text{wf-syscls } P$
and $\text{RedT}: \text{mthr.if.RedT } s \ \text{ttas} \ s'$
and $\text{redT}: \text{mthr.if.redT } s' \ (t, ta') \ s''$
and $\text{read}: \text{NormalAction } (\text{ReadMem } ad \ al \ v) \in \text{set } \{ta'\}_o$
and $ns: \text{non-speculative } P \ (\lambda-. \ \{ \}) \ (\text{l-list-of } (\text{map } \text{snd } E \ @ \ \text{concat } (\text{map } (\lambda(t, ta). \{ta\}_o) \ ttas)))$
and $ka: \text{known-addr } \text{start-tid} \ (f \ (\text{fst } (\text{method } P \ C \ M)) \ M \ (\text{fst } (\text{snd } (\text{method } P \ C \ M)))) \ (\text{fst } (\text{snd}$
 $(\text{snd } (\text{method } P \ C \ M)))) \ (\text{the } (\text{snd } (\text{snd } (\text{snd } (\text{method } P \ C \ M)))))) \ \text{params} \subseteq \text{allocated } \text{start-heap}$
and $wt: \text{ts-ok } (\text{init-fin-lift } wf) \ (\text{thr } s) \ (\text{shr } s)$
and $\text{type-adal}: P, \text{shr } s' \vdash \text{ad}@al : T$
shows $\exists w. w \in \text{new-actions-for } P \ (\text{l-list-of } (E \ @ \ \text{concat } (\text{map } (\lambda(t, ta). \text{map } (\text{Pair } t) \ \{ta\}_o) \ ttas)))$
 (ad, al)
 $(\text{is } \exists w. \ ?\text{new-w } w)$
using $\text{RedT } \text{redT } \text{read } ns[\text{unfolded } E\text{-def}] \ ka$ **unfolding** $s\text{-def}$
proof($\text{cases rule: read-ex-NewHeapElem}$)
case ($\text{start } CTn$)
then obtain n **where** $n: \text{start-heap-obs} ! n = \text{NewHeapElem } ad \ CTn$
and $len: n < \text{length } \text{start-heap-obs}$
unfolding in-set-conv-nth **by** blast
from ns **have** $\text{non-speculative } P \ (w\text{-values } P \ (\lambda-. \ \{ \}) \ (\text{map } \text{snd } E)) \ (\text{l-list-of } (\text{concat } (\text{map } (\lambda(t, ta). \{ta\}_o) \ ttas)))$

unfolding *lappend-llist-of-llist-of*[*symmetric*]
by(*simp add: non-speculative-lappend del: lappend-llist-of-llist-of*)
with *RedT wt have hext: start-heap* \leq *shr s'*
unfolding *s-def E-def using start-state-vs-conf*[*OF wf*]
by(*auto dest!: init-fin-RedT-hext-incr simp add: start-state-def split-beta init-fin-lift-state-conv-simps*)

from *start have typeof-addr start-heap ad =* $\lfloor CTn \rfloor$
by(*auto dest: NewHeapElem-start-heap-obsD*[*OF wf*])
with *hext have typeof-addr (shr s') ad =* $\lfloor CTn \rfloor$ **by**(*rule typeof-addr-hext-mono*)
with *type-adal have (ad, al) \in action-loc-aux P (NormalAction (NewHeapElem ad CTn)) using n*
len
by *cases (auto simp add: action-obs-def lnth-lappend1 lift-start-obs-def)*
with *n len have ?new-w (Suc n)*
by(*simp add: new-actions-for-def actions-def E-def action-obs-def lift-start-obs-def nth-append*)
thus *?thesis ..*

next
case (*Red ttas' s'' t' ta' s''' ttas'' CTn*)
note *ttas =* $\langle ttas = ttas' @ (t', ta') \# ttas'' \rangle$

from $\langle NormalAction (NewHeapElem ad CTn) \in set \{\{ta'\}_o\} \rangle$
obtain *obs obs' where obs: $\{\{ta'\}_o\} = obs @ NormalAction (NewHeapElem ad CTn) \# obs'$*
by(*auto dest: split-list*)

let *?n = length (lift-start-obs start-tid start-heap-obs)*
let *?wa = ?n + length (concat (map ($\lambda(t, ta). \{\{ta'\}_o\} ttas')$)) + length obs*

have *?wa = ?n + length (concat (map ($\lambda(t, ta). map (Pair t) \{\{ta'\}_o\} ttas')$)) + length obs*
by(*simp add: length-concat o-def split-def*)
also have $\dots < length (E @ concat (map ($\lambda(t, ta). map (Pair t) \{\{ta'\}_o\} ttas'))$
using *obs ttas by (simp add: E-def)*
also
from *ttas obs*
have (*E @ concat (map ($\lambda(t, ta). map (Pair t) \{\{ta'\}_o\} ttas)$)! ?wa = (t', NormalAction (NewHeapElem ad CTn))*)
by(*auto simp add: E-def lift-start-obs-def nth-append o-def split-def length-concat*)
moreover
from $\langle mthr.if.redT s'' (t', ta') s''' \rangle \langle NormalAction (NewHeapElem ad CTn) \in set \{\{ta'\}_o\} \rangle$
obtain *x-wa x-wa' where ts''t': thr s'' t' =* $\lfloor (x-wa, no-wait-locks) \rfloor$
and *red-wa: mthr.init-fin t' (x-wa, shr s'') ta' (x-wa', shr s''')*
by(*cases*) *fastforce+*$

from *start-state-vs-conf*[*OF wf*]
have *vs: vs-conf P (shr s) vs unfolding vs-def E-def s-def*
by(*simp add: init-fin-lift-state-conv-simps start-state-def split-def*)

from *ns*
have *ns: non-speculative P vs (llist-of (concat (map ($\lambda(t, ta). \{\{ta'\}_o\} ttas')$)))*
and *ns': non-speculative P (w-values P vs (concat (map ($\lambda(t, ta). \{\{ta'\}_o\} ttas')$))) (llist-of $\{\{ta'\}_o\}$)*
and *ns'': non-speculative P (w-values P (w-values P vs (concat (map ($\lambda(t, ta). \{\{ta'\}_o\} ttas')$)))*
 $\{\{ta'\}_o\}$ *(llist-of (concat (map ($\lambda(t, ta). \{\{ta'\}_o\} ttas''))$*)
unfolding *ttas vs-def*
by(*simp-all add: lappend-llist-of-llist-of*[*symmetric*] *non-speculative-lappend del: lappend-llist-of-llist-of*)
from $\langle mthr.if.RedT (init-fin-lift-state status (start-state f P C M params)) ttas' s'' \rangle wt ns$
have *ts-ok'': ts-ok (init-fin-lift wfx) (thr s'') (shr s'') using vs unfolding vs-def s-def*

```

  by(rule if-RedT-non-speculative-invar)
  with  $ts''t'$  have  $wfxt'$ :  $wfx\ t'$  ( $snd\ x-wa$ ) ( $shr\ s''$ ) by( $cases\ x-wa$ )( $auto\ dest: ts-okD$ )

  from  $\langle mthr.if.RedT\ (init-fin-lift-state\ status\ (start-state\ f\ P\ C\ M\ params))\ ttas'\ s'' \rangle\ wt\ ns$ 
  have  $vs''$ :  $vs-conf\ P\ (shr\ s'')\ (w-values\ P\ (w-values\ P\ (\lambda-. \{\})\ (map\ snd\ E))\ (concat\ (map\ (\lambda(t, ta). \{ta\}_o)\ ttas'))$ 
  unfolding  $s-def\ E-def\ vs-def$ 
  by(rule if-RedT-non-speculative-invar)( $simp\ add: start-state-def\ split-beta\ init-fin-lift-state-conv-simps\ start-state-vs-conf[OF\ wf]$ )
  from  $if-redT-non-speculative-vs-conf[OF\ \langle mthr.if.redT\ s''\ (t', ta')\ s''' \rangle\ ts-ok'' - vs'',\ of\ length\ \{ta'\}_o]$ 
   $ns'$ 
  have  $vs'''$ :  $vs-conf\ P\ (shr\ s''')\ (w-values\ P\ (w-values\ P\ vs\ (concat\ (map\ (\lambda(t, ta). \{ta\}_o)\ ttas'))$ 
   $\{ta'\}_o)$ 
  by( $simp\ add: vs-def$ )

  from  $\langle mthr.if.redT\ s''\ (t', ta')\ s''' \rangle\ ts-ok''\ ns'\ vs''$ 
  have  $ts-ok$  ( $init-fin-lift\ wfx$ ) ( $thr\ s'''$ ) ( $shr\ s'''$ )
  unfolding  $vs-def$  by(rule if-redT-non-speculative-invar)
  with  $\langle mthr.if.RedT\ s'''\ ttas''\ s' \rangle$ 
  have  $hext$ :  $shr\ s''' \sqsubseteq shr\ s'$  using  $ns''\ vs'''$ 
  by(rule  $init-fin-RedT-hext-incr$ )

  from  $red-wa$  have  $typeof-addr\ (shr\ s''')$   $ad = \lfloor CTn \rfloor$ 
  using  $wfxt' \langle NormalAction\ (NewHeapElem\ ad\ CTn) \in set\ \{ta'\}_o \rangle$  by  $cases(auto\ dest: red-NewHeapElemD)$ 
  with  $hext$  have  $typeof-addr\ (shr\ s')$   $ad = \lfloor CTn \rfloor$  by(rule  $typeof-addr-hext-mono$ )
  with  $type-adal$  have  $(ad, al) \in action-loc-aux\ P\ (NormalAction\ (NewHeapElem\ ad\ CTn))$  by  $cases$ 
   $auto$ 
  ultimately have  $?new-w\ ?wa$ 
  by( $simp\ add: new-actions-for-def\ actions-def\ action-obs-def$ )
  thus  $?thesis ..$ 
qed

```

lemma *non-speculative-read-into-cut-and-update*:

```

  fixes  $status\ f\ C\ M\ params\ E$ 
  defines  $E \equiv lift-start-obs\ start-tid\ start-heap-obs$ 
  and  $vs \equiv w-values\ P\ (\lambda-. \{\})\ (map\ snd\ E)$ 
  and  $s \equiv init-fin-lift-state\ status\ (start-state\ f\ P\ C\ M\ params)$ 
  and  $vs' \equiv mrw-values\ P\ Map.empty\ (map\ snd\ E)$ 
  assumes  $wf$ :  $wf-syscls\ P$ 
  and  $nsr$ :  $if.non-speculative-read\ n\ s\ vs$ 
  and  $wt$ :  $ts-ok$  ( $init-fin-lift\ wfx$ ) ( $thr\ s$ ) ( $shr\ s$ )
  and  $ka$ :  $known-addr\ start-tid\ (f\ (fst\ (method\ P\ C\ M))\ M\ (fst\ (snd\ (method\ P\ C\ M))))\ (fst\ (snd\ (snd\ (method\ P\ C\ M))))\ (the\ (snd\ (snd\ (snd\ (method\ P\ C\ M))))\ params) \subseteq allocated\ start-heap$ 
  shows  $if.cut-and-update\ s\ vs'$ 
  proof(rule  $if.cut-and-updateI$ )
  fix  $ttas\ s'\ t\ x\ ta\ x'\ m'$ 
  assume  $Red$ :  $mthr.if.RedT\ s\ ttas\ s'$ 
  and  $sc$ :  $ta-seq-consist\ P\ vs'\ (llist-of\ (concat\ (map\ (\lambda(t, ta). \{ta\}_o)\ ttas))$ 
  and  $tst$ :  $thr\ s'\ t = \lfloor (x, no-wait-locks) \rfloor$ 
  and  $red$ :  $t \vdash (x, shr\ s') -ta \rightarrow i\ (x', m')$ 
  and  $aok$ :  $mthr.if.actions-ok\ s'\ t\ ta$ 
  let  $?vs = w-values\ P\ vs\ (concat\ (map\ (\lambda(t, ta). \{ta\}_o)\ ttas))$ 
  let  $?vs' = mrw-values\ P\ vs'\ (concat\ (map\ (\lambda(t, ta). \{ta\}_o)\ ttas))$ 

```

from *start-state-vs-conf*[*OF wf*]
have *vs*: *vs-conf* *P* (*shr s*) *vs* **unfolding** *vs-def* *E-def* *s-def*
by(*simp add: init-fin-lift-state-conv-simps start-state-def split-def*)

from *sc* **have** *ns*: *non-speculative* *P* *vs* (*llist-of* (*concat* (*map* ($\lambda(t, ta). \{ta\}_o$) *ttas*)))
by(*rule ta-seq-consist-into-non-speculative*)(*auto simp add: vs'-def vs-def del: subsetI intro: w-values-mrw-values-conf*)

from *ns* **have** *ns'*: *non-speculative* *P* ($\lambda-. \{ \}$) (*llist-of* (*map snd* (*lift-start-obs* *start-tid* *start-heap-obs*))
@ *concat* (*map* ($\lambda(t, ta). \{ta\}_o$) *ttas*)))
unfolding *lappend-llist-of-llist-of*[*symmetric*] *vs-def*
by(*simp add: non-speculative-lappend E-def non-speculative-start-heap-obs del: lappend-llist-of-llist-of*)

have *vs-vs''*: $\bigwedge adal. set-option (?vs' adal) \subseteq ?vs adal \times UNIV$
by(*rule w-values-mrw-values-conf*)(*auto simp add: vs'-def vs-def del: subsetI intro: w-values-mrw-values-conf*)
from *Red wt ns vs*
have *wt'*: *ts-ok* (*init-fin-lift wfx*) (*thr s'*) (*shr s'*)
by(*rule if-RedT-non-speculative-invar*)
hence *wtt*: *init-fin-lift wfx t x* (*shr s'*) **using** *tst* **by**(*rule ts-okD*)

{ **fix** *i*
have $\exists ta' x'' m''. t \vdash (x, shr s') -ta' \rightarrow i (x'', m'') \wedge mthr.if.actions-ok s' t ta' \wedge$
 $length \{ta'\}_o \leq max n (length \{ta\}_o) \wedge$
 $ta-seq-consist P ?vs' (llist-of (take i \{ta'\}_o)) \wedge$
 $eq-upto-seq-inconsist P (take i \{ta\}_o) (take i \{ta'\}_o) ?vs' \wedge$
 $(ta-seq-consist P ?vs' (llist-of (take i \{ta\}_o)) \longrightarrow ta' = ta)$

proof(*induct i*)
case *0*
show *?case* **using** *red aok*
by(*auto simp del: split-paired-Ex simp add: eq-upto-seq-inconsist-simps*)

next
case (*Suc i*)
then obtain *ta' x'' m''*
where *red'*: $t \vdash (x, shr s') -ta' \rightarrow i (x'', m'')$
and *aok'*: *mthr.if.actions-ok s' t ta'*
and *len*: $length \{ta'\}_o \leq max n (length \{ta\}_o)$
and *sc'*: *ta-seq-consist* *P* *?vs'* (*llist-of* (*take i* $\{ta'\}_o$))
and *eusi*: *eq-upto-seq-inconsist* *P* (*take i* $\{ta\}_o$) (*take i* $\{ta'\}_o$) *?vs'*
and *ta'-ta*: *ta-seq-consist* *P* *?vs'* (*llist-of* (*take i* $\{ta\}_o$)) $\implies ta' = ta$
by *blast*
let *?vs''* = *mrw-values* *P* *?vs'* (*take i* $\{ta'\}_o$)
show *?case*
proof(*cases i < length \{ta'\}_o \wedge \neg ta-seq-consist P ?vs' (llist-of (take (Suc i) \{ta'\}_o)) \wedge \neg*
ta-seq-consist P ?vs' (llist-of (take (Suc i) \{ta\}_o)))
case *True*
hence *i*: $i < length \{ta'\}_o$ **and** $\neg ta-seq-consist P ?vs'' (LCons (\{ta'\}_o ! i) LNil)$ **using** *sc'*
by(*auto simp add: take-Suc-conv-app-nth lappend-llist-of-llist-of*[*symmetric*] *ta-seq-consist-lappend*
simp del: lappend-llist-of-llist-of)
then obtain *ad al v* **where** *ta'-i*: $\{ta'\}_o ! i = NormalAction (ReadMem ad al v)$
by(*auto split: action.split-asm obs-event.split-asm*)
from *ta'-i True* **have** *read*: *NormalAction* (*ReadMem ad al v*) $\in set \{ta'\}_o$ **by**(*auto simp add:*
in-set-conv-nth)
with *red'* **have** $ad \in known-addr-if t x$ **by**(*rule if-red-read-knows-addr*)
hence $ad \in if.known-addr-state s'$ **using** *tst* **by**(*rule if.known-addr-stateI*)
moreover from *init-fin-red-read-typeable*[*OF red' wtt read*]

```

obtain  $T$  where  $\text{type-adal}: P, \text{shr } s' \vdash \text{ad}@al : T ..$ 

from  $\text{redT-updWs-total}[\text{of } t \text{ wset } s' \{ta\}_w] \text{red}' \text{tst } \text{aok}'$ 
obtain  $s''$  where  $\text{redT}': \text{mthr.if.redT } s' (t, ta') s''$  by( $\text{auto dest!}: \text{mthr.if.redT.redT-normal}$ )
with  $\text{wf Red}$ 
have  $\exists w. w \in \text{new-actions-for } P (\text{l-list-of } (E @ \text{concat } (\text{map } (\lambda(t, ta). \text{map } (Pair t) \{ta\}_o) \text{ttas}))) (ad, al)$ 
  (is  $\exists w. ?\text{new-w } w$ )
using  $\text{read ns}' \text{ka wt type-adal}$  unfolding  $s\text{-def } E\text{-def}$  by( $\text{rule read-non-speculative-new-actions-for}$ )
then obtain  $w$  where  $w: ?\text{new-w } w ..$ 
have  $(ad, al) \in \text{dom } ?vs'$ 
proof( $\text{cases } w < \text{length } E$ )
  case True
    with  $w$  have  $(ad, al) \in \text{dom } vs'$  unfolding  $vs'\text{-def new-actions-for-def}$ 
      by( $\text{clarsimp}$ )( $\text{erule mrw-values-new-actionD}[\text{rotated } 1], \text{auto simp del: split-paired-Ex simp}$ 
 $\text{add: set-conv-nth action-obs-def nth-append intro!}: \text{exI}[\text{where } x=w]$ )
      also have  $\text{dom } vs' \subseteq \text{dom } ?vs'$  by( $\text{rule mrw-values-dom-mono}$ )
      finally show  $?thesis .$ 
    next
      case False
        with  $w$  show  $?thesis$  unfolding  $\text{new-actions-for-def}$ 
          apply( $\text{clarsimp}$ )
          apply( $\text{erule mrw-values-new-actionD}[\text{rotated } 1]$ )
          apply( $\text{simp-all add: set-conv-nth action-obs-def nth-append actions-def}$ )
          apply( $\text{rule exI}[\text{where } x=w - \text{length } E]$ )
          apply( $\text{subst nth-map}[\text{where } f=\text{snd}, \text{symmetric}]$ )
          apply( $\text{simp-all add: length-concat o-def split-def map-concat}$ )
          done
        qed
        hence  $(ad, al) \in \text{dom } (\text{mrw-values } P ?vs' (\text{take } i \{ta\}_o))$ 
          by( $\text{rule subsetD}[\text{OF mrw-values-dom-mono}]$ )
        then obtain  $v' b$  where  $v': \text{mrw-values } P ?vs' (\text{take } i \{ta\}_o) (ad, al) = [(v', b)]$  by  $\text{auto}$ 
        moreover from  $vs\text{-vs}'[\text{of } (ad, al)]$ 
        have  $\text{set-option } (\text{mrw-values } P ?vs' (\text{take } i \{ta\}_o) (ad, al)) \subseteq w\text{-values } P ?vs (\text{take } i \{ta\}_o)$ 
 $(ad, al) \times UNIV$ 
          by( $\text{rule w-values-mrw-values-conf}$ )
        ultimately have  $v' \in w\text{-values } P ?vs (\text{take } i \{ta\}_o) (ad, al)$  by  $\text{simp}$ 
        moreover from  $sc'$ 
        have  $\text{non-speculative } P (w\text{-values } P vs (\text{concat } (\text{map } (\lambda(t, ta). \{ta\}_o) \text{ttas}))) (\text{l-list-of } (\text{take } i \{ta\}_o))$ 
          by( $\text{blast intro: ta-seq-consist-into-non-speculative vs-vs}' \text{del: subsetI}$ )
        ultimately obtain  $ta'' x'' m''$ 
          where  $\text{red}'': t \vdash (x, \text{shr } s') -ta'' \rightarrow i (x'', m'')$ 
          and  $\text{aok}'': \text{mthr.if.actions-ok } s' t ta''$ 
          and  $i': i < \text{length } \{ta''\}_o$ 
          and  $\text{eq}: \text{take } i \{ta''\}_o = \text{take } i \{ta\}_o$ 
          and  $ta''\text{-}i: \{ta''\}_o ! i = \text{NormalAction } (\text{ReadMem } ad \text{al } v')$ 
          and  $\text{len}': \text{length } \{ta''\}_o \leq \max n (\text{length } \{ta\}_o)$ 
          using  $\text{if.non-speculative-readD}[\text{OF nsr Red ns tst red}' \text{aok}' i - ta'\text{-}i, \text{of } v']$  by  $\text{auto}$ 
        from  $\text{len}' \text{len}$  have  $\text{length } \{ta''\}_o \leq \max n (\text{length } \{ta\}_o)$  by  $\text{simp}$ 
        moreover have  $\text{ta-seq-consist } P ?vs' (\text{l-list-of } (\text{take } (\text{Suc } i) \{ta''\}_o))$ 
          using  $\text{eq } sc' i' ta''\text{-}i v'$ 
          by( $\text{simp add: take-Suc-conv-app-nth lappend-l-list-of-l-list-of}[\text{symmetric}] \text{ta-seq-consist-lappend}$ 
 $\text{del: lappend-l-list-of-l-list-of}$ )

```

```

moreover
have eusi': eq-upto-seq-inconsist P (take (Suc i)  $\{ta\}_o$ ) (take (Suc i)  $\{ta'\}_o$ ) ?vs'
proof(cases i < length  $\{ta\}_o$ )
  case True
    with i' i len eq eusi ta'-i ta''-i v' show ?thesis
by(auto simp add: take-Suc-conv-app-nth ta'-ta eq-upto-seq-inconsist-simps intro: eq-upto-seq-inconsist-appendI)
next
  case False
    with i ta'-ta have  $\neg ta\text{-seq-consist } P ?vs' (l\text{list-of } (take\ i\ \{ta\}_o))$  by auto
    then show ?thesis using False i' eq eusi
    by(simp add: take-Suc-conv-app-nth eq-upto-seq-inconsist-append2)
qed
moreover {
  assume ta-seq-consist P ?vs' (llist-of (take (Suc i)  $\{ta\}_o$ ))
  with True have ta'' = ta by simp }
ultimately show ?thesis using red'' aok'' True by blast
next
case False
hence ta-seq-consist P ?vs' (llist-of (take (Suc i)  $\{ta\}_o$ ))  $\vee$ 
  length  $\{ta'\}_o \leq i \vee$ 
  ta-seq-consist P ?vs' (llist-of (take (Suc i)  $\{ta'\}_o$ ))
  (is ?case1  $\vee$  ?case2  $\vee$  ?case3) by auto
thus ?thesis
proof(elim disjCE)
  assume ?case1
  moreover
hence eq-upto-seq-inconsist P (take (Suc i)  $\{ta\}_o$ ) (take (Suc i)  $\{ta\}_o$ ) ?vs'
    by(rule ta-seq-consist-imp-eq-upto-seq-inconsist-refl)
ultimately show ?thesis using red aok by fastforce
next
  assume ?case2 and  $\neg ?case1$ 
have eq-upto-seq-inconsist P (take (Suc i)  $\{ta\}_o$ ) (take (Suc i)  $\{ta'\}_o$ ) ?vs'
proof(cases i < length  $\{ta\}_o$ )
  case True
    from  $\langle ?case2 \rangle \langle \neg ?case1 \rangle$  have  $\neg ta\text{-seq-consist } P ?vs' (l\text{list-of } (take\ i\ \{ta\}_o))$  by(auto
simp add: ta'-ta)
    hence eq-upto-seq-inconsist P (take i  $\{ta\}_o @ [\{ta\}_o ! i]$ ) (take i  $\{ta'\}_o @ []$ ) ?vs'
    by(blast intro: eq-upto-seq-inconsist-appendI[OF eusi])
    thus ?thesis using True  $\langle ?case2 \rangle$  by(simp add: take-Suc-conv-app-nth)
  next
    case False with eusi  $\langle ?case2 \rangle$  show ?thesis by simp
qed
with red' aok' len sc' eusi  $\langle ?case2 \rangle \langle \neg ?case1 \rangle$  show ?thesis
    by (fastforce simp add: take-all simp del: split-paired-Ex)
next
  assume ?case3 and  $\neg ?case1$  and  $\neg ?case2$ 
  with len eusi ta'-ta
  have eq-upto-seq-inconsist P (take (Suc i)  $\{ta\}_o$ ) (take (Suc i)  $\{ta'\}_o$ ) ?vs'
by(cases i < length  $\{ta\}_o$ )(auto simp add: take-Suc-conv-app-nth lappend-llist-of-llist-of[symmetric])
ta-seq-consist-lappend intro: eq-upto-seq-inconsist-appendI eq-upto-seq-inconsist-append2 cong: action.case-cong
obs-event.case-cong)
  with red' aok'  $\langle ?case3 \rangle len \langle \neg ?case1 \rangle$  show ?thesis by blast
qed
qed

```

```

qed }
from this[of max n (length  $\{ta\}_o$ )]
show  $\exists ta' x'' m''. t \vdash (x, shr\ s') -ta' \rightarrow i(x'', m'') \wedge mthr.if.actions-ok\ s'\ t\ ta' \wedge ta-seq-consist\ P$ 
 $?vs' (l\!l\!i\!s\!t\text{-of}\ \{ta'\}_o) \wedge eq\text{-upto}\text{-seq}\text{-inconsistent}\ P\ \{ta\}_o\ \{ta'\}_o\ ?vs'$ 
by(auto simp del: split-paired-Ex cong: conj-cong)
qed

```

lemma *non-speculative-read-into-hb-completion:*

```

fixes status f C M params E
defines  $E \equiv lift\text{-start}\text{-obs}\ start\text{-tid}\ start\text{-heap}\text{-obs}$ 
and  $vs \equiv w\text{-values}\ P\ (\lambda\cdot.\ \{\})\ (map\ snd\ E)$ 
and  $s \equiv init\text{-fin}\text{-lift}\text{-state}\ status\ (start\text{-state}\ f\ P\ C\ M\ params)$ 
assumes wf: wf-syscls P
and nsr: if.non-speculative-read n s vs
and wt: ts-ok (init-fin-lift wfx) (thr s) (shr s)
and ka: known-addrs start-tid (f (fst (method P C M))) M (fst (snd (method P C M))) (fst (snd (snd (method P C M)))) (the (snd (snd (snd (method P C M)))))) params)  $\subseteq$  allocated start-heap
shows if.hb-completion s E

```

proof

```

fix ttas  $s' t x ta x' m' i$ 
assume Red: mthr.if.RedT s ttas  $s'$ 
and ns: non-speculative P (w-values P ( $\lambda\cdot.\ \{\})$ ) (map snd E) (l\!l\!i\!s\!t\text{-of} (concat (map ( $\lambda(t, ta).$   $\{ta\}_o$ ) ttas)))
and tst: thr  $s' t = \lfloor(x, no\text{-wait}\text{-locks})\rfloor$ 
and red:  $t \vdash (x, shr\ s') -ta \rightarrow i(x', m')$ 
and aok: mthr.if.actions-ok  $s' t ta$ 
and nsi: non-speculative P (w-values P (w-values P ( $\lambda\cdot.\ \{\})$ ) (map snd E)) (concat (map ( $\lambda(t, ta).$   $\{ta\}_o$ ) ttas))) (l\!l\!i\!s\!t\text{-of} (take i  $\{ta\}_o$ ))

```

```

let  $?E = E @ concat (map (\lambda(t, ta). map (Pair\ t)\ \{ta\}_o\ ttas) @ map (Pair\ t)\ (take\ i\ \{ta\}_o))$ 

```

```

let  $?vs = w\text{-values}\ P\ vs\ (concat (map (\lambda(t, ta). \{ta\}_o\ ttas))$ 

```

```

from ns have ns': non-speculative P ( $\lambda\cdot.\ \{\})$  (l\!l\!i\!s\!t\text{-of} (map snd (lift-start-obs start-tid start-heap-obs)
@ concat (map ( $\lambda(t, ta). \{ta\}_o$ ) ttas)))
unfolding lappend-llist-of-llist-of[symmetric]
by(simp add: non-speculative-lappend E-def non-speculative-start-heap-obs del: lappend-llist-of-llist-of)

```

```

from start-state-vs-conf[OF wf]

```

```

have vs: vs-conf P (shr s) vs unfolding vs-def E-def s-def

```

```

by(simp add: init-fin-lift-state-conv-simps start-state-def split-def)

```

```

from Red wt ns vs

```

```

have wt': ts-ok (init-fin-lift wfx) (thr  $s'$ ) (shr  $s'$ )

```

```

unfolding vs-def by(rule if-RedT-non-speculative-invar)

```

```

hence wtt: init-fin-lift wfx  $t\ x$  (shr  $s'$ ) using tst by(rule ts-okD)

```

```

{ fix j

```

```

have  $\exists ta' x'' m''. t \vdash (x, shr\ s') -ta' \rightarrow i(x'', m'') \wedge mthr.if.actions-ok\ s'\ t\ ta' \wedge length\ \{ta'\}_o \leq$ 
 $max\ n\ (length\ \{ta\}_o) \wedge$ 

```

```

 $take\ i\ \{ta'\}_o = take\ i\ \{ta\}_o \wedge$ 

```

```

 $ta\text{-hb-consistent}\ P\ ?E\ (l\!l\!i\!s\!t\text{-of}\ (map\ (Pair\ t)\ (take\ j\ (drop\ i\ \{ta'\}_o)))) \wedge$ 

```

```

 $(i < length\ \{ta\}_o \longrightarrow i < length\ \{ta'\}_o) \wedge$ 

```

```

 $(if\ \exists ad\ al\ v.\ \{ta\}_o ! i = NormalAction\ (ReadMem\ ad\ al\ v)\ then\ sim\text{-action}\ else$ 

```

```

 $(=)) (\{ta\}_o ! i) (\{ta'\}_o ! i)$ 

```



```

proof(induct j)
  case 0 from red aok show ?case by(fastforce simp del: split-paired-Ex)
next
  case (Suc j)
  then obtain ta' x'' m''
    where red':  $t \vdash (x, \text{shr } s') -ta' \rightarrow i (x'', m'')$ 
    and aok': mthr.if.actions-ok s' t ta'
    and len:  $\text{length } \{\!|ta'|\!\}_o \leq \max n (\text{length } \{\!|ta|\!\}_o)$ 
    and eq:  $\text{take } i \{\!|ta'|\!\}_o = \text{take } i \{\!|ta|\!\}_o$ 
    and hb: ta-hb-consistent P ?E (llist-of (map (Pair t) (take j (drop i \{\!|ta'|\!\}_o))))
    and len-i:  $i < \text{length } \{\!|ta|\!\}_o \rightarrow i < \text{length } \{\!|ta'|\!\}_o$ 
    and sim-i: (if  $\exists ad \ al \ v. \{\!|ta|\!\}_o ! i = \text{NormalAction } (\text{ReadMem } ad \ al \ v)$  then sim-action else
(=)) ( $\{\!|ta|\!\}_o ! i$ ) ( $\{\!|ta'|\!\}_o ! i$ )
    by blast
  show ?case
proof(cases i + j < length \{\!|ta'|\!\}_o)
  case False
    with red' aok' len eq hb len-i sim-i show ?thesis by(fastforce simp del: split-paired-Ex)
next
  case True
    note j = this
    show ?thesis
    proof(cases  $\exists ad \ al \ v. \{\!|ta'|\!\}_o ! (i + j) = \text{NormalAction } (\text{ReadMem } ad \ al \ v)$ )
    case True
      then obtain ad al v where ta'-j: \{\!|ta'|\!\}_o ! (i + j) = NormalAction (ReadMem ad al v) by
blast
      hence read: NormalAction (ReadMem ad al v) \in set \{\!|ta'|\!\}_o using j by(auto simp add:
in-set-conv-nth)
      with red' have ad \in known-addr-if t x by(rule if-red-read-knows-addr)
      hence ad \in if.known-addr-state s' using tst by(rule if.known-addr-stateI)
      from init-fin-red-read-typeable[OF red' wtt read] obtain T
      where type-adal: P,shr s' \vdash ad@al : T ..

      from redT-updWs-total[of t wset s' \{\!|ta'|\!\}_w] red' tst aok'
      obtain s'' where redT': mthr.if.redT s' (t, ta') s'' by(auto dest!: mthr.if.redT.redT-normal)
      with wf Red
      have  $\exists w. w \in \text{new-actions-for } P (\text{llist-of } (E @ \text{concat } (\text{map } (\lambda(t, ta). \text{map } (\text{Pair } t) \{\!|ta|\!\}_o)$ 
ttas))) (ad, al)
      (is  $\exists w. ?\text{new-w } w$ )
      using read ns' ka wt type-adal unfolding s-def E-def
      by(rule read-non-speculative-new-actions-for)
      then obtain w where w: ?new-w w ..

      define E'' where E'' = ?E @ map (Pair t) (take (Suc j) (drop i \{\!|ta'|\!\}_o))

      from Red redT' have mthr.if.RedT s (ttas @ [(t, ta')]) s'' unfolding mthr.if.RedT-def ..
      hence tsa: thread-start-actions-ok (llist-of (lift-start-obs start-tid start-heap-obs @ concat (map
( $\lambda(t, ta). \text{map } (\text{Pair } t) \{\!|ta|\!\}_o$ ) ttas @ [(t, ta')]))))
      unfolding s-def by(rule thread-start-actions-ok-init-fin-RedT)
      hence thread-start-actions-ok (llist-of E'') unfolding E-def[symmetric] E''-def
      by(rule thread-start-actions-ok-prefix)(rule lprefix-llist-ofI, simp, metis append-take-drop-id
eq map-append)
      moreover from w have w \in actions (llist-of E'')
      unfolding E''-def by(auto simp add: new-actions-for-def actions-def)

```

moreover have $\text{length } ?E + j \in \text{actions } (\text{llist-of } E'')$ **using** j **by**(*auto simp add: E''-def actions-def*)
moreover from w **have** *is-new-action* (*action-obs* (*llist-of* E'') w)
by(*auto simp add: new-actions-for-def action-obs-def actions-def nth-append E''-def*)
moreover have $\neg \text{is-new-action } (\text{action-obs } (\text{llist-of } E'') (\text{length } ?E + j))$
using j $ta'-j$ **by**(*auto simp add: action-obs-def nth-append min-def E''-def*)(*subst (asm) nth-map, simp-all*)
ultimately have $hb-w: P, \text{llist-of } E'' \vdash w \leq_{hb} \text{length } ?E + j$
by(*rule happens-before-new-not-new*)

define *writes* **where**
 $\text{writes} = \{w. P, \text{llist-of } E'' \vdash w \leq_{hb} \text{length } ?E + j \wedge w \in \text{write-actions } (\text{llist-of } E'') \wedge$
 $(ad, al) \in \text{action-loc } P (\text{llist-of } E'') \ w\}$

define w' **where** $w' = \text{Max-torder } (\text{action-order } (\text{llist-of } E'')) \ \text{writes}$

have *writes-actions*: $\text{writes} \subseteq \text{actions } (\text{llist-of } E'')$ **unfolding** *writes-def actions-def*
by(*auto dest!: happens-before-into-action-order elim!: action-orderE simp add: actions-def*)
also have *finite* \dots **by**(*simp add: actions-def*)
finally (*finite-subset*) **have** *finite* *writes* .
moreover from $hb-w$ w **have** $w\text{-writes}: w \in \text{writes}$
by(*auto 4 3 simp add: writes-def new-actions-for-def action-obs-def actions-def nth-append E''-def intro!: write-actions.intros elim!: is-new-action.cases*)
hence $\text{writes} \neq \{\}$ **by** *auto*

with *torder-action-order* $\langle \text{finite } \text{writes} \rangle$
have $w'\text{-writes}: w' \in \text{writes}$ **using** *writes-actions* **unfolding** $w'\text{-def}$ **by**(*rule Max-torder-in-set*)
moreover
 $\{$ **fix** w''
assume $w'' \in \text{writes}$
with *torder-action-order* $\langle \text{finite } \text{writes} \rangle$
have $\text{llist-of } E'' \vdash w'' \leq_a w'$ **using** *writes-actions* **unfolding** $w'\text{-def}$ **by**(*rule Max-torder-above*)
 $\}$

note $w'\text{-maximal} = \text{this}$

define v' **where** $v' = \text{value-written } P (\text{llist-of } E'') \ w' (ad, al)$

from *nsi ta-hb-consistent-into-non-speculative*[*OF hb*]
have $nsi': \text{non-speculative } P (w\text{-values } P \ \text{vs } (\text{concat } (\text{map } (\lambda(t, ta). \{ta\}_o) \ \text{ttas}))) (\text{llist-of } (take (i + j) \{ta'\}_o))$
unfolding *take-add lappend-llist-of-llist-of*[*symmetric*] *non-speculative-lappend vs-def eq*
by(*simp add: non-speculative-lappend o-def map-concat split-def del: lappend-llist-of-llist-of*)

from $w'\text{-writes}$ **have** $adal-w': (ad, al) \in \text{action-loc } P (\text{llist-of } E'') \ w'$ **by**(*simp add: writes-def*)
from $w'\text{-writes}$ **have** $w' \in \text{write-actions } (\text{llist-of } E'')$
unfolding *writes-def* **by** *blast*
then obtain *is-write-action* (*action-obs* (*llist-of* E'') w')
and $w'\text{-actions}: w' \in \text{actions } (\text{llist-of } E'')$ **by** *cases*
hence $v' \in w\text{-values } P (\lambda. \{\}) (\text{map } \text{snd } E'') (ad, al)$
proof *cases*
case (*NewHeapElem ad' CTn*)
hence *NormalAction* (*NewHeapElem ad' CTn*) $\in \text{set } (\text{map } \text{snd } E'')$
using $w'\text{-actions}$ **unfolding** *in-set-conv-nth*
by(*auto simp add: actions-def action-obs-def cong: conj-cong*)

moreover have $ad' = ad$
and $(ad, al) \in \text{action-loc-aux } P \text{ (NormalAction (NewHeapElem ad CTn))}$
using $adal-w' \text{ NewHeapElem by auto}$
ultimately show *?thesis using NewHeapElem unfolding v'-def*
by $(\text{simp add: value-written.simps w-values-new-actionD})$

next

case $(\text{WriteMem ad' al' v'})$
hence $\text{NormalAction (WriteMem ad' al' v')} \in \text{set (map snd E')}$
using $w'\text{-actions unfolding in-set-conv-nth}$
by $(\text{auto simp add: actions-def action-obs-def cong: conj-cong})$
moreover have $ad' = ad \text{ al' = al}$ **using** $adal-w' \text{ WriteMem by auto}$
ultimately show *?thesis using WriteMem unfolding v'-def*
by $(\text{simp add: value-written.simps w-values-WriteMemD})$

qed

hence $v' \in w\text{-values } P \text{ vs (concat (map (\lambda(t, ta). \{ta\}_o) ttas) @ take (i + j) \{ta'\}_o) (ad, al)}$
using $j \text{ ta}'\text{-j eq unfolding E''-def vs-def}$

by $(\text{simp add: o-def split-def map-concat take-add take-Suc-conv-app-nth})$

from $\text{if.non-speculative-readD[OF nsr Red ns[folded vs-def] tst red' aok' j nsi' ta'\text{-j this]}$

obtain $ta'' x'' m''$

where $\text{red}'': t \vdash (x, \text{shr } s') -ta'' \rightarrow i (x'', m'')$

and $\text{aok}'': \text{mthr.if.actions-ok } s' t ta''$

and $j': i + j < \text{length } \{ta''\}_o$

and $\text{eq}'': \text{take } (i + j) \{ta''\}_o = \text{take } (i + j) \{ta'\}_o$

and $ta''\text{-j}: \{ta''\}_o ! (i + j) = \text{NormalAction (ReadMem ad al v')}$

and $\text{len}'': \text{length } \{ta''\}_o \leq \max n (\text{length } \{ta'\}_o)$ **by** blast

define EE **where** $EE = ?E @ \text{map (Pair } t) (\text{take } j (\text{drop } i \{ta''\}_o))$

define E' **where** $E' = ?E @ \text{map (Pair } t) (\text{take } j (\text{drop } i \{ta''\}_o)) @ [(t, \text{NormalAction (ReadMem ad al v')}]$

from $\text{len}' \text{ len}$ **have** $\text{length } \{ta''\}_o \leq \max n (\text{length } \{ta\}_o)$ **by** simp

moreover from $\text{eq}' \text{ eq } j j'$ **have** $\text{take } i \{ta''\}_o = \text{take } i \{ta\}_o$

by $(\text{auto simp add: take-add min-def})$

moreover {

note hb

also have $\text{eq}'': \text{take } j (\text{drop } i \{ta'\}_o) = \text{take } j (\text{drop } i \{ta''\}_o)$

using $\text{eq}' j j'$ **by** $(\text{simp add: take-add min-def})$

also have $ta\text{-hb-consistent } P (?E @ \text{list-of (l\text{list-of (map (Pair } t) (\text{take } j (\text{drop } i \{ta''\}_o))))})$
 $(\text{l\text{list-of [(t, \{ta''\}_o ! (i + j))])})$

unfolding $\text{l\text{list-of.simps ta-hb-consistent-LCons ta-hb-consistent-LNil ta''\text{-j prod.simps}}$

$\text{action.simps obs-event.simps list-of-l\text{list-of append-assoc E'\text{-def[symmetric, unfolded append-assoc]}}$

unfolding $EE\text{-def[symmetric, unfolded append-assoc]}$

proof $(\text{intro conjI TrueI exI[where } x=w'] \text{ strip})$

have $\text{l\text{list-of } E''} [\approx] \text{l\text{list-of } E'}$ **using** $j \text{ len eq'' ta''\text{-j unfolding E''\text{-def E'\text{-def}}$

by $(\text{auto simp add: sim-actions-def list-all2-append List.list-all2-refl split-beta take-Suc-conv-app-nth take-map[symmetric]})$

moreover have $\text{length } E'' = \text{length } E'$ **using** $j j'$ **by** $(\text{simp add: E''\text{-def E'\text{-def}}})$

ultimately have $\text{sim: ltake (enat (length } E')) (\text{l\text{list-of } E''} [\approx] \text{ltake (enat (length } E'))}$

$(\text{l\text{list-of } E'})$ **by** simp

from $w'\text{-actions } \langle \text{length } E'' = \text{length } E' \rangle$

have $w'\text{-len: } w' < \text{length } E'$ **by** $(\text{simp add: actions-def})$

from $\langle w' \in \text{write-actions (l\text{list-of } E'') \rangle \text{ sim}$

```

show  $w' \in \text{write-actions } (\text{llist-of } E')$  by(rule write-actions-change-prefix)(simp add: w'-len)
from adal-w' action-loc-change-prefix[OF sim, of w' P]
show  $(ad, al) \in \text{action-loc } P (\text{llist-of } E')$   $w'$  by(simp add: w'-len)

from ta'-j j have length ?E + j  $\in$  read-actions (llist-of E'')
  by(auto intro!: read-actions.intros simp add: action-obs-def actions-def E''-def min-def
nth-append)(auto)
hence  $w' \neq \text{length } ?E + j$  using  $\langle w' \in \text{write-actions } (\text{llist-of } E'') \rangle$ 
  by(auto dest: read-actions-not-write-actions)
with w'-len have  $w' < \text{length } ?E + j$  by(simp add: E'-def)
from j j' len' eq''
have ltake (enat (length ?E + j)) (llist-of E'') = ltake (enat (length ?E + j)) (llist-of E')
  by(auto simp add: E''-def E'-def min-def take-Suc-conv-app-nth)
from value-written-change-prefix[OF this, of w' P]  $\langle w' < \text{length } ?E + j \rangle$ 
show value-written P (llist-of E')  $w' (ad, al) = v'$  unfolding v'-def by simp

from  $\langle \text{thread-start-actions-ok } (\text{llist-of } E'') \rangle \langle \text{llist-of } E'' [\approx] \text{llist-of } E' \rangle$ 
have tsa'': thread-start-actions-ok (llist-of E')
  by(rule thread-start-actions-ok-change)

from w'-writes j j' len len' have P, llist-of E''  $\vdash w' \leq_{\text{hb}} \text{length } EE$ 
  by(auto simp add: EE-def writes-def min-def ac-simps)
thus P, llist-of E'  $\vdash w' \leq_{\text{hb}} \text{length } EE$  using tsa'' sim
  by(rule happens-before-change-prefix)(simp add: w'-len, simp add: EE-def E'-def)

fix w''
assume w'':  $w'' \in \text{write-actions } (\text{llist-of } E')$ 
  and adal-w'':  $(ad, al) \in \text{action-loc } P (\text{llist-of } E')$   $w''$ 

from w'' have w''-len:  $w'' < \text{length } E'$  by(cases)(simp add: actions-def)

from w'' sim[symmetric] have w'':  $w'' \in \text{write-actions } (\text{llist-of } E'')$ 
  by(rule write-actions-change-prefix)(simp add: w''-len)
from adal-w'' action-loc-change-prefix[OF sim[symmetric], of w'' P] w''-len
have adal-w'':  $(ad, al) \in \text{action-loc } P (\text{llist-of } E'')$   $w''$  by simp
{
  presume w'-w'':  $\text{llist-of } E' \vdash w' \leq_a w''$ 
    and w''-hb:  $P, \text{llist-of } E' \vdash w'' \leq_{\text{hb}} \text{length } EE$ 
  from w''-hb  $\langle \text{thread-start-actions-ok } (\text{llist-of } E'') \rangle$  sim[symmetric]
  have P, llist-of E''  $\vdash w'' \leq_{\text{hb}} \text{length } EE$ 
    by(rule happens-before-change-prefix)(simp add: w''-len, simp add: E'-def EE-def)
  with w'' adal-w'' j j' len len' have  $w'' \in \text{writes}$ 
    by(auto simp add: writes-def EE-def min-def ac-simps split: if-split-asm)
  hence llist-of E''  $\vdash w'' \leq_a w'$  by(rule w'-maximal)
  hence llist-of E'  $\vdash w'' \leq_a w'$  using sim
    by(rule action-order-change-prefix)(simp-all add: w'-len w''-len)
  thus  $w'' = w' w'' = w'$  using w'-w'' by(rule antisymPD[OF antisym-action-order])+
}

{ assume P, llist-of E'  $\vdash w' \leq_{\text{hb}} w'' \wedge P, \text{llist-of } E' \vdash w'' \leq_{\text{hb}} \text{length } EE$ 
  thus llist-of E'  $\vdash w' \leq_a w''$  P, llist-of E'  $\vdash w'' \leq_{\text{hb}} \text{length } EE$ 
  by(blast dest: happens-before-into-action-order)+ }
assume is-volatile P al  $\wedge P, \text{llist-of } E' \vdash w' \leq_{\text{so}} w'' \wedge P, \text{llist-of } E' \vdash w'' \leq_{\text{so}} \text{length } EE$ 
then obtain vol: is-volatile P al

```

and so: $P, \text{llist-of } E' \vdash w' \leq_{so} w''$
and so': $P, \text{llist-of } E' \vdash w'' \leq_{so} \text{length } EE$ **by** *blast*
from so show $\text{llist-of } E' \vdash w' \leq_a w''$ **by** (*blast elim: sync-orderE*)

show $P, \text{llist-of } E' \vdash w'' \leq_{hb} \text{length } EE$
proof (*cases is-new-action (action-obs (llist-of E') w'')*)

case True
with $\langle w'' \in \text{write-actions (llist-of } E') \rangle$ $ta''\text{-}j$ **show** *?thesis*
by *cases(rule happens-before-new-not-new[OF tsa''], auto simp add: actions-def EE-def E'-def action-obs-def min-def nth-append)*

next
case False
with $\langle w'' \in \text{write-actions (llist-of } E') \rangle$ $\langle (ad, al) \in \text{action-loc } P \text{ (llist-of } E') w'' \rangle$
obtain v'' **where** $\text{action-obs (llist-of } E') w'' = \text{NormalAction (WriteMem ad al } v'')$
by *cases(auto elim: is-write-action.cases)*
with $ta''\text{-}j$ w'' j j' len len'
have $P \vdash (\text{action-tid (llist-of } E') w'', \text{action-obs (llist-of } E') w'') \rightsquigarrow_{sw} (\text{action-tid (llist-of } E') (\text{length } EE), \text{action-obs (llist-of } E') (\text{length } EE))$
by (*auto simp add: E'-def EE-def action-obs-def min-def nth-append Volatile*)
with so' have $P, \text{llist-of } E' \vdash w'' \leq_{sw} \text{length } EE$ **by** (*rule sync-withI*)
thus *?thesis unfolding po-sw-def [abs-def] by(blast intro: tranclp.r-into-trancl)*
qed }

qed
ultimately have *ta-hb-consistent P ?E (lappend (llist-of (map (Pair t) (take j (drop i {ta''}_o)))) (llist-of [(t, {ta''}_o ! (i + j))]))*
by (*rule ta-hb-consistent-lappendI simp*)
hence *ta-hb-consistent P ?E (llist-of (map (Pair t) (take (Suc j) (drop i {ta''}_o))))*
using j' **unfolding** *lappend-llist-of-llist-of* **by** (*simp add: take-Suc-conv-app-nth*) }
moreover from $len-i$ **have** $i < \text{length } \{ta\}_o \longrightarrow i < \text{length } \{ta''\}_o$ **using** $eq' j'$ **by** *auto*
moreover from $sim-i$ $eq' ta''\text{-}j ta'\text{-}j$
have (*if* $\exists ad\ al\ v. \{ta\}_o ! i = \text{NormalAction (ReadMem ad al } v)$ *then sim-action else (=)*)
 $(\{ta\}_o ! i) (\{ta''\}_o ! i)$
by (*cases j = 0)(auto split: if-split-asm, (metis add-strict-left-mono add-0-right nth-take)+*)
ultimately show *?thesis using red'' aok'' by blast*

next
case False
hence *ta-hb-consistent P (?E @ list-of (llist-of (map (Pair t) (take j (drop i {ta''}_o)))) (llist-of [(t, {ta''}_o ! (i + j))]))*
by (*simp add: ta-hb-consistent-LCons split: action.split obs-event.split*)
with hb
have *ta-hb-consistent P ?E (lappend (llist-of (map (Pair t) (take j (drop i {ta''}_o)))) (llist-of [(t, {ta''}_o ! (i + j))]))*
by (*rule ta-hb-consistent-lappendI simp*)
hence *ta-hb-consistent P ?E (llist-of (map (Pair t) (take (Suc j) (drop i {ta''}_o))))*
using j **unfolding** *lappend-llist-of-llist-of* **by** (*simp add: take-Suc-conv-app-nth*)
with red' aok' len eq $len-i$ $sim-i$ **show** *?thesis by blast*

qed
qed
qed }

from *this[of max n (length {ta}_o)]*
show $\exists ta' x'' m''. t \vdash (x, \text{shr } s') -ta' \rightarrow i (x'', m'') \wedge \text{mthr.if.actions-ok } s' t ta' \wedge$
 $\text{take } i \{ta'\}_o = \text{take } i \{ta\}_o \wedge$
 $\text{ta-hb-consistent } P ?E (\text{llist-of (map (Pair t) (drop } i \{ta'\}_o))) \wedge$
 $(i < \text{length } \{ta\}_o \longrightarrow i < \text{length } \{ta'\}_o) \wedge$

```

      (if  $\exists ad\ al\ v. \{ta\}_o ! i = NormalAction (ReadMem\ ad\ al\ v)$  then sim-action else
(=)) ( $\{ta\}_o ! i$ ) ( $\{ta'\}_o ! i$ )
  by(simp del: split-paired-Ex cong: conj-cong split del: if-split) blast
qed

end

end

```

8.12 Type-safety proof for the Java memory model

```

theory JMM-Typesafe
imports
  JMM-Framework
begin

```

Create a dynamic list *heap-independent* of theorems for replacing heap-dependent constants by heap-independent ones.

```

ML <
structure Heap-Independent-Rules = Named-Thms
(
  val name = @{binding heap-independent}
  val description = Simplification rules for heap-independent constants
)
>
setup <Heap-Independent-Rules.setup>

locale heap-base' =
  h: heap-base
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate  $\lambda-. \text{typeof-addr heap-read heap-write}$ 
  for addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
  and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
  and spurious-wakeups :: bool
  and empty-heap :: 'heap
  and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
  and typeof-addr :: 'addr  $\rightarrow$  htype
  and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
  and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
begin

```

```

definition typeof-h :: 'addr val  $\Rightarrow$  ty option
where typeof-h = h.typeof-h undefined
lemma typeof-h-conv-typeof-h [heap-independent, iff]: h.typeof-h h = typeof-h
by(rule ext)(case-tac x, simp-all add: typeof-h-def)
lemmas typeof-h-simps [simp] = h.typeof-h.simps [unfolded heap-independent]

```

```

definition cname-of :: 'addr  $\Rightarrow$  cname
where cname-of = h.cname-of undefined
lemma cname-of-conv-cname-of [heap-independent, iff]: h.cname-of h = cname-of
by(simp add: cname-of-def h.cname-of-def[abs-def])

```

definition *addr-loc-type* :: 'm prog \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow ty \Rightarrow bool
where *addr-loc-type* P = h.addr-loc-type P undefined
notation *addr-loc-type* ($\langle \vdash \text{-}@ \vdash \text{-} \rightarrow$ [50, 50, 50, 50] 51)
lemma *addr-loc-type-conv-addr-loc-type* [heap-independent, iff]:
 h.addr-loc-type P h = addr-loc-type P
by(simp add: addr-loc-type-def h.addr-loc-type-def)
lemmas *addr-loc-type-cases* [cases pred: addr-loc-type] =
 h.addr-loc-type.cases[unfolded heap-independent]
lemmas *addr-loc-type-intros* = h.addr-loc-type.intros[unfolded heap-independent]

definition *typeof-addr-loc* :: 'm prog \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow ty
where *typeof-addr-loc* P = h.typeof-addr-loc P undefined
lemma *typeof-addr-loc-conv-typeof-addr-loc* [heap-independent, iff]:
 h.typeof-addr-loc P h = typeof-addr-loc P
by(simp add: typeof-addr-loc-def h.typeof-addr-loc-def[abs-def])

definition *conf* :: 'a prog \Rightarrow 'addr val \Rightarrow ty \Rightarrow bool
where *conf* P \equiv h.conf P undefined
notation *conf* ($\langle \vdash \text{-} \leq \vdash \text{-}$ [51,51,51] 50)
lemma *conf-conv-conf* [heap-independent, iff]: h.conf P h = conf P
by(simp add: conf-def heap-base.conf-def[abs-def])
lemmas *defval-conf* [simp] = h.defval-conf[unfolded heap-independent]

definition *lconf* :: 'm prog \Rightarrow (vname \rightarrow 'addr val) \Rightarrow (vname \rightarrow ty) \Rightarrow bool
where *lconf* P = h.lconf P undefined
notation *lconf* ($\langle \vdash \text{-} (\leq) \vdash \text{-}$ [51,51,51] 50)
lemma *lconf-conv-lconf* [heap-independent, iff]: h.lconf P h = lconf P
by(simp add: lconf-def h.lconf-def[abs-def])

definition *confs* :: 'm prog \Rightarrow 'addr val list \Rightarrow ty list \Rightarrow bool
where *confs* P = h.confs P undefined
notation *confs* ($\langle \vdash \text{-} [\leq] \vdash \text{-}$ [51,51,51] 50)
lemma *confs-conv-confs* [heap-independent, iff]: h.confs P h = confs P
by(simp add: confs-def)

definition *tconf* :: 'm prog \Rightarrow 'thread-id \Rightarrow bool
where *tconf* P = h.tconf P undefined
notation *tconf* ($\langle \vdash \text{-} \sqrt{\vdash} \vdash \text{-}$ [51,51] 50)
lemma *tconf-conv-tconf* [heap-independent, iff]: h.tconf P h = tconf P
by(simp add: tconf-def h.tconf-def[abs-def])

definition *vs-conf* :: 'm prog \Rightarrow ('addr \times addr-loc \Rightarrow 'addr val set) \Rightarrow bool
where *vs-conf* P = h.vs-conf P undefined
lemma *vs-conf-conv-vs-conf* [heap-independent, iff]: h.vs-conf P h = vs-conf P
by(simp add: vs-conf-def h.vs-conf-def[abs-def])

lemmas *vs-confI* = h.vs-confI[unfolded heap-independent]
lemmas *vs-confD* = h.vs-confD[unfolded heap-independent]

use non-speculativity to express that only type-correct values are read

primrec *vs-type-all* :: 'm prog \Rightarrow 'addr \times addr-loc \Rightarrow 'addr val set
where *vs-type-all* P (ad, al) = {v. $\exists T. P \vdash \text{ad}@al : T \wedge P \vdash v : \leq T$ }

lemma *vs-conf-vs-type-all* [simp]: vs-conf P (vs-type-all P)

by(*rule* *h.vs-confI*[*unfolded heap-independent*])(*simp*)

lemma *w-addr-vs-type-all*: *w-addr* (*vs-type-all* *P*) \subseteq *dom typeof-addr*
by(*auto simp addr: w-addr-def h.conf-def*[*unfolded heap-independent*])

lemma *w-addr-vs-type-all-in-vs-type-all*:

$(\bigcup ad \in w\text{-addr} \ (vs\text{-type-all } P). \{(ad, al) \mid al. \exists T. P \vdash ad@al : T\}) \subseteq \{adal. vs\text{-type-all } P \ adal \neq \{\}\}$

by(*auto simp addr: w-addr-def vs-type-all-def intro: defval-conf*)

declare *vs-type-all.simps* [*simp del*]

lemmas *vs-conf-insert-iff* = *h.vs-conf-insert-iff*[*unfolded heap-independent*]

end

locale *heap'* =

h: heap

addr2thread-id thread-id2addr

spurious-wakeups

empty-heap allocate $\lambda\cdot$. *typeof-addr heap-read heap-write*

P

for *addr2thread-id* :: ('*addr* :: *addr*) \Rightarrow '*thread-id*

and *thread-id2addr* :: '*thread-id* \Rightarrow '*addr*

and *spurious-wakeups* :: *bool*

and *empty-heap* :: '*heap*

and *allocate* :: '*heap* \Rightarrow *h**type* \Rightarrow ('*heap* \times '*addr*) *set*

and *typeof-addr* :: '*addr* \rightarrow *h**type*

and *heap-read* :: '*heap* \Rightarrow '*addr* \Rightarrow *addr-loc* \Rightarrow '*addr val* \Rightarrow *bool*

and *heap-write* :: '*heap* \Rightarrow '*addr* \Rightarrow *addr-loc* \Rightarrow '*addr val* \Rightarrow '*heap* \Rightarrow *bool*

and *P* :: '*m prog*

sublocale *heap'* < *heap-base'* .

context *heap'* **begin**

lemma *vs-conf-w-value-WriteMemD*:

$\llbracket vs\text{-conf } P \ (w\text{-value } P \ vs \ ob); \ ob = \text{NormalAction } (\text{WriteMem } ad \ al \ v) \rrbracket$

$\implies \exists T. P \vdash ad@al : T \wedge P \vdash v : \leq T$

by(*auto elim: vs-confD*)

lemma *vs-conf-w-values-WriteMemD*:

$\llbracket vs\text{-conf } P \ (w\text{-values } P \ vs \ obs); \ \text{NormalAction } (\text{WriteMem } ad \ al \ v) \in \text{set } obs \rrbracket$

$\implies \exists T. P \vdash ad@al : T \wedge P \vdash v : \leq T$

apply(*induct obs arbitrary: vs*)

apply(*auto 4 3 elim: vs-confD intro: w-values-mono*[*THEN subsetD*])

done

lemma *w-values-vs-type-all-start-heap-obs*:

assumes *wf: wf-syscls* *P*

shows *w-values* *P* (*vs-type-all* *P*) (*map snd* (*lift-start-obs* *h.start-tid* *h.start-heap-obs*)) = *vs-type-all* *P*

(**is** ?*lhs* = ?*rhs*)


```

proof(rule antisym, rule le-funI, rule subsetI)
  fix adal v
  assume v: v ∈ ?lhs adal
  obtain ad al where adal: adal = (ad, al) by(cases adal)
  show v ∈ ?rhs adal
  proof(rule ccontr)
    assume v': ¬ ?thesis
    from in-w-valuesD[OF v[unfolded adal] this[unfolded adal]]
    obtain obs' wa obs''
      where eq: map snd (lift-start-obs h.start-tid h.start-heap-obs) = obs' @ wa # obs''
      and write: is-write-action wa
      and loc: (ad, al) ∈ action-loc-aux P wa
      and vwa: value-written-aux P wa al = v
      by blast+
    from write show False
  proof cases
    case (WriteMem ad' al' v')
      with vwa loc eq have WriteMem ad al v ∈ set h.start-heap-obs
        by(auto simp add: map-eq-append-conv Cons-eq-append-conv lift-start-obs-def)
      from h.start-heap-write-typeable[OF this] v' adal
      show ?thesis by(auto simp add: vs-type-all-def)
    next
      case (NewHeapElem ad' hT)
      with vwa loc eq have NewHeapElem ad hT ∈ set h.start-heap-obs
        by(auto simp add: map-eq-append-conv Cons-eq-append-conv lift-start-obs-def)
      hence typeof-addr ad = [hT]
        by(rule h.NewHeapElem-start-heap-obsD[OF wf])
      with v' adal loc vwa NewHeapElem show ?thesis
        by(auto simp add: vs-type-all-def intro: addr-loc-type-intros h.addr-loc-default-conf[unfolded
heap-independent])
      qed
      qed
qed(rule w-values-greater)

end

```

```

lemma lprefix-lappend2I: lprefix xs ys ⇒ lprefix xs (lappend ys zs)
by(auto simp add: lappend-assoc lprefix-conv-lappend)

```

```

locale known-addr-typing' =
  h: known-addr-typing
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate λ-. typeof-addr heap-read heap-write
  allocated known-addr
  final r wfx
  P
for addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'addr → htype

```

```

and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and allocated :: 'heap ⇒ 'addr set
and known-addr :: 'thread-id ⇒ 'x ⇒ 'addr set
and final :: 'x ⇒ bool
and r :: ('addr, 'thread-id, 'x, 'heap, 'addr, ('addr, 'thread-id) obs-event) semantics (← ⊢ - - -> →)
[50,0,0,50] 80)
and wfx :: 'thread-id ⇒ 'x ⇒ 'heap ⇒ bool
and P :: 'md prog
+
assumes NewHeapElem-typed: — Should this be moved to known_addr_typing?
[[ t ⊢ (x, h) -ta→ (x', h'); NewHeapElem ad CTn ∈ set {ta}o; typeof-addr ad ≠ None ]]
⇒ typeof-addr ad = [CTn]

```

sublocale known-addr-typing' < heap' **by** unfold-locales

context known-addr-typing' **begin**

lemma known-addr-typeable-in-vs-type-all:

```

h.if.known-addr-state s ⊆ dom typeof-addr
⇒ (⋃ a ∈ h.if.known-addr-state s. {(a, al)|al. ∃ T. P ⊢ a@al : T}) ⊆ {adal. vs-type-all P adal ≠ {} }

```

by(auto 4 4 dest: subsetD simp add: vs-type-all.simps intro: defval-conf)

lemma if-NewHeapElem-typed:

```

[[ t ⊢ xh -ta→i x'h'; NormalAction (NewHeapElem ad CTn) ∈ set {ta}o; typeof-addr ad ≠ None ]]
⇒ typeof-addr ad = [CTn]

```

by(cases rule: h.mthr.init-fin.cases)(auto dest: NewHeapElem-typed)

lemma if-redT-NewHeapElem-typed:

```

[[ h.mthr.if.redT s (t, ta) s'; NormalAction (NewHeapElem ad CTn) ∈ set {ta}o; typeof-addr ad ≠ None ]]
⇒ typeof-addr ad = [CTn]

```

by(cases rule: h.mthr.if.redT.cases)(auto dest: if-NewHeapElem-typed)

lemma non-speculative-written-value-typeable:

```

assumes wfx-start: ts-ok wfx (thr (h.start-state f P C M vs)) h.start-heap
and wfp: wf-syscls P
and E: E ∈ h.ℰ-start f P C M vs status
and write: w ∈ write-actions E
and adal: (ad, al) ∈ action-loc P E w
and ns: non-speculative P (vs-type-all P) (lmap snd (ltake (enat w) E))
shows ∃ T. P ⊢ ad@al : T ∧ P ⊢ value-written P E w (ad, al) :≤ T

```

proof —

```

let ?start-state = init-fin-lift-state status (h.start-state f P C M vs)
and ?start-obs = lift-start-obs h.start-tid h.start-heap-obs
and ?v = value-written P E w (ad, al)

```

from write **have** iwa: is-write-action (action-obs E w) **by** cases

from E **obtain** E' **where** E': E = lappend (llist-of ?start-obs) E'

and ℰ: E' ∈ h.mthr.if.ℰ ?start-state **by** blast

from ℰ **obtain** E'' **where** E'': E' = lconcat (lmap (λ(t, ta). llist-of (map (Pair t) {ta}o)) E'')

and Runs: h.mthr.if.mthr.Runs ?start-state E''

```

by-(rule h.mthr.if.ℰ.cases[OF ℰ])

have wfx': ts-ok (init-fin-lift wfx) (thr ?start-state) (shr ?start-state)
  using wfx-start by(simp add: h.shr-start-state)

from ns E'
have ns: non-speculative P (vs-type-all P) (lmap snd (ldropn (length (lift-start-obs h.start-tid h.start-heap-obs))
  (ltake (enat w) E)))
  by(subst (asm) lappend-ltake-ldrop[where n=enat (length (lift-start-obs h.start-tid h.start-heap-obs)),
  symmetric])(simp add: non-speculative-lappend min-def ltake-lappend1 w-values-vs-type-all-start-heap-obs[OF
  wfP] ldrop-enat split: if-split-asm)

show ?thesis
proof(cases w < length ?start-obs)
  case True
  hence in-start: action-obs E w ∈ set (map snd ?start-obs)
    unfolding in-set-conv-nth E' by(simp add: lnth-lappend action-obs-def map-nth exI[where x=w])

  from iwa show ?thesis
  proof(cases)
    case (WriteMem ad' al' v')
    with adal have ad' = ad al' = al ?v = v' by(simp-all add: value-written.simps)
    with WriteMem in-start have WriteMem ad al ?v ∈ set h.start-heap-obs by auto
    thus ?thesis by(rule h.start-heap-write-typeable[unfolded heap-independent])
  next
    case (NewHeapElem ad' CTn)
    with adal have [simp]: ad' = ad by auto
    with NewHeapElem in-start have NewHeapElem ad CTn ∈ set h.start-heap-obs by auto
    with wfP have typeof-addr ad = [CTn] by(rule h.NewHeapElem-start-heap-obsD)
    with adal NewHeapElem show ?thesis
    by(cases al)(auto simp add: value-written.simps intro: addr-loc-type-intros h.addr-loc-default-conf[unfolded
    heap-independent])
  qed
  next
  case False
  define w' where w' = w - length ?start-obs
  with write False have w'-len: enat w' < llength E'
    by(cases llength E')(auto simp add: actions-def E' elim: write-actions.cases)
  with Runs obtain m-w n-w t-w ta-w
    where E'-w: lnth E' w' = (t-w, {ta-w}_o ! n-w)
    and n-w: n-w < length {ta-w}_o
    and m-w: enat m-w < llength E''
    and w-sum: w' = (∑ i<m-w. length {snd (lnth E'' i)}_o) + n-w
    and E''-m-w: lnth E'' m-w = (t-w, ta-w)
    unfolding E'' by(rule h.mthr.if.actions-ℰE-aux)

  from E'-w have obs-w: action-obs E w = {ta-w}_o ! n-w
    using False E' w'-def by(simp add: action-obs-def lnth-lappend)

  let ?E'' = ldropn (Suc m-w) E''
  let ?m-E'' = ltake (enat m-w) E''
  have E'-unfold: E' = lappend ?m-E'' (LCons (lnth E'' m-w) ?E'')
    unfolding ldropn-Suc-conv-ldropn[OF m-w] by simp
  hence h.mthr.if.mthr.Runs ?start-state (lappend ?m-E'' (LCons (lnth E'' m-w) ?E''))

```

```

    using Runs by simp
  then obtain  $\sigma'$  where  $\sigma\text{-}\sigma'$ : h.mthr.if.mthr.Trsys ?start-state (list-of ?m-E'')  $\sigma'$ 
    and Runs': h.mthr.if.mthr.Runs  $\sigma'$  (LCons (lnth E'' m-w) ?E'')
    by(rule h.mthr.if.mthr.Runs-lappendE) simp
  from Runs' obtain  $\sigma'''$  where red-w: h.mthr.if.redT  $\sigma'$  (t-w, ta-w)  $\sigma'''$ 
    and Runs'': h.mthr.if.mthr.Runs  $\sigma'''$  ?E''
    unfolding E''-m-w by cases

  let ?EE'' = lmap snd (lappend (lconcat (lmap ( $\lambda(t, ta).$  llist-of (map (Pair t) {ta}_o)) ?m-E''))
(llist-of (map (Pair t-w) (take (n-w + 1) {ta-w}_o)))))
  have len-EE'': llength ?EE'' = enat (w' + 1) using n-w m-w
    apply(simp add: w-sum)
    apply(subst llength-lconcat-lfinite-conv-sum)
    apply(simp-all add: split-beta plus-enat-simps(1)[symmetric] add-Suc-right[symmetric] del: plus-enat-simps(1)
add-Suc-right)
    apply(subst sum-comp-morphism[symmetric, where h=enat])
    apply(simp-all add: zero-enat-def min-def le-Suc-eq)
    apply(rule sum.cong)
    apply(auto simp add: lnth-ltake less-trans[where y=enat m-w])
  done
  have prefix: lprefix ?EE'' (lmap snd E') unfolding E''
    by(subst (2) E'-unfold)(rule lmap-lprefix, clarsimp simp add: lmap-lappend-distrib E''-m-w
lprefix-lappend2I[OF lprefix-llist-ofI[OF exI[where x=map (Pair t-w) (drop (n-w + 1) {ta-w}_o)]])
map-append[symmetric])

  from iwa False have iwa': is-write-action (action-obs E' w') by(simp add: E' action-obs-def
lnth-lappend w'-def)
  from ns False
  have non-speculative P (vs-type-all P) (lmap snd (ltake (enat w') E'))
    by(simp add: E' ltake-lappend lmap-lappend-distrib non-speculative-lappend ldropsn-lappend2
w'-def)
  with iwa'
  have non-speculative P (vs-type-all P) (lappend (lmap snd (ltake (enat w') E')) (LCons (action-obs
E' w') LNil))
    by cases(simp-all add: non-speculative-lappend)
  also have lappend (lmap snd (ltake (enat w') E')) (LCons (action-obs E' w') LNil) = lmap snd
(ltake (enat (w' + 1)) E')
    using w'-len by(simp add: ltake-Suc-conv-snoc-lnth lmap-lappend-distrib action-obs-def)
  also {
    have lprefix (lmap snd (ltake (enat (w' + 1)) E')) (lmap snd E') by(rule lmap-lprefix) simp
    with prefix have lprefix ?EE'' (lmap snd (ltake (enat (w' + 1)) E'))  $\vee$ 
lprefix (lmap snd (ltake (enat (w' + 1)) E')) ?EE''
    by(rule lprefix-down-linear)
    moreover have llength (lmap snd (ltake (enat (w' + 1)) E')) = enat (w' + 1)
    using w'-len by(cases llength E') simp-all
    ultimately have lmap snd (ltake (enat (w' + 1)) E') = ?EE''
    using len-EE'' by(auto dest: lprefix-llength-eq-imp-eq) }
  finally
  have ns1: non-speculative P (vs-type-all P) (llist-of (concat (map ( $\lambda(t, ta).$  {ta}_o) (list-of ?m-E''))))
    and ns2: non-speculative P (w-values P (vs-type-all P) (map snd (list-of (lconcat (lmap ( $\lambda(t,$ 
ta). llist-of (map (Pair t) {ta}_o)) ?m-E''))))) (llist-of (take (Suc n-w) {ta-w}_o))
    by(simp-all add: lmap-lappend-distrib non-speculative-lappend split-beta lconcat-llist-of[symmetric]
lmap-lconcat llist.map-comp o-def split-def list-of-lmap[symmetric] del: list-of-lmap)

```

```

have vs-conf P (vs-type-all P) by simp
with  $\sigma$ - $\sigma'$  wfx' ns1
have wfx': ts-ok (init-fin-lift wfx) (thr  $\sigma'$ ) (shr  $\sigma'$ )
and vs-conf: vs-conf P (w-values P (vs-type-all P) (concat (map ( $\lambda(t, ta). \{ta\}_o$ ) (list-of ?m-E'')))
  by(rule h.if-RedT-non-speculative-invar[unfolded h.mthr.if.RedT-def heap-independent])+

have concat (map ( $\lambda(t, ta). \{ta\}_o$ ) (list-of ?m-E'')) = map snd (list-of (lconcat (lmap ( $\lambda(t, ta). \{ta\}_o$ ) (list-of (map (Pair t)  $\{ta\}_o$ )) ?m-E''))))
  by(simp add: split-def lmap-lconcat llist.map-comp o-def list-of-lconcat map-concat)
with vs-conf have vs-conf P (w-values P (vs-type-all P) ...) by simp
with red-w wfx' ns2
have vs-conf': vs-conf P (w-values P (w-values P (vs-type-all P) (map snd (list-of (lconcat (lmap ( $\lambda(t, ta). \{ta\}_o$ ) (list-of (map (Pair t)  $\{ta\}_o$ )) ?m-E''))))) (take (Suc n-w)  $\{ta-w\}_o$ ))
  (is vs-conf - ?vs')
  by(rule h.if-redT-non-speculative-vs-conf[unfolded heap-independent])

from len-EE'' have enat w' < llength ?EE'' by simp
from w'-len have lnth ?EE'' w' = action-obs E' w'
  using lprefix-lnthD[OF prefix <enat w' < llength ?EE''] by(simp add: action-obs-def)
hence ...  $\in$  lset ?EE'' using <enat w' < llength ?EE''> unfolding lset-conv-lnth by(auto intro!: exI)
  also have ...  $\subseteq$  set (map snd (list-of (lconcat (lmap ( $\lambda(t, ta). \{ta\}_o$ ) (list-of (map (Pair t)  $\{ta\}_o$ )) ?m-E'')))) @ take (Suc n-w)  $\{ta-w\}_o$ 
  by(auto 4 4 intro: rev-image-eqI rev-bexI simp add: split-beta lset-lconcat-lfinite dest: lset-lappend[THEN subsetD])
  also have action-obs E' w' = action-obs E w
    using False by(simp add: E' w'-def lnth-lappend action-obs-def)
  also note obs-w-in-set = calculation and calculation = nothing

from iwa have ?v  $\in$  w-values P (vs-type-all P) (map snd (list-of (lconcat (lmap ( $\lambda(t, ta). \{ta\}_o$ ) (list-of (map (Pair t)  $\{ta\}_o$ )) ?m-E'')))) @ take (Suc n-w)  $\{ta-w\}_o$  (ad, al)
  proof(cases)
    case (WriteMem ad' al' v')
      with adal have ad' = ad al' = al ?v = v' by(simp-all add: value-written.simps)
      with obs-w-in-set WriteMem show ?thesis
        by -(rule w-values-WriteMemD, simp)
    next
      case (NewHeapElem ad' CTn)
        with adal have [simp]: ad' = ad and v: ?v = addr-loc-default P CTn al
          by(auto simp add: value-written.simps)
        with obs-w-in-set NewHeapElem adal show ?thesis
          by(unfold v)(rule w-values-new-actionD, simp-all)
  qed
hence ?v  $\in$  ?vs' (ad, al) by simp
with vs-conf' show  $\exists T. P \vdash ad@al : T \wedge P \vdash ?v : \leq T$ 
  by(rule h.vs-confD[unfolded heap-independent])
qed
qed

```

lemma hb-read-value-typeable:

```

assumes wfx-start: ts-ok wfx (thr (h.start-state f P C M vs)) h.start-heap
  (is ts-ok wfx (thr ?start-state) -)
and wfp: wf-syscls P
and E: E  $\in$  h.E-start f P C M vs status

```

```

and wf:  $P \vdash (E, ws) \surd$ 
and races:  $\bigwedge a \text{ ad } al \ v. \llbracket \text{enat } a < \text{llength } E; \text{action-obs } E \ a = \text{NormalAction } (\text{ReadMem } \text{ad } \text{al } \text{v}); \neg$ 
 $P, E \vdash ws \ a \leq_{hb} a \rrbracket$ 
 $\implies \exists T. P \vdash \text{ad}@al : T \wedge P \vdash v : \leq T$ 
and r:  $\text{enat } a < \text{llength } E$ 
and read:  $\text{action-obs } E \ a = \text{NormalAction } (\text{ReadMem } \text{ad } \text{al } \text{v})$ 
shows  $\exists T. P \vdash \text{ad}@al : T \wedge P \vdash v : \leq T$ 
using r read
proof(induction a arbitrary: ad al v rule: less-induct)
  case (less a)
    note  $r = \langle \text{enat } a < \text{llength } E \rangle$ 
    and read =  $\langle \text{action-obs } E \ a = \text{NormalAction } (\text{ReadMem } \text{ad } \text{al } \text{v}) \rangle$ 
    show ?case
    proof(cases P, E  $\vdash$  ws a  $\leq_{hb}$  a)
      case False with r read show ?thesis by(rule races)
    next
      case True
      note  $hb = \text{this}$ 
      hence  $ao: E \vdash ws \ a \leq a \ a$  by(rule happens-before-into-action-order)

from wf have  $ws: \text{is-write-seen } P \ E \ ws$  by(rule wf-exec-is-write-seenD)
from r have  $a \in \text{actions } E$  by(simp add: actions-def)
hence  $a \in \text{read-actions } E$  using read ..
from is-write-seenD[OF ws this read]
have  $\text{write}: ws \ a \in \text{write-actions } E$ 
  and  $\text{adal-w}: (ad, al) \in \text{action-loc } P \ E \ (ws \ a)$ 
  and  $\text{written}: \text{value-written } P \ E \ (ws \ a) \ (ad, al) = v$  by simp-all
from write have  $\text{iwa}: \text{is-write-action } (\text{action-obs } E \ (ws \ a))$  by cases

let ?start-state = init-fin-lift-state status (h.start-state f P C M ws)
  and ?start-obs = lift-start-obs h.start-tid h.start-heap-obs

show ?thesis
proof(cases ws a < a)
  case True
  let ?EE'' = lmap snd (ltake (enat (ws a)) E)

  have non-speculative P (vs-type-all P) ?EE''
  proof(rule non-speculative-nthI)
    fix  $i \text{ ad}' \text{ al}' \text{ v}'$ 
    assume  $i: \text{enat } i < \text{llength } ?EE''$ 
    and  $\text{nth-}i: \text{lnth } ?EE'' \ i = \text{NormalAction } (\text{ReadMem } \text{ad}' \text{ al}' \text{ v}')$ 

    from  $i$  have  $i < ws \ a$  by simp
    hence  $i': i < a$  using True by(simp)
    moreover
    with r have  $\text{enat } i < \text{llength } E$  by(metis enat-ord-code(2) order-less-trans)
    moreover
    with  $\text{nth-}i \ i \ \langle i < ws \ a \rangle$ 
    have  $\text{action-obs } E \ i = \text{NormalAction } (\text{ReadMem } \text{ad}' \text{ al}' \text{ v}')$ 
    by(simp add: action-obs-def lnth-ltake ac-simps)
    ultimately have  $\exists T. P \vdash \text{ad}'@al' : T \wedge P \vdash v' : \leq T$  by(rule less.IH)
    hence  $v' \in \text{vs-type-all } P \ (\text{ad}', \text{al}')$  by(simp add: vs-type-all.simps)
    thus  $v' \in \text{w-values } P \ (\text{vs-type-all } P) \ (\text{list-of } (\text{ltake } (\text{enat } i) \ ?EE'')) \ (\text{ad}', \text{al}')$ 

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  by(rule w-values-mono[THEN subsetD])
qed
with wfx-start wfP E write adal-w
show ?thesis unfolding written[symmetric] by(rule non-speculative-written-value-typeable)
next
case False

from E obtain E' where E': E = lappend (llist-of ?start-obs) E'
  and  $\mathcal{E}$ : E'  $\in$  h.mthr.if. $\mathcal{E}$  ?start-state by blast
from  $\mathcal{E}$  obtain E'' where E'': E' = lconcat (lmap ( $\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \{ta\}_o)$ ) E'')
  and Runs: h.mthr.if.mthr.Runs ?start-state E''
  by-(rule h.mthr.if. $\mathcal{E}$ .cases[OF  $\mathcal{E}$ ])

have wfx': ts-ok (init-fin-lift wfx) (thr ?start-state) (shr ?start-state)
  using wfx-start by(simp add: h.shr-start-state)

have a-start:  $\neg a < \text{length } ?start\text{-obs}$ 
proof
  assume a < length ?start-obs
  with read have NormalAction (ReadMem ad al v)  $\in$  snd ' set ?start-obs
    unfolding set-map[symmetric] in-set-conv-nth
    by(auto simp add: E' lnth-lappend action-obs-def)
  hence ReadMem ad al v  $\in$  set h.start-heap-obs by auto
  thus False by(simp add: h.start-heap-obs-not-Read)
qed
hence ws-a-not-le:  $\neg ws\ a < \text{length } ?start\text{-obs}$  using False by simp

define w where w = ws a - length ?start-obs
from write ws-a-not-le w-def
have enat w < llength (lconcat (lmap ( $\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \{ta\}_o)$ ) E''))
  by(cases llength (lconcat (lmap ( $\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \{ta\}_o)$ ) E''))(auto simp add:
actions-def E' E'' elim: write-actions.cases)
with Runs obtain m-w n-w t-w ta-w
  where E'-w: lnth E' w = (t-w,  $\{ta-w\}_o ! n-w$ )
  and n-w: n-w < length  $\{ta-w\}_o$ 
  and m-w: enat m-w < llength E''
  and w-sum: w = ( $\sum_{i < m-w} \text{length } \{snd (lnth E'' i)\}_o$ ) + n-w
  and E''-m-w: lnth E'' m-w = (t-w, ta-w)
  unfolding E'' by(rule h.mthr.if.actions- $\mathcal{E}$ E-aux)

from E'-w have obs-w: action-obs E (ws a) =  $\{ta-w\}_o ! n-w$ 
  using ws-a-not-le E' w-def by(simp add: action-obs-def lnth-lappend)

let ?E'' = ldropn (Suc m-w) E''
let ?m-E'' = ltake (enat m-w) E''
have E'-unfold: E'' = lappend ?m-E'' (LCons (lnth E'' m-w) ?E'')
  unfolding ldropn-Suc-conv-ldropn[OF m-w] by simp
hence h.mthr.if.mthr.Runs ?start-state (lappend ?m-E'' (LCons (lnth E'' m-w) ?E''))
  using Runs by simp
then obtain  $\sigma'$  where  $\sigma$ - $\sigma'$ : h.mthr.if.mthr.Trsys ?start-state (list-of ?m-E'')  $\sigma'$ 
  and Runs': h.mthr.if.mthr.Runs  $\sigma'$  (LCons (lnth E'' m-w) ?E'')
  by(rule h.mthr.if.mthr.Runs-lappendE) simp
from Runs' obtain  $\sigma'''$  where red-w: h.mthr.if.redT  $\sigma'$  (t-w, ta-w)  $\sigma'''$ 
  and Runs'': h.mthr.if.mthr.Runs  $\sigma'''$  ?E''

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unfolding  $E''$ - $m$ - $w$  by cases

from write  $\langle a \in \text{read-actions } E \rangle$  have  $ws\ a \neq a$  by(auto dest: read-actions-not-write-actions)
with False have  $ws\ a > a$  by simp
with ao have new: is-new-action (action-obs E (ws a))
  by(simp add: action-order-def split: if-split-asm)
then obtain CTn where obs-w': action-obs E (ws a) = NormalAction (NewHeapElem ad CTn)
  using adal-w by cases auto

define  $a'$  where  $a' = a - \text{length } ?\text{start-obs}$ 
with False  $w$ -def
have  $\text{enat } a' < \text{llength } (\text{lconcat } (\text{lmap } (\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \{\!\!|\}ta\!\!\!|_o)) E''))$ 
  by(simp add: le-less-trans[OF -  $\langle \text{enat } w < \text{llength } (\text{lconcat } (\text{lmap } (\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \{\!\!|\}ta\!\!\!|_o)) E'')) \rangle]$ )
with Runs obtain  $m$ - $a$   $n$ - $a$   $t$ - $a$   $ta$ - $a$ 
  where  $E'$ - $a$ :  $\text{lnth } E' a' = (t$ - $a, \{\!\!|\}ta$ - $a\!\!\!|_o ! n$ - $a)$ 
  and  $n$ - $a$ :  $n$ - $a < \text{length } \{\!\!|\}ta$ - $a\!\!\!|_o$ 
  and  $m$ - $a$ :  $\text{enat } m$ - $a < \text{llength } E''$ 
  and  $a$ - $\text{sum}$ :  $a' = (\sum i < m$ - $a. \text{length } \{\!\!|\}\text{snd } (\text{lnth } E'' i)\!\!\!|_o) + n$ - $a$ 
  and  $E''$ - $m$ - $a$ :  $\text{lnth } E'' m$ - $a = (t$ - $a, ta$ - $a)$ 
  unfolding  $E''$  by(rule h.mthr.if.actions- $\mathcal{E}E$ -aux)

from  $a$ - $\text{start } E'$ - $a$  read have  $\text{obs}$ - $a$ :  $\{\!\!|\}ta$ - $a\!\!\!|_o ! n$ - $a = \text{NormalAction } (\text{ReadMem } ad\ al\ v)$ 
  using  $E'$   $w$ -def by(simp add: action-obs-def lnth-lappend a'-def)

let  $?E'' = \text{ldropn } (\text{Suc } m$ - $a) E''$ 
let  $?m$ - $E'' = \text{ltake } (\text{enat } m$ - $a) E''$ 
have  $E'$ -unfold:  $E'' = \text{lappend } ?m$ - $E'' (\text{LCons } (\text{lnth } E'' m$ - $a) ?E'')$ 
  unfolding ldropn-Suc-conv-ldropn[OF m-a] by simp
hence h.mthr.if.mthr.Runs ?start-state (lappend ?m-E'' (LCons (lnth E'' m-a) ?E''))
  using Runs by simp
then obtain  $\sigma''$  where  $\sigma$ - $\sigma''$ : h.mthr.if.mthr.Trsys ?start-state (list-of ?m-E'')  $\sigma''$ 
  and  $\text{Runs}''$ : h.mthr.if.mthr.Runs  $\sigma'' (\text{LCons } (\text{lnth } E'' m$ - $a) ?E'')$ 
  by(rule h.mthr.if.mthr.Runs-lappendE) simp
from  $\text{Runs}''$  obtain  $\sigma'''$  where  $\text{red}$ - $a$ : h.mthr.if.redT  $\sigma'' (t$ - $a, ta$ - $a) \sigma'''$ 
  and  $\text{Runs}''$ : h.mthr.if.mthr.Runs  $\sigma''' ?E''$ 
  unfolding  $E''$ - $m$ - $a$  by cases

let  $?EE'' = \text{llist-of } (\text{concat } (\text{map } (\lambda(t, ta). \{\!\!|\}ta\!\!\!|_o) (\text{list-of } ?m$ - $E''))$ 
from  $m$ - $a$  have  $\text{enat } m$ - $a \leq \text{llength } E''$  by simp
hence  $\text{len-}EE''$ :  $\text{llength } ?EE'' = \text{enat } (a' - n$ - $a)$ 
  by(simp add: a-sum length-concat sum-list-sum-nth atLeast0LessThan length-list-of-conv-the-enat
min-def split-beta lnth-ltake)
have prefix: lprefix ?EE'' (lmap snd E') unfolding  $E''$ 
  by(subst (2) E'-unfold)(simp add: lmap-lappend-distrib lmap-lconcat llist.map-comp o-def
split-def lconcat-llist-of[symmetric] lmap-llist-of[symmetric] lprefix-lappend2I del: lmap-llist-of)

have  $ns$ : non-speculative P (vs-type-all P) ?EE''
proof(rule non-speculative-nthI)
  fix  $i$   $ad'$   $al'$   $v'$ 
  assume  $i$ :  $\text{enat } i < \text{llength } ?EE''$ 
  and  $\text{lnth}$ - $i$ :  $\text{lnth } ?EE'' i = \text{NormalAction } (\text{ReadMem } ad' al' v')$ 
  and non-speculative P (vs-type-all P) (ltake (enat i) ?EE'')

```


let $?i = i + \text{length } ?\text{start-obs}$
from $i \text{ len-EE''}$ **have** $i < a'$ **by** simp
hence $i': ?i < a$ **by**($\text{simp add: } a'\text{-def}$)
moreover
hence $\text{enat } ?i < \text{llength } E$ **using** $\langle \text{enat } a < \text{llength } E \rangle$ **by**($\text{simp add: less-trans}$ [**where** $y = \text{enat}$
a)])
moreover have $\text{enat } i < \text{llength } E'$ **using** i
by $-(\text{rule less-le-trans}[OF - \text{lprefix-llength-le}[OF \text{ prefix}], \text{simplified}], \text{simp})$
from $\text{lprefix-lnthD}[OF \text{ prefix } i]$ lnth-i
have $\text{lnth } (\text{lmap snd } E') i = \text{NormalAction } (\text{ReadMem } ad' al' v')$ **by** simp
hence $\text{action-obs } E ?i = \text{NormalAction } (\text{ReadMem } ad' al' v')$ **using** $\langle \text{enat } i < \text{llength } E' \rangle$
by($\text{simp add: } E' \text{ action-obs-def lnth-lappend } E''$)
ultimately have $\exists T. P \vdash ad'@al' : T \wedge P \vdash v' : \leq T$ **by**(rule less.IH)
hence $v' \in \text{vs-type-all } P (ad', al')$ **by**($\text{simp add: vs-type-all.simps}$)
thus $v' \in \text{w-values } P (\text{vs-type-all } P) (\text{list-of } (\text{ltake } (\text{enat } i) ?EE'')) (ad', al')$
by($\text{rule w-values-mono}[THEN \text{ subsetD}]$)
qed
have $\text{vs-conf } P (\text{vs-type-all } P)$ **by** simp
with $\sigma\text{-}\sigma'' \text{ wfx}' ns$
have $\text{wfx}'' : \text{ts-ok } (\text{init-fin-lift } \text{wfx}) (\text{thr } \sigma'') (\text{shr } \sigma'')$
and $\text{vs}'' : \text{vs-conf } P (\text{w-values } P (\text{vs-type-all } P) (\text{concat } (\text{map } (\lambda(t, ta). \{ta\}_o) (\text{list-of } ?m\text{-}E''))))$
by($\text{rule h.if-RedT-non-speculative-invar}[\text{unfolded heap-independent h.mthr.if.RedT-def}]$)+
note red-w moreover
from $n\text{-w obs-w obs-w'}$ **have** $\text{NormalAction } (\text{NewHeapElem } ad CTn) \in \text{set } \{ta\text{-w}\}_o$
unfolding in-set-conv-nth by auto
moreover
have $ta\text{-a-read} : \text{NormalAction } (\text{ReadMem } ad al v) \in \text{set } \{ta\text{-a}\}_o$
using $n\text{-a obs-a unfolding in-set-conv-nth by blast}$
from red-a have $\exists T. P \vdash ad@al : T$
proof(cases)
case ($\text{redT-normal } x x' h'$)
from $\text{wfx}'' \langle \text{thr } \sigma'' t\text{-a} = \lfloor (x, \text{no-wait-locks}) \rfloor \rangle$
have $\text{init-fin-lift } \text{wfx } t\text{-a } x (\text{shr } \sigma'')$ **by**(rule ts-okD)
with $\langle t\text{-a} \vdash (x, \text{shr } \sigma'') - ta\text{-a} \rightarrow i (x', h') \rangle$
show $?thesis$ **using** $ta\text{-a-read}$
by($\text{rule h.init-fin-red-read-typeable}[\text{unfolded heap-independent}]$)
next
case redT-acquire **thus** $?thesis$ **using** $n\text{-a obs-a ta-a-read by auto}$
qed
hence $\text{typeof-addr } ad \neq \text{None}$ **by**($\text{auto elim: addr-loc-type-cases}$)
ultimately have $\text{typeof-addr } ad = \lfloor CTn \rfloor$ **by**($\text{rule if-redT-NewHeapElem-typed}$)
with $\text{written } adal\text{-w obs-w'}$ **show** $?thesis$
by($\text{cases } al$)($\text{auto simp add: value-written.simps intro: addr-loc-type-intros h.addr-loc-default-conf}[\text{unfolded heap-independent}]$)
qed
qed
qed
theorem
assumes $\text{wfx-start} : \text{ts-ok } \text{wfx} (\text{thr } (h.\text{start-state } f P C M \text{ vs})) h.\text{start-heap}$
and $\text{wfp} : \text{wf-syscls } P$

and *justified*: $P \vdash (E, ws)$ weakly-justified-by J
and J : $\text{range } (\text{justifying-exec} \circ J) \subseteq h.\mathcal{E}\text{-start } f P C M \text{ vs status}$
shows *read-value-typeable-justifying*:
 $\llbracket 0 < n; \text{enat } a < \text{llength } (\text{justifying-exec } (J n));$
 $\text{action-obs } (\text{justifying-exec } (J n)) a = \text{NormalAction } (\text{ReadMem } ad \ al \ v) \rrbracket$
 $\implies \exists T. P \vdash ad@al : T \wedge P \vdash v : \leq T$
and *read-value-typeable-justified*:
 $\llbracket E \in h.\mathcal{E}\text{-start } f P C M \text{ vs status}; P \vdash (E, ws) \checkmark;$
 $\text{enat } a < \text{llength } E; \text{action-obs } E a = \text{NormalAction } (\text{ReadMem } ad \ al \ v) \rrbracket$
 $\implies \exists T. P \vdash ad@al : T \wedge P \vdash v : \leq T$
proof –
let $?E = \lambda n. \text{justifying-exec } (J n)$
and $? \varphi = \lambda n. \text{action-translation } (J n)$
and $?C = \lambda n. \text{committed } (J n)$
and $?ws = \lambda n. \text{justifying-ws } (J n)$
let $? \mathcal{E} = h.\mathcal{E}\text{-start } f P C M \text{ vs status}$
and $?start\text{-obs} = \text{lift-start-obs } h.\text{start-tid } h.\text{start-heap-obs}$
{ fix $a \ n$
assume $\text{enat } a < \text{llength } (\text{justifying-exec } (J n))$
and $\text{action-obs } (\text{justifying-exec } (J n)) a = \text{NormalAction } (\text{ReadMem } ad \ al \ v)$
and $n > 0$
thus $\exists T. P \vdash ad@al : T \wedge P \vdash v : \leq T$
proof(*induction* n *arbitrary*: $a \ ad \ al \ v$)
case 0 **thus** $?case$ **by** *simp*
next
case $(\text{Suc } n')$
define n **where** $n = \text{Suc } n'$
with Suc **have** $n: 0 < n$ **and** $a: \text{enat } a < \text{llength } (?E \ n)$
and $a\text{-obs}: \text{action-obs } (?E \ n) a = \text{NormalAction } (\text{ReadMem } ad \ al \ v)$
by *simp-all*
have $wf\text{-}n: P \vdash (?E \ n, ?ws \ n) \checkmark$
using *justified* **by**(*simp add: justification-well-formed-def*)
from J **have** $E: ?E \ n \in ? \mathcal{E}$
and $E': ?E \ n' \in ? \mathcal{E}$ **by** *auto*
from $a \ a\text{-obs}$ $wf\text{-}start \ wfP \ E \ wf\text{-}n$ **show** $?case$
proof(*rule* *hb-read-value-typeable*[*rotated* -2])
fix $a' \ ad' \ al' \ v'$
assume $a': \text{enat } a' < \text{llength } (?E \ n)$
and $a'\text{-obs}: \text{action-obs } (?E \ n) a' = \text{NormalAction } (\text{ReadMem } ad' \ al' \ v')$
and $nhb: \neg P, ?E \ n \vdash ?ws \ n \ a' \leq hb \ a'$
from a' **have** $a' \in \text{actions } (?E \ n)$ **by**(*simp add: actions-def*)
hence $read\text{-}a': a' \in \text{read-actions } (?E \ n)$ **using** $a'\text{-obs} \ ..$
with *justified* nhb **have** $committed': ? \varphi \ n \ a' \in ? \varphi \ n' \ ' \ ?C \ n'$
unfolding *is-weakly-justified-by.simps* $n\text{-def}$ *uncommitted-reads-see-hb-def* **by** *blast*

from *justified* **have** $wfa\text{-}n: wf\text{-action-translation } E \ (J \ n)$
and $wfa\text{-}n': wf\text{-action-translation } E \ (J \ n')$ **by**(*simp-all add: wf-action-translations-def*)
hence $inj\text{-}n: inj\text{-on } (? \varphi \ n) \ (\text{actions } (?E \ n))$
and $inj\text{-}n': inj\text{-on } (? \varphi \ n') \ (\text{actions } (?E \ n'))$
by(*blast dest: wf-action-translation-on-inj-onD*)
from *justified* **have** $C\text{-}n: ?C \ n \subseteq \text{actions } (?E \ n)$
and $C\text{-}n': ?C \ n' \subseteq \text{actions } (?E \ n')$
and $wf\text{-}n': P \vdash (?E \ n', ?ws \ n') \checkmark$
by(*simp-all add: committed-subset-actions-def justification-well-formed-def*)

from *justified* **have** $? \varphi n' \text{ ' } ? C n' \subseteq ? \varphi n \text{ ' } ? C n$
unfolding *n-def* **by**(*simp add: is-commit-sequence-def*)
with *n-def committed'* **have** $? \varphi n a' \in ? \varphi n \text{ ' } ? C n$ **by** *auto*
with *inj-n C-n* **have** *committed: a' ∈ ?C n*
using $\langle a' \in \text{actions } (?E n) \rangle$ **by**(*auto dest: inj-onD*)
with *justified read-a'* **have** *ws-committed: ws (?φ n a') ∈ ?φ n ' ?C n*
by(*rule weakly-justified-write-seen-hb-read-committed*)

from *wf-n* **have** *ws-n: is-write-seen P (?E n) (?ws n)* **by**(*rule wf-exec-is-write-seenD*)
from *is-write-seenD[OF this read-a' a'-obs]*
have *ws-write: ?ws n a' ∈ write-actions (?E n)*
and *adal: (ad', al') ∈ action-loc P (?E n) (?ws n a')*
and *written: value-written P (?E n) (?ws n a') (ad', al') = v' by simp-all*

define *a'' where a'' = inv-into (actions (?E n')) (?φ n') (?φ n a')*
from *C-n' n committed'* **have** $? \varphi n a' \in ? \varphi n' \text{ ' } \text{actions } (?E n')$ **by** *auto*
hence *a'': ?φ n' a'' = ?φ n a'*
and *a''-action: a'' ∈ actions (?E n')* **using** *inj-n' committed' n*
by(*simp-all add: a''-def f-inv-into-f inv-into-into*)
hence *committed'': a'' ∈ ?C n' using committed' n inj-n' C-n'* **by**(*fastforce dest: inj-onD*)

from *committed committed'' wfa-n wfa-n' a''* **have** *action-obs (?E n') a'' ≈ action-obs (?E n)*
by(*auto dest!: wf-action-translation-on-actionD intro: sim-action-trans sim-action-sym*)
with *a'-obs committed'' C-n'* **have** *read-a'': a'' ∈ read-actions (?E n')*
by(*auto intro: read-actions.intros*)

then obtain *ad'' al'' v''*
where *a''-obs: action-obs (?E n') a'' = NormalAction (ReadMem ad'' al'' v'')* **by** *cases*

from *committed''* **have** *n' > 0 using justified*
by(*cases n')(simp-all add: is-commit-sequence-def*)
then obtain *n'' where n'': n' = Suc n'' by(cases n') simp-all*

from *justified* **have** *wfa-n'': wf-action-translation E (J n'')* **by**(*simp add: wf-action-translations-def*)
hence *inj-n'': inj-on (?φ n'') (actions (?E n''))* **by**(*blast dest: wf-action-translation-on-inj-onD*)
from *justified* **have** *C-n'': ?C n'' ⊆ actions (?E n'')* **by**(*simp add: committed-subset-actions-def*)

from *justified committed' committed'' n-def read-a' read-a'' n*
have $? \varphi n (?ws n (\text{inv-into } (\text{actions } (?E n)) (? \varphi n) (? \varphi n' a''))) = ws (? \varphi n' a'')$
by(*simp add: write-seen-committed-def*)
hence $? \varphi n (?ws n a') = ws (? \varphi n a')$ **using** *inj-n* $\langle a' \in \text{actions } (?E n) \rangle$ **by**(*simp add: a''*)

from *ws-committed* **obtain** *w where w: ws (?φ n a') = ?φ n w*
and *committed-w: w ∈ ?C n by blast*
from *committed-w C-n* **have** *w ∈ actions (?E n) by blast*
hence *w-def: w = ?ws n a' using* $\langle ? \varphi n (?ws n a') = ws (? \varphi n a') \rangle$ *inj-n ws-write*
unfolding *w* **by**(*auto dest: inj-onD*)
have *committed-ws: ?ws n a' ∈ ?C n using committed-w* **by**(*simp add: w-def*)

with *wfa-n* **have** *sim-ws: action-obs (?E n) (?ws n a') ≈ action-obs E (?φ n (?ws n a'))*
by(*blast dest: wf-action-translation-on-actionD*)

a'

from *wfa-n committed-ws* **have** *sim-ws: action-obs* ($?E\ n$) ($?ws\ n\ a'$) \approx *action-obs* E ($? \varphi\ n$) ($?ws\ n\ a'$)
by (*blast dest: wf-action-translation-on-actionD*)
with *adal* **have** *adal-E: (ad', al') \in action-loc P E* ($? \varphi\ n$) ($?ws\ n\ a'$)
by (*simp add: action-loc-aux-sim-action*)

have $\exists w \in$ *write-actions* ($?E\ n'$). (ad', al') \in *action-loc P* ($?E\ n'$) $w \wedge$ *value-written P* ($?E\ n'$) w (ad', al') = v'
proof (*cases* $? \varphi\ n'\ a'' \in ? \varphi\ n'' \text{ ' } ?C\ n''$)
case *True*
then obtain a''' **where** $a''': ? \varphi\ n''\ a''' = ? \varphi\ n'\ a''$
and *committed''': $a''' \in ?C\ n''$* **by** *auto*
from *committed''': $C-n''$* **have** *a''' -action: $a''' \in$ actions* ($?E\ n''$) **by** *auto*

from *committed'' committed''': wfa-n' wfa-n'' a'''* **have** *action-obs* ($?E\ n''$) $a''' \approx$ *action-obs* ($?E\ n'$) a''
by (*auto dest!: wf-action-translation-on-actionD intro: sim-action-trans sim-action-sym*)
with *read-a'' committed''': $C-n''$* **have** *read-a''': $a''' \in$ read-actions* ($?E\ n''$)
by *cases(auto intro: read-actions.intros)*

hence $? \varphi\ n'$ ($?ws\ n'$ (*inv-into* (*actions* ($?E\ n'$)) ($? \varphi\ n'$) ($? \varphi\ n''\ a''$))) = *ws* ($? \varphi\ n''\ a''$)
using *justified committed'''*
unfolding *is-weakly-justified-by.simps n'' Let-def write-seen-committed-def* **by** *blast*
also have *inv-into* (*actions* ($?E\ n'$)) ($? \varphi\ n'$) ($? \varphi\ n''\ a''$) = a''
using a''' *inj-n' a''-action* **by** (*simp*)
also note a''' **also note** a''
finally have $\varphi-n'$: $? \varphi\ n'$ ($?ws\ n'\ a''$) = *ws* ($? \varphi\ n\ a'$) .
then have *ws* ($? \varphi\ n\ a'$) = $? \varphi\ n'$ ($?ws\ n'\ a''$) ..
with $\langle ? \varphi\ n\ (?ws\ n\ a') = ws\ (? \varphi\ n\ a') \rangle$ [*symmetric*]
have *eq-ws: $? \varphi\ n'$ ($?ws\ n'\ a''$) = $? \varphi\ n$ ($?ws\ n\ a')$* **by** *simp*

from *wf-n'[THEN wf-exec-is-write-seenD, THEN is-write-seenD, OF read-a'' a''-obs]*
have *ws-write': $?ws\ n'\ a'' \in$ write-actions* ($?E\ n'$) **by** *simp*

from *justified read-a'' committed''*
have *ws* ($? \varphi\ n'\ a''$) \in $? \varphi\ n'$ ' $?C\ n'$ **by** (*rule weakly-justified-write-seen-hb-read-committed*)
then obtain w' **where** $w': ws$ ($? \varphi\ n'\ a''$) = $? \varphi\ n'$ w'
and *committed-w': $w' \in ?C\ n'$* **by** *blast*
from *committed-w' C-n'* **have** $w' \in$ *actions* ($?E\ n'$) **by** *blast*
hence w' -*def: $w' = ?ws\ n'\ a''$* **using** $\varphi-n'$ *inj-n' ws-write'*
unfolding $w'\ a''$ [*symmetric*] **by** (*auto dest: inj-onD*)
with *committed-w' have committed-ws'': $?ws\ n'\ a'' \in$ committed* ($J\ n'$) **by** *simp*
with *committed-ws wfa-n wfa-n' eq-ws*
have *action-obs* ($?E\ n'$) ($?ws\ n'\ a''$) \approx *action-obs* ($?E\ n$) ($?ws\ n\ a'$)
by (*auto dest!: wf-action-translation-on-actionD intro: sim-action-trans sim-action-sym*)
hence *adal-eq: action-loc P* ($?E\ n'$) ($?ws\ n'\ a''$) = *action-loc P* ($?E\ n$) ($?ws\ n\ a'$)
by (*simp add: action-loc-aux-sim-action*)
with *adal* **have** *adal': (ad', al') \in action-loc P* ($?E\ n'$) ($?ws\ n'\ a''$) **by** (*simp add: action-loc-aux-sim-action*)

from *committed-ws''* **have** $?ws\ n'\ a'' \in$ *actions* ($?E\ n'$) **using** $C-n'$ **by** *blast*
with *ws-write* (*action-obs* ($?E\ n'$) ($?ws\ n'\ a''$) \approx *action-obs* ($?E\ n$) ($?ws\ n\ a'$))
have *ws-write'': $?ws\ n'\ a'' \in$ write-actions* ($?E\ n'$)
by (*cases*) (*auto intro: write-actions.intros simp add: sim-action-is-write-action-eq*)

from *wfa-n' committed-ws''*
have *sim-ws': action-obs (?E n') (?ws n' a'') ≈ action-obs E (?φ n' (?ws n' a''))*
by(*blast dest: wf-action-translation-on-actionD*)
with *adal' have adal'-E: (ad', al') ∈ action-loc P E (?φ n' (?ws n' a''))*
by(*simp add: action-loc-aux-sim-action*)

from *justified committed-ws ws-write adal-E*
have *value-written P (?E n) (?ws n a') (ad', al') = value-written P E (?φ n (?ws n a')) (ad',*
al')
unfolding *is-weakly-justified-by.simps Let-def value-written-committed-def* **by** *blast*
also note *eq-ws[symmetric]*
also from *justified committed-ws'' ws-write'' adal'-E*
have *value-written P E (?φ n' (?ws n' a'')) (ad', al') = value-written P (?E n') (?ws n' a'')*
(ad', al')
unfolding *is-weakly-justified-by.simps Let-def value-written-committed-def* **by**(*blast dest:*
sym)
finally show *?thesis using written ws-write'' adal' by auto*
next
case *False*
with *justified read-a'' committed''*
have *ws (?φ n' a') ∈ ?φ n'' ' ?C n''*
unfolding *is-weakly-justified-by.simps Let-def n'' committed-reads-see-committed-writes-weak-def*
by *blast*
with *a'' obtain w where w: ?φ n'' w = ws (?φ n a')*
and *committed-w: w ∈ ?C n'' by auto*
from *justified have ?φ n'' ' ?C n'' ⊆ ?φ n' ' ?C n' by(simp add: is-commit-sequence-def n'')*
with *committed-w w[symmetric] have ws (?φ n a') ∈ ?φ n' ' ?C n' by(auto)*
then obtain *w' where w': ws (?φ n a') = ?φ n' w' and committed-w': w' ∈ ?C n' by blast*
from *wfa-n' committed-w' have action-obs (?E n') w' ≈ action-obs E (?φ n' w')*
by(*blast dest: wf-action-translation-on-actionD*)
from *this[folded w', folded ⟨?φ n (?ws n a') = ws (?φ n a')⟩ sim-ws[symmetric]*
have *sim-w': action-obs (?E n') w' ≈ action-obs (?E n) (?ws n a') by(rule sim-action-trans)*
with *ws-write committed-w' C-n' have write-w': w' ∈ write-actions (?E n')*
by(*cases*)(*auto intro!: write-actions.intros simp add: sim-action-is-write-action-eq*)
hence *value-written P (?E n') w' (ad', al') = value-written P E (?φ n' w') (ad', al')*
using *adal-E committed-w' justified*
unfolding *⟨?φ n (?ws n a') = ws (?φ n a')⟩ w' is-weakly-justified-by.simps Let-def*
value-written-committed-def **by** *blast*
also note *w'[symmetric]*
also note *⟨?φ n (?ws n a') = ws (?φ n a')⟩[symmetric]*
also have *value-written P E (?φ n (?ws n a')) (ad', al') = value-written P (?E n) (?ws n a')*
(ad', al')
using *justified committed-ws ws-write adal-E*
unfolding *is-weakly-justified-by.simps Let-def value-written-committed-def* **by**(*blast dest:*
sym)
also have *(ad', al') ∈ action-loc P (?E n') w' using sim-w' adal by(simp add: ac-*
tion-loc-aux-sim-action)
ultimately show *?thesis using written write-w' by auto*
qed
then obtain *w where w: w ∈ write-actions (?E n')*
and *adal: (ad', al') ∈ action-loc P (?E n') w*
and *written: value-written P (?E n') w (ad', al') = v' by blast*
from *w have w-len: enat w < llength (?E n')*
by(*cases*)(*simp add: actions-def*)

```

let ?EE'' = lmap snd (ltake (enat w) (?E n'))
have non-speculative P (vs-type-all P) ?EE''
proof(rule non-speculative-nthI)
  fix i ad al v
  assume i: enat i < llength ?EE''
    and i-nth: lnth ?EE'' i = NormalAction (ReadMem ad al v)
    and ns: non-speculative P (vs-type-all P) (ltake (enat i) ?EE'')

  from i w-len have i < w by(simp add: min-def not-le split: if-split-asm)
  with w-len have enat i < llength (?E n') by(simp add: less-trans[where y=enat w])
  moreover
  from i-nth i < i < w w-len
  have action-obs (?E n') i = NormalAction (ReadMem ad al v)
    by(simp add: action-obs-def ac-simps less-trans[where y=enat w] lnth-ltake)
  moreover from n'' have 0 < n' by simp
  ultimately have  $\exists T. P \vdash ad@al : T \wedge P \vdash v : \leq T$  by(rule Suc.IH)
  hence  $v \in vs\text{-type-all } P (ad, al)$  by(simp add: vs-type-all.simps)
  thus  $v \in w\text{-values } P (vs\text{-type-all } P) (list\text{-of } (ltake (enat i) ?EE'')) (ad, al)$ 
    by(rule w-values-mono[THEN subsetD])
qed
with wfx-start wfP E' w adal
show  $\exists T. P \vdash ad'@al' : T \wedge P \vdash v' : \leq T$ 
  unfolding written[symmetric] by(rule non-speculative-written-value-typeable)
qed
}
note justifying = this

assume a: enat a < llength E
  and read: action-obs E a = NormalAction (ReadMem ad al v)
  and E: E  $\in$  h.E-start f P C M vs status
  and wf: P  $\vdash$  (E, ws)  $\checkmark$ 
from a have action: a  $\in$  actions E by(auto simp add: actions-def action-obs-def)
with justified obtain n a' where a': a =  $\varphi$  n a'
  and committed': a'  $\in$  ?C n by(auto simp add: is-commit-sequence-def)
from justified have C-n: ?C n  $\subseteq$  actions (?E n)
  and C-Sn: ?C (Suc n)  $\subseteq$  actions (?E (Suc n))
  and wf-tr: wf-action-translation E (J n)
  and wf-tr': wf-action-translation E (J (Suc n))
  by(auto simp add: committed-subset-actions-def wf-action-translations-def)
from C-n committed' have action': a'  $\in$  actions (?E n) by blast
from wf-tr committed' a'
have action-tid E a = action-tid (?E n) a' action-obs E a  $\approx$  action-obs (?E n) a'
  by(auto simp add: wf-action-translation-on-def intro: sim-action-sym)
with read obtain v'
  where action-obs (?E n) a' = NormalAction (ReadMem ad al v')
  by(clarsimp simp add: action-obs-def)
with action' have read': a'  $\in$  read-actions (?E n) ..

from justified have  $\varphi$  n  $\wedge$  ?C n  $\subseteq$   $\varphi$  (Suc n)  $\wedge$  ?C (Suc n)
  by(simp add: is-commit-sequence-def)
with committed' a' have a  $\in$  ... by auto
then obtain a'' where a'': a =  $\varphi$  (Suc n) a''

```

and *committed''*: $a'' \in ?C (Suc\ n)$ **by** *auto*
from *committed'' C-Sn* **have** *action''*: $a'' \in actions\ (?E\ (Suc\ n))$ **by** *blast*
with *wf-tr'* **have** $a'' = inv\ into\ (actions\ (?E\ (Suc\ n)))\ (?\varphi\ (Suc\ n))\ a$
by(*simp add: a'' wf-action-translation-on-def*)
with *justified read' committed' a'* **have** $ws\ a = ?\varphi\ (Suc\ n)\ (?ws\ (Suc\ n)\ a'')$
by(*simp add: write-seen-committed-def*)
from *wf-tr' committed'' a''*
have *action-tid E a = action-tid (?E (Suc n)) a''*
and *action-obs E a \approx action-obs (?E (Suc n)) a''*
by(*auto simp add: wf-action-translation-on-def intro: sim-action-sym*)
with *read obtain v''*
where *a-obs''*: $action\ obs\ (?E\ (Suc\ n))\ a'' = NormalAction\ (ReadMem\ ad\ al\ v'')$
by(*clarsimp simp add: action-obs-def*)
with *action'' have read''*: $a'' \in read\ actions\ (?E\ (Suc\ n))$
by(*auto intro: read-actions.intros simp add: action-obs-def*)
have $a \in read\ actions\ E\ action\ obs\ E\ a = NormalAction\ (ReadMem\ ad\ al\ v)$
using *action read* **by**(*auto intro: read-actions.intros simp add: action-obs-def read*)
from *is-write-seenD[OF wf-exec-is-write-seenD[OF wf] this]*
have *v-eq*: $v = value\ written\ P\ E\ (ws\ a)\ (ad,\ al)$
and *adal*: $(ad,\ al) \in action\ loc\ P\ E\ (ws\ a)$ **by** *simp-all*
from *justified* **have** $P \vdash (?E\ (Suc\ n),\ ?ws\ (Suc\ n)) \checkmark$ **by**(*simp add: justification-well-formed-def*)
from *is-write-seenD[OF wf-exec-is-write-seenD[OF this] read'' a-obs'']*
have *write''*: $?ws\ (Suc\ n)\ a'' \in write\ actions\ (?E\ (Suc\ n))$
and *written''*: $value\ written\ P\ (?E\ (Suc\ n))\ (?ws\ (Suc\ n)\ a'')\ (ad,\ al) = v''$
by *simp-all*
from *justified read'' committed''*
have $ws\ (?\varphi\ (Suc\ n)\ a'') \in ?\varphi\ (Suc\ n)\ ' ?C\ (Suc\ n)$
by(*rule weakly-justified-write-seen-hb-read-committed*)
then **obtain** *w* **where** $w: ws\ (?\varphi\ (Suc\ n)\ a'') = ?\varphi\ (Suc\ n)\ w$
and *committed-w*: $w \in ?C\ (Suc\ n)$ **by** *blast*
with *C-Sn* **have** $w \in actions\ (?E\ (Suc\ n))$ **by** *blast*
moreover **have** $ws\ (?\varphi\ (Suc\ n)\ a'') = ?\varphi\ (Suc\ n)\ (?ws\ (Suc\ n)\ a'')$
using *ws-a a''* **by** *simp*
ultimately **have** *w-def*: $w = ?ws\ (Suc\ n)\ a''$
using *wf-action-translation-on-inj-onD[OF wf-tr'] write''*
unfolding *w* **by**(*auto dest: inj-onD*)
with *committed-w* **have** $?ws\ (Suc\ n)\ a'' \in ?C\ (Suc\ n)$ **by** *simp*
hence $value\ written\ P\ E\ (ws\ a)\ (ad,\ al) = value\ written\ P\ (?E\ (Suc\ n))\ (?ws\ (Suc\ n)\ a'')\ (ad,\ al)$
using *adal justified write''* **by**(*simp add: value-written-committed-def ws-a*)
with *v-eq written''* **have** $v = v''$ **by** *simp*
from *read''* **have** $enat\ a'' < llength\ (?E\ (Suc\ n))$ **by**(*cases*)(*simp add: actions-def*)
thus $\exists T. P \vdash ad@al : T \wedge P \vdash v : \leq T$
by(*rule justifying*)(*simp-all add: a-obs'' (v = v'')*)
qed

corollary *weakly-legal-read-value-typeable*:

assumes *wfx-start*: $ts\ ok\ wfx\ (thr\ (h.start\ state\ f\ P\ C\ M\ ws))\ h.start\ heap$
and *wfP*: $wf\ syscls\ P$

```

and legal: weakly-legal-execution  $P$  ( $h.\mathcal{E}$ -start  $f P C M$  vs status) ( $E, ws$ )
and  $a$ : enat  $a < \text{length } E$ 
and read: action-obs  $E a = \text{NormalAction (ReadMem ad al v)}$ 
shows  $\exists T. P \vdash \text{ad@al} : T \wedge P \vdash v : \leq T$ 
proof –
  from legal obtain  $J$ 
    where  $P \vdash (E, ws)$  weakly-justified-by  $J$ 
    and range (justifying-exec  $\circ J$ )  $\subseteq h.\mathcal{E}$ -start  $f P C M$  vs status
    and  $E \in h.\mathcal{E}$ -start  $f P C M$  vs status
    and  $P \vdash (E, ws) \checkmark$  by(rule legal-executionE)
    with wfx-start wfp show ?thesis using a read by(rule read-value-typeable-justified)
qed

corollary legal-read-value-typeable:
  [ ts-ok wfx (thr ( $h.\text{start-state } f P C M$  vs))  $h.\text{start-heap}$ ; wf-syscls  $P$ ;
    legal-execution  $P$  ( $h.\mathcal{E}$ -start  $f P C M$  vs status) ( $E, ws$ );
    enat  $a < \text{length } E$ ; action-obs  $E a = \text{NormalAction (ReadMem ad al v)}$  ]
   $\implies \exists T. P \vdash \text{ad@al} : T \wedge P \vdash v : \leq T$ 
by(erule (1) weakly-legal-read-value-typeable)(rule legal-imp-weakly-legal-execution)

end
end

```

8.13 JMM Instantiation with Jinja – common parts

```

theory JMM-Common
imports
  JMM-Framework
  JMM-Typesafe
  ../Common/BinOp
  ../Common/ExternalCallWF
begin

context heap begin

lemma heap-copy-loc-not-New: assumes heap-copy-loc  $a a' al h ob h'$ 
  shows NewHeapElem  $a'' x \in \text{set } ob \implies \text{False}$ 
using assms
by(auto elim: heap-copy-loc.cases)

lemma heap-copies-not-New:
  assumes heap-copies  $a a' als h obs h'$ 
  and NewHeapElem  $a'' x \in \text{set } obs$ 
  shows False
using assms
by induct(auto dest: heap-copy-loc-not-New)

lemma heap-clone-New-same-addr-same:
  assumes heap-clone  $P h a h' [(obs, a')]$ 
  and  $obs ! i = \text{NewHeapElem } a'' x i < \text{length } obs$ 
  and  $obs ! j = \text{NewHeapElem } a'' x' j < \text{length } obs$ 
  shows  $i = j$ 

```


using *assms*

apply *cases*

apply(*fastforce simp add: nth-Cons' gr0-conv-Suc in-set-conv-nth split: if-split-asm dest: heap-copies-not-New*)
done

lemma *red-external-New-same-addr-same:*

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle;$
 $\{ta\}_o ! i = NewHeapElem a' x; i < length \{ta\}_o;$
 $\{ta\}_o ! j = NewHeapElem a' x'; j < length \{ta\}_o \rrbracket$
 $\implies i = j$

by(*auto elim!: red-external.cases simp add: nth-Cons' split: if-split-asm dest: heap-clone-New-same-addr-same*)

lemma *red-external-aggr-New-same-addr-same:*

$\llbracket (ta, va, h') \in red-external-aggr P t a M vs h;$
 $\{ta\}_o ! i = NewHeapElem a' x; i < length \{ta\}_o;$
 $\{ta\}_o ! j = NewHeapElem a' x'; j < length \{ta\}_o \rrbracket$
 $\implies i = j$

by(*auto simp add: external-WT-defs.simps red-external-aggr-def nth-Cons' split: if-split-asm if-split-asm dest: heap-clone-New-same-addr-same*)

lemma *heap-copy-loc-read-typeable:*

assumes *heap-copy-loc a a' al h obs h'*
and *ReadMem ad al' v ∈ set obs*
and $P, h \vdash a@al : T$
shows $ad = a \wedge al' = al$

using *assms* **by** *cases auto*

lemma *heap-copies-read-typeable:*

assumes *heap-copies a a' als h obs h'*
and *ReadMem ad al' v ∈ set obs*
and *list-all2 (λal T. P, h ⊢ a@al : T) als Ts*
shows $ad = a \wedge al' \in set als$

using *assms*

proof(*induct arbitrary: Ts*)

case *Nil* **thus** *?case* **by** *simp*

next

case (*Cons al h ob h' als obs h''*)
from $\langle list-all2 (\lambda al T. P, h \vdash a@al : T) (al \# als) Ts \rangle$
obtain $T Ts'$ **where** $Ts [simp]: Ts = T \# Ts'$
and $P, h \vdash a@al : T$
and $list-all2 (\lambda al T. P, h \vdash a@al : T) als Ts'$
by(*auto simp add: list-all2-Cons1*)

from $\langle ReadMem ad al' v \in set (ob @ obs) \rangle$

show *?case* **unfolding** *set-append Un-iff*

proof

assume *ReadMem ad al' v ∈ set ob*
with $\langle heap-copy-loc a a' al h ob h' \rangle$
have $ad = a \wedge al' = al$ **using** $\langle P, h \vdash a@al : T \rangle$
by(*rule heap-copy-loc-read-typeable*)
thus *?thesis* **by** *simp*

next

assume *ReadMem ad al' v ∈ set obs*
moreover **from** $\langle heap-copy-loc a a' al h ob h' \rangle$
have $h \sqsubseteq h'$ **by**(*rule next-heap-copy-loc*)

```

from ⟨list-all2 (λal T. P, h ⊢ a@al : T) als Ts'⟩
have list-all2 (λal T. P, h' ⊢ a@al : T) als Ts'
  by(rule List.list-all2-mono)(rule addr-loc-type-hext-mono[OF - ⟨h ≤ h'⟩])
ultimately have ad = a ∧ al' ∈ set als by(rule Cons)
thus ?thesis by simp
qed
qed

lemma heap-clone-read-typeable:
  assumes clone: heap-clone P h a h' [(obs, a')]
  and read: ReadMem ad al v ∈ set obs
  shows ad = a ∧ (∃ T'. P, h ⊢ ad@al : T')
using clone
proof cases
  case (ObjClone C H' FDTs obs')
  let ?als = map (λ((F, D), Tm). CField D F) FDTs
  let ?Ts = map (λ(FD, T). fst (the (map-of FDTs FD))) FDTs
  note ⟨heap-copies a a' ?als H' obs' h'⟩
  moreover
  from ⟨obs = NewHeapElem a' (Class-type C) # obs'⟩ read
  have ReadMem ad al v ∈ set obs' by simp
  moreover
  from ⟨(H', a') ∈ allocate h (Class-type C)⟩ have h ≤ H' by(rule hext-allocate)
  hence typeof-addr H' a = [Class-type C] using ⟨typeof-addr h a = [Class-type C]⟩
    by(rule typeof-addr-hext-mono)
  hence type: list-all2 (λal T. P, H' ⊢ a@al : T) ?als ?Ts
    using ⟨P ⊢ C has-fields FDTs⟩
    unfolding list-all2-map1 list-all2-map2 list-all2-same
    by(fastforce intro: addr-loc-type.intros simp add: has-field-def dest: weak-map-of-SomeI)
  ultimately have ad = a ∧ al ∈ set ?als by(rule heap-copies-read-typeable)
  hence [simp]: ad = a and al ∈ set ?als by simp-all
  then obtain F D T where [simp]: al = CField D F and ((F, D), T) ∈ set FDTs by auto
  with type ⟨h ≤ H'⟩ ⟨typeof-addr h a = [Class-type C]⟩ show ?thesis
    unfolding list-all2-map1 list-all2-map2 list-all2-same
    by(fastforce elim!: ballE[where x=((F, D), T)] addr-loc-type.cases dest: typeof-addr-hext-mono
intro: addr-loc-type.intros)
next
  case (ArrClone T n H' FDTs obs')
  let ?als = map (λ((F, D), Tfm). CField D F) FDTs @ map ACell [0..<n]
  let ?Ts = map (λ(FD, T). fst (the (map-of FDTs FD))) FDTs @ replicate n T
  note FDTs = ⟨P ⊢ Object has-fields FDTs⟩
  note ⟨heap-copies a a' ?als H' obs' h'⟩
  moreover from ⟨obs = NewHeapElem a' (Array-type T n) # obs'⟩ read
  have ReadMem ad al v ∈ set obs' by simp
  moreover from ⟨(H', a') ∈ allocate h (Array-type T n)⟩
  have h ≤ H' by(rule hext-allocate)
  with ⟨typeof-addr h a = [Array-type T n]⟩
  have type': typeof-addr H' a = [Array-type T n]
    by(auto dest: typeof-addr-hext-mono hext-arrD)
  hence type: list-all2 (λal T. P, H' ⊢ a@al : T) ?als ?Ts using FDTs
    by(fastforce intro: list-all2-all-nthI addr-loc-type.intros simp add: has-field-def list-all2-append
list-all2-map1 list-all2-map2 list-all2-same dest: weak-map-of-SomeI)
  ultimately have ad = a ∧ al ∈ set ?als by(rule heap-copies-read-typeable)
  hence [simp]: ad = a and al ∈ set ?als by simp-all

```

hence $al \in \text{set } (\text{map } (\lambda((F, D), Tfm). \text{CField } D \ F) \ \text{FDTs}) \vee al \in \text{set } (\text{map } \text{ACell } [0..<n])$ **by** *simp*
thus *?thesis*
proof
assume $al \in \text{set } (\text{map } (\lambda((F, D), Tfm). \text{CField } D \ F) \ \text{FDTs})$
then obtain $F \ D \ Tfm$ **where** $[simp]: al = \text{CField } D \ F$ **and** $((F, D), Tfm) \in \text{set } \text{FDTs}$ **by** *auto*
with $\text{type } \text{type}' \langle h \leq H \rangle \langle \text{typeof-addr } h \ a = \lfloor \text{Array-type } T \ n \rfloor \rangle$ **show** *?thesis*
by(*fastforce elim!*: $\text{ballE}[\text{where } x = ((F, D), Tfm)] \ \text{addr-loc-type.cases}$ *intro*: $\text{addr-loc-type.intros}$
simp add: $\text{list-all2-append list-all2-map1 list-all2-map2 list-all2-same}$)
next
assume $al \in \text{set } (\text{map } \text{ACell } [0..<n])$
then obtain n' **where** $[simp]: al = \text{ACell } n'$ **and** $n' < n$ **by** *auto*
with $\text{type } \text{type}' \langle h \leq H \rangle \langle \text{typeof-addr } h \ a = \lfloor \text{Array-type } T \ n \rfloor \rangle$
show *?thesis* **by**(*fastforce dest*: $\text{list-all2-nthD}[\text{where } p = n'] \ \text{elim}$: $\text{addr-loc-type.cases}$ *intro*: $\text{addr-loc-type.intros}$)
qed
qed

lemma *red-external-read-mem-typeable*:
assumes $\text{red}: P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow \text{ext} \langle va, h' \rangle$
and $\text{read}: \text{ReadMem } ad \ al \ v \in \text{set } \{\{ta\}\}_o$
shows $\exists T'. P, h \vdash ad@al : T'$
using *red read*
by $\text{cases}(\text{fastforce dest}: \text{heap-clone-read-typeable} \ \text{intro}: \text{addr-loc-type.intros})+$

end

context *heap-conf* **begin**

lemma *heap-clone-typeof-addrD*:
assumes $\text{heap-clone } P \ h \ a \ h' \ [(\text{obs}, a')]$
and $hconf \ h$
shows $\text{NewHeapElem } a'' \ x \in \text{set } \text{obs} \implies a'' = a' \wedge \text{typeof-addr } h' \ a' = \text{Some } x$
using *assms*
by(*fastforce elim!*: heap-clone.cases *dest*: allocate-SomeD hext-heap-copies $\text{heap-copies-not-New}$ $\text{typeof-addr-is-type}$
elim: hext-objD hext-arrD)

lemma *red-external-New-typeof-addrD*:
 $\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow \text{ext} \langle va, h' \rangle; \text{NewHeapElem } a' \ x \in \text{set } \{\{ta\}\}_o; hconf \ h \rrbracket$
 $\implies \text{typeof-addr } h' \ a' = \text{Some } x$
by(*erule red-external.cases*)(*auto dest*: $\text{heap-clone-typeof-addrD}$)

lemma *red-external-aggr-New-typeof-addrD*:
 $\llbracket (ta, va, h') \in \text{red-external-aggr } P \ t \ a \ M \ vs \ h; \text{NewHeapElem } a' \ x \in \text{set } \{\{ta\}\}_o;$
 $\text{is-native } P \ (\text{the } (\text{typeof-addr } h \ a)) \ M; hconf \ h \rrbracket$
 $\implies \text{typeof-addr } h' \ a' = \text{Some } x$
apply(*auto simp add*: is-native.simps $\text{external-WT-defs.simps}$ $\text{red-external-aggr-def}$ $\text{split: if-split-asm}$)
apply(*blast dest*: $\text{heap-clone-typeof-addrD}$)
done

end

context *heap-conf* **begin**

lemma *heap-copy-loc-non-speculative-typeable*:
assumes $\text{copy}: \text{heap-copy-loc } ad \ ad' \ al \ h \ \text{obs } h'$

and *sc*: *non-speculative P vs (llist-of (map NormalAction obs))*
and *vs*: *vs-conf P h vs*
and *hconf*: *hconf h*
and *wt*: $P, h \vdash ad@al : T$, $P, h \vdash ad'@al : T$
shows *heap-base.heap-copy-loc (heap-read-typed P) heap-write ad ad' al h obs h'*
proof –
from *copy obtain v where obs: obs = [ReadMem ad al v, WriteMem ad' al v]*
and *read: heap-read h ad al v and write: heap-write h ad' al v h' by cases*
from *obs sc have v ∈ vs (ad, al) by auto*
with *vs wt have v: P, h ⊢ v :≤ T by (blast dest: vs-confD addr-loc-type-fun)+*
with *read wt have heap-read-typed P h ad al v*
by (*auto intro: heap-read-typedI dest: addr-loc-type-fun*)
thus *?thesis using write unfolding obs by (rule heap-base.heap-copy-loc.intros)*
qed

lemma *heap-copy-loc-non-speculative-vs-conf*:
assumes *copy: heap-copy-loc ad ad' al h obs h'*
and *sc: non-speculative P vs (llist-of (take n (map NormalAction obs)))*
and *vs: vs-conf P h vs*
and *hconf: hconf h*
and *wt: P, h ⊢ ad@al : T*, $P, h \vdash ad'@al : T$
shows *vs-conf P h' (w-values P vs (take n (map NormalAction obs)))*
proof –
from *copy obtain v where obs: obs = [ReadMem ad al v, WriteMem ad' al v]*
and *read: heap-read h ad al v and write: heap-write h ad' al v h' by cases*
from *write have hext: h ≼ h' by (rule hext-heap-write)*
with *vs have vs': vs-conf P h' vs by (rule vs-conf-hext)*
show *?thesis*
proof (*cases n > 0*)
case *True*
with *obs sc have v ∈ vs (ad, al) by (auto simp add: take-Cons')*
with *vs wt have v: P, h ⊢ v :≤ T by (blast dest: vs-confD addr-loc-type-fun)+*
with *hext wt have P, h' ⊢ ad'@al : T*, $P, h' \vdash v :≤ T$
by (*blast intro: addr-loc-type-hext-mono conf-hext*)
thus *?thesis using vs' obs*
by (*auto simp add: take-Cons' intro!: vs-confI split: if-split-asm dest: vs-confD*)
next
case *False thus ?thesis using vs' by simp*
qed
qed

lemma *heap-copies-non-speculative-typeable*:
assumes *heap-copies ad ad' als h obs h'*
and *non-speculative P vs (llist-of (map NormalAction obs))*
and *vs-conf P h vs*
and *hconf h*
and *list-all2 (λal T. P, h ⊢ ad@al : T) als Ts list-all2 (λal T. P, h ⊢ ad'@al : T) als Ts*
shows *heap-base.heap-copies (heap-read-typed P) heap-write ad ad' als h obs h'*
using *assms*
proof (*induct arbitrary: Ts vs*)
case *Nil show ?case by (auto intro: heap-base.heap-copies.intros)*
next
case (*Cons al h ob h' als obs h''*)
note *sc = ⟨non-speculative P vs (llist-of (map NormalAction (ob @ obs)))⟩*

and $vs = \langle vs\text{-conf } P \ h \ vs \rangle$
and $hconf = \langle hconf \ h \rangle$
and $wt = \langle list\text{-all2 } (\lambda al \ T. \ P, h \vdash ad@al : T) \ (al \# \ als) \ Ts \rangle \langle list\text{-all2 } (\lambda al \ T. \ P, h \vdash ad'@al : T) \ (al \# \ als) \ Ts \rangle$

have $sc1$: *non-speculative* $P \ vs \ (l\text{list-of } (map \ NormalAction \ ob))$
and $sc2$: *non-speculative* $P \ (w\text{-values } P \ vs \ (map \ NormalAction \ ob)) \ (l\text{list-of } (map \ NormalAction \ obs))$

using sc **by** (*simp-all add: non-speculative-lappend lappend-llist-of-llist-of[symmetric] del: lappend-llist-of-llist-of*)

from wt **obtain** $T \ Ts'$ **where** $Ts: Ts = T \# \ Ts'$
and $wt1: P, h \vdash ad@al : T \ P, h \vdash ad'@al : T$
and $wt2: list\text{-all2 } (\lambda al \ T. \ P, h \vdash ad@al : T) \ als \ Ts' \ list\text{-all2 } (\lambda al \ T. \ P, h \vdash ad'@al : T) \ als \ Ts'$
by (*auto simp add: list-all2-Cons1*)
from $\langle heap\text{-copy-loc } ad \ ad' \ al \ h \ ob \ h' \rangle \ sc1 \ vs \ hconf \ wt1$
have $copy$: *heap-base.heap-copy-loc* (*heap-read-typed* P) *heap-write* $ad \ ad' \ al \ h \ ob \ h'$
by (*rule heap-copy-loc-non-speculative-typeable*)
from *heap-copy-loc-non-speculative-vs-conf*[*OF* $\langle heap\text{-copy-loc } ad \ ad' \ al \ h \ ob \ h' \rangle - vs \ hconf \ wt1$, *of length ob*] $sc1$
have vs' : *vs-conf* $P \ h' \ (w\text{-values } P \ vs \ (map \ NormalAction \ ob))$ **by** *simp*

from $\langle heap\text{-copy-loc } ad \ ad' \ al \ h \ ob \ h' \rangle$
have $h \trianglelefteq h'$ **by** (*rule hext-heap-copy-loc*)
with $wt2$ **have** $wt2'$: $list\text{-all2 } (\lambda al \ T. \ P, h' \vdash ad@al : T) \ als \ Ts' \ list\text{-all2 } (\lambda al \ T. \ P, h' \vdash ad'@al : T) \ als \ Ts'$
by $-(erule \ List.list\text{-all2-mono}[OF \ - \ addr\text{-loc-type-hext-mono}, \ assumption+])$

from $copy \ hconf \ wt1$ **have** $hconf'$: $hconf \ h'$
by (*rule heap-conf-read.hconf-heap-copy-loc-mono*[*OF* *heap-conf-read-heap-read-typed*])

from $sc2 \ vs' \ hconf' \ wt2'$ **have** *heap-base.heap-copies* (*heap-read-typed* P) *heap-write* $ad \ ad' \ als \ h' \ obs \ h''$ **by** (*rule Cons*)

with $copy$ **show** $?case$ **by** (*rule heap-base.heap-copies.Cons*)
qed

lemma *heap-copies-non-speculative-vs-conf*:

assumes *heap-copies* $ad \ ad' \ als \ h \ obs \ h'$
and *non-speculative* $P \ vs \ (l\text{list-of } (take \ n \ (map \ NormalAction \ obs)))$
and *vs-conf* $P \ h \ vs$
and $hconf \ h$
and $list\text{-all2 } (\lambda al \ T. \ P, h \vdash ad@al : T) \ als \ Ts \ list\text{-all2 } (\lambda al \ T. \ P, h \vdash ad'@al : T) \ als \ Ts$
shows *vs-conf* $P \ h' \ (w\text{-values } P \ vs \ (take \ n \ (map \ NormalAction \ obs)))$

using *assms*

proof (*induction arbitrary: Ts vs n*)

case *Nil* **thus** $?case$ **by** *simp*

next

case (*Cons* $al \ h \ ob \ h' \ als \ obs \ h''$)
note $sc = \langle non\text{-speculative } P \ vs \ (l\text{list-of } (take \ n \ (map \ NormalAction \ (ob \ @ \ obs)))) \rangle$
and $hcl = \langle heap\text{-copy-loc } ad \ ad' \ al \ h \ ob \ h' \rangle$
and $vs = \langle vs\text{-conf } P \ h \ vs \rangle$
and $hconf = \langle hconf \ h \rangle$
and $wt = \langle list\text{-all2 } (\lambda al \ T. \ P, h \vdash ad@al : T) \ (al \# \ als) \ Ts \rangle \langle list\text{-all2 } (\lambda al \ T. \ P, h \vdash ad'@al : T) \ (al \# \ als) \ Ts \rangle$
let $?vs' = w\text{-values } P \ vs \ (take \ n \ (map \ NormalAction \ ob))$

```

from sc have sc1: non-speculative P vs (llist-of (take n (map NormalAction ob)))
  and sc2: non-speculative P ?vs' (llist-of (take (n - length ob) (map NormalAction obs)))
by(simp-all add: lappend-llist-of-llist-of[symmetric] non-speculative-lappend del: lappend-llist-of-llist-of)

from wt obtain T Ts' where Ts: Ts = T # Ts'
  and wt1: P, h ⊢ ad@al : T, h ⊢ ad'@al : T
  and wt2: list-all2 (λal T. P, h ⊢ ad@al : T) als Ts' list-all2 (λal T. P, h ⊢ ad'@al : T) als Ts'
  by(auto simp add: list-all2-Cons1)

from hcl sc1 vs hconf wt1 have vs': vs-conf P h' ?vs' by(rule heap-copy-loc-non-speculative-vs-conf)

show ?case
proof(cases n < length ob)
  case True
    from ⟨heap-copies ad ad' als h' obs h'⟩ have h' ⊑ h'' by(rule hext-heap-copies)
    with vs' have vs-conf P h'' ?vs' by(rule vs-conf-hext)
    thus ?thesis using True by simp
  next
    case False
    note sc2 vs'
    moreover from False sc1 have sc1': non-speculative P vs (llist-of (map NormalAction ob)) by
simp
    with hcl have heap-base.heap-copy-loc (heap-read-typed P) heap-write ad ad' al h ob h'
      using vs hconf wt1 by(rule heap-copy-loc-non-speculative-typeable)
    hence hconf h' using hconf wt1
      by(rule heap-conf-read.hconf-heap-copy-loc-mono[OF heap-conf-read-heap-read-typed])
    moreover
    from hcl have h ⊑ h' by(rule hext-heap-copy-loc)
    with wt2 have wt2': list-all2 (λal T. P, h' ⊢ ad@al : T) als Ts' list-all2 (λal T. P, h' ⊢ ad'@al :
T) als Ts'
      by -(erule List.list-all2-mono[OF - addr-loc-type-hext-mono], assumption+)
    ultimately have vs-conf P h'' (w-values P ?vs' (take (n - length ob) (map NormalAction obs)))
      by(rule Cons.IH)
    with False show ?thesis by simp
  qed
qed

lemma heap-clone-non-speculative-typeable-Some:
  assumes clone: heap-clone P h ad h' [(obs, ad')]
  and sc: non-speculative P vs (llist-of (map NormalAction obs))
  and vs: vs-conf P h vs
  and hconf: hconf h
  shows heap-base.heap-clone allocate typeof-addr (heap-read-typed P) heap-write P h ad h' [(obs, ad')]
using clone
proof(cases)
  case (ObjClone C h'' FDTs obs')
    note FDTs = ⟨P ⊢ C has-fields FDTs⟩
    and obs = ⟨obs = NewHeapElem ad' (Class-type C) # obs'⟩
    let ?als = map (λ((F, D), Tfm). CField D F) FDTs
    let ?Ts = map (λ(FD, T). fst (the (map-of FDTs FD))) FDTs
    let ?vs = w-value P vs (NormalAction (NewHeapElem ad' (Class-type C)) :: ('addr, 'thread-id
obs-event))
    from ⟨(h'', ad') ∈ allocate h (Class-type C)⟩ have hext: h ⊑ h'' by(rule hext-heap-ops)

```

hence $\text{type: typeof-addr } h'' \text{ ad} = \lfloor \text{Class-type } C \rfloor$ **using** $\langle \text{typeof-addr } h \text{ ad} = \lfloor \text{Class-type } C \rfloor \rangle$
by(rule typeof-addr-hext-mono)

note $\langle \text{heap-copies ad ad' ?als } h'' \text{ obs' } h' \rangle$
moreover from sc **have** $\text{non-speculative } P \text{ ?vs (l1ist-of (map NormalAction obs'))}$
by(simp add: obs)

moreover from $\langle P \vdash C \text{ has-fields FDTs} \rangle$
have $\text{is-class } P \ C$ **by**(rule has-fields-is-class)

hence $\text{is-htype } P \ (\text{Class-type } C)$ **by** simp

with $vs \langle (h'', \text{ad}') \in \text{allocate } h \ (\text{Class-type } C) \rangle$
have $\text{vs-conf } P \ h'' \ \text{?vs}$ **by**(rule vs-conf-allocate)

moreover from $\langle (h'', \text{ad}') \in \text{allocate } h \ (\text{Class-type } C) \rangle$ $\text{hconf} \langle \text{is-htype } P \ (\text{Class-type } C) \rangle$
have $\text{hconf } h''$ **by**(rule hconf-allocate-mono)

moreover from type FDTs **have** $\text{list-all2 } (\lambda al \ T. \ P, h'' \vdash \text{ad}@al : T) \ \text{?als} \ \text{?Ts}$
unfolding $\text{list-all2-map1 list-all2-map2 list-all2-same}$
by(fastforce intro: addr-loc-type.intros simp add: has-field-def dest: weak-map-of-SomeI)

moreover from $\langle (h'', \text{ad}') \in \text{allocate } h \ (\text{Class-type } C) \rangle$ $\langle \text{is-htype } P \ (\text{Class-type } C) \rangle$
have $\text{typeof-addr } h'' \ \text{ad}' = \lfloor \text{Class-type } C \rfloor$ **by**(auto dest: allocate-SomeD)

with FDTs **have** $\text{list-all2 } (\lambda al \ T. \ P, h'' \vdash \text{ad}'@al : T) \ \text{?als} \ \text{?Ts}$
unfolding $\text{list-all2-map1 list-all2-map2 list-all2-same}$
by(fastforce intro: addr-loc-type.intros simp add: has-field-def dest: weak-map-of-SomeI)

ultimately
have $\text{copy: heap-base.heap-copies (heap-read-typed } P) \ \text{heap-write ad ad' (map } (\lambda((F, D), \text{Tfm}).$
 $\text{CField } D \ F) \ \text{FDTs)} \ h'' \ \text{obs' } h'$
by(rule heap-copies-non-speculative-typeable)+

from $\langle \text{typeof-addr } h \ \text{ad} = \lfloor \text{Class-type } C \rfloor \rangle$ $\langle (h'', \text{ad}') \in \text{allocate } h \ (\text{Class-type } C) \rangle$ FDTs copy
show $\text{?thesis unfolding obs}$ **by**(rule heap-base.heap-clone.intros)

next
case $(\text{ArrClone } T \ n \ h'' \ \text{FDTs} \ \text{obs'})$
note $\text{obs} = \langle \text{obs} = \text{NewHeapElem ad' (Array-type } T \ n) \ \# \ \text{obs'} \rangle$
and $\text{new} = \langle (h'', \text{ad}') \in \text{allocate } h \ (\text{Array-type } T \ n) \rangle$
and $\text{FDTs} = \langle P \vdash \text{Object has-fields FDTs} \rangle$
let $\text{?als} = \text{map } (\lambda((F, D), \text{Tfm}). \ \text{CField } D \ F) \ \text{FDTs} \ @ \ \text{map } \text{ACell } [0..<n]$
let $\text{?Ts} = \text{map } (\lambda(FD, T). \ \text{fst (the (map-of FDTs FD))}) \ \text{FDTs} \ @ \ \text{replicate } n \ T$
let $\text{?vs} = \text{w-value } P \ \text{vs} \ (\text{NormalAction (NewHeapElem ad' (Array-type } T \ n) :: ('addr, 'thread-id)$
 $\text{obs-event}))$
from new **have** $\text{hext: } h \sqsubseteq h''$ **by**(rule hext-heap-ops)

hence $\text{type: typeof-addr } h'' \ \text{ad} = \lfloor \text{Array-type } T \ n \rfloor$ **using** $\langle \text{typeof-addr } h \ \text{ad} = \lfloor \text{Array-type } T \ n \rfloor \rangle$
by(rule typeof-addr-hext-mono)

note $\langle \text{heap-copies ad ad' ?als } h'' \ \text{obs' } h' \rangle$
moreover from sc **have** $\text{non-speculative } P \ \text{?vs (l1ist-of (map NormalAction obs'))}$ **by**(simp add: obs)

moreover from $\langle \text{typeof-addr } h \ \text{ad} = \lfloor \text{Array-type } T \ n \rfloor \rangle$ $\langle \text{hconf } h \rangle$ **have** $\text{is-htype } P \ (\text{Array-type } T \ n)$
by(auto dest: typeof-addr-is-type)

with vs new **have** $\text{vs-conf } P \ h'' \ \text{?vs}$ **by**(rule vs-conf-allocate)

moreover from $\text{new hconf} \langle \text{is-htype } P \ (\text{Array-type } T \ n) \rangle$ **have** $\text{hconf } h''$ **by**(rule hconf-allocate-mono)

moreover
from type FDTs **have** $\text{list-all2 } (\lambda al \ T. \ P, h'' \vdash \text{ad}@al : T) \ \text{?als} \ \text{?Ts}$
by(fastforce intro: list-all2-all-nthI addr-loc-type.intros simp add: has-field-def list-all2-append list-all2-map1 list-all2-map2 list-all2-same dest: weak-map-of-SomeI)

moreover from $\text{new} \langle \text{is-htype } P \ (\text{Array-type } T \ n) \rangle$
have $\text{typeof-addr } h'' \ \text{ad}' = \lfloor \text{Array-type } T \ n \rfloor$
by(auto dest: allocate-SomeD)

hence *list-all2* $(\lambda al T. P, h'' \vdash ad'@al : T) ?als ?Ts$ **using** *FDTs*
by(*fastforce intro: list-all2-all-nthI addr-loc-type.intros simp add: has-field-def list-all2-append list-all2-map1 list-all2-map2 list-all2-same dest: weak-map-of-SomeI*)
ultimately have *copy: heap-base.heap-copies (heap-read-typed P) heap-write ad ad' (map ($\lambda((F, D), Tfm). CField D F$) FDTs @ map ACell [0.. n]) h'' obs' h'*
by(*rule heap-copies-non-speculative-typeable*)
from $\langle typeof-addr h ad = [Array-type T n] \rangle$ **new FDTs copy show** *?thesis*
unfolding *obs by(rule heap-base.heap-clone.ArrClone)*
qed

lemma *heap-clone-non-speculative-vs-conf-Some:*

assumes *clone: heap-clone P h ad h' [(obs, ad[^])]*
and *sc: non-speculative P vs (llist-of (take n (map NormalAction obs)))*
and *vs: vs-conf P h vs*
and *hconf: hconf h*
shows *vs-conf P h' (w-values P vs (take n (map NormalAction obs)))*
using *clone*
proof(*cases*)
case (*ObjClone C h'' FDTs obs[^]*)
note *FDTs = $\langle P \vdash C \text{ has-fields FDTs} \rangle$*
and *obs = $\langle obs = NewHeapElem ad' (Class-type C) \# obs' \rangle$*
let *?als = map ($\lambda((F, D), Tfm). CField D F$) FDTs*
let *?Ts = map ($\lambda(FD, T). fst (the (map-of FDTs FD))$) FDTs*
let *?vs = w-value P vs (NormalAction (NewHeapElem ad' (Class-type C) :: ('addr, 'thread-id) obs-event))*
from $\langle (h'', ad') \in allocate h (Class-type C) \rangle$ **have** *hext: $h \trianglelefteq h''$* **by**(*rule hext-heap-ops*)
hence *type: typeof-addr h'' ad = [Class-type C]* **using** $\langle typeof-addr h ad = [Class-type C] \rangle$
by(*rule typeof-addr-hext-mono*)

note $\langle heap-copies ad ad' ?als h'' obs' h' \rangle$
moreover from *sc have non-speculative P ?vs (llist-of (take (n - 1) (map NormalAction obs[^])))*
by(*simp add: obs take-Cons' split: if-split-asm*)
moreover from $\langle P \vdash C \text{ has-fields FDTs} \rangle$
have *is-class P C* **by**(*rule has-fields-is-class*)
hence *is-htype P (Class-type C)* **by** *simp*
with $\langle (h'', ad') \in allocate h (Class-type C) \rangle$
have *vs-conf P h'' ?vs* **by**(*rule vs-conf-allocate*)
moreover from $\langle (h'', ad') \in allocate h (Class-type C) \rangle$ *hconf $\langle is-htype P (Class-type C) \rangle$*
have *hconf h''* **by**(*rule hconf-allocate-mono*)
moreover from *type FDTs have list-all2 ($\lambda al T. P, h'' \vdash ad'@al : T$) ?als ?Ts*
unfolding *list-all2-map1 list-all2-map2 list-all2-same*
by(*fastforce intro: addr-loc-type.intros simp add: has-field-def dest: weak-map-of-SomeI*)
moreover from $\langle (h'', ad') \in allocate h (Class-type C) \rangle$ *$\langle is-htype P (Class-type C) \rangle$*
have *typeof-addr h'' ad' = [Class-type C]* **by**(*auto dest: allocate-SomeD*)
with *FDTs have list-all2 ($\lambda al T. P, h'' \vdash ad'@al : T$) ?als ?Ts*
unfolding *list-all2-map1 list-all2-map2 list-all2-same*
by(*fastforce intro: addr-loc-type.intros simp add: has-field-def dest: weak-map-of-SomeI*)
ultimately
have *vs': vs-conf P h' (w-values P ?vs (take (n - 1) (map NormalAction obs[^])))*
by(*rule heap-copies-non-speculative-vs-conf*)
show *?thesis*
proof(*cases n > 0*)
case *True*
with *obs vs' show ?thesis by(simp add: take-Cons')*


```

next
  case False
  from ⟨heap-copies ad ad' ?als h'' obs' h'⟩ have h'' ≤ h' by(rule heaxt-heap-copies)
  with ⟨h ≤ h''⟩ have h ≤ h' by(rule heaxt-trans)
  with vs have vs-conf P h' vs by(rule vs-conf-heaxt)
  thus ?thesis using False by simp
qed
next
case (ArrClone T N h'' FDTs obs')
note obs = ⟨obs = NewHeapElem ad' (Array-type T N) # obs'⟩
and new = ⟨(h'', ad') ∈ allocate h (Array-type T N)⟩
and FDTs = ⟨P ⊢ Object has-fields FDTs⟩
let ?als = map (λ((F, D), Tfm). CField D F) FDTs @ map ACell [0..<N]
let ?Ts = map (λ(FD, T). fst (the (map-of FDTs FD))) FDTs @ replicate N T
let ?vs = w-value P vs (NormalAction (NewHeapElem ad' (Array-type T N) :: ('addr, 'thread-id)
obs-event))
from new have heaxt: h ≤ h'' by(rule heaxt-heap-ops)
hence type: typeof-addr h'' ad = [Array-type T N] using ⟨typeof-addr h ad = [Array-type T N]⟩
by(rule typeof-addr-heaxt-mono)

note ⟨heap-copies ad ad' ?als h'' obs' h'⟩
moreover from sc have non-speculative P ?vs (l1ist-of (take (n - 1) (map NormalAction obs')))
by(simp add: obs take-Cons' split: if-split-asm)
moreover from ⟨typeof-addr h ad = [Array-type T N]⟩ ⟨hconf h⟩ have is-htype P (Array-type T
N)
by(auto dest: typeof-addr-is-type)
with vs new have vs-conf P h'' ?vs by(rule vs-conf-allocate)
moreover from new hconf ⟨is-htype P (Array-type T N)⟩ have hconf h'' by(rule hconf-allocate-mono)
moreover
from type FDTs have list-all2 (λal T. P, h'' ⊢ ad@al : T) ?als ?Ts
by(fastforce intro: list-all2-all-nthI addr-loc-type.intros simp add: has-field-def list-all2-append
list-all2-map1 list-all2-map2 list-all2-same dest: weak-map-of-SomeI)
moreover from new ⟨is-htype P (Array-type T N)⟩
have typeof-addr h'' ad' = [Array-type T N]
by(auto dest: allocate-SomeD)
hence list-all2 (λal T. P, h'' ⊢ ad'@al : T) ?als ?Ts using FDTs
by(fastforce intro: list-all2-all-nthI addr-loc-type.intros simp add: has-field-def list-all2-append
list-all2-map1 list-all2-map2 list-all2-same dest: weak-map-of-SomeI)
ultimately have vs': vs-conf P h' (w-values P ?vs (take (n - 1) (map NormalAction obs')))
by(rule heap-copies-non-speculative-vs-conf)
show ?thesis
proof(cases n > 0)
case True
with obs vs' show ?thesis by(simp add: take-Cons')
next
case False
from ⟨heap-copies ad ad' ?als h'' obs' h'⟩ have h'' ≤ h' by(rule heaxt-heap-copies)
with ⟨h ≤ h''⟩ have h ≤ h' by(rule heaxt-trans)
with vs have vs-conf P h' vs by(rule vs-conf-heaxt)
thus ?thesis using False by simp
qed
qed

```

lemma heap-clone-non-speculative-typeable-None:

assumes *heap-clone* $P\ h\ ad\ h'\ None$
shows *heap-base.heap-clone allocate typeof-addr (heap-read-typed P) heap-write P h ad h' None*
using *assms*
by(*cases*)(*blast intro: heap-base.heap-clone.intros*) $+$

lemma *red-external-non-speculative-typeable:*

assumes *red*: $P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle$
and *sc*: *non-speculative P Vs (llist-of (map NormalAction {ta}_o))*
and *vs*: *vs-conf P h Vs*
and *hconf*: *hconf h*
shows *heap-base.red-external addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr (heap-read-typed P) heap-write P t h a M vs ta va h'*
using *assms*
by(*cases*)(*auto intro: heap-base.red-external.intros heap-clone-non-speculative-typeable-None heap-clone-non-speculative-dest: hext-heap-clone elim: vs-conf-hext*)

lemma *red-external-non-speculative-vs-conf:*

assumes *red*: $P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle$
and *sc*: *non-speculative P Vs (llist-of (take n (map NormalAction {ta}_o)))*
and *vs*: *vs-conf P h Vs*
and *hconf*: *hconf h*
shows *vs-conf P h' (w-values P Vs (take n (map NormalAction {ta}_o)))*
using *assms*
by(*cases*)(*auto intro: heap-base.red-external.intros heap-clone-non-speculative-vs-conf-Some dest: hext-heap-clone elim: vs-conf-hext simp add: take-Cons'*)

lemma *red-external-aggr-non-speculative-typeable:*

assumes *red*: $(ta, va, h') \in red-external-aggr\ P\ t\ a\ M\ vs\ h$
and *sc*: *non-speculative P Vs (llist-of (map NormalAction {ta}_o))*
and *vs*: *vs-conf P h Vs*
and *hconf*: *hconf h*
and *native*: *is-native P (the (typeof-addr h a)) M*
shows $(ta, va, h') \in heap-base.red-external-aggr\ addr2thread-id\ thread-id2addr\ spurious-wakeups\ empty-heap\ allocate\ typeof-addr\ (heap-read-typed\ P)\ heap-write\ P\ t\ a\ M\ vs\ h$
using *assms*
by(*cases the (typeof-addr h a)*)(*auto 4 3 simp add: is-native.simps external-WT-defs.simps red-external-aggr-def heap-base.red-external-aggr-def split: if-split-asm split del: if-split del: disjCI intro: heap-clone-non-speculative-typeable-heap-clone-non-speculative-typeable-Some dest: sees-method-decl-above*)

lemma *red-external-aggr-non-speculative-vs-conf:*

assumes *red*: $(ta, va, h') \in red-external-aggr\ P\ t\ a\ M\ vs\ h$
and *sc*: *non-speculative P Vs (llist-of (take n (map NormalAction {ta}_o)))*
and *vs*: *vs-conf P h Vs*
and *hconf*: *hconf h*
and *native*: *is-native P (the (typeof-addr h a)) M*
shows *vs-conf P h' (w-values P Vs (take n (map NormalAction {ta}_o)))*
using *assms*
by(*cases the (typeof-addr h a)*)(*auto 4 3 simp add: is-native.simps external-WT-defs.simps red-external-aggr-def heap-base.red-external-aggr-def take-Cons' split: if-split-asm split del: if-split del: disjCI intro: heap-clone-non-speculative-dest: hext-heap-clone elim: vs-conf-hext dest: sees-method-decl-above*)

end

declare *split-paired-Ex* [*simp del*]

```

declare eq-upto-seq-inconsist-simps [simp]

context heap-progress begin

lemma heap-copy-loc-non-speculative-read:
  assumes hrt: heap-read-typeable hconf P
  and vs: vs-conf P h vs
  and type: P,h ⊢ a@al : T P,h ⊢ a'@al : T
  and hconf: hconf h
  and copy: heap-copy-loc a a' al h obs h'
  and i: i < length obs
  and read: obs ! i = ReadMem a'' al'' v
  and v: v' ∈ w-values P vs (map NormalAction (take i obs)) (a'', al'')
  shows ∃ obs' h''. heap-copy-loc a a' al h obs' h'' ∧ i < length obs' ∧ take i obs' = take i obs ∧
    obs' ! i = ReadMem a'' al'' v' ∧ length obs' ≤ length obs ∧
    non-speculative P vs (llist-of (map NormalAction obs'))

using copy
proof cases
  case (1 v'')
  with read i have [simp]: i = 0 v'' = v a'' = a al'' = al
  by (simp-all add: nth-Cons split: nat.split-asm)
  from v have v' ∈ vs (a, al) by simp
  with vs type have conf: P,h ⊢ v' ≤ T by (auto dest: addr-loc-type-fun vs-confD)
  let ?obs'' = [ReadMem a al v', WriteMem a' al v']
  from hrt type(1) conf hconf have heap-read h a al v' by (rule heap-read-typeableD)
  moreover from heap-write-total[OF hconf type(2) conf]
  obtain h'' where heap-write h a' al v' h'' ..
  ultimately have heap-copy-loc a a' al h ?obs'' h'' ..
  thus ?thesis using 1 ⟨v' ∈ vs (a, al)⟩ by (auto)
qed

lemma heap-copies-non-speculative-read:
  assumes hrt: heap-read-typeable hconf P
  and copies: heap-copies a a' als h obs h'
  and vs: vs-conf P h vs
  and type1: list-all2 (λal T. P,h ⊢ a@al : T) als Ts
  and type2: list-all2 (λal T. P,h ⊢ a'@al : T) als Ts
  and hconf: hconf h
  and i: i < length obs
  and read: obs ! i = ReadMem a'' al'' v
  and v: v' ∈ w-values P vs (map NormalAction (take i obs)) (a'', al'')
  and ns: non-speculative P vs (llist-of (map NormalAction (take i obs)))
  shows ∃ obs' h''. heap-copies a a' als h obs' h'' ∧ i < length obs' ∧ take i obs' = take i obs ∧
    obs' ! i = ReadMem a'' al'' v' ∧ length obs' ≤ length obs
  (is ?concl als h obs vs i)
using copies vs type1 type2 hconf i read v ns
proof (induction arbitrary: Ts vs i)
  case Nil thus ?case by simp
next
  case (Cons al h ob h' als obs h'' Ts vs)
  note copy = ⟨heap-copy-loc a a' al h ob h'⟩
  note vs = ⟨vs-conf P h vs⟩
  note type1 = ⟨list-all2 (λal T. P,h ⊢ a@al : T) (al # als) Ts⟩
  and type2 = ⟨list-all2 (λal T. P,h ⊢ a'@al : T) (al # als) Ts⟩

```

```

note hconf = ⟨hconf h⟩
note i = ⟨i < length (ob @ obs)⟩
note read = ⟨(ob @ obs) ! i = ReadMem a'' al'' v⟩
note v = ⟨v' ∈ w-values P vs (map NormalAction (take i (ob @ obs))) (a'', al'')⟩
note ns = ⟨non-speculative P vs (llist-of (map NormalAction (take i (ob @ obs))))⟩

from type1 obtain T Ts' where Ts: Ts = T # Ts'
  and type1': P, h ⊢ a@al : T
  and type1'': list-all2 (λal T. P, h ⊢ a@al : T) als Ts'
  by(auto simp add: list-all2-Cons1)
from type2 Ts have type2': P, h ⊢ a'@al : T
  and type2'': list-all2 (λal T. P, h ⊢ a'@al : T) als Ts'
  by simp-all
show ?case
proof(cases i < length ob)
  case True
  with read v
  have ob ! i = ReadMem a'' al'' v
    and v' ∈ w-values P vs (map NormalAction (take i ob)) (a'', al'') by(simp-all add: nth-append)
  from heap-copy-loc-non-speculative-read[OF hrt vs type1' type2' hconf copy True this]
  obtain ob' H'' where copy': heap-copy-loc a a' al h ob' H''
    and i': i < length ob' and take i ob' = take i ob
    and ob' ! i = ReadMem a'' al'' v'
    and length ob' ≤ length ob
    and ns: non-speculative P vs (llist-of (map NormalAction ob')) by blast
  moreover {
    from copy' have hext: h ⊑ H'' by(rule hext-heap-copy-loc)
    have hconf H''
    by(rule heap-conf-read.hconf-heap-copy-loc-mono[OF heap-conf-read-heap-read-typed])(rule heap-copy-loc-non-
copy' ns vs hconf type1' type2'], fact+)
    moreover
    from type1'' have list-all2 (λal T. P, H'' ⊢ a@al : T) als Ts'
      by(rule List.list-all2-mono)(rule addr-loc-type-hext-mono[OF - hext])
    moreover from type2'' have list-all2 (λal T. P, H'' ⊢ a'@al : T) als Ts'
      by(rule List.list-all2-mono)(rule addr-loc-type-hext-mono[OF - hext])
    moreover note calculation }
  from heap-copies-progress[OF this]
  obtain obs' h''' where *: heap-copies a a' als H'' obs' h''' by blast
  moreover note heap-copies-length[OF *]
  moreover note heap-copy-loc-length[OF copy']
  moreover note heap-copies-length[OF ⟨heap-copies a a' als h' obs h''⟩]
  ultimately show ?thesis using True by(auto intro!: heap-copies.Cons exI simp add: nth-append)
next
  case False
  let ?vs' = w-values P vs (map NormalAction ob)
  let ?i' = i - length ob

  from ns False obtain ns': non-speculative P vs (llist-of (map NormalAction ob))
    and ns'': non-speculative P ?vs' (llist-of (map NormalAction (take ?i' obs)))
  by(simp add: lappend-llist-of-llist-of[symmetric] non-speculative-lappend del: lappend-llist-of-llist-of)

  from heap-copy-loc-non-speculative-vs-conf[OF copy - vs hconf type1' type2', where n=length ob]
  ns'
  have vs-conf P h' ?vs' by simp

```

moreover
from *copy* **have** *hext*: $h \sqsubseteq h'$ **by**(*rule hext-heap-copy-loc*)
from *type1''* **have** *list-all2* ($\lambda al T. P, h' \vdash a @ al : T$) *als* *Ts'*
by(*rule List.list-all2-mono*)(*rule addr-loc-type-hext-mono*[*OF - hext*])
moreover from *type2''* **have** *list-all2* ($\lambda al T. P, h' \vdash a' @ al : T$) *als* *Ts'*
by(*rule List.list-all2-mono*)(*rule addr-loc-type-hext-mono*[*OF - hext*])
moreover have *hconf* *h'*
by(*rule heap-conf-read.hconf-heap-copy-loc-mono*[*OF heap-conf-read-heap-read-typed*])(*rule heap-copy-loc-non-speculative-t-copy ns' vs hconf type1' type2'*), *fact+*)
moreover from *i* **False** **have** $?i' < \text{length } \text{obs}$ **by** *simp*
moreover from *read* **False** **have** $\text{obs} ! ?i' = \text{ReadMem } a'' al'' v$ **by**(*simp add: nth-append*)
moreover from *v* **False** **have** $v' \in w\text{-values } P ?vs'$ (*map NormalAction (take ?i' obs)*) (*a''*, *al''*)
by(*simp*)
ultimately have $?concl \text{ als } h' \text{ obs } ?vs' ?i'$ **using** *ns''* **by**(*rule Cons.IH*)
thus $?thesis$ **using** *False copy* **by** *safe(auto intro!: heap-copies.Cons exI simp add: nth-append)*
qed
qed

lemma *heap-clone-non-speculative-read*:

assumes *hrt*: *heap-read-typeable hconf P*
and *clone*: *heap-clone P h a h' [(obs, a')]*
and *vs*: *vs-conf P h vs*
and *hconf*: *hconf h*
and *i*: $i < \text{length } \text{obs}$
and *read*: $\text{obs} ! i = \text{ReadMem } a'' al'' v$
and *v*: $v' \in w\text{-values } P \text{ vs } (\text{map } \text{NormalAction } (\text{take } i \text{ obs})) (a'', al'')$
and *ns*: *non-speculative P vs (llist-of (map NormalAction (take i obs)))*
shows $\exists \text{obs}' h''. \text{heap-clone } P h a h'' [(obs', a')] \wedge i < \text{length } \text{obs}' \wedge \text{take } i \text{ obs}' = \text{take } i \text{ obs} \wedge$
 $\text{obs}' ! i = \text{ReadMem } a'' al'' v' \wedge \text{length } \text{obs}' \leq \text{length } \text{obs}$

using *clone*

proof *cases*

case (*ObjClone C h'' FDTs obs'*)

note $\text{obs} = \langle \text{obs} = \text{NewHeapElem } a' (\text{Class-type } C) \# \text{obs}' \rangle$
note $\text{FDTs} = \langle P \vdash C \text{ has-fields } \text{FDTs} \rangle$
let $?als = \text{map } (\lambda(F, D), Tm). \text{CField } D F) \text{FDTs}$
let $?Ts = \text{map } (\lambda(FD, T). \text{fst } (\text{the } (\text{map-of } \text{FDTs } FD))) \text{FDTs}$
let $?vs = w\text{-value } P \text{ vs } (\text{NormalAction } (\text{NewHeapElem } a' (\text{Class-type } C))) :: ('addr, 'thread-id)$
obs-event action
let $?i = i - 1$
from *i* **read** *obs* **have** $i-0: i > 0$ **by**(*simp add: nth-Cons' split: if-split-asm*)

from $\langle P \vdash C \text{ has-fields } \text{FDTs} \rangle$ **have** *is-class P C* **by**(*rule has-fields-is-class*)

with $\langle (h'', a') \in \text{allocate } h (\text{Class-type } C) \rangle$

have *type-a'*: *typeof-addr h'' a' = [Class-type C]* **and** *hext*: $h \sqsubseteq h''$

by(*auto dest: allocate-SomeD hext-allocate*)

note $\langle \text{heap-copies } a a' ?als h'' \text{obs}' h' \rangle$

moreover from $\langle \text{typeof-addr } h a = [\text{Class-type } C] \rangle$ *hconf* **have** *is-htype P (Class-type C)*

by(*rule typeof-addr-is-type*)

with $\langle (h'', a') \in \text{allocate } h (\text{Class-type } C) \rangle$

have *vs-conf P h'' ?vs* **by**(*rule vs-conf-allocate*)

moreover

from *hext* $\langle \text{typeof-addr } h a = [\text{Class-type } C] \rangle$

```

have typeof-addr h'' a = [Class-type C] by(rule typeof-addr-heat-mono)
hence list-all2 (λal T. P, h'' ⊢ a@al : T) ?als ?Ts using FDTs
  unfolding list-all2-map1 list-all2-map2 list-all2-same
  by(fastforce intro: addr-loc-type.intros simp add: has-field-def dest: weak-map-of-SomeI)
moreover from FDTs type-a'
have list-all2 (λal T. P, h'' ⊢ a'@al : T) ?als ?Ts
  unfolding list-all2-map1 list-all2-map2 list-all2-same
  by(fastforce intro: addr-loc-type.intros simp add: has-field-def dest: weak-map-of-SomeI)
moreover from ⟨(h'', a') ∈ allocate h (Class-type C)⟩ hconf ⟨is-htype P (Class-type C)⟩
have hconf h'' by(rule hconf-allocate-mono)
moreover from i read i-0 obs have ?i < length obs' obs' ! ?i = ReadMem a'' al'' v by simp-all
moreover from v i-0 obs
have v' ∈ w-values P ?vs (map NormalAction (take ?i obs')) (a'', al'') by(simp add: take-Cons')
moreover from ns i-0 obs
have non-speculative P ?vs (llist-of (map NormalAction (take ?i obs'))) by(simp add: take-Cons')
ultimately have ∃ obs'' h'''. heap-copies a a' ?als h'' obs'' h''' ∧
  ?i < length obs'' ∧ take ?i obs'' = take ?i obs' ∧ obs'' ! ?i = ReadMem a'' al''
v' ∧
  length obs'' ≤ length obs'
  by(rule heap-copies-non-speculative-read[OF hrt])
thus ?thesis using ⟨typeof-addr h a = [Class-type C]⟩ ⟨(h'', a') ∈ allocate h (Class-type C)⟩ FDTs
obs i-0
by(auto 4 4 intro: heap-clone.ObjClone simp add: take-Cons')
next
case (ArrClone T n h'' FDTs obs')

note obs = ⟨obs = NewHeapElem a' (Array-type T n) # obs'⟩
note FDTs = ⟨P ⊢ Object has-fields FDTs⟩
let ?als = map (λ((F, D), Tfm). CField D F) FDTs @ map ACell [0..<n]
let ?Ts = map (λ(FD, T). fst (the (map-of FDTs FD))) FDTs @ replicate n T
let ?vs = w-value P vs (NormalAction (NewHeapElem a' (Array-type T n)) :: ('addr, 'thread-id)
obs-event action)
let ?i = i - 1
from i read obs have i-0: i > 0 by(simp add: nth-Cons' split: if-split-asm)

from ⟨typeof-addr h a = [Array-type T n]⟩ hconf
have is-htype P (Array-type T n) by(rule typeof-addr-is-type)
with ⟨(h'', a') ∈ allocate h (Array-type T n)⟩
have type-a': typeof-addr h'' a' = [Array-type T n]
  and hext: h ≼ h''
  by(auto dest: allocate-SomeD hext-allocate)

note ⟨heap-copies a a' ?als h'' obs' h'⟩
moreover from vs ⟨(h'', a') ∈ allocate h (Array-type T n)⟩ ⟨is-htype P (Array-type T n)⟩
have vs-conf P h'' ?vs by(rule vs-conf-allocate)
moreover from hext ⟨typeof-addr h a = [Array-type T n]⟩
have type'a: typeof-addr h'' a = [Array-type T n]
  by(auto intro: hext-arrD)
from type'a FDTs have list-all2 (λal T. P, h'' ⊢ a@al : T) ?als ?Ts
  by(fastforce intro: list-all2-all-nthI addr-loc-type.intros simp add: has-field-def list-all2-append
list-all2-map1 list-all2-map2 list-all2-same dest: weak-map-of-SomeI)
moreover from type-a' FDTs
have list-all2 (λal T. P, h'' ⊢ a'@al : T) ?als ?Ts
  by(fastforce intro: list-all2-all-nthI addr-loc-type.intros simp add: has-field-def list-all2-append

```

list-all2-map1 list-all2-map2 list-all2-same dest: weak-map-of-SomeI
moreover from $\langle (h'', a') \in \text{allocate } h \text{ (Array-type } T \ n) \rangle \text{ hconf } \langle \text{is-htype } P \text{ (Array-type } T \ n) \rangle$
have $\text{hconf } h''$ **by**(rule *hconf-allocate-mono*)
moreover from $i \text{ read } i\text{-}0 \text{ obs}$ **have** $?i < \text{length } \text{obs}' \text{ obs}' ! ?i = \text{ReadMem } a'' \text{ al}'' \ v$ **by** *simp-all*
moreover from $v \ i\text{-}0 \text{ obs}$
have $v' \in w\text{-values } P \ ?vs \ (\text{map } \text{NormalAction } (\text{take } ?i \ \text{obs}')) \ (a'', \ \text{al}'')$ **by**(*simp add: take-Cons'*)
moreover from $ns \ i\text{-}0 \ \text{obs}$
have *non-speculative* $P \ ?vs \ (\text{llist-of } (\text{map } \text{NormalAction } (\text{take } ?i \ \text{obs}')))$ **by**(*simp add: take-Cons'*)
ultimately have $\exists \text{obs}'' \ h'''. \ \text{heap-copies } a' \ ?als \ h'' \ \text{obs}'' \ h''' \wedge$
 $?i < \text{length } \text{obs}'' \wedge \text{take } ?i \ \text{obs}'' = \text{take } ?i \ \text{obs}' \wedge \text{obs}'' ! ?i = \text{ReadMem } a'' \ \text{al}''$
 $v' \wedge$
 $\text{length } \text{obs}'' \leq \text{length } \text{obs}'$
by(rule *heap-copies-non-speculative-read[OF hrt]*)
thus *?thesis* **using** $\langle \text{typeof-addr } h \ a = \lfloor \text{Array-type } T \ n \rfloor \rangle \langle (h'', a') \in \text{allocate } h \text{ (Array-type } T \ n) \rangle$
FDTs obs i-0
by(*auto 4 4 intro: heap-clone.ArrClone simp add: take-Cons'*)
qed

lemma *red-external-non-speculative-read:*

assumes *hrt: heap-read-typeable hconf P*
and *vs: vs-conf P (shr s) vs*
and *red: P, t $\vdash \langle a \cdot M(vs'), \text{shr } s \rangle -ta \rightarrow \text{ext } \langle va, h' \rangle$*
and *aok: final-thread.actions-ok final s t ta*
and *hconf: hconf (shr s)*
and *i: i < length $\{ta\}_o$*
and *read: $\{ta\}_o ! i = \text{ReadMem } a'' \ \text{al}'' \ v$*
and *v: v' $\in w\text{-values } P \ vs \ (\text{map } \text{NormalAction } (\text{take } i \ \{ta\}_o)) \ (a'', \ \text{al}'')$*
and *ns: non-speculative P vs (llist-of (map NormalAction (take i $\{ta\}_o$)))*
shows $\exists ta'' \ va'' \ h''. \ P, t \vdash \langle a \cdot M(vs'), \text{shr } s \rangle -ta'' \rightarrow \text{ext } \langle va'', h' \rangle \wedge \text{final-thread.actions-ok final s t}$
 $ta'' \wedge$

$$i < \text{length } \{ta''\}_o \wedge \text{take } i \ \{ta''\}_o = \text{take } i \ \{ta\}_o \wedge$$

$$\{ta''\}_o ! i = \text{ReadMem } a'' \ \text{al}'' \ v' \wedge \text{length } \{ta''\}_o \leq \text{length } \{ta\}_o$$

using *red i read*

proof *cases*

case [*simp*]: (*RedClone obs a'*)
from *heap-clone-non-speculative-read[OF hrt $\langle \text{heap-clone } P \text{ (shr } s) \ a \ h' \lfloor (obs, a') \rfloor \rangle \ vs \ \text{hconf, of } i$*
 $a'' \ \text{al}'' \ v \ v'] \ i \ \text{read } v \ ns$
show *?thesis* **using** *aok*
by(*fastforce intro: red-external.RedClone simp add: final-thread.actions-ok-iff*)
qed(*auto simp add: nth-Cons*)

lemma *red-external-aggr-non-speculative-read:*

assumes *hrt: heap-read-typeable hconf P*
and *vs: vs-conf P (shr s) vs*
and *red: (ta, va, h') $\in \text{red-external-aggr } P \ t \ a \ M \ vs' \ (\text{shr } s)$*
and *native: is-native P (the (typeof-addr (shr s) a)) M*
and *aok: final-thread.actions-ok final s t ta*
and *hconf: hconf (shr s)*
and *i: i < length $\{ta\}_o$*
and *read: $\{ta\}_o ! i = \text{ReadMem } a'' \ \text{al}'' \ v$*
and *v: v' $\in w\text{-values } P \ vs \ (\text{map } \text{NormalAction } (\text{take } i \ \{ta\}_o)) \ (a'', \ \text{al}'')$*
and *ns: non-speculative P vs (llist-of (map NormalAction (take i $\{ta\}_o$)))*
shows $\exists ta'' \ va'' \ h''. \ (ta'', va'', h'') \in \text{red-external-aggr } P \ t \ a \ M \ vs' \ (\text{shr } s) \wedge \text{final-thread.actions-ok}$
 $\text{final } s \ t \ ta'' \wedge$

$$i < \text{length } \{ta''\}_o \wedge \text{take } i \{ta''\}_o = \text{take } i \{ta\}_o \wedge \\ \{ta''\}_o ! i = \text{ReadMem } a'' \text{ al'' } v' \wedge \text{length } \{ta''\}_o \leq \text{length } \{ta\}_o$$

using *red native aok hconf i read v ns*
apply(*simp add: red-external-aggr-def final-thread.actions-ok-iff ex-disj-distrib conj-disj-distribR split nth-Cons' del: if-split split: if-split-asm disj-split-asm*)
apply(*drule heap-clone-non-speculative-read[OF hrt - vs hconf, of - - - i a'' al'' v v']*)
apply *simp-all*
apply(*fastforce*)
done

end

declare *split-paired-Ex [simp]*
declare *eq-upto-seq-inconsist-simps [simp del]*

context *allocated-heap begin*

lemma *heap-copy-loc-allocated-same:*
assumes *heap-copy-loc a a' al h obs h'*
shows *allocated h' = allocated h*
using *assms*
by *cases(auto del: subsetI simp: heap-write-allocated-same)*

lemma *heap-copy-loc-allocated-mono:*
heap-copy-loc a a' al h obs h' \implies allocated h \subseteq allocated h'
by(*simp add: heap-copy-loc-allocated-same*)

lemma *heap-copies-allocated-same:*
assumes *heap-copies a a' al h obs h'*
shows *allocated h' = allocated h*
using *assms*
by(*induct*)(*auto simp add: heap-copy-loc-allocated-same*)

lemma *heap-copies-allocated-mono:*
heap-copies a a' al h obs h' \implies allocated h \subseteq allocated h'
by(*simp add: heap-copies-allocated-same*)

lemma *heap-clone-allocated-mono:*
assumes *heap-clone P h a h' aobs*
shows *allocated h \subseteq allocated h'*
using *assms*
by *cases(blast del: subsetI intro: heap-copies-allocated-mono allocate-allocated-mono intro: subset-trans)+*

lemma *red-external-allocated-mono:*
assumes *P, t \vdash (a.M(vs), h) -ta \rightarrow ext (va, h')*
shows *allocated h \subseteq allocated h'*
using *assms*
by(*cases*)(*blast del: subsetI intro: heap-clone-allocated-mono heap-write-allocated-same*)+

lemma *red-external-aggr-allocated-mono:*
 $\llbracket (ta, va, h') \in \text{red-external-aggr } P \text{ t a M vs h; is-native } P \text{ (the (typeof-addr h a)) M} \rrbracket \\ \implies \text{allocated } h \subseteq \text{allocated } h'$
by(*cases the (typeof-addr h a)*)(*auto simp add: is-native.simps external-WT-defs.simps red-external-aggr-def*)

split: if-split-asm dest: heap-clone-allocated-mono sees-method-decl-above)

lemma *heap-clone-allocatedD*:

assumes *heap-clone* P h a h' $[(obs, a')]$
and *NewHeapElem* a'' $x \in set$ obs
shows $a'' \in allocated$ $h' \wedge a'' \notin allocated$ h

using *assms*

by *cases(auto dest: allocate-allocatedD heap-copies-allocated-mono heap-copies-not-New)*

lemma *red-external-allocatedD*:

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle - ta \rightarrow ext \langle va, h' \rangle; NewHeapElem a' x \in set \{ta\}_o \rrbracket$
 $\implies a' \in allocated$ $h' \wedge a' \notin allocated$ h

by(*erule red-external.cases*)(*auto dest: heap-clone-allocatedD*)

lemma *red-external-aggr-allocatedD*:

$\llbracket (ta, va, h') \in red-external-aggr P t a M vs h; NewHeapElem a' x \in set \{ta\}_o;$
 $is-native P (the (typeof-addr h a)) M \rrbracket$
 $\implies a' \in allocated$ $h' \wedge a' \notin allocated$ h

by(*auto simp add: is-native.simps external-WT-defs.simps red-external-aggr-def split: if-split-asm dest: heap-clone-allocatedD sees-method-decl-above*)

lemma *heap-clone-NewHeapElemD*:

assumes *heap-clone* P h a h' $[(obs, a')]$
and $ad \in allocated$ h'
and $ad \notin allocated$ h
shows $\exists CTn. NewHeapElem ad CTn \in set$ obs

using *assms*

by *cases(auto dest!: allocate-allocatedD heap-copies-allocated-same)*

lemma *heap-clone-fail-allocated-same*:

assumes *heap-clone* P h a h' *None*
shows $allocated$ $h' = allocated$ h

using *assms*

by(*cases*)(*auto*)

lemma *red-external-NewHeapElemD*:

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle - ta \rightarrow ext \langle va, h' \rangle; a' \in allocated$ $h'; a' \notin allocated$ $h \rrbracket$
 $\implies \exists CTn. NewHeapElem a' CTn \in set \{ta\}_o$

by(*erule red-external.cases*)(*auto dest: heap-clone-NewHeapElemD heap-clone-fail-allocated-same*)

lemma *red-external-aggr-NewHeapElemD*:

$\llbracket (ta, va, h') \in red-external-aggr P t a M vs h; a' \in allocated$ $h'; a' \notin allocated$ $h;$
 $is-native P (the (typeof-addr h a)) M \rrbracket$
 $\implies \exists CTn. NewHeapElem a' CTn \in set \{ta\}_o$

by(*cases the (typeof-addr h a)*)(*auto simp add: is-native.simps external-WT-defs.simps red-external-aggr-def split: if-split-asm dest: heap-clone-fail-allocated-same heap-clone-NewHeapElemD sees-method-decl-above*)

end

context *heap-base* **begin**

lemma *binop-known-addr*:

assumes *ok: start-heap-ok*

shows $binop$ bop $v1$ $v2 = \llbracket Inl v \rrbracket \implies ka-Val v \subseteq ka-Val v1 \cup ka-Val v2 \cup set$ *start-addr*

and $\text{binop } bop \ v1 \ v2 = [Inr \ a] \implies a \in \text{ka-Val } v1 \cup \text{ka-Val } v2 \cup \text{set start-addr}$
apply(cases bop, auto split: if-split-asm)[1]
apply(cases bop, auto split: if-split-asm simp add: addr-of-sys-xcpt-start-addr[OF ok])
done

lemma *heap-copy-loc-known-addr-ReadMem*:

assumes $\text{heap-copy-loc } a \ a' \ al \ h \ ob \ h'$
and $\text{ReadMem } ad \ al' \ v \in \text{set } ob$
shows $ad = a$

using *assms* **by** cases simp

lemma *heap-copies-known-addr-ReadMem*:

assumes $\text{heap-copies } a \ a' \ als \ h \ obs \ h'$
and $\text{ReadMem } ad \ al \ v \in \text{set } obs$
shows $ad = a$

using *assms*

by(induct)(auto dest: heap-copy-loc-known-addr-ReadMem)

lemma *heap-clone-known-addr-ReadMem*:

assumes $\text{heap-clone } P \ h \ a \ h' \ [(obs, a')]$
and $\text{ReadMem } ad \ al \ v \in \text{set } obs$
shows $ad = a$

using *assms*

by cases(auto dest: heap-copies-known-addr-ReadMem)

lemma *red-external-known-addr-ReadMem*:

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow \text{ext } \langle va, h' \rangle; \text{ReadMem } ad \ al \ v \in \text{set } \{ta\}_o \rrbracket$
 $\implies ad \in \{\text{thread-id2addr } t, a\} \cup (\bigcup (\text{ka-Val } \text{' set } vs)) \cup \text{set start-addr}$

by(erule red-external.cases)(simp-all add: heap-clone-known-addr-ReadMem)

lemma *red-external-aggr-known-addr-ReadMem*:

$\llbracket (ta, va, h') \in \text{red-external-aggr } P \ t \ a \ M \ vs \ h; \text{ReadMem } ad \ al \ v \in \text{set } \{ta\}_o \rrbracket$
 $\implies ad \in \{\text{thread-id2addr } t, a\} \cup (\bigcup (\text{ka-Val } \text{' set } vs)) \cup \text{set start-addr}$

apply(auto simp add: red-external-aggr-def split: if-split-asm dest: heap-clone-known-addr-ReadMem)
done

lemma *heap-copy-loc-known-addr-WriteMem*:

assumes $\text{heap-copy-loc } a \ a' \ al \ h \ ob \ h'$
and $ob ! n = \text{WriteMem } ad \ al' \ (\text{Addr } a'') \ n < \text{length } ob$
shows $a'' \in \text{new-obs-addr} \ (\text{take } n \ ob)$

using *assms*

by cases(auto simp add: nth-Cons new-obs-addr-def split: nat.split-asm)

lemma *heap-copies-known-addr-WriteMem*:

assumes $\text{heap-copies } a \ a' \ als \ h \ obs \ h'$
and $obs ! n = \text{WriteMem } ad \ al \ (\text{Addr } a'') \ n < \text{length } obs$
shows $a'' \in \text{new-obs-addr} \ (\text{take } n \ obs)$

using *assms*

by(induct arbitrary: n)(auto simp add: nth-append new-obs-addr-def dest: heap-copy-loc-known-addr-WriteMem split: if-split-asm)

lemma *heap-clone-known-addr-WriteMem*:

assumes $\text{heap-clone } P \ h \ a \ h' \ [(obs, a')]$
and $obs ! n = \text{WriteMem } ad \ al \ (\text{Addr } a'') \ n < \text{length } obs$

shows $a'' \in \text{new-obs-addr} (\text{take } n \text{ obs})$
using *assms*
by *cases(auto simp add: nth-Cons new-obs-addr-def split: nat.split-asm dest: heap-copies-known-addr-WriteMem)*

lemma *red-external-known-addr-WriteMem:*

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle; \{\!|ta|\!\}_o ! n = \text{WriteMem } ad \text{ al } (\text{Addr } a'); n < \text{length } \{\!|ta|\!\}_o \rrbracket$
 $\implies a' \in \{\text{thread-id2addr } t, a\} \cup (\bigcup (ka\text{-Val } ' \text{ set } vs)) \cup \text{set start-addr} \cup \text{new-obs-addr} (\text{take } n \{\!|ta|\!\}_o)$

by(*erule red-external.cases*)(*auto dest: heap-clone-known-addr-WriteMem*)

lemma *red-external-aggr-known-addr-WriteMem:*

$\llbracket (ta, va, h') \in \text{red-external-aggr } P \text{ t a } M \text{ vs } h;$
 $\{\!|ta|\!\}_o ! n = \text{WriteMem } ad \text{ al } (\text{Addr } a'); n < \text{length } \{\!|ta|\!\}_o \rrbracket$
 $\implies a' \in \{\text{thread-id2addr } t, a\} \cup (\bigcup (ka\text{-Val } ' \text{ set } vs)) \cup \text{set start-addr} \cup \text{new-obs-addr} (\text{take } n \{\!|ta|\!\}_o)$

apply(*auto simp add: red-external-aggr-def split: if-split-asm dest: heap-clone-known-addr-WriteMem*)
done

lemma *red-external-known-addr-mono:*

assumes *ok: start-heap-ok*
and *red: P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle*
shows $(\text{case } va \text{ of } \text{RetVal } v \Rightarrow ka\text{-Val } v \mid \text{RetExc } a \Rightarrow \{a\} \mid \text{RetStaySame} \Rightarrow \{\}) \subseteq \{\text{thread-id2addr } t, a\} \cup (\bigcup (ka\text{-Val } ' \text{ set } vs)) \cup \text{set start-addr} \cup \text{new-obs-addr } \{\!|ta|\!\}_o$

using *red*

by *cases(auto simp add: addr-of-sys-xcpt-start-addr[OF ok] new-obs-addr-def heap-clone.simps)*

lemma *red-external-aggr-known-addr-mono:*

assumes *ok: start-heap-ok*
and *red: (ta, va, h') \in red-external-aggr P t a M vs h is-native P (the (typeof-addr h a)) M*
shows $(\text{case } va \text{ of } \text{RetVal } v \Rightarrow ka\text{-Val } v \mid \text{RetExc } a \Rightarrow \{a\} \mid \text{RetStaySame} \Rightarrow \{\}) \subseteq \{\text{thread-id2addr } t, a\} \cup (\bigcup (ka\text{-Val } ' \text{ set } vs)) \cup \text{set start-addr} \cup \text{new-obs-addr } \{\!|ta|\!\}_o$

using *red*

apply(*cases the (typeof-addr h a)*)

apply(*auto simp add: red-external-aggr-def addr-of-sys-xcpt-start-addr[OF ok] new-obs-addr-def heap-clone.simps split: if-split-asm*)

apply(*auto simp add: is-native.simps elim!: external-WT-defs.cases dest: sees-method-decl-above*)

done

lemma *red-external-NewThread-idD:*

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle; \text{NewThread } t' (C, M', a') h'' \in \text{set } \{\!|ta|\!\}_t \rrbracket$
 $\implies t' = \text{addr2thread-id } a \wedge a' = a$

by(*erule red-external.cases*) *simp-all*

lemma *red-external-aggr-NewThread-idD:*

$\llbracket (ta, va, h') \in \text{red-external-aggr } P \text{ t a } M \text{ vs } h;$
 $\text{NewThread } t' (C, M', a') h'' \in \text{set } \{\!|ta|\!\}_t \rrbracket$
 $\implies t' = \text{addr2thread-id } a \wedge a' = a$

apply(*auto simp add: red-external-aggr-def split: if-split-asm*)

done

end

locale *heap'' =*
heap'

```

    addr2thread-id thread-id2addr
    spurious-wakeups
    empty-heap allocate typeof-addr heap-read heap-write
    P
for addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and P :: 'm prog
+
assumes allocate-typeof-addr-SomeD: [(h', a) ∈ allocate h hT; typeof-addr a ≠ None] ⇒ typeof-addr
a = [hT]
begin

lemma heap-copy-loc-New-type-match:
  [(h.heap-copy-loc a a' al h obs h'; NewHeapElem ad CTn ∈ set obs; typeof-addr ad ≠ None]
  ⇒ typeof-addr ad = [CTn]
by(erule h.heap-copy-loc.cases) simp

lemma heap-copies-New-type-match:
  [(h.heap-copies a a' als h obs h'; NewHeapElem ad CTn ∈ set obs; typeof-addr ad ≠ None]
  ⇒ typeof-addr ad = [CTn]
by(induct rule: h.heap-copies.induct)(auto dest: heap-copy-loc-New-type-match)

lemma heap-clone-New-type-match:
  [(h.heap-clone P h a h' [(obs, a)]]; NewHeapElem ad CTn ∈ set obs; typeof-addr ad ≠ None]
  ⇒ typeof-addr ad = [CTn]
by(erule h.heap-clone.cases)(auto dest: allocate-typeof-addr-SomeD heap-copies-New-type-match)

lemma red-external-New-type-match:
  [(h.red-external P t a M vs h ta va h'; NewHeapElem ad CTn ∈ set {ta}o; typeof-addr ad ≠ None]
  ⇒ typeof-addr ad = [CTn]
by(erule h.red-external.cases)(auto dest: heap-clone-New-type-match)

lemma red-external-aggr-New-type-match:
  [(ta, va, h') ∈ h.red-external-aggr P t a M vs h; NewHeapElem ad CTn ∈ set {ta}o; typeof-addr ad
  ≠ None]
  ⇒ typeof-addr ad = [CTn]
by(auto simp add: h.red-external-aggr-def split: if-split-asm dest: heap-clone-New-type-match)

end

end
theory JMM-J
imports
  JMM-Framework
  ../J/Threaded
begin

sublocale J-heap-base < red-mthr:

```

```

heap-multithreaded-base
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  final-expr mred P convert-RA
for P
.

context J-heap-base begin

abbreviation J- $\mathcal{E}$  ::
  'addr J-prog  $\Rightarrow$  cname  $\Rightarrow$  mname  $\Rightarrow$  'addr val list  $\Rightarrow$  status
 $\Rightarrow$  ('thread-id  $\times$  ('addr, 'thread-id) obs-event action) llist set
where
  J- $\mathcal{E}$  P  $\equiv$  red-mthr. $\mathcal{E}$ -start P J-local-start P

end

end

```

8.14 JMM Instantiation for J

```

theory DRF-J
imports
  JMM-Common
  JMM-J
  ../J/ProgressThreaded
  SC-Legal
begin

primrec ka :: 'addr expr  $\Rightarrow$  'addr set
and kas :: 'addr expr list  $\Rightarrow$  'addr set
where
  ka (new C) = {}
| ka (newA T[e]) = ka e
| ka (Cast T e) = ka e
| ka (e instanceof T) = ka e
| ka (Val v) = ka-Val v
| ka (Var V) = {}
| ka (e1 «bop» e2) = ka e1  $\cup$  ka e2
| ka (V := e) = ka e
| ka (a[e]) = ka a  $\cup$  ka e
| ka (a[e] := e') = ka a  $\cup$  ka e  $\cup$  ka e'
| ka (a.length) = ka a
| ka (e.F{D}) = ka e
| ka (e.F{D} := e') = ka e  $\cup$  ka e'
| ka (e.compareAndSwap(D.F, e', e'')) = ka e  $\cup$  ka e'  $\cup$  ka e''
| ka (e.M(es)) = ka e  $\cup$  kas es
| ka {V:T=vo; e} = ka e  $\cup$  (case vo of None  $\Rightarrow$  {} | Some v  $\Rightarrow$  ka-Val v)
| ka (Synchronized x e e') = ka e  $\cup$  ka e'
| ka (InSynchronized x a e) = insert a (ka e)
| ka (e;; e') = ka e  $\cup$  ka e'
| ka (if (e) e1 else e2) = ka e  $\cup$  ka e1  $\cup$  ka e2

```

| $ka (while (b) e) = ka b \cup ka e$
| $ka (throw e) = ka e$
| $ka (try e catch(C V) e') = ka e \cup ka e'$

| $kas [] = \{\}$
| $kas (e \# es) = ka e \cup kas es$

definition $ka\text{-locals} :: 'addr\ locals \Rightarrow 'addr\ set$
where $ka\text{-locals } xs = \{a. Addr\ a \in ran\ xs\}$

lemma $ka\text{-Val-subset-ka-locals}$:
 $xs\ V = \lfloor v \rfloor \Longrightarrow ka\text{-Val } v \subseteq ka\text{-locals } xs$
by(cases v)(auto simp add: ka-locals-def ran-def)

lemma $ka\text{-locals-update-subset}$:
 $ka\text{-locals } (xs(V := None)) \subseteq ka\text{-locals } xs$
 $ka\text{-locals } (xs(V \mapsto v)) \subseteq ka\text{-Val } v \cup ka\text{-locals } xs$
by(auto simp add: ka-locals-def ran-def)

lemma $ka\text{-locals-empty}$ [simp]: $ka\text{-locals } Map.empty = \{\}$
by(simp add: ka-locals-def)

lemma $kas-append$ [simp]: $kas (es @ es') = kas es \cup kas es'$
by(induct es) auto

lemma $kas-map-Val$ [simp]: $kas (map\ Val\ vs) = \bigcup (ka\text{-Val } 'set\ vs)$
by(induct vs) auto

lemma $ka\text{-blocks}$:
 $\llbracket length\ pns = length\ Ts; length\ vs = length\ Ts \rrbracket$
 $\Longrightarrow ka (blocks\ pns\ Ts\ vs\ body) = \bigcup (ka\text{-Val } 'set\ vs) \cup ka\ body$
by(induct pns Ts vs body rule: blocks.induct)(auto)

lemma $WT\text{-ka}$: $P, E \vdash e :: T \Longrightarrow ka\ e = \{\}$
and $WTs\text{-kas}$: $P, E \vdash es [::] Ts \Longrightarrow kas\ es = \{\}$
by(induct rule: WT-WTs.inducts)(auto simp add: typeof-ka)

context $J\text{-heap-base}$ **begin**

primrec $J\text{-known-addr}$ s :: $'thread\ id \Rightarrow 'addr\ expr \times 'addr\ locals \Rightarrow 'addr\ set$
where $J\text{-known-addr}s\ t (e, xs) = insert (thread\ id2addr\ t) (ka\ e \cup ka\text{-locals } xs \cup set\ start\ addr)s$

lemma **assumes** $wf: wf\text{-J-prog } P$
and $ok: start\ heap\ ok$
shows $red\text{-known-addr}s\ mono$:
 $P, t \vdash \langle e, s \rangle \text{-ta} \rightarrow \langle e', s' \rangle \Longrightarrow J\text{-known-addr}s\ t (e', lcl\ s') \subseteq J\text{-known-addr}s\ t (e, lcl\ s) \cup new\ obs\ addr}s\ \{\{ta\}\}_o$
and $reds\text{-known-addr}s\ mono$:
 $P, t \vdash \langle es, s \rangle \text{[-ta} \rightarrow \langle es', s' \rangle \Longrightarrow kas\ es' \cup ka\text{-locals } (lcl\ s') \subseteq insert (thread\ id2addr\ t) (kas\ es \cup ka\text{-locals } (lcl\ s) \cup new\ obs\ addr}s\ \{\{ta\}\}_o \cup set\ start\ addr}s$
proof(induct rule: red-reds.inducts)
case $RedVar$ **thus** ?case **by**(auto dest: ka-Val-subset-ka-locals)
next
case $RedLAss$ **thus** ?case **by**(auto simp add: ka-locals-def ran-def)

```

next
  case RedBinOp thus ?case by(auto dest: binop-known-addr[OF ok])
next
  case RedBinOpFail thus ?case by(auto dest: binop-known-addr[OF ok])
next
  case RedCall thus ?case
    by(auto simp add: ka-blocks new-obs-addr-def wf-mdecl-def dest!: sees-wf-mdecl[OF wf] WT-ka)
next
  case (RedCallExternal s a T M Ts T D vs ta va h') thus ?case
    by(cases va)(auto dest!: red-external-known-addr-mono[OF ok])
next
  case (BlockRed e h l V vo ta e' h' l')
  thus ?case using ka-locals-update-subset[where xs = l and V=V] ka-locals-update-subset[where
xs = l' and V=V]
  apply(cases l V)
  apply(auto simp del: fun-upd-apply del: subsetI)
  apply(blast dest: ka-Val-subset-ka-locals)+
  done
qed(simp-all add: new-obs-addr-def addr-of-sys-xcpt-start-addr[OF ok] subset-Un1 subset-Un2 sub-
set-insert ka-Val-subset-new-obs-Addr-ReadMem ka-blocks del: fun-upd-apply, blast+)

lemma red-known-addr-ReadMem:
  [[ P,t ⊢ ⟨e, s⟩ -ta→ ⟨e', s'⟩; ReadMem ad al v ∈ set {ta}_o ]] ⇒ ad ∈ J-known-addr t (e, lcl s)
  and reds-known-addr-ReadMem:
  [[ P,t ⊢ ⟨es, s⟩ [-ta→] ⟨es', s'⟩; ReadMem ad al v ∈ set {ta}_o ]]
  ⇒ ad ∈ insert (thread-id2addr t) (kas es ∪ ka-locals (lcl s)) ∪ set start-addr
proof(induct rule: red-reds.inducts)
  case RedCallExternal thus ?case by simp (blast dest: red-external-known-addr-ReadMem)
next
  case (BlockRed e h l V vo ta e' h' l')
  thus ?case using ka-locals-update-subset[where xs = l and V=V] ka-locals-update-subset[where
xs = l' and V=V]
  by(auto simp del: fun-upd-apply)
qed(simp-all, blast+)

lemma red-known-addr-WriteMem:
  [[ P,t ⊢ ⟨e, s⟩ -ta→ ⟨e', s'⟩; {ta}_o ! n = WriteMem ad al (Addr a); n < length {ta}_o ]]
  ⇒ a ∈ J-known-addr t (e, lcl s) ∨ a ∈ new-obs-addr (take n {ta}_o)
  and reds-known-addr-WriteMem:
  [[ P,t ⊢ ⟨es, s⟩ [-ta→] ⟨es', s'⟩; {ta}_o ! n = WriteMem ad al (Addr a); n < length {ta}_o ]]
  ⇒ a ∈ insert (thread-id2addr t) (kas es ∪ ka-locals (lcl s)) ∪ set start-addr ∪ new-obs-addr (take
n {ta}_o)
proof(induct rule: red-reds.inducts)
  case RedCASSucceed thus ?case by(auto simp add: nth-Cons split: nat.split-asm)
next
  case RedCallExternal thus ?case by simp (blast dest: red-external-known-addr-WriteMem)
next
  case (BlockRed e h l V vo ta e' h' l')
  thus ?case using ka-locals-update-subset[where xs = l and V=V] ka-locals-update-subset[where
xs = l' and V=V]
  by(auto simp del: fun-upd-apply)
qed(simp-all, blast+)

end

```

context *J-heap* begin

lemma

assumes *wf*: *wf-J-prog P*

and *ok*: *start-heap-ok*

shows *red-known-addr-new-thread*:

$\llbracket P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; NewThread\ t'\ x'\ h' \in set\ \{\{ta\}_t\} \rrbracket$

$\implies J\text{-known-addr}\ t'\ x' \subseteq J\text{-known-addr}\ t\ (e, lcl\ s)$

and *reds-known-addr-new-thread*:

$\llbracket P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; NewThread\ t'\ x'\ h' \in set\ \{\{ta\}_t\} \rrbracket$

$\implies J\text{-known-addr}\ t'\ x' \subseteq insert\ (thread\text{-id2addr}\ t)\ (kas\ es \cup ka\text{-locals}\ (lcl\ s) \cup set\ start\text{-addr})$

proof (induct rule: *red-reds.inducts*)

case *RedCallExternal* thus ?case

apply *clarsimp*

apply (frule (1) *red-external-new-thread-sub-thread*)

apply (frule (1) *red-external-NewThread-idD*)

apply *clarsimp*

apply (drule (1) *addr2thread-id-inverse*)

apply *simp*

apply (drule *sub-Thread-sees-run[OF wf]*)

apply *clarsimp*

apply (auto 4 4 dest: *sees-wf-mdecl[OF wf]* *WT-ka simp add: wf-mdecl-def*)

done

next

case (*BlockRed e h l V vo ta e' h' l'*)

thus ?case using *ka-locals-update-subset* [where *xs = l* and *V = V*] *ka-locals-update-subset* [where *xs = l'* and *V = V*]

by (cases *l V*) (auto *simp del: fun-upd-apply*)

qed (*simp-all, blast+*)

lemma *red-New-same-addr-same*:

$\llbracket convert\text{-extTA}\ extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle;$

$\{\{ta\}_o\} ! i = NewHeapElem\ a\ x; i < length\ \{\{ta\}_o\};$

$\{\{ta\}_o\} ! j = NewHeapElem\ a\ x'; j < length\ \{\{ta\}_o\} \rrbracket$

$\implies i = j$

and *reds-New-same-addr-same*:

$\llbracket convert\text{-extTA}\ extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle;$

$\{\{ta\}_o\} ! i = NewHeapElem\ a\ x; i < length\ \{\{ta\}_o\};$

$\{\{ta\}_o\} ! j = NewHeapElem\ a\ x'; j < length\ \{\{ta\}_o\} \rrbracket$

$\implies i = j$

apply (induct rule: *red-reds.inducts*)

apply (auto dest: *red-external-New-same-addr-same simp add: nth-Cons split: nat.split-asm*)

done

end

locale *J-allocated-heap = allocated-heap +*

constrains *addr2thread-id* :: ('addr :: addr) \Rightarrow 'thread-id

and *thread-id2addr* :: 'thread-id \Rightarrow 'addr

and *spurious-wakeups* :: bool

and *empty-heap* :: 'heap

and *allocate* :: 'heap \Rightarrow htype \Rightarrow ('heap \times 'addr) set

and *typeof-addr* :: 'heap \Rightarrow 'addr \rightarrow htype
and *heap-read* :: 'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr val \Rightarrow bool
and *heap-write* :: 'heap \Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr val \Rightarrow 'heap \Rightarrow bool
and *P* :: 'addr J-prog

sublocale *J-allocated-heap* < *J-heap*
by(*unfold-locales*)

context *J-allocated-heap* **begin**

lemma *red-allocated-mono*: $P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \Longrightarrow \text{allocated } (hp\ s) \subseteq \text{allocated } (hp\ s')$
and *reds-allocated-mono*: $P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \Longrightarrow \text{allocated } (hp\ s) \subseteq \text{allocated } (hp\ s')$
by(*induct rule: red-reds.inducts*)(*auto dest: allocate-allocatedD heap-write-allocated-same red-external-allocated-mono del: subsetI*)

lemma *red-allocatedD*:

$\llbracket P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{NewHeapElem } ad\ CTn \in \text{set } \{ta\}_o \rrbracket \Longrightarrow ad \in \text{allocated } (hp\ s') \wedge ad \notin \text{allocated } (hp\ s)$

and *reds-allocatedD*:

$\llbracket P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{NewHeapElem } ad\ CTn \in \text{set } \{ta\}_o \rrbracket \Longrightarrow ad \in \text{allocated } (hp\ s') \wedge ad \notin \text{allocated } (hp\ s)$

by(*induct rule: red-reds.inducts*)(*auto dest: allocate-allocatedD heap-write-allocated-same red-external-allocatedD*)

lemma *red-allocated-NewHeapElemD*:

$\llbracket P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; ad \in \text{allocated } (hp\ s'); ad \notin \text{allocated } (hp\ s) \rrbracket \Longrightarrow \exists CTn. \text{NewHeapElem } ad\ CTn \in \text{set } \{ta\}_o$

and *reds-allocated-NewHeapElemD*:

$\llbracket P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; ad \in \text{allocated } (hp\ s'); ad \notin \text{allocated } (hp\ s) \rrbracket \Longrightarrow \exists CTn. \text{NewHeapElem } ad\ CTn \in \text{set } \{ta\}_o$

by(*induct rule: red-reds.inducts*)(*auto dest: allocate-allocatedD heap-write-allocated-same red-external-NewHeapElemD*)

lemma *mred-allocated-multithreaded*:

$\text{allocated-multithreaded } \text{addr2thread-id } \text{thread-id2addr } \text{empty-heap } \text{allocate } \text{typeof-addr } \text{heap-write } \text{allocated } \text{final-expr } (mred\ P)\ P$

proof

fix *t x m ta x' m'*

assume $mred\ P\ t\ (x, m)\ ta\ (x', m')$

thus $\text{allocated } m \subseteq \text{allocated } m'$

by(*auto dest: red-allocated-mono del: subsetI simp add: split-beta*)

next

fix *x t m ta x' m' ad CTn*

assume $mred\ P\ t\ (x, m)\ ta\ (x', m')$

and $\text{NewHeapElem } ad\ CTn \in \text{set } \{ta\}_o$

thus $ad \in \text{allocated } m' \wedge ad \notin \text{allocated } m$

by(*auto dest: red-allocatedD simp add: split-beta*)

next

fix *t x m ta x' m' ad*

assume $mred\ P\ t\ (x, m)\ ta\ (x', m')$

and $ad \in \text{allocated } m' \wedge ad \notin \text{allocated } m$

thus $\exists CTn. \text{NewHeapElem } ad\ CTn \in \text{set } \{ta\}_o$

by(*auto dest: red-allocated-NewHeapElemD simp add: split-beta*)

next

fix *t x m ta x' m' i a CTn j CTn'*

assume $mred\ P\ t\ (x, m)\ ta\ (x', m')$

```

    and  $\{ta\}_o ! i = \text{NewHeapElem } a \text{ CTn } i < \text{length } \{ta\}_o$ 
    and  $\{ta\}_o ! j = \text{NewHeapElem } a \text{ CTn } j < \text{length } \{ta\}_o$ 
    thus  $i = j$  by(auto dest: red-New-same-addr-same simp add: split-beta)
qed

```

end

```

sublocale J-allocated-heap < red-mthr: allocated-multithreaded
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write allocated
  final-expr mred P
  P
by(rule mred-allocated-multithreaded)

```

context *J-allocated-heap* **begin**

lemma *mred-known-addr:*

assumes *wf: wf-J-prog P*

and *ok: start-heap-ok*

shows *known-addr* *addr2thread-id thread-id2addr empty-heap allocate typeof-addr heap-write allocated J-known-addr* *final-expr (mred P) P*

proof

fix $t x m ta x' m'$

assume $mred P t (x, m) ta (x', m')$

thus $J\text{-known-addr } t x' \subseteq J\text{-known-addr } t x \cup \text{new-obs-addr } \{ta\}_o$

by(auto del: *subsetI simp add: split-beta dest: red-known-addr-mono[OF wf ok]*)

next

fix $t x m ta x' m' t' x'' m''$

assume $mred P t (x, m) ta (x', m')$

and $\text{NewThread } t' x'' m'' \in \text{set } \{ta\}_t$

thus $J\text{-known-addr } t' x'' \subseteq J\text{-known-addr } t x$

by(auto del: *subsetI simp add: split-beta dest: red-known-addr-new-thread[OF wf ok]*)

next

fix $t x m ta x' m' ad al v$

assume $mred P t (x, m) ta (x', m')$

and $\text{ReadMem } ad al v \in \text{set } \{ta\}_o$

thus $ad \in J\text{-known-addr } t x$

by(auto simp add: *split-beta dest: red-known-addr-ReadMem*)

next

fix $t x m ta x' m' n ad al ad'$

assume $mred P t (x, m) ta (x', m')$

and $\{ta\}_o ! n = \text{WriteMem } ad al (\text{Addr } ad') n < \text{length } \{ta\}_o$

thus $ad' \in J\text{-known-addr } t x \vee ad' \in \text{new-obs-addr } (\text{take } n \{ta\}_o)$

by(auto simp add: *split-beta dest: red-known-addr-WriteMem*)

qed

end

context *J-heap* **begin**

lemma *red-read-typeable:*

$\llbracket \text{convert-extTA } extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; P, E, hp s \vdash e : T; \text{ReadMem } ad al v \in \text{set } \{ta\}_o \rrbracket$

$\implies \exists T'. P, hp\ s \vdash ad@al : T'$
and *reds-read-typeable*:
 $\llbracket convert-extTA\ extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; P, E, hp\ s \vdash es\ [:]\ Ts; ReadMem\ ad\ al\ v \in set\ \{\{ta\}_o\} \rrbracket$
 $\implies \exists T'. P, hp\ s \vdash ad@al : T'$
proof(*induct arbitrary: E T and E Ts rule: red-reds.inducts*)
case *RedAAcc thus ?case*
by(*fastforce intro: addr-loc-type.intros simp add: nat-less-iff word-sle-eq*)
next
case *RedFAcc thus ?case*
by(*fastforce intro: addr-loc-type.intros*)
next
case *RedCASSucceed thus ?case*
by(*fastforce intro: addr-loc-type.intros*)
next
case *RedCASFail thus ?case*
by(*fastforce intro: addr-loc-type.intros*)
next
case *RedCallExternal thus ?case*
by(*auto intro: red-external-read-mem-typeable*)
qed *auto*
end

primrec *new-types* :: ('a, 'b, 'addr) exp \Rightarrow ty set
and *new-typess* :: ('a, 'b, 'addr) exp list \Rightarrow ty set
where

new-types (*new C*) = {*Class C*}
| *new-types* (*newA T[e]*) = *insert* (*T[]*) (*new-types e*)
| *new-types* (*Cast T e*) = *new-types e*
| *new-types* (*e instanceof T*) = *new-types e*
| *new-types* (*Val v*) = {}
| *new-types* (*Var V*) = {}
| *new-types* (*e1 «bop» e2*) = *new-types e1* \cup *new-types e2*
| *new-types* (*V := e*) = *new-types e*
| *new-types* (*a[e]*) = *new-types a* \cup *new-types e*
| *new-types* (*a[e] := e'*) = *new-types a* \cup *new-types e* \cup *new-types e'*
| *new-types* (*a.length*) = *new-types a*
| *new-types* (*e.F{D}*) = *new-types e*
| *new-types* (*e.F{D} := e'*) = *new-types e* \cup *new-types e'*
| *new-types* (*e.compareAndSwap(D.F, e', e'')*) = *new-types e* \cup *new-types e'* \cup *new-types e''*
| *new-types* (*e.M(es)*) = *new-types e* \cup *new-typess es*
| *new-types* {*V:T=vo; e*} = *new-types e*
| *new-types* (*Synchronized x e e'*) = *new-types e* \cup *new-types e'*
| *new-types* (*InSynchronized x a e*) = *new-types e*
| *new-types* (*e;; e'*) = *new-types e* \cup *new-types e'*
| *new-types* (*if (e) e1 else e2*) = *new-types e* \cup *new-types e1* \cup *new-types e2*
| *new-types* (*while (b) e*) = *new-types b* \cup *new-types e*
| *new-types* (*throw e*) = *new-types e*
| *new-types* (*try e catch(C V) e'*) = *new-types e* \cup *new-types e'*

| *new-typess* [] = {}
| *new-typess* (*e # es*) = *new-types e* \cup *new-typess es*

lemma *new-types-blocks*:

$\llbracket \text{length } pns = \text{length } Ts; \text{length } vs = \text{length } Ts \rrbracket \implies \text{new-types } (\text{blocks } pns \text{ vs } Ts \ e) = \text{new-types } e$
apply(*induct rule: blocks.induct*)
apply(*simp-all*)
done

context *J-heap-base* **begin**

lemma *WTrt-new-types-types*: $P, E, h \vdash e : T \implies \text{new-types } e \subseteq \text{types } P$
and *WTrts-new-typess-types*: $P, E, h \vdash es \ [:] Ts \implies \text{new-typess } es \subseteq \text{types } P$
by(*induct rule: WTrt-WTrts.inducts*) *simp-all*

end

lemma *WT-new-types-types*: $P, E \vdash e :: T \implies \text{new-types } e \subseteq \text{types } P$
and *WTs-new-typess-types*: $P, E \vdash es \ [::] Ts \implies \text{new-typess } es \subseteq \text{types } P$
by(*induct rule: WT-WTs.inducts*) *simp-all*

context *J-heap-conf* **begin**

lemma *red-New-typeof-addrD*:

$\llbracket \text{convert-extTA } extTA, P, t \vdash \langle e, s \rangle \text{ --ta--} \langle e', s' \rangle; \text{new-types } e \subseteq \text{types } P; hconf \ (hp \ s); \text{NewHeapElem } a \ x \in \text{set } \{ta\}_o \rrbracket$

$\implies \text{typeof-addr } (hp \ s') \ a = \text{Some } x$

and *reds-New-typeof-addrD*:

$\llbracket \text{convert-extTA } extTA, P, t \vdash \langle es, s \rangle \text{ [-ta--]} \langle es', s' \rangle; \text{new-typess } es \subseteq \text{types } P; hconf \ (hp \ s); \text{NewHeapElem } a \ x \in \text{set } \{ta\}_o \rrbracket$

$\implies \text{typeof-addr } (hp \ s') \ a = \text{Some } x$

apply(*induct rule: red-reds.inducts*)

apply(*auto dest: allocate-SomeD red-external-New-typeof-addrD*)

done

lemma *J-conf-read-heap-read-typed*:

J-conf-read addr2thread-id thread-id2addr empty-heap allocate typeof-addr (heap-read-typed P) heap-write hconf P

proof –

interpret *conf: heap-conf-read*

addr2thread-id thread-id2addr

spurious-wakeups

empty-heap allocate typeof-addr heap-read-typed P heap-write hconf

P

by(*rule heap-conf-read-heap-read-typed*)

show *?thesis* **by**(*unfold-locales*)

qed

lemma *red-non-speculative-vs-conf*:

$\llbracket \text{convert-extTA } extTA, P, t \vdash \langle e, s \rangle \text{ --ta--} \langle e', s' \rangle; P, E, hp \ s \vdash e : T; \text{non-speculative } P \text{ vs } (\text{llist-of } (\text{take } n \ (\text{map } \text{NormalAction } \{ta\}_o))); \text{vs-conf } P \ (hp \ s) \ \text{vs}; hconf \ (hp \ s) \rrbracket$

$\implies \text{vs-conf } P \ (hp \ s') \ (\text{w-values } P \ \text{vs} \ (\text{take } n \ (\text{map } \text{NormalAction } \{ta\}_o)))$

and *reds-non-speculative-vs-conf*:

$\llbracket \text{convert-extTA } extTA, P, t \vdash \langle es, s \rangle \text{ [-ta--]} \langle es', s' \rangle; P, E, hp \ s \vdash es \ [:] Ts; \text{non-speculative } P \ \text{vs} \ (\text{llist-of } (\text{take } n \ (\text{map } \text{NormalAction } \{ta\}_o))); \text{vs-conf } P \ (hp \ s) \ \text{vs}; hconf \ (hp \ s) \rrbracket$

$\implies vs\text{-conf } P (hp\ s') (w\text{-values } P\ vs\ (take\ n\ (map\ NormalAction\ \{ta\}_o)))$
proof(*induct arbitrary: E T and E Ts rule: red-reds.inducts*)
case (*RedAAss h a U n i w V h' xs*)
from $\langle sint\ i < int\ n \rangle \langle 0 \leq s\ i \rangle$ **have** $nat\ (sint\ i) < n$
by (*simp add: word-sle-eq nat-less-iff*)
with $\langle typeof\text{-addr } h\ a = [Array\text{-type } U\ n] \rangle$ **have** $P, h \vdash a @ ACell\ (nat\ (sint\ i)) : U$
by (*auto intro: addr-loc-type.intros*)
moreover from $\langle heap\text{-write } h\ a\ (ACell\ (nat\ (sint\ i)))\ w\ h' \rangle$ **have** $h \leq h'$ **by** (*rule hext-heap-write*)
ultimately have $P, h' \vdash a @ ACell\ (nat\ (sint\ i)) : U$ **by** (*rule addr-loc-type-hext-mono*)
moreover from $\langle typeof_h\ w = [V] \rangle \langle P \vdash V \leq U \rangle$ **have** $P, h \vdash w : \leq U$ **by** (*simp add: conf-def*)
with $\langle h \leq h' \rangle$ **have** $P, h' \vdash w : \leq U$ **by** (*rule conf-hext*)
ultimately have $\exists T. P, h' \vdash a @ ACell\ (nat\ (sint\ i)) : T \wedge P, h' \vdash w : \leq T$ **by** *blast*
thus *?case using RedAAss*
by (*auto intro!: vs-confI split: if-split-asm dest: vs-confD simp add: take-Cons'*)(*blast dest: vs-confD*
hext-heap-write intro: addr-loc-type-hext-mono conf-hext)+
next
case (*RedFAss h e D F v h' xs*)
hence $\exists T. P, h' \vdash e @ CField\ D\ F : T \wedge P, h' \vdash v : \leq T$
by(*force dest!: hext-heap-write intro!: addr-loc-type.intros intro: typeof-addr-hext-mono type-of-hext-type-of*
simp add: conf-def)
thus *?case using RedFAss*
by(*auto intro!: vs-confI simp add: take-Cons' split: if-split-asm dest: vs-confD*)(*blast dest: vs-confD*
hext-heap-write intro: addr-loc-type-hext-mono conf-hext)+
next
case (*RedCASSucceed h a D F v v' h' l*)
hence $\exists T. P, h' \vdash a @ CField\ D\ F : T \wedge P, h' \vdash v' : \leq T$
by(*force dest!: hext-heap-write intro!: addr-loc-type.intros intro: typeof-addr-hext-mono type-of-hext-type-of*
simp add: conf-def take-Cons')
thus *?case using RedCASSucceed*
by(*auto simp add: take-Cons' split: if-split-asm dest: vs-confD intro!: vs-confI*)
(blast dest: vs-confD hext-heap-write intro: addr-loc-type-hext-mono conf-hext)+
next
case *RedCallExternal* **thus** *?case by*(*auto intro: red-external-non-speculative-vs-conf*)
qed(*auto dest: vs-conf-allocate hext-allocate intro: vs-conf-hext simp add: take-Cons'*)

lemma *red-non-speculative-typeable*:
 $\llbracket convert\text{-extTA } extTA, P, t \vdash \langle e, s \rangle \text{-ta} \rightarrow \langle e', s' \rangle; P, E, hp\ s \vdash e : T;$
 $non\text{-speculative } P\ vs\ (l\text{list-of } (map\ NormalAction\ \{ta\}_o)); vs\text{-conf } P\ (hp\ s)\ vs; hconf\ (hp\ s) \rrbracket$
 $\implies J\text{-heap-base.red } addr2thread\text{-id } thread\text{-id}2addr\ spurious\text{-wakeups } empty\text{-heap } allocate\ typeof\text{-addr}$
 $(heap\text{-read-typed } P) heap\text{-write } (convert\text{-extTA } extTA) P\ t\ e\ s\ ta\ e'\ s'$
and *reds-non-speculative-typeable*:
 $\llbracket convert\text{-extTA } extTA, P, t \vdash \langle es, s \rangle \text{-ta} \rightarrow \langle es', s' \rangle; P, E, hp\ s \vdash es\ [:] Ts;$
 $non\text{-speculative } P\ vs\ (l\text{list-of } (map\ NormalAction\ \{ta\}_o)); vs\text{-conf } P\ (hp\ s)\ vs; hconf\ (hp\ s) \rrbracket$
 $\implies J\text{-heap-base.reds } addr2thread\text{-id } thread\text{-id}2addr\ spurious\text{-wakeups } empty\text{-heap } allocate\ typeof\text{-addr}$
 $(heap\text{-read-typed } P) heap\text{-write } (convert\text{-extTA } extTA) P\ t\ es\ s\ ta\ es'\ s'$
proof(*induct arbitrary: E T and E Ts rule: red-reds.inducts*)
case *RedCall* **thus** *?case by*(*blast intro: J-heap-base.red-reds.RedCall*)
next
case *RedCallExternal* **thus** *?case*
by(*auto intro: J-heap-base.red-reds.RedCallExternal red-external-non-speculative-typeable*)
qed(*auto intro: J-heap-base.red-reds.intros intro!: heap-read-typedI dest: vs-confD addr-loc-type-fun*)

end

sublocale *J-heap-base* < *red-mthr*:

if-multithreaded
final-expr
mred P
convert-RA

for *P*

by(*unfold-locales*)

locale *J-allocated-heap-conf* =

J-heap-conf
addr2thread-id thread-id2addr
spurious-wakeups
empty-heap allocate typeof-addr heap-read heap-write hconf
P

+

J-allocated-heap
addr2thread-id thread-id2addr
spurious-wakeups
empty-heap allocate typeof-addr heap-read heap-write
allocated
P

for *addr2thread-id* :: ('*addr* :: *addr*) ⇒ '*thread-id*

and *thread-id2addr* :: '*thread-id* ⇒ '*addr*

and *spurious-wakeups* :: *bool*

and *empty-heap* :: '*heap*

and *allocate* :: '*heap* ⇒ *htype* ⇒ ('*heap* × '*addr*) *set*

and *typeof-addr* :: '*heap* ⇒ '*addr* → *htype*

and *heap-read* :: '*heap* ⇒ '*addr* ⇒ *addr-loc* ⇒ '*addr val* ⇒ *bool*

and *heap-write* :: '*heap* ⇒ '*addr* ⇒ *addr-loc* ⇒ '*addr val* ⇒ '*heap* ⇒ *bool*

and *hconf* :: '*heap* ⇒ *bool*

and *allocated* :: '*heap* ⇒ '*addr set*

and *P* :: '*addr J-prog*

begin

lemma *mred-known-addr-typing*:

assumes *wf*: *wf-J-prog P*

and *ok*: *start-heap-ok*

shows *known-addr-typing addr2thread-id thread-id2addr empty-heap allocate typeof-addr heap-write allocated J-known-addr final-expr (mred P) (λt x h. ∃ ET. sconf-type-ok ET t x h) P*

proof –

interpret *known-addr*

addr2thread-id thread-id2addr

spurious-wakeups

empty-heap allocate typeof-addr heap-read heap-write

allocated J-known-addr

final-expr mred P P

using *wf ok by*(*rule mred-known-addr*)

show *?thesis*

proof

fix *t x m ta x' m'*

assume *mred P t (x, m) ta (x', m')*

thus $m \sqsubseteq m'$ **by**(*auto dest: red-heap-incr simp add: split-beta*)

```

next
  fix t x m ta x' m' vs
  assume red: mred P t (x, m) ta (x', m')
    and ts-ok:  $\exists ET. \text{sconf-type-ok } ET \ t \ x \ m$ 
    and vs: vs-conf P m vs
    and ns: non-speculative P vs (llist-of (map NormalAction  $\{ta\}_o$ ))

  let ?mred = J-heap-base.mred addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate
  typeof-addr (heap-read-typed P) heap-write P

  have lift: lifting-inv final-expr ?mred sconf-type-ok
    by(intro J-conf-read.lifting-inv-sconf-subject-ok J-conf-read-heap-read-typed wf)
  moreover
  from ts-ok obtain ET where type: sconf-type-ok ET t x m ..
  with red vs ns have red': ?mred t (x, m) ta (x', m')
  by(auto simp add: split-beta sconf-type-ok-def sconf-def type-ok-def dest: red-non-speculative-typeable)
  ultimately have sconf-type-ok ET t x' m' using type
    by(rule lifting-inv.invariant-red[where r=?mred])
  thus  $\exists ET. \text{sconf-type-ok } ET \ t \ x' \ m' \ ..$ 
  { fix t'' x'' m''
    assume New: NewThread t'' x'' m''  $\in$  set  $\{ta\}_t$ 
    with red have m'' = snd (x', m') by(rule red-mthr.new-thread-memory)
    with lift red' type New
    show  $\exists ET. \text{sconf-type-ok } ET \ t'' \ x'' \ m''$ 
    by-(rule lifting-inv.invariant-NewThread[where r=?mred], simp-all) }
  { fix t'' x''
    assume  $\exists ET. \text{sconf-type-ok } ET \ t'' \ x'' \ m$ 
    with lifting-inv.invariant-other[where r=?mred, OF lift red' type]
    show  $\exists ET. \text{sconf-type-ok } ET \ t'' \ x'' \ m' \ \text{by } \text{blast}$  }
next
  fix t x m ta x' m' vs n
  assume red: mred P t (x, m) ta (x', m')
    and ts-ok:  $\exists ET. \text{sconf-type-ok } ET \ t \ x \ m$ 
    and vs: vs-conf P m vs
    and ns: non-speculative P vs (llist-of (take n (map NormalAction  $\{ta\}_o$ )))
  thus vs-conf P m' (w-values P vs (take n (map NormalAction  $\{ta\}_o$ )))
  by(cases x)(auto dest: red-non-speculative-vs-conf simp add: sconf-type-ok-def type-ok-def sconf-def)
next
  fix t x m ta x' m' ad al v
  assume mred P t (x, m) ta (x', m')
    and  $\exists ET. \text{sconf-type-ok } ET \ t \ x \ m$ 
    and ReadMem ad al v  $\in$  set  $\{ta\}_o$ 
  thus  $\exists T. P, m \vdash \text{ad}@al : T$ 
  by(fastforce simp add: sconf-type-ok-def type-ok-def sconf-def split-beta dest: red-read-typeable)
next
  fix t x m ta x' m' ad hT
  assume mred P t (x, m) ta (x', m')
    and  $\exists ET. \text{sconf-type-ok } ET \ t \ x \ m$ 
    and NewHeapElem ad hT  $\in$  set  $\{ta\}_o$ 
  thus typeof-addr m' ad =  $\lfloor hT \rfloor$ 
  by(auto dest: red-New-typeof-addrD[where x=hT] dest!: WTrt-new-types-types simp add: split-beta
  sconf-type-ok-def sconf-def type-ok-def)
qed
qed

```

end

context *J-allocated-heap-conf* begin

lemma *executions-sc*:

assumes *wf*: *wf-J-prog P*
 and *wf-start*: *wf-start-state P C M vs*
 and *vs2*: $\bigcup (ka\text{-}Val \text{ ' } set \text{ } vs) \subseteq set \text{ } start\text{-}addrs$
 shows *executions-sc-hb* (*J-E P C M vs status*) *P*
 (is *executions-sc-hb* ?*E P*)

proof –

from *wf-start* obtain *Ts T pns body D* where *ok*: *start-heap-ok*
 and *sees*: $P \vdash C \text{ sees } M:Ts \rightarrow T = [(pns, body)] \text{ in } D$
 and *vs1*: $P, start\text{-}heap \vdash vs [:\leq] Ts$ by *cases auto*

interpret *known-addr-typing*
addr2thread-id thread-id2addr
spurious-wakeups
empty-heap allocate type-of-addr heap-read heap-write
allocated J-known-addr
final-expr mred P $\lambda t \ x \ h. \exists ET. scon\text{-}f\text{-}type\text{-}ok \ ET \ t \ x \ h \ P$
 using *wf ok* by (rule *mred-known-addr-typing*)

from *wf-prog-wf-syscls*[*OF wf*] *J-start-state-sconf-type-ok*[*OF wf wf-start*]

show ?*thesis*

proof (rule *executions-sc-hb*)

from *wf sees* have *wf-mdecl wf-J-mdecl P D (M, Ts, T, [(pns, body)])* by (rule *sees-wf-mdecl*)
 then obtain *T'* where *len1*: $length \ pns = length \ Ts$ and *wt*: $P, [this \rightarrow Class \ D, pns \ [\mapsto] \ Ts] \vdash body$

:: *T'*

by (auto simp add: *wf-mdecl-def*)

from *vs1* have *len2*: $length \ vs = length \ Ts$ by (rule *list-all2-lengthD*)

show *J-known-addr start-tid* ($(\lambda (pns, body) \ vs. (blocks \ (this \ \# \ pns) \ (Class \ (fst \ (method \ P \ C \ M)) \ \# \ fst \ (snd \ (method \ P \ C \ M)))) \ (Null \ \# \ vs) \ body, Map.empty)$) (the (snd (snd (snd (method P C M))))))
 $vs) \subseteq allocated \ start\text{-}heap$

using *sees vs2 len1 len2 WT-ka*[*OF wt*]

by (auto simp add: *split-beta start-addr-allocated ka-blocks intro: start-tid-start-addr*[*OF wf-prog-wf-syscls* [*OF wf*] *ok*])

qed

qed

end

declare *split-paired-Ex* [*simp del*]

context *J-progress* begin

lemma *ex-WTrt-simps*:

$P, E, h \vdash e : T \implies \exists E \ T. P, E, h \vdash e : T$

by *blast*

abbreviation (*input*) *J-non-speculative-read-bound* :: *nat*

where *J-non-speculative-read-bound* $\equiv 2$

lemma assumes *hrt: heap-read-typeable hconf P*

and *vs: vs-conf P (shr s) vs*

and *hconf: hconf (shr s)*

shows *red-non-speculative-read:*

$\llbracket P, t \vdash \langle e, (shr\ s, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle; \exists E\ T. P, E, shr\ s \vdash e : T;$
red-mthr.mthr.if.actions-ok s t ta;

$I < \text{length } \llbracket ta \rrbracket_o; \llbracket ta \rrbracket_o ! I = \text{ReadMem } a''\ al''\ v; v' \in w\text{-values } P\ vs\ (\text{map } \text{NormalAction } (\text{take } I\ \llbracket ta \rrbracket_o))\ (a'',\ al'');$

non-speculative P vs (llist-of (map NormalAction (take I \llbracket ta \rrbracket_o))) \rrbracket

$\implies \exists ta'\ e''\ xs''\ h''. P, t \vdash \langle e, (shr\ s, xs) \rangle -ta' \rightarrow \langle e'', (h'', xs'') \rangle \wedge$

red-mthr.mthr.if.actions-ok s t ta' \wedge

$I < \text{length } \llbracket ta' \rrbracket_o \wedge \text{take } I\ \llbracket ta' \rrbracket_o = \text{take } I\ \llbracket ta \rrbracket_o \wedge$

$\llbracket ta' \rrbracket_o ! I = \text{ReadMem } a''\ al''\ v' \wedge \text{length } \llbracket ta' \rrbracket_o \leq \text{max } J\text{-non-speculative-read-bound } (\text{length } \llbracket ta \rrbracket_o)$

and *reds-non-speculative-read:*

$\llbracket P, t \vdash \langle es, (shr\ s, xs) \rangle [-ta \rightarrow] \langle es', (h', xs') \rangle; \exists E\ Ts. P, E, shr\ s \vdash es\ [:]\ Ts;$
red-mthr.mthr.if.actions-ok s t ta;

$I < \text{length } \llbracket ta \rrbracket_o; \llbracket ta \rrbracket_o ! I = \text{ReadMem } a''\ al''\ v; v' \in w\text{-values } P\ vs\ (\text{map } \text{NormalAction } (\text{take } I\ \llbracket ta \rrbracket_o))\ (a'',\ al'');$

non-speculative P vs (llist-of (map NormalAction (take I \llbracket ta \rrbracket_o))) \rrbracket

$\implies \exists ta'\ es''\ xs''\ h''. P, t \vdash \langle es, (shr\ s, xs) \rangle [-ta' \rightarrow] \langle es'', (h'', xs'') \rangle \wedge$

red-mthr.mthr.if.actions-ok s t ta' \wedge

$I < \text{length } \llbracket ta' \rrbracket_o \wedge \text{take } I\ \llbracket ta' \rrbracket_o = \text{take } I\ \llbracket ta \rrbracket_o \wedge$

$\llbracket ta' \rrbracket_o ! I = \text{ReadMem } a''\ al''\ v' \wedge \text{length } \llbracket ta' \rrbracket_o \leq \text{max } J\text{-non-speculative-read-bound } (\text{length } \llbracket ta \rrbracket_o)$

proof(*induct e hxs \equiv (shr s, xs) ta e' hxs' \equiv (h', xs')*)

and *es hxs \equiv (shr s, xs) ta es' hxs' \equiv (h', xs')*

arbitrary: xs xs' and xs xs' rule: red-reds.inducts

case (*RedAAcc a U n i v e*)

hence [*simp*]: $I = 0\ al'' = \text{ACell } (\text{nat } (\text{sint } i))\ a'' = a$

and $v': v' \in vs\ (a, \text{ACell } (\text{nat } (\text{sint } i)))$ **by** *simp-all*

from *RedAAcc* **have** *adal: P, shr s \vdash a @ ACCell (nat (sint i)) : U*

by(*auto intro: addr-loc-type.intros simp add: nat-less-iff word-sle-eq*)

from $v' vs\ \text{adal}$ **have** $P, shr\ s \vdash v' : \leq U$ **by**(*auto dest!: vs-confD dest: addr-loc-type-fun*)

with *hrt adal* **have** *heap-read (shr s) a (ACCell (nat (sint i))) v' using hconf by (rule heap-read-typeableD)*

with $\langle \text{typeof-addr } (shr\ s)\ a = \lfloor \text{Array-type } U\ n \rfloor \rangle \langle 0 \leq i \rangle \langle \text{sint } i < \text{int } n \rangle$

$\langle \text{red-mthr.mthr.if.actions-ok } s\ t\ \llbracket \text{ReadMem } a\ (\text{ACCell } (\text{nat } (\text{sint } i)))\ v \rrbracket \rangle$

show *?case by (fastforce intro: red-reds.RedAAcc)*

next

case (*RedFAcc a D F v*)

hence [*simp*]: $I = 0\ al'' = \text{CField } D\ F\ a'' = a$

and $v': v' \in vs\ (a, \text{CField } D\ F)$ **by** *simp-all*

from *RedFAcc* **obtain** $E\ T$ **where** $P, E, shr\ s \vdash \text{addr } a \cdot F\ \{D\} : T$ **by** *blast*

with *RedFAcc* **have** *adal: P, shr s \vdash a @ CField D F : T by (auto 4 4 intro: addr-loc-type.intros)*

from $v' vs\ \text{adal}$ **have** $P, shr\ s \vdash v' : \leq T$ **by**(*auto dest!: vs-confD dest: addr-loc-type-fun*)

with *hrt adal* **have** *heap-read (shr s) a (CField D F) v' using hconf by (rule heap-read-typeableD)*

with $\langle \text{red-mthr.mthr.if.actions-ok } s\ t\ \llbracket \text{ReadMem } a\ (\text{CField } D\ F)\ v \rrbracket \rangle$

show *?case by (fastforce intro: red-reds.RedFAcc)*

next

case (*RedCASSucceed a D F v'' v'''*)

hence [*simp*]: $I = 0\ al'' = \text{CField } D\ F\ a'' = a\ v'' = v$

and $v': v' \in vs\ (a, \text{CField } D\ F)$ **by**(*auto simp add: take-Cons' split: if-split-asm*)

from *RedCASSucceed.prem1* **obtain** $E\ T$ **where**

$P, E, shr\ s \vdash \text{addr } a \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v'', \text{Val } v''') : T$ **by** *clarify*

```

then obtain  $T$  where  $adal: P, shr\ s \vdash a@CField\ D\ F : T$ 
  and  $v'': P, shr\ s \vdash v'' \leq T$  and  $v''': P, shr\ s \vdash v''' \leq T$ 
  by( $fastforce\ intro: addr-loc-type.intros\ simp\ add: conf-def$ )
from  $v'$  vs  $adal$  have  $P, shr\ s \vdash v' \leq T$  by( $auto\ dest!: vs-confD\ dest: addr-loc-type-fun$ )
from  $hrt\ adal\ this\ hconf$  have  $read: heap-read\ (shr\ s)\ a\ (CField\ D\ F)\ v'$  by( $rule\ heap-read-typeableD$ )
show  $?case$ 
proof( $cases\ v' = v''$ )
  case  $True$ 
    then show  $?thesis$  using  $RedCASSucceed$ 
      by( $fastforce\ intro: red-reds.RedCASSucceed$ )
    next
      case  $False$ 
        then show  $?thesis$  using  $read\ RedCASSucceed$ 
          by( $fastforce\ intro: RedCASFail$ )
        qed
      next
        case ( $RedCASFail\ a\ D\ F\ v''\ v'''$ )
          hence [ $simp$ ]:  $I = 0\ al'' = CField\ D\ F\ a'' = a\ v'' = v$ 
            and  $v': v' \in vs\ (a,\ CField\ D\ F)$  by( $auto\ simp\ add: take-Cons'\ split: if-split-asm$ )
          from  $RedCASFail.premis(1)$  obtain  $E\ T$  where
             $P, E, shr\ s \vdash addr\ a.compareAndSwap(D.F,\ Val\ v'',\ Val\ v''') : T$  by( $iprover$ )
          then obtain  $T$  where  $adal: P, shr\ s \vdash a@CField\ D\ F : T$ 
            and  $v''': P, shr\ s \vdash v''' \leq T$  and  $v''': P, shr\ s \vdash v'''' \leq T$ 
            by( $fastforce\ intro: addr-loc-type.intros\ simp\ add: conf-def$ )
          from  $v'$  vs  $adal$  have  $P, shr\ s \vdash v' \leq T$  by( $auto\ dest!: vs-confD\ dest: addr-loc-type-fun$ )
          from  $hrt\ adal\ this\ hconf$  have  $read: heap-read\ (shr\ s)\ a\ (CField\ D\ F)\ v'$  by( $rule\ heap-read-typeableD$ )
          show  $?case$ 
          proof( $cases\ v' = v''''$ )
            case  $True$ 
              from  $heap-write-total[OF\ hconf\ adal\ v''']$  obtain  $h'$  where
                 $heap-write\ (shr\ s)\ a\ (CField\ D\ F)\ v''''\ h' ..$ 
              with  $read\ RedCASFail\ True$  show  $?thesis$ 
                by( $fastforce\ intro: RedCASSucceed$ )
            next
              case  $False$ 
                with  $read\ RedCASFail$  show  $?thesis$  by( $fastforce\ intro: red-reds.RedCASFail$ )
              qed
            next
              case ( $RedCallExternal\ a\ U\ M\ Ts\ Tr\ D\ ps\ ta'\ va\ h'\ ta\ e'$ )
                from  $\langle P, t \vdash \langle a.M(ps), hp\ (shr\ s,\ xs) \rangle -ta' \rightarrow ext\ \langle va, h' \rangle$ 
                have  $red: P, t \vdash \langle a.M(ps), shr\ s \rangle -ta' \rightarrow ext\ \langle va, h' \rangle$  by  $simp$ 
                from  $RedCallExternal$  have  $aok: red-mthr.mthr.if.actions-ok\ s\ t\ ta'$  by  $simp$ 
                from  $RedCallExternal$  have  $I < length\ \{ta'\}_o$ 
                  and  $\{ta'\}_o ! I = ReadMem\ a''\ al''\ v$ 
                  and  $v' \in w-values\ P\ vs\ (map\ NormalAction\ (take\ I\ \{ta'\}_o))\ (a'',\ al'')$ 
                  and  $non-speculative\ P\ vs\ (llist-of\ (map\ NormalAction\ (take\ I\ \{ta'\}_o)))$  by  $simp-all$ 
                from  $red-external-non-speculative-read[OF\ hrt\ vs\ red\ aok\ hconf\ this]$ 
                   $\langle typeof-addr\ (hp\ (shr\ s,\ xs))\ a = \lfloor U \rfloor \rangle$ 
                   $\langle P \vdash class-type-of\ U\ sees\ M: Ts \rightarrow Tr = Native\ in\ D \rangle$ 
                   $\langle ta = extTA2J\ P\ ta' \rangle$ 
                   $\langle I < length\ \{ta'\}_o \rangle$ 
                show  $?case$  by( $fastforce\ intro: red-reds.RedCallExternal$ )
              next
                case  $NewArrayRed$  thus  $?case$  by( $clarsimp\ simp\ add: split-paired-Ex\ ex-WTrt-simps$ )( $blast\ intro: red-reds.NewArrayRed$ )

```

```

next
  case CastRed thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.CastRed)
next
  case InstanceOfRed thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.InstanceOfRed)
next
  case BinOpRed1 thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.BinOpRed1)
next
  case BinOpRed2 thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.BinOpRed2)
next
  case LAssRed thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.LAssRed)
next
  case AAccRed1 thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.AAccRed1)
next
  case AAccRed2 thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.AAccRed2)
next
  case AAssRed1 thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.AAssRed1)
next
  case AAssRed2 thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.AAssRed2)
next
  case AAssRed3 thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.AAssRed3)+
next
  case ALengthRed thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.ALengthRed)
next
  case FAccRed thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.FAccRed)
next
  case FAssRed1 thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.FAssRed1)
next
  case FAssRed2 thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.FAssRed2)
next
  case CASRed1 thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.CASRed1)
next
  case CASRed2 thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.CASRed2)
next
  case CASRed3 thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.CASRed3)
next
  case CallObj thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.CallObj)

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1480

```
next
  case CallParams thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.CallParams)
next
  case BlockRed thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(fastforce intro: red-reds.BlockRed)+
next
  case SynchronizedRed1 thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.SynchronizedRed1)
next
  case SynchronizedRed2 thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.SynchronizedRed2)
next
  case SeqRed thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.SeqRed)
next
  case CondRed thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.CondRed)
next
  case ThrowRed thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.ThrowRed)
next
  case TryRed thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.TryRed)
next
  case ListRed1 thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.ListRed1)
next
  case ListRed2 thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.ListRed2)
qed(simp-all)

end
```

```
sublocale J-allocated-heap-conf < if-known-addr-base
  J-known-addr
  final-expr mred P convert-RA
.
```

```
declare split-paired-Ex [simp]
declare eq-upto-seq-inconsist-simps [simp del]
```

```
locale J-allocated-progress =
  J-progress
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write hconf
  P
+
  J-allocated-heap-conf
  addr2thread-id thread-id2addr
  spurious-wakeups
```

```

empty-heap allocate typeof-addr heap-read heap-write hconf
allocated
P
for addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool
and allocated :: 'heap ⇒ 'addr set
and P :: 'addr J-prog
begin

lemma non-speculative-read:
  assumes wf: wf-J-prog P
  and hrt: heap-read-typeable hconf P
  and wf-start: wf-start-state P C M vs
  and ka:  $\bigcup (ka\text{-Val } 'set\ vs) \subseteq set\ start\ addr$ s
  shows red-mthr.if.non-speculative-read J-non-speculative-read-bound
    (init-fin-lift-state status (J-start-state P C M vs))
    (w-values P (λ-. {})) (map snd (lift-start-obs start-tid start-heap-obs)))
  (is red-mthr.if.non-speculative-read - ?start-state ?start-vs)
proof(rule red-mthr.if.non-speculative-readI)
  fix ttas s' t x ta x' m' i ad al v v'
  assume τRed: red-mthr.mthr.if.RedT P ?start-state ttas s'
  and sc: non-speculative P ?start-vs (llist-of (concat (map (λ(t, ta). {ta}_o) ttas)))
  and ts't: thr s' t = [(x, no-wait-locks)]
  and red: red-mthr.init-fin P t (x, shr s') ta (x', m')
  and aok: red-mthr.mthr.if.actions-ok s' t ta
  and i: i < length {ta}_o
  and ns': non-speculative P (w-values P ?start-vs (concat (map (λ(t, ta). {ta}_o) ttas))) (llist-of
  (take i {ta}_o))
  and read: {ta}_o ! i = NormalAction (ReadMem ad al v)
  and v': v' ∈ w-values P ?start-vs (concat (map (λ(t, ta). {ta}_o) ttas)) @ take i {ta}_o (ad, al)

  from wf-start obtain Ts T pns body D where ok: start-heap-ok
  and sees: P ⊢ C sees M:Ts→T = [(pns, body)] in D
  and conf: P, start-heap ⊢ vs [≤] Ts by cases auto

  let ?conv = λttas. concat (map (λ(t, ta). {ta}_o) ttas)
  let ?vs' = w-values P ?start-vs (?conv ttas)
  let ?wt-ok = init-fin-lift-inv sconf-type-ok
  let ?ET-start = J-sconf-type-ET-start P C M
  let ?start-obs = map snd (lift-start-obs start-tid start-heap-obs)
  let ?start-state = init-fin-lift-state status (J-start-state P C M vs)

interpret known-addr-typing
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  allocated J-known-addr
```

```

final-expr mred P λt x h. ∃ ET. sconf-type-ok ET t x h P
using wf ok by(rule mred-known-addr-typing)

from wf sees have wf-mdecl wf-J-mdecl P D (M, Ts, T, [(pns, body)]) by(rule sees-wf-mdecl)
then obtain T' where len1: length pns = length Ts and wt: P,[this→Class D,pns [→] Ts] ⊢ body
:: T'
  by(auto simp add: wf-mdecl-def)
from conf have len2: length vs = length Ts by(rule list-all2-lengthD)

from wf wf-start have ts-ok-start: ts-ok (init-fin-lift (λt x h. ∃ ET. sconf-type-ok ET t x h)) (thr
?start-state) (shr ?start-state)
  unfolding ts-ok-init-fin-lift-init-fin-lift-state shr-start-state by(rule J-start-state-sconf-type-ok)
  have sc': non-speculative P ?start-vs (lmap snd (lconcat (lmap (λ(t, ta). llist-of (map (Pair t)
{ta}_o)) (llist-of ttas))))
  using sc by(simp add: lmap-lconcat llist.map-comp o-def split-def lconcat-llist-of[symmetric])

from start-state-vs-conf[OF wf-prog-wf-syscls[OF wf]]
have vs-conf-start: vs-conf P (shr ?start-state) ?start-vs
  by(simp add:init-fin-lift-state-conv-simps start-state-def split-beta)
with τRed ts-ok-start sc
have wt': ts-ok (init-fin-lift (λt x h. ∃ ET. sconf-type-ok ET t x h)) (thr s') (shr s')
  and vs': vs-conf P (shr s') ?vs' by(rule if-RedT-non-speculative-invar)+

from red i read obtain e xs e' xs' ta'
  where x: x = (Running, e, xs) and x': x' = (Running, e', xs')
  and ta: ta = convert-TA-initial (convert-obs-initial ta')
  and red': P,t ⊢ ⟨e, (shr s', xs)⟩ -ta'→ ⟨e', (m', xs')⟩
  by cases fastforce+

from ts't wt' x obtain E T where wte: P,E,shr s' ⊢ e : T
  and hconf: hconf (shr s')
  by(auto dest!: ts-okD simp add: sconf-type-ok-def sconf-def type-ok-def)

have aok': red-mthr.mthr.if.actions-ok s' t ta' using aok unfolding ta by simp

from i read v' ta ns' have i < length {ta'}_o and {ta'}_o ! i = ReadMem ad al v
  and v' ∈ w-values P ?vs' (map NormalAction (take i {ta'}_o)) (ad, al)
  and non-speculative P ?vs' (llist-of (map NormalAction (take i {ta'}_o)))
  by(simp-all add: take-map)

from red-non-speculative-read[OF hrt vs' hconf red' - aok' this] wte
obtain ta'' e'' xs'' h''
  where red'': P,t ⊢ ⟨e, (shr s', xs)⟩ -ta''→ ⟨e'', (h'', xs'')⟩
  and aok'': red-mthr.mthr.if.actions-ok s' t ta''
  and i'': i < length {ta''}_o
  and eq'': take i {ta''}_o = take i {ta'}_o
  and read'': {ta''}_o ! i = ReadMem ad al v'
  and len'': length {ta''}_o ≤ max J-non-speculative-read-bound (length {ta'}_o) by blast

let ?x' = (Running, e'', xs'')
let ?ta' = convert-TA-initial (convert-obs-initial ta'')
from red'' have red-mthr.init-fin P t (x, shr s') ?ta' (?x', h'')
  unfolding x by -(rule red-mthr.init-fin.NormalAction, simp)
moreover from aok'' have red-mthr.mthr.if.actions-ok s' t ?ta' by simp

```

moreover from i'' have $i < \text{length } \{\{?ta'\}_o\}$ by *simp*
moreover from eq'' have $\text{take } i \ \{\{?ta'\}_o\} = \text{take } i \ \{\{ta\}_o\}$ unfolding ta by(*simp add: take-map*)
moreover from $\text{read'' } i''$ have $\{\{?ta'\}_o\} ! i = \text{NormalAction } (\text{ReadMem } ad \ al \ v')$ by(*simp add: nth-map*)
moreover from len'' have $\text{length } \{\{?ta'\}_o\} \leq \text{max } J\text{-non-speculative-read-bound } (\text{length } \{\{ta\}_o\})$
unfolding ta by *simp*
ultimately
show $\exists ta' x'' m''. \text{red-mthr.init-fin } P \ t \ (x, \text{shr } s') \ ta' \ (x'', m'') \wedge$
 $\text{red-mthr.mthr.if.actions-ok } s' \ t \ ta' \wedge$
 $i < \text{length } \{\{ta'\}_o\} \wedge \text{take } i \ \{\{ta'\}_o\} = \text{take } i \ \{\{ta\}_o\} \wedge$
 $\{\{ta'\}_o\} ! i = \text{NormalAction } (\text{ReadMem } ad \ al \ v') \wedge$
 $\text{length } \{\{ta'\}_o\} \leq \text{max } J\text{-non-speculative-read-bound } (\text{length } \{\{ta\}_o\})$
by *blast*
qed

lemma *J-cut-and-update*:

assumes wf : $wf\text{-}J\text{-prog } P$

and hrt : $\text{heap-read-typeable } h\text{conf } P$

and $wf\text{-start}$: $wf\text{-start-state } P \ C \ M \ vs$

and ka : $\bigcup (ka\text{-Val } ' \ \text{set } vs) \subseteq \text{set start-addr}$

shows $\text{red-mthr.if.cut-and-update } (\text{init-fin-lift-state status } (J\text{-start-state } P \ C \ M \ vs))$
 $(\text{mrw-values } P \ \text{Map.empty } (\text{map snd } (\text{lift-start-obs start-tid start-heap-obs})))$

proof –

from $wf\text{-start}$ obtain $Ts \ T \ pns \ \text{body } D$ where ok : start-heap-ok

and sees : $P \vdash C \ \text{sees } M$: $Ts \rightarrow T = \lfloor (pns, \text{body}) \rfloor$ in D

and conf : $P, \text{start-heap} \vdash vs \ [:\leq] \ Ts$ by *cases auto*

interpret known-addr-typing

$\text{addr2thread-id thread-id2addr}$

spurious-wakeups

$\text{empty-heap allocate typeof-addr heap-read heap-write}$

$\text{allocated } J\text{-known-addr}$

$\text{final-expr } m\text{red } P \ \lambda t \ x \ h. \ \exists ET. \ \text{sconf-type-ok } ET \ t \ x \ h \ P$

using $wf \ ok$ by(*rule mred-known-addr-typing*)

let $?start\text{-vs} = w\text{-values } P \ (\lambda-. \ \{\}) \ (\text{map snd } (\text{lift-start-obs start-tid start-heap-obs}))$

let $?wt\text{-ok} = \text{init-fin-lift-inv sconf-type-ok}$

let $?ET\text{-start} = J\text{-sconf-type-ET-start } P \ C \ M$

let $?start\text{-obs} = \text{map snd } (\text{lift-start-obs start-tid start-heap-obs})$

let $?start\text{-state} = \text{init-fin-lift-state status } (J\text{-start-state } P \ C \ M \ vs)$

from $wf \ \text{sees}$ have $wf\text{-mdecl } wf\text{-}J\text{-mdecl } P \ D \ (M, \ Ts, \ T, \ \lfloor (pns, \text{body}) \rfloor)$ by(*rule sees-wf-mdecl*)

then obtain T' where $len1$: $\text{length } pns = \text{length } Ts$ and wt : $P, [\text{this} \mapsto \text{Class } D, pns \ [\mapsto] \ Ts] \vdash \text{body}$
:: T'

by(*auto simp add: wf-mdecl-def*)

from conf have $len2$: $\text{length } vs = \text{length } Ts$ by(*rule list-all2-lengthD*)

note $wf\text{-prog-wf-syscls}[OF \ wf] \ \text{non-speculative-read}[OF \ wf \ hrt \ wf\text{-start} \ ka]$

moreover

from $wf \ wf\text{-start}$ have $ts\text{-ok-start}$: $ts\text{-ok } (\text{init-fin-lift } (\lambda t \ x \ h. \ \exists ET. \ \text{sconf-type-ok } ET \ t \ x \ h)) \ (\text{thr } ?start\text{-state}) \ (\text{shr } ?start\text{-state})$

unfolding $ts\text{-ok-init-fin-lift-init-fin-lift-state shr-start-state}$ by(*rule J-start-state-sconf-type-ok*)

moreover

have ka : $J\text{-known-addr start-tid } ((\lambda (pns, \text{body}) \ vs. \ (\text{blocks } (\text{this } \# \ pns) \ (\text{Class } (\text{fst } (\text{method } P \ C$

$M)) \# \text{fst} (\text{snd} (\text{method } P \ C \ M))) (\text{Null} \ \# \ \text{vs}) \ \text{body}, \ \text{Map.empty})) (\text{the} (\text{snd} (\text{snd} (\text{snd} (\text{method } P \ C \ M)))))) \ \text{vs}) \subseteq \text{allocated start-heap}$
using *sees ka len1 len2 WT-ka*[OF wt]
by(*auto simp add: split-beta start-addr-allocated ka-blocks intro: start-tid-start-addr*[OF wf-prog-wf-syscls[OF wf] ok])
ultimately show *?thesis* **by**(*rule non-speculative-read-into-cut-and-update*)
qed

lemma *J-drf*:

assumes *wf: wf-J-prog P*
and *hrt: heap-read-typeable hconf P*
and *wf-start: wf-start-state P C M vs*
and *ka: $\bigcup (ka\text{-Val} \ ' \ \text{set } \text{vs}) \subseteq \text{set start-addr}$*
shows *drf (J- \mathcal{E} P C M vs status) P*

proof –

from *wf-start* **obtain** *Ts T pns body D* **where** *ok: start-heap-ok*
and *sees: P \vdash C sees M: Ts \rightarrow T = [(pns, body)] in D*
and *conf: P, start-heap \vdash vs [: \leq] Ts* **by** *cases auto*

from *J-cut-and-update*[OF *assms*] *wf-prog-wf-syscls*[OF *wf*] *J-start-state-sconf-type-ok*[OF *wf wf-start*]
show *?thesis*

proof(*rule known-addr-typing.drf*[OF *mred-known-addr-typing*[OF *wf ok*]])

from *wf sees* **have** *wf-mdecl wf-J-mdecl P D (M, Ts, T, [(pns, body)])* **by**(*rule sees-wf-mdecl*)

then obtain *T'* **where** *len1: length pns = length Ts* **and** *wt: P, [this \rightarrow Class D, pns \mapsto] Ts] \vdash body*
 $:: T'$

by(*auto simp add: wf-mdecl-def*)

from *conf* **have** *len2: length vs = length Ts* **by**(*rule list-all2-lengthD*)

show *J-known-addr start-tid (($\lambda(pns, body)$ vs. (blocks (this $\#$ pns) (Class (fst (method P C M)) $\#$ fst (snd (method P C M))) (Null $\#$ vs) body, Map.empty)) (the (snd (snd (snd (method P C M)))))) vs) \subseteq allocated start-heap*

using *sees ka len1 len2 WT-ka*[OF wt]

by(*auto simp add: split-beta start-addr-allocated ka-blocks intro: start-tid-start-addr*[OF wf-prog-wf-syscls[OF wf] ok])

qed

qed

lemma *J-sc-legal*:

assumes *wf: wf-J-prog P*
and *hrt: heap-read-typeable hconf P*
and *wf-start: wf-start-state P C M vs*
and *ka: $\bigcup (ka\text{-Val} \ ' \ \text{set } \text{vs}) \subseteq \text{set start-addr}$*
shows *sc-legal (J- \mathcal{E} P C M vs status) P*

proof –

from *wf-start* **obtain** *Ts T pns body D* **where** *ok: start-heap-ok*

and *sees: P \vdash C sees M: Ts \rightarrow T = [(pns, body)] in D*

and *conf: P, start-heap \vdash vs [: \leq] Ts* **by** *cases auto*

interpret *known-addr-typing*

addr2thread-id thread-id2addr

spurious-wakeups

empty-heap allocate typeof-addr heap-read heap-write

allocated J-known-addr

final-expr mred P $\lambda t \ x \ h. \ \exists ET. \ \text{sconf-type-ok } ET \ t \ x \ h \ P$

using *wf ok* **by**(*rule mred-known-addr-typing*)


```

let ?start-vs = w-values P (λ-. {}) (map snd (lift-start-obs start-tid start-heap-obs))
let ?wt-ok = init-fin-lift-inv sconf-type-ok
let ?ET-start = J-sconf-type-ET-start P C M
let ?start-obs = map snd (lift-start-obs start-tid start-heap-obs)
let ?start-state = init-fin-lift-state status (J-start-state P C M vs)

from wf sees have wf-mdecl wf-J-mdecl P D (M, Ts, T, [(pns, body)]) by(rule sees-wf-mdecl)
then obtain T' where len1: length pns = length Ts and wt: P,[this→Class D,pns [→] Ts] ⊢ body
:: T'
  by(auto simp add: wf-mdecl-def)
from conf have len2: length vs = length Ts by(rule list-all2-lengthD)

note wf-prog-wf-syscls[OF wf] non-speculative-read[OF wf hrt wf-start ka]
moreover
from wf wf-start have ts-ok-start: ts-ok (init-fin-lift (λ t x h. ∃ ET. sconf-type-ok ET t x h)) (thr
?start-state) (shr ?start-state)
  unfolding ts-ok-init-fin-lift-init-fin-lift-state shr-start-state by(rule J-start-state-sconf-type-ok)
  moreover have ka-allocated: J-known-addr start-tid ((λ(pns, body) vs. (blocks (this # pns) (Class
fst (method P C M)) # fst (snd (method P C M))) (Null # vs) body, Map.empty)) (the (snd (snd
(snd (method P C M)))))) vs) ⊆ allocated start-heap
  using sees ka len1 len2 WT-ka[OF wt]
  by(auto simp add: split-beta start-addr-allocated ka-blocks intro: start-tid-start-addr[OF wf-prog-wf-syscls[OF
wf] ok])
  ultimately have red-mthr.if.hb-completion ?start-state (lift-start-obs start-tid start-heap-obs)
    by(rule non-speculative-read-into-hb-completion)

thus ?thesis using wf-prog-wf-syscls[OF wf] J-start-state-sconf-type-ok[OF wf wf-start]
  by(rule sc-legal)(rule ka-allocated)
qed

lemma J-jmm-consistent:
  assumes wf: wf-J-prog P
  and hrt: heap-read-typeable hconf P
  and wf-start: wf-start-state P C M vs
  and ka: ⋃ (ka-Val ' set vs) ⊆ set start-addr
  shows jmm-consistent (J- $\mathcal{E}$  P C M vs status) P
  (is jmm-consistent ? $\mathcal{E}$  P)
proof –
  interpret drf ? $\mathcal{E}$  P using assms by(rule J-drf)
  interpret sc-legal ? $\mathcal{E}$  P using assms by(rule J-sc-legal)
  show ?thesis by unfold-locales
qed

lemma J-ex-sc-exec:
  assumes wf: wf-J-prog P
  and hrt: heap-read-typeable hconf P
  and wf-start: wf-start-state P C M vs
  and ka: ⋃ (ka-Val ' set vs) ⊆ set start-addr
  shows ∃ E ws. E ∈ J- $\mathcal{E}$  P C M vs status ∧ P ⊢ (E, ws) ✓ ∧ sequentially-consistent P (E, ws)
  (is ∃ E ws. - ∈ ? $\mathcal{E}$  ∧ -)
proof –
  interpret jmm: executions-sc-hb ? $\mathcal{E}$  P using assms by –(rule executions-sc)

let ?start-state = init-fin-lift-state status (J-start-state P C M vs)

```

```

let ?start-mrw = mrw-values P Map.empty (map snd (lift-start-obs start-tid start-heap-obs))

from red-mthr.if.sequential-completion-Runs[OF red-mthr.if.cut-and-update-imp-sc-completion[OF
J-cut-and-update[OF assms]] ta-seq-consist-convert-RA]
obtain ttas where Red: red-mthr.mthr.if.mthr.Runs P ?start-state ttas
  and sc: ta-seq-consist P ?start-mrw (lconcat (lmap ( $\lambda(t, ta)$ . llist-of  $\{\{ta\}_o\}$ ) ttas)) by blast
  let ?E = lappend (llist-of (lift-start-obs start-tid start-heap-obs)) (lconcat (lmap ( $\lambda(t, ta)$ . llist-of
(map (Pair t)  $\{\{ta\}_o\}$ ) ttas))
  from Red have ?E  $\in$  ?E by(blast intro: red-mthr.mthr.if.E.intros)
  moreover from Red have tsa: thread-start-actions-ok ?E
    by(blast intro: red-mthr.thread-start-actions-ok-init-fin red-mthr.mthr.if.E.intros)
  from sc have ta-seq-consist P Map.empty (lmap snd ?E)
    unfolding lmap-lappend-distrib lmap-lconcat llist.map-comp split-def o-def lmap-llist-of map-map
snd-conv
    by(simp add: ta-seq-consist-lappend ta-seq-consist-start-heap-obs)
  from ta-seq-consist-imp-sequentially-consistent[OF tsa jmm.E-new-actions-for-fun[OF  $\langle ?E \in ?E \rangle$ ]
this]
  obtain ws where sequentially-consistent P ( $?E, ws$ )  $P \vdash (?E, ws) \checkmark$  by iprover
  ultimately show ?thesis by blast
qed

```

theorem *J-consistent*:

```

assumes wf: wf-J-prog P
and hrt: heap-read-typeable hconf P
and wf-start: wf-start-state P C M vs
and ka:  $\bigcup (ka\text{-Val } 'set\ vs) \subseteq set\ start\ addrs$ 
shows  $\exists E\ ws.$  legal-execution P (J-E P C M vs status) (E, ws)

```

proof –

```

let ?E = J-E P C M vs status
interpret sc-legal ?E P using assms by(rule J-sc-legal)
from J-ex-sc-exec[OF assms]
obtain E ws where E  $\in$  ?E  $P \vdash (E, ws) \checkmark$  sequentially-consistent P (E, ws) by blast
hence legal-execution P ?E (E, ws) by(rule SC-is-legal)
thus ?thesis by blast

```

qed

end

end

theory JMM-JVM

imports

```

JMM-Framework
../JVM/JVMThreaded

```

begin

sublocale JVM-heap-base < execd-mthr:

```

heap-multithreaded-base
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  JVM-final mexecd P convert-RA

```

for P

.

context *JVM-heap-base* **begin**

abbreviation *JVMd- \mathcal{E}* ::

'addr jvm-prog \Rightarrow *cname* \Rightarrow *mname* \Rightarrow *'addr val list* \Rightarrow *status*
 \Rightarrow (*'thread-id* \times (*'addr*, *'thread-id*) *obs-event action*) *llist set*

where *JVMd- \mathcal{E}* *P* \equiv *execd-mthr. \mathcal{E} -start P JVM-local-start P*

end

end

8.15 JMM Instantiation for bytecode

theory *DRF-JVM*

imports

JMM-Common

JMM-JVM

../BV/BVProgressThreaded

SC-Legal

begin

8.15.1 DRF guarantee for the JVM

abbreviation (*input*) *ka-xcp* :: *'addr option* \Rightarrow *'addr set*

where *ka-xcp* \equiv *set-option*

primrec *jvm-ka* :: *'addr jvm-thread-state* \Rightarrow *'addr set*

where

jvm-ka (*xcp*, *frs*) =

ka-xcp xcp \cup (\bigcup (*stk*, *loc*, *C*, *M*, *pc*) \in *set frs*. (\bigcup *v* \in *set stk*. *ka-Val v*) \cup (\bigcup *v* \in *set loc*. *ka-Val v*))

context *heap* **begin**

lemma *red-external-aggr-read-mem-typeable*:

$\llbracket (ta, va, h') \in \text{red-external-aggr } P \ t \ a \ M \ \text{vs } h; \text{ ReadMem } ad \ al \ v \in \text{set } \{\{ta\}_o \} \rrbracket$

$\implies \exists T'. P, h \vdash ad@al : T'$

by(*auto simp add: red-external-aggr-def split-beta split: if-split-asm dest: heap-clone-read-typeable*)

end

context *JVM-heap-base* **begin**

definition *jvm-known-addr*s :: *'thread-id* \Rightarrow *'addr jvm-thread-state* \Rightarrow *'addr set*

where *jvm-known-addr*s *t xcpfrs* = $\{ \text{thread-id}2\text{addr } t \} \cup \text{jvm-ka } xcpfrs \cup \text{set start-addr}$ s

end

context *JVM-heap* **begin**

lemma *exec-instr-known-addr*s:

assumes *ok: start-heap-ok*

and *exec: (ta, xcp', h', frs') \in exec-instr i P t h stk loc C M pc frs*

and *check: check-instr i P h stk loc C M pc frs*

shows $jvm-known-addr\ s\ t\ (xcp',\ frs') \subseteq jvm-known-addr\ s\ t\ (None,\ (stk,\ loc,\ C,\ M,\ pc)\ \#\ frs) \cup new-obs-addr\ s\ \{ta\}_o$

proof –

note $[simp] = jvm-known-addr\ s\ def\ new-obs-addr\ s\ def\ addr-of-sys-xcpt-start-addr[OF\ ok]\ subset-Un1\ subset-Un2\ subset-insert\ ka-Val\ subset-new-obs-Addr-ReadMem\ SUP-subset-mono\ split-beta\ neq-Nil-conv\ tl-conv-drop\ set-drop-subset\ is-Ref-def$

from *exec check show ?thesis*

proof (*cases i*)

case *Load with exec check show ?thesis by auto*

next

case (*Store V with exec check show ?thesis*)

using *set-update-subset-insert[of loc V]*

by (*clarsimp simp del: set-update-subsetI*) *blast*

next

case (*Push v*)

with *check have ka-Val v = {} by(cases v) simp-all*

with *Push exec check show ?thesis by(simp)*

next

case (*CAS F D*)

then *show ?thesis using exec check*

by (*clarsimp split: if-split-asm*)(*fastforce dest!: in-set-dropD*)**+**

next

case (*Invoke M' n*)

show *?thesis*

proof (*cases stk ! n = Null*)

case *True with exec check Invoke show ?thesis by(simp)*

next

case $[simp]:\ False$

with *check Invoke obtain a where stkn: stk ! n = Addr a n < length stk by auto*

hence $a: a \in (\bigcup v \in set\ stk.\ ka-Val\ v)$ **by** (*fastforce dest: nth-mem*)

show *?thesis*

proof (*cases snd (snd (snd (method P (class-type-of (the (typeof-addr h (the-Addr (stk ! n)))) M')) = Native)*)

case *True*

with *exec check Invoke a stkn show ?thesis*

apply *clarsimp*

apply (*drule red-external-aggr-known-addr\ s\ mono[OF ok], simp*)

apply (*auto dest!: in-set-takeD dest: bspec subsetD split: extCallRet.split-asm simp add:*

has-method-def is-native.simps)

done

next

case *False*

with *exec check Invoke a stkn show ?thesis*

by (*auto simp add: set-replicate-conv-if dest!: in-set-takeD*)

qed

qed

next

case *Swap with exec check show ?thesis*

by (*cases stk*)(*simp, case-tac list, auto*)

next

case (*BinOpInstr bop with exec check show ?thesis*)

using *binop-known-addr\ s\ [OF ok, of bop hd (drop (Suc 0) stk) hd stk]*

```

    apply(cases stk)
    apply(simp, case-tac list, simp)
    apply clarsimp
    apply(drule (2) binop-progress)
    apply(auto 6 2 split: sum.split-asm)
  done
next
  case MExit with exec check show ?thesis by(auto split: if-split-asm)
qed(clarsimp split: if-split-asm)+
qed

lemma exec-d-known-addr-mono:
  assumes ok: start-heap-ok
  and exec: mexecd P t (xcpfrs, h) ta (xcpfrs', h')
  shows jvm-known-addr t xcpfrs'  $\subseteq$  jvm-known-addr t xcpfrs  $\cup$  new-obs-addr  $\{ta\}_o$ 
using exec
apply(cases xcpfrs)
apply(cases xcpfrs')
apply(simp add: split-beta)
apply(erule jvmd-NormalE)
apply(cases fst xcpfrs)
  apply(fastforce simp add: check-def split-beta del: subsetI dest!: exec-instr-known-addr[OF ok])
  apply(fastforce simp add: jvm-known-addr-def split-beta dest!: in-set-dropD)
done

lemma exec-instr-known-addr-ReadMem:
  assumes exec: (ta, xcp', h', frs')  $\in$  exec-instr i P t h stk loc C M pc frs
  and check: check-instr i P h stk loc C M pc frs
  and read: ReadMem ad al v  $\in$  set  $\{ta\}_o$ 
  shows ad  $\in$  jvm-known-addr t (None, (stk, loc, C, M, pc) # frs)
using assms
proof(cases i)
  case ALoad thus ?thesis using assms
    by(cases stk)(case-tac [2] list, auto simp add: split-beta is-Ref-def jvm-known-addr-def split:
if-split-asm)
next
  case (Invoke M n)
  with check have stk ! n  $\neq$  Null  $\longrightarrow$  the-Addr (stk ! n)  $\in$  ka-Val (stk ! n) stk ! n  $\in$  set stk
    by(auto simp add: is-Ref-def)
  with assms Invoke show ?thesis
    by(auto simp add: split-beta is-Ref-def simp del: ka-Val.simps nth-mem split: if-split-asm dest!:
red-external-aggr-known-addr-ReadMem in-set-takeD del: is-AddrE)(auto simp add: jvm-known-addr-def
simp del: ka-Val.simps nth-mem del: is-AddrE)
next
  case Getfield thus ?thesis using assms
    by(auto simp add: jvm-known-addr-def neq-Nil-conv is-Ref-def split: if-split-asm)
next
  case CAS thus ?thesis using assms
    apply(cases stk; simp)
    subgoal for v stk
      apply(cases stk; simp)
      subgoal for v stk
        by(cases stk)(auto split: if-split-asm simp add: jvm-known-addr-def is-Ref-def)
    done

```

done
qed(*auto simp add: split-beta is-Ref-def neq-Nil-conv split: if-split-asm*)

lemma *mexecd-known-addr-ReadMem:*

$\llbracket \text{mexecd } P \ t \ (xcpfrs, h) \ ta \ (xcpfrs', h'); \text{ReadMem } ad \ al \ v \in \text{set } \{ta\}_o \rrbracket$
 $\implies ad \in \text{jvm-known-addr } t \ xcpfrs$

apply(*cases xcpfrs*)
apply(*cases xcpfrs'*)
apply *simp*
apply(*erule jvmd-NormalE*)
apply(*cases fst xcpfrs*)
apply(*auto simp add: check-def dest: exec-instr-known-addr-ReadMem*)
done

lemma *exec-instr-known-addr-WriteMem:*

assumes *exec: (ta, xcp', h', frs') \in exec-instr i P t h stk loc C M pc frs*

and *check: check-instr i P h stk loc C M pc frs*

and *write: \{ta\}_o ! n = WriteMem ad al (Addr a) n < length \{ta\}_o*

shows $a \in \text{jvm-known-addr } t \ (\text{None}, (stk, loc, C, M, pc) \# frs) \vee a \in \text{new-obs-addr } (\text{take } n \ \{ta\}_o)$

using *assms*

proof(*cases i*)

case (*Invoke M n*)

with *check* **have** $stk ! n \neq \text{Null} \longrightarrow \text{the-Addr } (stk ! n) \in \text{ka-Val } (stk ! n) \ \text{stk} ! n \in \text{set } stk$

by(*auto simp add: is-Ref-def*)

thus *?thesis* **using** *assms Invoke*

by(*auto simp add: is-Ref-def split-beta split: if-split-asm simp del: ka-Val.simps nth-mem dest!: red-external-aggr-known-addr-WriteMem in-set-takeD del: is-AddrE*)(*auto simp add: jvm-known-addr-def del: is-AddrE*)

next

case *AStore* **with** *assms* **show** *?thesis*

by(*cases stk*)(*auto simp add: jvm-known-addr-def split: if-split-asm*)

next

case *Putfield* **with** *assms* **show** *?thesis*

by(*cases stk*)(*auto simp add: jvm-known-addr-def split: if-split-asm*)

next

case *CAS* **with** *assms* **show** *?thesis*

apply(*cases stk; simp*)

subgoal **for** $v \ \text{stk}$

apply(*cases stk; simp*)

subgoal **for** $v \ \text{stk}$

by(*cases stk*)(*auto split: if-split-asm simp add: take-Cons' jvm-known-addr-def*)

done

done

qed(*auto simp add: split-beta split: if-split-asm*)

lemma *mexecd-known-addr-WriteMem:*

$\llbracket \text{mexecd } P \ t \ (xcpfrs, h) \ ta \ (xcpfrs', h'); \ \{ta\}_o ! n = \text{WriteMem } ad \ al \ (\text{Addr } a); \ n < \text{length } \{ta\}_o \rrbracket$
 $\implies a \in \text{jvm-known-addr } t \ xcpfrs \vee a \in \text{new-obs-addr } (\text{take } n \ \{ta\}_o)$

apply(*cases xcpfrs*)

apply(*cases xcpfrs'*)

apply *simp*

apply(*erule jvmd-NormalE*)

apply(*cases fst xcpfrs*)

apply(*auto simp add: check-def dest: exec-instr-known-addr-WriteMem*)

done

lemma *exec-instr-known-addr-new-thread*:

assumes *exec*: $(ta, xcp', h', frs') \in \text{exec-instr } i \ P \ t \ h \ \text{stk} \ \text{loc} \ C \ M \ \text{pc} \ \text{frs}$

and *check*: *check-instr* $i \ P \ h \ \text{stk} \ \text{loc} \ C \ M \ \text{pc} \ \text{frs}$

and *new*: $\text{NewThread } t' \ x' \ h'' \in \text{set } \{ta\}_t$

shows *jvm-known-addr* $t' \ x' \subseteq \text{jvm-known-addr} \ t \ (\text{None}, (\text{stk}, \text{loc}, C, M, \text{pc}) \# \text{frs})$

using *assms*

proof(*cases i*)

case (*Invoke M n*)

with *assms* **have** $\text{stk} \ ! \ n \neq \text{Null} \longrightarrow \text{the-Addr } (\text{stk} \ ! \ n) \in \text{ka-Val } (\text{stk} \ ! \ n) \wedge \text{thread-id2addr} \ (\text{addr2thread-id } (\text{the-Addr } (\text{stk} \ ! \ n))) = \text{the-Addr } (\text{stk} \ ! \ n) \ \text{stk} \ ! \ n \in \text{set } \text{stk}$

apply(*auto simp add: is-Ref-def split: if-split-asm*)

apply(*frule red-external-aggr-NewThread-idD, simp, simp*)

apply(*drule red-external-aggr-new-thread-sub-thread*)

apply(*auto intro: addr2thread-id-inverse*)

done

with *assms Invoke* **show** *?thesis*

apply(*auto simp add: is-Ref-def split-beta split: if-split-asm simp del: nth-mem del: is-AddrE*)

apply(*drule red-external-aggr-NewThread-idD*)

apply(*auto simp add: extNTA2JVM-def jvm-known-addr-def split-beta simp del: nth-mem del: is-AddrE*)

done

qed(*auto simp add: split-beta split: if-split-asm*)

lemma *mexecd-known-addr-new-thread*:

$\llbracket \text{mexecd } P \ t \ (xcpfrs, h) \ ta \ (xcpfrs', h'); \text{NewThread } t' \ x' \ h'' \in \text{set } \{ta\}_t \rrbracket$

$\implies \text{jvm-known-addr} \ t' \ x' \subseteq \text{jvm-known-addr} \ t \ xcpfrs$

apply(*cases xcpfrs*)

apply(*cases xcpfrs'*)

apply *simp*

apply(*erule jvmd-NormalE*)

apply(*cases fst xcpfrs*)

apply(*auto 4 3 simp add: check-def dest: exec-instr-known-addr-new-thread*)

done

lemma *exec-instr-New-same-addr-same*:

$\llbracket (ta, xcp', h', frs') \in \text{exec-instr } \text{ins} \ P \ t \ h \ \text{stk} \ \text{loc} \ C \ M \ \text{pc} \ \text{frs};$

$\{ta\}_o \ ! \ i = \text{NewHeapElem } a \ x; \ i < \text{length } \{ta\}_o;$

$\{ta\}_o \ ! \ j = \text{NewHeapElem } a \ x'; \ j < \text{length } \{ta\}_o \rrbracket$

$\implies i = j$

apply(*cases ins*)

apply(*auto simp add: nth-Cons' split: prod.split-asm if-split-asm*)

apply(*auto split: extCallRet.split-asm dest: red-external-aggr-New-same-addr-same*)

done

lemma *exec-New-same-addr-same*:

$\llbracket (ta, xcp', h', frs') \in \text{exec } P \ t \ (xcp, h, frs);$

$\{ta\}_o \ ! \ i = \text{NewHeapElem } a \ x; \ i < \text{length } \{ta\}_o;$

$\{ta\}_o \ ! \ j = \text{NewHeapElem } a \ x'; \ j < \text{length } \{ta\}_o \rrbracket$

$\implies i = j$

apply(*cases (P, t, xcp, h, frs) rule: exec.cases*)

apply(*auto dest: exec-instr-New-same-addr-same*)

done

lemma *exec-1-d-New-same-addr-same*:

```

[[ P, t ⊢ Normal (xcp, h, frs) -ta-jvmd→ Normal (xcp', h', frs');
   {ta}_o ! i = NewHeapElem a x; i < length {ta}_o;
   {ta}_o ! j = NewHeapElem a x'; j < length {ta}_o ]
⇒ i = j

```

by(*erule jvmd-NormalE*)(*rule exec-New-same-addr-same*)

end

locale *JVM-allocated-heap* = *allocated-heap* +

```

constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and allocated :: 'heap ⇒ 'addr set
and P :: 'addr jvm-prog

```

sublocale *JVM-allocated-heap* < *JVM-heap*

by(*unfold-locales*)

context *JVM-allocated-heap* **begin**

lemma *exec-instr-allocated-mono*:

```

[[ (ta, xcp', h', frs') ∈ exec-instr i P t h stk loc C M pc frs; check-instr i P h stk loc C M pc frs ]
⇒ allocated h ⊆ allocated h'

```

apply(*cases i*)

apply(*auto 4 4 simp add: split-beta has-method-def is-native.simps split: if-split-asm sum.split-asm*
intro: allocate-allocated-mono dest: heap-write-allocated-same dest!: red-external-aggr-allocated-mono
del: subsetI)

done

lemma *mexecd-allocated-mono*:

```

mexecd P t (xcpfrs, h) ta (xcpfrs', h') ⇒ allocated h ⊆ allocated h'

```

apply(*cases xcpfrs*)

apply(*cases xcpfrs'*)

apply(*simp*)

apply(*erule jvmd-NormalE*)

apply(*cases fst xcpfrs*)

apply(*auto del: subsetI simp add: check-def dest: exec-instr-allocated-mono*)

done

lemma *exec-instr-allocatedD*:

```

[[ (ta, xcp', h', frs') ∈ exec-instr i P t h stk loc C M pc frs;
   check-instr i P h stk loc C M pc frs; NewHeapElem ad CTn ∈ set {ta}_o ]
⇒ ad ∈ allocated h' ∧ ad ∉ allocated h

```

apply(*cases i*)

apply(*auto 4 4 split: if-split-asm prod.split-asm dest: allocate-allocatedD dest!: red-external-aggr-allocatedD*)

simp add: has-method-def is-native.simps)
done

lemma *mexecd-allocatedD:*

$\llbracket \text{mexecd } P \ t \ (xcpfrs, h) \ ta \ (xcpfrs', h'); \text{NewHeapElem } ad \ CTn \in \text{set } \{ta\}_o \rrbracket$
 $\implies ad \in \text{allocated } h' \wedge ad \notin \text{allocated } h$

apply(*cases xcpfrs*)

apply(*cases xcpfrs'*)

apply(*simp*)

apply(*erule jvmd-NormalE*)

apply(*cases fst xcpfrs*)

apply(*auto del: subsetI dest: exec-instr-allocatedD simp add: check-def*)

done

lemma *exec-instr-NewHeapElemD:*

$\llbracket (ta, xcp', h', frs') \in \text{exec-instr } i \ P \ t \ h \ stk \ loc \ C \ M \ pc \ frs; \text{check-instr } i \ P \ h \ stk \ loc \ C \ M \ pc \ frs;$
 $ad \in \text{allocated } h'; ad \notin \text{allocated } h \rrbracket$
 $\implies \exists CTn. \text{NewHeapElem } ad \ CTn \in \text{set } \{ta\}_o$

apply(*cases i*)

apply(*auto 4 3 split: if-split-asm prod.split-asm sum.split-asm dest: allocate-allocatedD heap-write-allocated-same dest!: red-external-aggr-NewHeapElemD simp add: is-native.simps has-method-def*)

done

lemma *mexecd-NewHeapElemD:*

$\llbracket \text{mexecd } P \ t \ (xcpfrs, h) \ ta \ (xcpfrs', h'); ad \in \text{allocated } h'; ad \notin \text{allocated } h \rrbracket$
 $\implies \exists CTn. \text{NewHeapElem } ad \ CTn \in \text{set } \{ta\}_o$

apply(*cases xcpfrs*)

apply(*cases xcpfrs'*)

apply(*simp*)

apply(*erule jvmd-NormalE*)

apply(*cases fst xcpfrs*)

apply(*auto dest: exec-instr-NewHeapElemD simp add: check-def*)

done

lemma *mexecd-allocated-multithreaded:*

allocated-multithreaded addr2thread-id thread-id2addr empty-heap allocate typeof-addr heap-write allocated JVM-final (mexecd P) P

proof

fix *t x m ta x' m'*

assume *mexecd P t (x, m) ta (x', m')*

thus *allocated m \subseteq allocated m'* **by**(*rule mexecd-allocated-mono*)

next

fix *x t m ta x' m' ad CTn*

assume *mexecd P t (x, m) ta (x', m')*

and *NewHeapElem ad CTn \in set {ta}_o*

thus *ad \in allocated m' \wedge ad \notin allocated m* **by**(*rule mexecd-allocatedD*)

next

fix *t x m ta x' m' ad*

assume *mexecd P t (x, m) ta (x', m')*

and *ad \in allocated m' ad \notin allocated m*

thus $\exists CTn. \text{NewHeapElem } ad \ CTn \in \text{set } \{ta\}_o$ **by**(*rule mexecd-NewHeapElemD*)

next

fix *t x m ta x' m' i a CTn j CTn'*

assume *mexecd P t (x, m) ta (x', m')*

```

    and  $\{ta\}_o ! i = \text{NewHeapElem } a \text{ CTn } i < \text{length } \{ta\}_o$ 
    and  $\{ta\}_o ! j = \text{NewHeapElem } a \text{ CTn}' j < \text{length } \{ta\}_o$ 
    thus  $i = j$  by(auto dest: exec-1-d-New-same-addr-same simp add: split-beta)
qed

```

end

```

sublocale JVM-allocated-heap < execd-mthr: allocated-multithreaded
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write allocated
  JVM-final mexecd P
  P
by(rule mexecd-allocated-multithreaded)

```

context JVM-allocated-heap **begin**

lemma *mexecd-known-addr*:

```

  assumes wf: wf-prog wfmd P
  and ok: start-heap-ok

```

```

  shows known-addr addr2thread-id thread-id2addr empty-heap allocate typeof-addr heap-write allocated
  jvm-known-addr JVM-final (mexecd P) P

```

proof

```

  fix  $t \ x \ m \ ta \ x' \ m'$ 
  assume mexecd P t (x, m) ta (x', m')
  thus jvm-known-addr  $t \ x' \subseteq \text{jvm-known-addr } t \ x \cup \text{new-obs-addr } \{ta\}_o$ 
  by(rule exec-d-known-addr-mono[OF ok])

```

next

```

  fix  $t \ x \ m \ ta \ x' \ m' \ t' \ x'' \ m''$ 
  assume mexecd P t (x, m) ta (x', m')
  and NewThread t' x'' m'' ∈ set {ta}_t
  thus jvm-known-addr  $t' \ x'' \subseteq \text{jvm-known-addr } t \ x$  by(rule mexecd-known-addr-new-thread)

```

next

```

  fix  $t \ x \ m \ ta \ x' \ m' \ ad \ al \ v$ 
  assume mexecd P t (x, m) ta (x', m')
  and ReadMem ad al v ∈ set {ta}_o
  thus  $ad \in \text{jvm-known-addr } t \ x$  by(rule mexecd-known-addr-ReadMem)

```

next

```

  fix  $t \ x \ m \ ta \ x' \ m' \ n \ ad \ al \ ad'$ 
  assume mexecd P t (x, m) ta (x', m')
  and  $\{ta\}_o ! n = \text{WriteMem } ad \ al \ (\text{Addr } ad') \ n < \text{length } \{ta\}_o$ 
  thus  $ad' \in \text{jvm-known-addr } t \ x \vee ad' \in \text{new-obs-addr } (\text{take } n \ \{ta\}_o)$ 
  by(rule mexecd-known-addr-WriteMem)

```

qed

end

context JVM-heap **begin**

lemma *exec-instr-read-typeable*:

```

  assumes exec: (ta, xcp', h', frs') ∈ exec-instr i P t h stk loc C M pc frs
  and check: check-instr i P h stk loc C M pc frs
  and read: ReadMem ad al v ∈ set {ta}_o
  shows  $\exists T'. P, h \vdash ad@al : T'$ 

```

```

using exec check read
proof(cases i)
  case ALoad
    with assms show ?thesis
      by(fastforce simp add: split-beta is-Ref-def nat-less-iff word-sless-alt intro: addr-loc-type.intros split: if-split-asm)
    next
      case (Getfield F D)
        with assms show ?thesis
          by(clarsimp simp add: split-beta is-Ref-def split: if-split-asm)(blast intro: addr-loc-type.intros dest: has-visible-field has-field-mono)
        next
          case (Invoke M n)
            with exec check read obtain a vs ta' va T
              where (ta', va, h')  $\in$  red-external-aggr P t a M vs h
              and ReadMem ad al v  $\in$  set  $\{\{ta'\}_o\}$ 
              by(auto split: if-split-asm simp add: is-Ref-def)
            thus ?thesis by(rule red-external-aggr-read-mem-typeable)
          next
            case (CAS F D)
              with assms show ?thesis
                by(clarsimp simp add: split-beta is-Ref-def conf-def split: if-split-asm)
                (force intro: addr-loc-type.intros dest: has-visible-field[THEN has-field-mono])
            qed(auto simp add: split-beta is-Ref-def split: if-split-asm)

lemma exec-1-d-read-typeable:
   $\llbracket P, t \vdash \text{Normal} (xcp, h, frs) \text{---}ta\text{---}jvmd \rightarrow \text{Normal} (xcp', h', frs');$ 
   $\text{ReadMem } ad \text{ } al \text{ } v \in \text{set } \{\{ta'\}_o\}$ 
   $\implies \exists T'. P, h \vdash ad@al : T'$ 
apply(erule jvmd-NormalE)
apply(cases (P, t, xcp, h, frs) rule: exec.cases)
apply(auto intro: exec-instr-read-typeable simp add: check-def)
done

end

sublocale JVM-heap-base < execd-mthr:
  if-multithreaded
  JVM-final
  mexecd P
  convert-RA
for P
by(unfold-locales)

context JVM-heap-conf begin

lemma JVM-conf-read-heap-read-typed:
  JVM-conf-read addr2thread-id thread-id2addr empty-heap allocate typeof-addr (heap-read-typed P)
  heap-write hconf P
proof –
  interpret conf: heap-conf-read
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read-typed P heap-write hconf

```

```

  P
  by(rule heap-conf-read-heap-read-typed)
  show ?thesis by(unfold-locales)
qed

```

lemma *exec-instr-New-typeof-addrD*:

```

[[ (ta, xcp', h', frs') ∈ exec-instr i P t h stk loc C M pc frs;
   check-instr i P h stk loc C M pc frs; hconf h;
   NewHeapElem a x ∈ set {ta}_o ]]
⇒ typeof-addr h' a = Some x

```

```

apply(cases i)

```

```

apply(auto dest: allocate-SomeD split: prod.split-asm if-split-asm)

```

```

apply(auto 4 4 split: extCallRet.split-asm dest!: red-external-aggr-New-typeof-addrD simp add: has-method-def
is-native.simps)

```

```

done

```

lemma *exec-1-d-New-typeof-addrD*:

```

[[ P,t ⊢ Normal (xcp, h, frs) -ta-jvmd→ Normal (xcp', h', frs'); NewHeapElem a x ∈ set {ta}_o;
   hconf h ]]
⇒ typeof-addr h' a = Some x

```

```

apply(erule jvmd-NormalE)

```

```

apply(cases xcp)

```

```

apply(auto dest: exec-instr-New-typeof-addrD simp add: check-def)

```

```

done

```

lemma *exec-instr-non-speculative-typeable*:

```

assumes exec: (ta, xcp', h', frs') ∈ exec-instr i P t h stk loc C M pc frs

```

```

and check: check-instr i P h stk loc C M pc frs

```

```

and sc: non-speculative P vs (llist-of (map NormalAction {ta}_o))

```

```

and vs-conf: vs-conf P h vs

```

```

and hconf: hconf h

```

```

shows (ta, xcp', h', frs') ∈ JVM-heap-base.exec-instr addr2thread-id thread-id2addr spurious-wakeups
empty-heap allocate typeof-addr (heap-read-typed P) heap-write i P t h stk loc C M pc frs

```

```

proof -

```

```

note [simp] = JVM-heap-base.exec-instr.simps

```

```

and [split] = if-split-asm prod.split-asm sum.split-asm

```

```

and [split del] = if-split

```

```

from assms show ?thesis

```

```

proof(cases i)

```

```

case ALoad with assms show ?thesis

```

```

by(auto 4 3 intro!: heap-read-typedI dest: vs-confD addr-loc-type-fun)

```

```

next

```

```

case Getfield with assms show ?thesis

```

```

by(auto 4 3 intro!: heap-read-typedI dest: vs-confD addr-loc-type-fun)

```

```

next

```

```

case CAS with assms show ?thesis

```

```

by(auto 4 3 intro!: heap-read-typedI dest: vs-confD addr-loc-type-fun)

```

```

next

```

```

case Invoke with assms show ?thesis

```

```

by(fastforce dest: red-external-aggr-non-speculative-typeable simp add: has-method-def is-native.simps)

```

```

qed(auto)

```

```

qed

```

lemma *exec-instr-non-speculative-vs-conf*:

```

assumes exec: (ta, xcp', h', frs') ∈ exec-instr i P t h stk loc C M pc frs
and check: check-instr i P h stk loc C M pc frs
and sc: non-speculative P vs (llist-of (take n (map NormalAction {ta}o)))
and vs-conf: vs-conf P h vs
and hconf: hconf h
shows vs-conf P h' (w-values P vs (take n (map NormalAction {ta}o)))
proof –
note [simp] = JVM-heap-base.exec-instr.simps take-Cons'
and [split] = if-split-asm prod.split-asm sum.split-asm
and [split del] = if-split
from assms show ?thesis
proof(cases i)
  case New with assms show ?thesis
    by(auto 4 4 dest: hext-allocate vs-conf-allocate intro: vs-conf-hext)
  next
  case NewArray with assms show ?thesis
    by(auto 4 4 dest: hext-allocate vs-conf-allocate intro: vs-conf-hext cong: if-cong)
  next
  case Invoke with assms show ?thesis
    by(fastforce dest: red-external-aggr-non-speculative-vs-conf simp add: has-method-def is-native.simps)
  next
  case AStore
  {
    assume hd (tl (tl stk)) ≠ Null
    and ¬ the-Intg (hd (tl stk)) < s 0
    and ¬ int (alen-of-htype (the (typeof-addr h (the-Addr (hd (tl (tl stk))))))) ≤ sint (the-Intg (hd (tl stk)))
    and P ⊢ the (typeofh (hd stk)) ≤ the-Array (ty-of-htype (the (typeof-addr h (the-Addr (hd (tl (tl stk)))))))
    moreover hence nat (sint (the-Intg (hd (tl stk)))) < alen-of-htype (the (typeof-addr h (the-Addr (hd (tl (tl stk)))))))
    by(auto simp add: not-le nat-less-iff word-sle-eq word-sless-eq not-less)
    with assms AStore have nat (sint (the-Intg (hd (tl stk)))) < alen-of-htype (the (typeof-addr h' (the-Addr (hd (tl (tl stk)))))))
    by(auto dest!: hext-arrD hext-heap-write)
    ultimately have ∃ T. P, h' ⊢ the-Addr (hd (tl (tl stk)))@ACell (nat (sint (the-Intg (hd (tl stk))))))
  : T ∧ P, h' ⊢ hd stk :≤ T
    using assms AStore
    by(auto 4 4 simp add: is-Ref-def conf-def dest!: hext-heap-write dest: hext-arrD intro!: addr-loc-type.intros
intro: typeof-addr-hext-mono type-of-hext-type-of) }
    thus ?thesis using assms AStore
    by(auto intro!: vs-confI)(blast intro: addr-loc-type-hext-mono conf-hext dest: hext-heap-write
vs-confD)+
  next
  case Putfield
  show ?thesis using assms Putfield
  by(auto intro!: vs-confI dest!: hext-heap-write)(blast intro: addr-loc-type.intros addr-loc-type-hext-mono
typeof-addr-hext-mono has-field-mono[OF has-visible-field] conf-hext dest: vs-confD)+
  next
  case CAS
  show ?thesis using assms CAS
  by(auto intro!: vs-confI dest!: hext-heap-write)(blast intro: addr-loc-type.intros addr-loc-type-hext-mono
typeof-addr-hext-mono has-field-mono[OF has-visible-field] conf-hext dest: vs-confD)+
  qed(auto)

```

qed

lemma *mexecd-non-speculative-typeable*:

$\llbracket P, t \vdash \text{Normal } (xcp, h, stk) -ta-jvmd \rightarrow \text{Normal } (xcp', h', frs'); \text{ non-speculative } P \text{ vs } (\text{llist-of } (\text{map } \text{NormalAction } \{ta\}_o));$
 $\text{vs-conf } P \text{ h vs; hconf h } \rrbracket$

$\implies \text{JVM-heap-base.exec-1-d addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr (heap-read-typed } P) \text{ heap-write } P \text{ t } (\text{Normal } (xcp, h, stk)) \text{ ta } (\text{Normal } (xcp', h', frs'))$

apply(*erule jvmd-NormalE*)

apply(*cases xcp*)

apply(*auto intro!*: *JVM-heap-base.exec-1-d.intros simp add: JVM-heap-base.exec-d-def check-def JVM-heap-base.ex*
intro: exec-instr-non-speculative-typeable)

done

lemma *mexecd-non-speculative-vs-conf*:

$\llbracket P, t \vdash \text{Normal } (xcp, h, stk) -ta-jvmd \rightarrow \text{Normal } (xcp', h', frs');$
 $\text{non-speculative } P \text{ vs } (\text{llist-of } (\text{take } n \text{ (map NormalAction } \{ta\}_o)));$
 $\text{vs-conf } P \text{ h vs; hconf h } \rrbracket$

$\implies \text{vs-conf } P \text{ h' (w-values } P \text{ vs } (\text{take } n \text{ (map NormalAction } \{ta\}_o)))$

apply(*erule jvmd-NormalE*)

apply(*cases xcp*)

apply(*auto intro!*: *JVM-heap-base.exec-1-d.intros simp add: JVM-heap-base.exec-d-def check-def JVM-heap-base.ex*
intro: exec-instr-non-speculative-vs-conf)

done

end

locale *JVM-allocated-heap-conf* =

JVM-heap-conf
addr2thread-id thread-id2addr
spurious-wakeups
empty-heap allocate typeof-addr heap-read heap-write hconf
P

+

JVM-allocated-heap
addr2thread-id thread-id2addr
spurious-wakeups
empty-heap allocate typeof-addr heap-read heap-write
allocated
P

for *addr2thread-id* :: ('*addr* :: *addr*) \Rightarrow '*thread-id*

and *thread-id2addr* :: '*thread-id* \Rightarrow '*addr*

and *spurious-wakeups* :: *bool*

and *empty-heap* :: '*heap*

and *allocate* :: '*heap* \Rightarrow *htype* \Rightarrow ('*heap* \times '*addr*) *set*

and *typeof-addr* :: '*heap* \Rightarrow '*addr* \rightarrow *htype*

and *heap-read* :: '*heap* \Rightarrow '*addr* \Rightarrow *addr-loc* \Rightarrow '*addr val* \Rightarrow *bool*

and *heap-write* :: '*heap* \Rightarrow '*addr* \Rightarrow *addr-loc* \Rightarrow '*addr val* \Rightarrow '*heap* \Rightarrow *bool*

and *hconf* :: '*heap* \Rightarrow *bool*

and *allocated* :: '*heap* \Rightarrow '*addr set*

and *P* :: '*addr jvm-prog*

begin

lemma *mexecd-known-addr-typing*:

assumes $wf: wf\text{-jvm-prog}_{\Phi} P$
and $ok: start\text{-heap-ok}$
shows $known\text{-addrs-typing } addr2thread\text{-id } thread\text{-id}2addr \text{ empty-heap } allocate \text{ typeof-addr } heap\text{-write}$
 $allocated \text{ jvm-known-addrs } JVM\text{-final } (mexecd P) (\lambda t (xcp, frstls) h. \Phi \vdash t: (xcp, h, frstls) \checkmark) P$
proof –
from wf **obtain** $wf\text{-md}$ **where** $wf\text{-prog } wf\text{-md } P$ **by**(blast dest: wt-jvm-progD)
then
interpret $known\text{-addrs}$
 $addr2thread\text{-id } thread\text{-id}2addr$
 $spurious\text{-wakeups}$
 $empty\text{-heap } allocate \text{ typeof-addr } heap\text{-read } heap\text{-write}$
 $allocated \text{ jvm-known-addrs}$
 $JVM\text{-final } mexecd P P$
using ok **by**(rule mexecd-known-addrs)

show $?thesis$
proof
fix $t x m ta x' m'$
assume $mexecd P t (x, m) ta (x', m')$
thus $m \sqsubseteq m'$ **by**(auto simp add: split-beta intro: exec-1-d-heat)
next
fix $t x m ta x' m' vs$
assume $exec: mexecd P t (x, m) ta (x', m')$
and $ts\text{-ok}: (\lambda(xcp, frstls) h. \Phi \vdash t:(xcp, h, frstls) \checkmark) x m$
and $vs: vs\text{-conf } P m vs$
and $ns: non\text{-speculative } P vs (l\text{list-of } (map \text{ NormalAction } \{ta\}_o))$

let $?mexecd = JVM\text{-heap-base.mexecd } addr2thread\text{-id } thread\text{-id}2addr \text{ spurious-wakeups } empty\text{-heap}$
 $allocate \text{ typeof-addr } (heap\text{-read-typed } P) \text{ heap-write } P$
have $lift: lifting\text{-wf } JVM\text{-final } ?mexecd (\lambda t (xcp, frstls) h. \Phi \vdash t: (xcp, h, frstls) \checkmark)$
by(intro JVM-conf-read.lifting-wf-correct-state-d JVM-conf-read-heap-read-typed wf)

from $exec ns vs ts\text{-ok}$ **have** $exec': ?mexecd t (x, m) ta (x', m')$
by(auto simp add: split-beta correct-state-def dest: mexecd-non-speculative-typeable)
thus $(\lambda(xcp, frstls) h. \Phi \vdash t:(xcp, h, frstls) \checkmark) x' m'$ **using** $ts\text{-ok}$
by(rule lifting-wf.preserves-red[OF lift])
{
fix $t'' x'' m''$
assume $New: NewThread t'' x'' m'' \in set \{ta\}_t$
with $exec$ **have** $m'' = snd (x', m')$ **by**(rule execd-mthr.new-thread-memory)
thus $(\lambda(xcp, frstls) h. \Phi \vdash t'':(xcp, h, frstls) \checkmark) x'' m''$
using $lifting\text{-wf.preserves-NewThread}[\text{where } ?r=?mexecd, OF lift \text{ exec}' ts\text{-ok}] New$
by auto }
{ **fix** $t'' x''$
assume $(\lambda(xcp, frstls) h. \Phi \vdash t'':(xcp, h, frstls) \checkmark) x'' m$
with $lift \text{ exec}' ts\text{-ok}$ **show** $(\lambda(xcp, frstls) h. \Phi \vdash t'':(xcp, h, frstls) \checkmark) x'' m'$
by(rule lifting-wf.preserves-other) }

next
fix $t x m ta x' m' vs n$
assume $exec: mexecd P t (x, m) ta (x', m')$
and $ts\text{-ok}: (\lambda(xcp, frstls) h. \Phi \vdash t:(xcp, h, frstls) \checkmark) x m$
and $vs: vs\text{-conf } P m vs$
and $ns: non\text{-speculative } P vs (l\text{list-of } (take n (map \text{ NormalAction } \{ta\}_o)))$
thus $vs\text{-conf } P m' (w\text{-values } P vs (take n (map \text{ NormalAction } \{ta\}_o)))$

```

  by(auto simp add: correct-state-def dest: mexecd-non-speculative-vs-conf)
next
fix t x m ta x' m' ad al v
assume mexecd P t (x, m) ta (x', m')
  and  $(\lambda(xcp, frstls) h. \Phi \vdash t:(xcp, h, frstls) \checkmark) x m$ 
  and ReadMem ad al v  $\in set \{ta\}_o$ 
thus  $\exists T. P, m \vdash ad@al : T$ 
  by(auto simp add: correct-state-def split-beta dest: exec-1-d-read-typeable)
next
fix t x m ta x' m' ad hT
assume mexecd P t (x, m) ta (x', m')
  and  $(\lambda(xcp, frstls) h. \Phi \vdash t:(xcp, h, frstls) \checkmark) x m$ 
  and NewHeapElem ad hT  $\in set \{ta\}_o$ 
thus typeof-addr m' ad =  $\lfloor hT \rfloor$ 
  by(auto dest: exec-1-d-New-typeof-addrD[where x=hT] simp add: split-beta correct-state-def)
qed
qed

lemma executions-sc:
  assumes wf: wf-jvm-prog $\Phi$  P
  and wf-start: wf-start-state P C M vs
  and vs2:  $\bigcup (ka-Val \text{ ' set vs} \subseteq set \text{ start-addr}$ 
  shows executions-sc-hb (JVMd- $\mathcal{E}$  P C M vs status) P
  (is executions-sc-hb ?E P)
proof -
  from wf-start obtain Ts T meth D where ok: start-heap-ok
  and sees:  $P \vdash C \text{ sees } M:Ts \rightarrow T = \lfloor meth \rfloor$  in D
  and vs1:  $P, start\text{-heap} \vdash vs \text{ [:}\leq\rfloor Ts$  by cases

  interpret known-addr-typing
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  allocated jvm-known-addr
  JVM-final mexecd P  $\lambda t (xcp, frstls) h. \Phi \vdash t: (xcp, h, frstls) \checkmark$  P
  using wf ok by(rule mexecd-known-addr-typing)

  from wf obtain wf-md where wf': wf-prog wf-md P by(blast dest: wt-jvm-progD)
  hence wf-syscls P by(rule wf-prog-wf-syscls)
  thus ?thesis
  proof(rule executions-sc-hb)
    from correct-jvm-state-initial[OF wf wf-start]
    show correct-state-ts  $\Phi$  (thr (JVM-start-state P C M vs)) start-heap
    by(simp add: correct-jvm-state-def start-state-def split-beta)
  next
    show jvm-known-addr start-tid (( $\lambda(mxs, mxl0, b) vs. (None, [([], Null \# vs @ replicate mxl0$ 
    undefined-value, fst (method P C M), M, 0))) (the (snd (snd (snd (method P C M)))))) vs)  $\subseteq allocated$ 
    start-heap
    using vs2
    by(auto simp add: split-beta start-addr-allocated jvm-known-addr-def intro: start-tid-start-addr[OF
     $\langle wf\text{-syscls } P \rangle ok]$ )
  qed
qed

```


end

declare *split-paired-Ex* [*simp del*]

declare *eq-upto-seq-inconsist-simps* [*simp*]

context *JVM-progress* begin

abbreviation (*input*) *jvm-non-speculative-read-bound* :: nat where

jvm-non-speculative-read-bound \equiv 2

lemma *exec-instr-non-speculative-read*:

assumes *hrt*: *heap-read-typeable hconf P*

and *vs*: *vs-conf P (shr s) vs*

and *hconf*: *hconf (shr s)*

and *exec-i*: $(ta, xcp', h', frs') \in \text{exec-instr } i \ P \ t \ (shr \ s) \ stk \ loc \ C \ M \ pc \ frs$

and *check*: *check-instr i P (shr s) stk loc C M pc frs*

and *aok*: *execd-mthr.mthr.if.actions-ok s t ta*

and *i*: $I < \text{length } \{ta\}_o$

and *read*: $\{ta\}_o ! I = \text{ReadMem } a'' \ al'' \ v$

and *v'*: $v' \in w\text{-values } P \ vs \ (\text{map } \text{NormalAction} \ (\text{take } I \ \{ta\}_o)) \ (a'', \ al'')$

and *ns*: *non-speculative P vs (llist-of (map NormalAction (take I {ta}_o)))*

shows $\exists ta' \ xcp'' \ h'' \ frs''. (ta', xcp'', h'', frs'') \in \text{exec-instr } i \ P \ t \ (shr \ s) \ stk \ loc \ C \ M \ pc \ frs \wedge$

execd-mthr.mthr.if.actions-ok s t ta' \wedge

$I < \text{length } \{ta'\}_o \wedge \text{take } I \ \{ta'\}_o = \text{take } I \ \{ta\}_o \wedge$

$\{ta'\}_o ! I = \text{ReadMem } a'' \ al'' \ v' \wedge$

$\text{length } \{ta'\}_o \leq \max \text{jvm-non-speculative-read-bound} \ (\text{length } \{ta\}_o)$

using *exec-i i read*

proof(*cases i*)

case [*simp*]: *ALoad*

let *?a* = *the-Addr (hd (tl stk))*

let *?i* = *the-Intg (hd stk)*

from *exec-i i read* have *Null*: *hd (tl stk) \neq Null*

and *bounds*: $0 \leq s \ ?i \ \text{sint } ?i < \text{int} \ (\text{alen-of-htype} \ (\text{the} \ (\text{typeof-addr} \ (shr \ s) \ ?a)))$

and [*simp*]: $I = 0 \ a'' = ?a \ al'' = \text{ACell} \ (\text{nat} \ (\text{sint } ?i))$

by(*auto split: if-split-asm*)

from *Null check* obtain *a T n*

where *a*: $\text{length } stk > 1 \ \text{hd} \ (tl \ stk) = \text{Addr } a$

and *type*: $\text{typeof-addr} \ (shr \ s) \ ?a = \lfloor \text{Array-type } T \ n \rfloor$ by(*fastforce simp add: is-Ref-def*)

from *bounds type* have *nat (sint ?i) < n*

by (*simp add: word-sle-eq nat-less-iff*)

with *type* have *adal*: $P, shr \ s \vdash ?a @ \text{ACell} \ (\text{nat} \ (\text{sint } ?i)) : T$

by(*rule addr-loc-type.intros*)

from *v' vs adal* have $P, shr \ s \vdash v' : \leq T$ by(*auto dest!: vs-confD dest: addr-loc-type-fun*)

with *hrt adal* have *heap-read (shr s) ?a (ACell (nat (sint ?i))) v' using hconf by (rule heap-read-typeableD)*

with *type Null aok exec-i* show *?thesis* apply *auto using bounds by fastforce+*

next

case [*simp*]: (*Getfield F D*)

let *?a* = *the-Addr (hd stk)*

from *exec-i i read* have *Null*: *hd stk \neq Null*

and [*simp*]: $I = 0 \ a'' = ?a \ al'' = \text{CField } D \ F$

by(*auto split: if-split-asm*)

with *check* obtain *U T fm C' a*

```

where sees:  $P \vdash D \text{ sees } F:T (fm) \text{ in } D$ 
and type:  $\text{typeof-addr } (shr\ s) \ ?a = \lfloor U \rfloor$ 
and sub:  $P \vdash \text{class-type-of } U \preceq^* D$ 
and a:  $hd\ stk = \text{Addr } a \text{ length } stk > 0$  by(auto simp add: is-Ref-def)
from has-visible-field[OF sees] sub
have  $P \vdash \text{class-type-of } U \text{ has } F:T (fm) \text{ in } D$  by(rule has-field-mono)
with type have  $adal: P, shr\ s \vdash ?a @ CField\ D\ F : T$ 
by(rule addr-loc-type.intros)
from  $v' \text{ vs } adal$  have  $P, shr\ s \vdash v' : \leq T$  by(auto dest!: vs-confD dest: addr-loc-type-fun)
with hrt adal have heap-read (shr s)  $?a$  (CField D F)  $v'$  using hconf by(rule heap-read-typeableD)
with type Null aok exec-i show ?thesis by(fastforce)
next
case [simp]: (CAS F D)
let  $?a = \text{the-Addr } (hd\ (tl\ (tl\ stk)))$ 

from exec-i i read have Null: hd (tl (tl stk))  $\neq$  Null
and [simp]:  $I = 0 \ a'' = ?a \ a'' = CField\ D\ F$ 
by(auto split: if-split-asm simp add: nth-Cons')
with check obtain  $U\ T\ fm\ C'\ a$ 
where sees:  $P \vdash D \text{ sees } F:T (fm) \text{ in } D$ 
and type:  $\text{typeof-addr } (shr\ s) \ ?a = \lfloor U \rfloor$ 
and sub:  $P \vdash \text{class-type-of } U \preceq^* D$ 
and a:  $hd\ (tl\ (tl\ stk)) = \text{Addr } a \text{ length } stk > 2$ 
and v:  $P, shr\ s \vdash hd\ stk : \leq T$ 
by(auto simp add: is-Ref-def)
from has-visible-field[OF sees] sub
have  $P \vdash \text{class-type-of } U \text{ has } F:T (fm) \text{ in } D$  by(rule has-field-mono)
with type have  $adal: P, shr\ s \vdash ?a @ CField\ D\ F : T$ 
by(rule addr-loc-type.intros)
from  $v' \text{ vs } adal$  have  $P, shr\ s \vdash v' : \leq T$  by(auto dest!: vs-confD dest: addr-loc-type-fun)
with hrt adal have read: heap-read (shr s)  $?a$  (CField D F)  $v'$  using hconf by(rule heap-read-typeableD)
show ?thesis
proof(cases v' = hd (tl stk))
case True
from heap-write-total[OF hconf adal v]  $a$  obtain  $h'$ 
where heap-write (shr s)  $a$  (CField D F) ( $hd\ stk$ )  $h'$  by auto
then show ?thesis using read a True aok exec-i by fastforce
next
case False
then show ?thesis using read a aok exec-i
by(fastforce intro!: disjI2)
qed
next
case [simp]: (Invoke M n)
let  $?a = \text{the-Addr } (stk\ !\ n)$ 
let  $?vs = rev\ (take\ n\ stk)$ 
from exec-i i read have Null: stk ! n  $\neq$  Null
and iec: snd (snd (snd (method P (class-type-of (the (typeof-addr (shr s) ?a))) M))) = Native
by(auto split: if-split-asm)
with check obtain  $a\ T\ Ts\ Tr\ D$ 
where  $a: stk\ !\ n = \text{Addr } a \ n < \text{length } stk$ 
and type:  $\text{typeof-addr } (shr\ s) \ ?a = \lfloor T \rfloor$ 
and extwt: P  $\vdash$  class-type-of T sees M:Ts $\rightarrow$ Tr = Native in D D.M(Ts) :: Tr
by(auto simp add: is-Ref-def has-method-def)

```

```

from extwt have native: is-native P T M by(auto simp add: is-native.simps)
from Null iec type exec-i obtain ta' va
  where red: (ta', va, h') ∈ red-external-aggr P t ?a M ?vs (shr s)
  and ta: ta = extTA2JVM P ta' by(fastforce)
from aok ta have aok': execd-mthr.mthr.if.actions-ok s t ta' by simp
from red-external-aggr-non-speculative-read[OF hrt vs red[unfolded a the-Addr.simps] - aok' hconf,
of I a'' al'' v v']
  native type i read v' ns a ta
obtain ta'' va'' h''
  where (ta'', va'', h'') ∈ red-external-aggr P t a M (rev (take n stk)) (shr s)
  and execd-mthr.mthr.if.actions-ok s t ta''
  and I < length {ta''}_o take I {ta''}_o = take I {ta'}_o
  and {ta''}_o ! I = ReadMem a'' al'' v' length {ta''}_o ≤ length {ta'}_o by auto
thus ?thesis using Null iec ta extwt a type
  by(cases va'') force+
qed(auto simp add: split-beta split: if-split-asm)

```

lemma exec-1-d-non-speculative-read:

```

assumes hrt: heap-read-typeable hconf P
and vs: vs-conf P (shr s) vs
and exec: P,t ⊢ Normal (xcp, shr s, frs) -ta-jvmd→ Normal (xcp', h', frs')
and aok: execd-mthr.mthr.if.actions-ok s t ta
and hconf: hconf (shr s)
and i: I < length {ta}_o
and read: {ta}_o ! I = ReadMem a'' al'' v
and v': v' ∈ w-values P vs (map NormalAction (take I {ta}_o)) (a'', al'')
and ns: non-speculative P vs (llist-of (map NormalAction (take I {ta}_o)))
shows ∃ ta' xcp'' h'' frs''. P,t ⊢ Normal (xcp, shr s, frs) -ta'-jvmd→ Normal (xcp'', h'', frs'') ∧
  execd-mthr.mthr.if.actions-ok s t ta' ∧
  I < length {ta'}_o ∧ take I {ta'}_o = take I {ta}_o ∧
  {ta'}_o ! I = ReadMem a'' al'' v' ∧
  length {ta'}_o ≤ max jvm-non-speculative-read-bound (length {ta}_o)

```

using assms

apply -

apply(erule jvmd-NormalE)

apply(cases (P, t, xcp, shr s, frs) rule: exec.cases)

apply simp

defer

apply simp

apply clarsimp

apply(drule (3) exec-instr-non-speculative-read)

apply(clarsimp simp add: check-def has-method-def)

apply simp

apply(rule i)

apply(rule read)

apply(rule v')

apply(rule ns)

apply(clarsimp simp add: exec-1-d.simps exec-d-def)

done

end

declare split-paired-Ex [simp]

declare eq-upto-seq-inconsist-simps [simp del]

```

locale JVM-allocated-progress =
  JVM-progress
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write hconf
  P
+
  JVM-allocated-heap-conf
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write hconf
  allocated
  P
for addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool
and allocated :: 'heap ⇒ 'addr set
and P :: 'addr jvm-prog
begin

lemma non-speculative-read:
  assumes wf: wf-jvm-prog ⊕ P
  and hrt: heap-read-typeable hconf P
  and wf-start: wf-start-state P C M vs
  and ka:  $\bigcup (ka-Val \text{ ' } set \text{ vs}) \subseteq set \text{ start-addr}$ 
  shows execd-mthr.if.non-speculative-read jvm-non-speculative-read-bound
    (init-fin-lift-state status (JVM-start-state P C M vs))
    (w-values P (λ-. { }) (map snd (lift-start-obs start-tid start-heap-obs)))
    (is execd-mthr.if.non-speculative-read - ?start-state ?start-vs)
proof(rule execd-mthr.if.non-speculative-readI)
  fix ttas s' t x ta x' m' i ad al v v'

  assume  $\tau Red$ : execd-mthr.mthr.if.RedT P ?start-state ttas s'
  and sc: non-speculative P ?start-vs (llist-of (concat (map (λ(t, ta). {ta}_o) ttas)))
  and ts't: thr s' t = [(x, no-wait-locks)]
  and red: execd-mthr.init-fin P t (x, shr s') ta (x', m')
  and aok: execd-mthr.mthr.if.actions-ok s' t ta
  and i: i < length {ta}_o
  and ns': non-speculative P (w-values P ?start-vs (concat (map (λ(t, ta). {ta}_o) ttas))) (llist-of
    (take i {ta}_o))
  and read:  $\{ta\}_o ! i = NormalAction (ReadMem ad al v)$ 
  and v': v' ∈ w-values P ?start-vs (concat (map (λ(t, ta). {ta}_o) ttas) @ take i {ta}_o) (ad, al)

from wf-start obtain Ts T meth D where ok: start-heap-ok
  and sees:  $P \vdash C \text{ sees } M:Ts \rightarrow T = \lfloor meth \rfloor \text{ in } D$ 
  and conf:  $P, start-heap \vdash vs \lfloor \leq \rfloor Ts$  by cases

```

```

let ?conv = λttas. concat (map (λ(t, ta). {ta}_o) ttas)
let ?vs' = w-values P ?start-vs (?conv ttas)
let ?wt-ok = init-fin-lift (λt (xcp, frstls) h. Φ ⊢ t: (xcp, h, frstls) √)
let ?start-obs = map snd (lift-start-obs start-tid start-heap-obs)

from wf obtain wf-md where wf': wf-prog wf-md P by(blast dest: wt-jvm-progD)

interpret known-addr-typing
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  allocated jvm-known-addr
  JVM-final mexecd P λt (xcp, frstls) h. Φ ⊢ t: (xcp, h, frstls) √
using wf ok by(rule mexecd-known-addr-typing)

from conf have len2: length vs = length Ts by(rule list-all2-lengthD)

from correct-jvm-state-initial[OF wf wf-start]
have correct-state-ts Φ (thr (JVM-start-state P C M vs)) start-heap
  by(simp add: correct-jvm-state-def start-state-def split-beta)
hence ts-ok-start: ts-ok ?wt-ok (thr ?start-state) (shr ?start-state)
  unfolding ts-ok-init-fin-lift-init-fin-lift-state by(simp add: start-state-def split-beta)

  have sc': non-speculative P ?start-vs (lmap snd (lconcat (lmap (λ(t, ta). llist-of (map (Pair t)
    {ta}_o)) (llist-of ttas))))
    using sc by(simp add: lmap-lconcat llist.map-comp o-def split-def lconcat-llist-of[symmetric])
from start-state-vs-conf[OF wf-prog-wf-syscls[OF wf]]
have vs-conf-start: vs-conf P (shr ?start-state) ?start-vs
  by(simp add:init-fin-lift-state-conv-simps start-state-def split-beta)
with τRed ts-ok-start sc
have wt': ts-ok ?wt-ok (thr s') (shr s')
  and vs': vs-conf P (shr s') ?vs' by(rule if-RedT-non-speculative-invar)+

from red i read obtain xcp frs xcp' frs' ta'
  where x: x = (Running, xcp, frs) and x': x' = (Running, xcp', frs')
  and ta: ta = convert-TA-initial (convert-obs-initial ta')
  and red': P,t ⊢ Normal (xcp, shr s', frs) -ta'-jvmd→ Normal (xcp', m', frs')
  by cases fastforce+

from ts't wt' x have hconf: hconf (shr s') by(auto dest!: ts-okD simp add: correct-state-def)

have aok': execd-mthr.mthr.if.actions-ok s' t ta' using aok unfolding ta by simp

from i read v' ns' ta have i < length {ta}'_o
  and {ta}'_o ! i = ReadMem ad al v
  and v' ∈ w-values P ?vs' (map NormalAction (take i {ta}'_o)) (ad, al)
  and non-speculative P ?vs' (llist-of (map NormalAction (take i {ta}'_o)))
  by(simp-all add: take-map)

from exec-1-d-non-speculative-read[OF hrt vs' red' aok' hconf this]
obtain ta'' xcp'' frs'' h''
  where red'': P,t ⊢ Normal (xcp, shr s', frs) -ta''-jvmd→ Normal (xcp'', h'', frs'')
  and aok'': execd-mthr.mthr.if.actions-ok s' t ta''
  and i'': i < length {ta}''_o

```

and eq'' : $take\ i\ \{\!\{ta''\}\!\}_o = take\ i\ \{\!\{ta'\}\!\}_o$
and $read''$: $\{\!\{ta''\}\!\}_o !\ i = ReadMem\ ad\ al\ v'$
and len'' : $length\ \{\!\{ta''\}\!\}_o \leq max\ jvm\ non\ speculative\ read\ bound\ (length\ \{\!\{ta'\}\!\}_o)$ **by** *blast*

let $?x' = (Running, xcp'', frs')$
let $?ta' = convert\ TA\ initial\ (convert\ obs\ initial\ ta'')$
from red'' **have** $execd\ mthr.\ init\ fin\ P\ t\ (x, shr\ s')\ ?ta'\ (?x', h'')$
unfolding x **by** $-(rule\ execd\ mthr.\ init\ fin.\ NormalAction, simp)$
moreover from aok'' **have** $execd\ mthr.\ mthr.\ if.\ actions\ ok\ s'\ t\ ?ta'$ **by** *simp*
moreover from i'' **have** $i < length\ \{\!\{?ta''\}\!\}_o$ **by** *simp*
moreover from eq'' **have** $take\ i\ \{\!\{?ta''\}\!\}_o = take\ i\ \{\!\{ta'\}\!\}_o$ **unfolding** ta **by** $(simp\ add:\ take\ map)$
moreover from $read''\ i''$ **have** $\{\!\{?ta''\}\!\}_o !\ i = NormalAction\ (ReadMem\ ad\ al\ v')$ **by** $(simp\ add:\ nth\ map)$
moreover from len'' **have** $length\ \{\!\{?ta''\}\!\}_o \leq max\ jvm\ non\ speculative\ read\ bound\ (length\ \{\!\{ta'\}\!\}_o)$
unfolding ta **by** *simp*
ultimately
show $\exists\ ta'\ x'' m''.\ execd\ mthr.\ init\ fin\ P\ t\ (x, shr\ s')\ ta'\ (x'', m'') \wedge$
 $execd\ mthr.\ mthr.\ if.\ actions\ ok\ s'\ t\ ta' \wedge$
 $i < length\ \{\!\{ta'\}\!\}_o \wedge take\ i\ \{\!\{ta'\}\!\}_o = take\ i\ \{\!\{ta'\}\!\}_o \wedge$
 $\{\!\{ta'\}\!\}_o !\ i = NormalAction\ (ReadMem\ ad\ al\ v') \wedge$
 $length\ \{\!\{ta'\}\!\}_o \leq max\ jvm\ non\ speculative\ read\ bound\ (length\ \{\!\{ta'\}\!\}_o)$
by *blast*
qed

lemma *JVM-cut-and-update*:

assumes wf : $wf\ jvm\ prog_{\Phi}\ P$
and hrt : $heap\ read\ typeable\ hconf\ P$
and $wf\ start$: $wf\ start\ state\ P\ C\ M\ vs$
and ka : $\bigcup (ka\ Val\ 'set\ vs) \subseteq set\ start\ adrs$
shows $execd\ mthr.\ if.\ cut\ and\ update\ (init\ fin\ lift\ state\ status\ (JVM\ start\ state\ P\ C\ M\ vs))$
 $(mrw\ values\ P\ Map.\ empty\ (map\ snd\ (lift\ start\ obs\ start\ tid\ start\ heap\ obs)))$

proof –

from $wf\ start$ **obtain** $Ts\ T\ meth\ D$ **where** ok : $start\ heap\ ok$
and $sees$: $P \vdash C\ sees\ M:Ts \rightarrow T = \lfloor meth \rfloor$ **in** D
and $conf$: $P, start\ heap \vdash vs\ [:\leq]\ Ts$ **by** *cases*

interpret *known-addr-typing*

$addr2thread\ id\ thread\ id2addr$
 $spurious\ wakeups$
 $empty\ heap\ allocate\ typeof\ addr\ heap\ read\ heap\ write$
 $allocated\ jvm\ known\ adrs$
 $JVM\ final\ mexecd\ P\ \lambda t\ (xcp, frstls)\ h.\ \Phi \vdash t:\ (xcp, h, frstls)\ \checkmark$
using $wf\ ok$ **by** $(rule\ mexecd\ known\ adrs\ typing)$

let $?start\ vs = w\ values\ P\ (\lambda-. \{\})\ (map\ snd\ (lift\ start\ obs\ start\ tid\ start\ heap\ obs))$

let $?wt\ ok = init\ fin\ lift\ (\lambda t\ (xcp, frstls)\ h.\ \Phi \vdash t:\ (xcp, h, frstls)\ \checkmark)$

let $?start\ obs = map\ snd\ (lift\ start\ obs\ start\ tid\ start\ heap\ obs)$

let $?start\ state = init\ fin\ lift\ state\ status\ (JVM\ start\ state\ P\ C\ M\ vs)$

from wf **obtain** $wf\ md$ **where** wf' : $wf\ prog\ wf\ md\ P$ **by** $(blast\ dest:\ wt\ jvm\ progD)$

hence $wf\ syscls\ P$ **by** $(rule\ wf\ prog\ wf\ syscls)$

moreover

note $non\ speculative\ read[OF\ wf\ hrt\ wf\ start\ ka]$

moreover have $ts\ ok\ ?wt\ ok\ (thr\ ?start\ state)\ (shr\ ?start\ state)$

using *correct-jvm-state-initial*[*OF wf wf-start*]
by(*simp add: correct-jvm-state-def start-state-def split-beta*)
moreover have *ka: jvm-known-addr start-tid ((λ(mxs, mxl0, b) vs. (None, [[], Null # vs @ replicate mxl0 undefined-value, fst (method P C M), M, 0]))) (the (snd (snd (snd (method P C M)))) vs) ⊆ allocated start-heap*
using *ka by(auto simp add: split-beta start-addr-allocated jvm-known-addr-def intro: start-tid-start-addr[OF ‹wf-syscls P› ok])*
ultimately show *?thesis by(rule non-speculative-read-into-cut-and-update)*
qed

lemma *JVM-drf:*

assumes *wf: wf-jvm-prog_Φ P*
and *hrt: heap-read-typeable hconf P*
and *wf-start: wf-start-state P C M vs*
and *ka: ⋃(ka-Val ‘ set vs) ⊆ set start-addr*
shows *drf (JVMd- \mathcal{E} P C M vs status) P*

proof –

from *wf-start obtain Ts T meth D where ok: start-heap-ok*
and *sees: P ⊢ C sees M:Ts→T = [meth] in D*
and *conf: P,start-heap ⊢ vs [⋆] Ts by cases*

from *wf obtain wf-md where wf’: wf-prog wf-md P by(blast dest: wt-jvm-progD)*

hence *wf-syscls P by(rule wf-prog-wf-syscls)*

with *JVM-cut-and-update[OF assms]*

show *?thesis*

proof(*rule known-addr-typing.drf[OF mexecd-known-addr-typing[OF wf ok]]*)

from *correct-jvm-state-initial[OF wf wf-start]*

show *correct-state-ts Φ (thr (JVM-start-state P C M vs)) start-heap*

by(*simp add: correct-jvm-state-def start-state-def split-beta*)

next

show *jvm-known-addr start-tid ((λ(mxs, mxl0, b) vs. (None, [[], Null # vs @ replicate mxl0 undefined-value, fst (method P C M), M, 0]))) (the (snd (snd (snd (method P C M)))) vs) ⊆ allocated start-heap*

using *ka by(auto simp add: split-beta start-addr-allocated jvm-known-addr-def intro: start-tid-start-addr[OF ‹wf-syscls P› ok])*

qed

qed

lemma *JVM-sc-legal:*

assumes *wf: wf-jvm-prog_Φ P*

and *hrt: heap-read-typeable hconf P*

and *wf-start: wf-start-state P C M vs*

and *ka: ⋃(ka-Val ‘ set vs) ⊆ set start-addr*

shows *sc-legal (JVMd- \mathcal{E} P C M vs status) P*

proof –

from *wf-start obtain Ts T meth D where ok: start-heap-ok*

and *sees: P ⊢ C sees M:Ts→T = [meth] in D*

and *conf: P,start-heap ⊢ vs [⋆] Ts by cases*

interpret *known-addr-typing*

addr2thread-id thread-id2addr

spurious-wakeups

empty-heap allocate typeof-addr heap-read heap-write

allocated jvm-known-addr

```

    JVM-final mexecd P λt (xcp, frstls) h. Φ ⊢ t: (xcp, h, frstls) √
    using wf ok by(rule mexecd-known-addr-tying)

from wf obtain wf-md where wf!: wf-prog wf-md P by(blast dest: wt-jvm-progD)
hence wf-syscls P by(rule wf-prog-wf-syscls)

let ?start-vs = w-values P (λ-. {}) (map snd (lift-start-obs start-tid start-heap-obs))
let ?wt-ok = init-fin-lift (λt (xcp, frstls) h. Φ ⊢ t: (xcp, h, frstls) √)
let ?start-obs = map snd (lift-start-obs start-tid start-heap-obs)
let ?start-state = init-fin-lift-state status (JVM-start-state P C M vs)

note ⟨wf-syscls P⟩ non-speculative-read[OF wf hrt wf-start ka]
moreover have ts-ok ?wt-ok (thr ?start-state) (shr ?start-state)
  using correct-jvm-state-initial[OF wf wf-start]
  by(simp add: correct-jvm-state-def start-state-def split-beta)
moreover
have ka-allocated: jvm-known-addr start-tid ((λ(mxs, mxl0, b) vs. (None, [[], Null # vs @ replicate
mxl0 undefined-value, fst (method P C M), M, 0]))) (the (snd (snd (snd (method P C M)))) vs) ⊆
allocated start-heap
  using ka by(auto simp add: split-beta start-addr-allocated jvm-known-addr-def intro: start-tid-start-addr[OF
⟨wf-syscls P⟩ ok])
  ultimately have execd-mthr.if.hb-completion ?start-state (lift-start-obs start-tid start-heap-obs)
    by(rule non-speculative-read-into-hb-completion)

thus ?thesis using ⟨wf-syscls P⟩
proof(rule sc-legal)
  from correct-jvm-state-initial[OF wf wf-start]
  show correct-state-ts Φ (thr (JVM-start-state P C M vs)) start-heap
    by(simp add: correct-jvm-state-def start-state-def split-beta)
  qed(rule ka-allocated)
qed

lemma JVM-jmm-consistent:
  assumes wf: wf-jvm-progΦ P
  and hrt: heap-read-typeable hconf P
  and wf-start: wf-start-state P C M vs
  and ka: ⋃ (ka-Val ‘ set vs) ⊆ set start-addr
  shows jmm-consistent (JVMd- $\mathcal{E}$  P C M vs status) P
    (is jmm-consistent ? $\mathcal{E}$  P)
proof –
  interpret drf ? $\mathcal{E}$  P using assms by(rule JVM-drf)
  interpret sc-legal ? $\mathcal{E}$  P using assms by(rule JVM-sc-legal)
  show ?thesis by unfold-locales
qed

lemma JVM-ex-sc-exec:
  assumes wf: wf-jvm-progΦ P
  and hrt: heap-read-typeable hconf P
  and wf-start: wf-start-state P C M vs
  and ka: ⋃ (ka-Val ‘ set vs) ⊆ set start-addr
  shows ∃ E ws. E ∈ JVMd- $\mathcal{E}$  P C M vs status ∧ P ⊢ (E, ws) √ ∧ sequentially-consistent P (E, ws)
    (is ∃ E ws. - ∈ ? $\mathcal{E}$  ∧ -)
proof –
  interpret jmm: executions-sc-hb ? $\mathcal{E}$  P using assms by –(rule executions-sc)

```



```

let ?start-state = init-fin-lift-state status (JVM-start-state P C M vs)
let ?start-mrw = mrw-values P Map.empty (map snd (lift-start-obs start-tid start-heap-obs))

from execd-mthr.if.sequential-completion-Runs[OF execd-mthr.if.cut-and-update-imp-sc-completion[OF
JVM-cut-and-update[OF assms]] ta-seq-consist-convert-RA]
obtain ttas where Red: execd-mthr.mthr.if.mthr.Runs P ?start-state ttas
  and sc: ta-seq-consist P ?start-mrw (lconcat (lmap (λ(t, ta). llist-of {ta}_o) ttas)) by blast
let ?E = lappend (llist-of (lift-start-obs start-tid start-heap-obs)) (lconcat (lmap (λ(t, ta). llist-of
(map (Pair t) {ta}_o) ttas))
from Red have ?E ∈ ?E by (blast intro: execd-mthr.mthr.if.E.intros)
moreover from Red have tsa: thread-start-actions-ok ?E
  by (blast intro: execd-mthr.thread-start-actions-ok-init-fin execd-mthr.mthr.if.E.intros)
from sc have ta-seq-consist P Map.empty (lmap snd ?E)
  unfolding lmap-lappend-distrib lmap-lconcat llist.map-comp split-def o-def lmap-llist-of map-map
snd-conv
  by (simp add: ta-seq-consist-lappend ta-seq-consist-start-heap-obs)
from ta-seq-consist-imp-sequentially-consistent[OF tsa jmm.E-new-actions-for-fun[OF ⟨?E ∈ ?E⟩
this]
obtain ws where sequentially-consistent P (?E, ws) P ⊢ (?E, ws) ✓ by iprover
ultimately show ?thesis by blast
qed

```

theorem *JVM-consistent*:

```

assumes wf: wf-jvm-progΦ P
and hrt: heap-read-typeable hconf P
and wf-start: wf-start-state P C M vs
and ka: ⋃(ka-Val ‘ set vs) ⊆ set start-addr
shows ∃ E ws. legal-execution P (JVMd- $\mathcal{E}$  P C M vs status) (E, ws)

```

proof –

```

let ?E = JVMd- $\mathcal{E}$  P C M vs status
interpret sc-legal ?E P using assms by (rule JVM-sc-legal)
from JVM-ex-sc-exec[OF assms]
obtain E ws where E ∈ ?E P ⊢ (E, ws) ✓ sequentially-consistent P (E, ws) by blast
hence legal-execution P ?E (E, ws) by (rule SC-is-legal)
thus ?thesis by blast

```

qed

end

One could now also prove that the aggressive JVM satisfies *drf*. The key would be that *welltyped-commute* also holds for *non-speculative* prefixes from start.

end

8.16 JMM heap implementation 1

theory *JMM-Type*

imports

```

  ../Common/ExternalCallWF
  ../Common/ConformThreaded
  JMM-Heap

```

begin

8.16.1 Definitions

The JMM heap only stores type information.

type-synonym $'addr$ *JMM-heap* = $'addr \rightarrow htype$

translations $(type)$ $'addr$ *JMM-heap* \leq $(type)$ $'addr \Rightarrow htype$ *option*

abbreviation $jmm-empty :: 'addr$ *JMM-heap* **where** $jmm-empty == Map.empty$

definition $jmm-allocate :: 'addr$ *JMM-heap* $\Rightarrow htype \Rightarrow ('addr$ *JMM-heap* $\times 'addr)$ *set*
where $jmm-allocate$ h $hT = (\lambda a. (h(a \mapsto hT), a)) \text{ ' } \{a. h\ a = None\}$

definition $jmm-typeof-addr :: 'addr$ *JMM-heap* $\Rightarrow 'addr \rightarrow htype$
where $jmm-typeof-addr$ $h = h$

definition $jmm-heap-read :: 'addr$ *JMM-heap* $\Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr$ *val* $\Rightarrow bool$
where $jmm-heap-read$ h a ad $v = True$

context

notes $[[inductive-internals]]$

begin

inductive $jmm-heap-write :: 'addr$ *JMM-heap* $\Rightarrow 'addr \Rightarrow addr-loc \Rightarrow 'addr$ *val* $\Rightarrow 'addr$ *JMM-heap*
 $\Rightarrow bool$

where $jmm-heap-write$ h a ad v h

end

definition $jmm-hconf :: 'm$ *prog* $\Rightarrow 'addr$ *JMM-heap* $\Rightarrow bool$ $(\langle - \vdash jmm - \sqrt{\rangle} [51, 51] 50)$
where $P \vdash jmm$ $h \sqrt{\} \longleftrightarrow ty-of-htype$ $'\ ran\ h \subseteq \{T. is-type\ P\ T\}$

definition $jmm-allocated :: 'addr$ *JMM-heap* $\Rightarrow 'addr$ *set*
where $jmm-allocated$ $h = dom$ $(jmm-typeof-addr\ h)$

definition $jmm-spurious-wakeups :: bool$
where $jmm-spurious-wakeups = True$

lemmas $jmm-heap-ops-defs =$
 $jmm-allocate-def\ jmm-typeof-addr-def$
 $jmm-heap-read-def\ jmm-heap-write-def$
 $jmm-allocated-def\ jmm-spurious-wakeups-def$

type-synonym $'addr$ *thread-id* = $'addr$

abbreviation $(input)$ $addr2thread-id :: 'addr \Rightarrow 'addr$ *thread-id*
where $addr2thread-id \equiv \lambda x. x$

abbreviation $(input)$ $thread-id2addr :: 'addr$ *thread-id* $\Rightarrow 'addr$
where $thread-id2addr \equiv \lambda x. x$

interpretation $jmm: heap-base$
 $addr2thread-id\ thread-id2addr$
 $jmm-spurious-wakeups$
 $jmm-empty\ jmm-allocate\ jmm-typeof-addr\ jmm-heap-read\ jmm-heap-write$

notation *jmm.heap* ($\langle \cdot, \cdot \rangle \sqsubseteq_{jmm} \rightarrow [51, 51] 50$)
notation *jmm.conf* ($\langle \cdot, \cdot \rangle \vdash_{jmm} - : \leq \rightarrow [51, 51, 51, 51] 50$)
notation *jmm.addr-loc-type* ($\langle \cdot, \cdot \rangle \vdash_{jmm} - @ - : \rightarrow [50, 50, 50, 50, 50] 51$)
notation *jmm.conf*s ($\langle \cdot, \cdot \rangle \vdash_{jmm} - [: \leq] \rightarrow [51, 51, 51, 51] 50$)
notation *jmm.tconf* ($\langle \cdot, \cdot \rangle \vdash_{jmm} - \sqrt{t} \rightarrow [51, 51, 51] 50$)

Now a variation of the JMM with a different read operation that permits to read only type-conformant values

interpretation *jmm'*: *heap-base*
addr2thread-id thread-id2addr
jmm-spurious-wakeups
jmm-empty jmm-allocate jmm-typeof-addr jmm.heap-read-typed P jmm-heap-write
for *P* .

notation *jmm'.hex*t ($\langle \cdot, \cdot \rangle \sqsubseteq_{jmm''} \rightarrow [51, 51] 50$)
notation *jmm'.conf* ($\langle \cdot, \cdot \rangle \vdash_{jmm''} - : \leq \rightarrow [51, 51, 51, 51] 50$)
notation *jmm'.addr-loc-type* ($\langle \cdot, \cdot \rangle \vdash_{jmm''} - @ - : \rightarrow [50, 50, 50, 50, 50] 51$)
notation *jmm'.conf*s ($\langle \cdot, \cdot \rangle \vdash_{jmm''} - [: \leq] \rightarrow [51, 51, 51, 51] 50$)
notation *jmm'.tconf* ($\langle \cdot, \cdot \rangle \vdash_{jmm''} - \sqrt{t} \rightarrow [51, 51, 51] 50$)

8.16.2 Heap locale interpretations

8.16.3 Locale *heap*

lemma *jmm-heap*: *heap addr2thread-id thread-id2addr jmm-allocate jmm-typeof-addr jmm-heap-write P*

proof

fix *h' a h hT*
assume (*h', a*) \in *jmm-allocate h hT*
thus *jmm-typeof-addr h' a = [hT]*
by(*auto simp add: jmm-heap-ops-defs*)
next
fix *h' :: ('addr :: addr) JMM-heap and h hT a*
assume (*h', a*) \in *jmm-allocate h hT*
thus $h \sqsubseteq_{jmm} h'$
by(*fastforce simp add: jmm-heap-ops-defs intro: jmm.hexT1*)

next

fix *h a al v and h' :: ('addr :: addr) JMM-heap*
assume *jmm-heap-write h a al v h'*
thus $h \sqsubseteq_{jmm} h'$ **by** *cases auto*

qed *simp*

interpretation *jmm*: *heap*
addr2thread-id thread-id2addr
jmm-spurious-wakeups
jmm-empty jmm-allocate jmm-typeof-addr jmm.heap-read jmm-heap-write
P
for *P*
by(*rule jmm-heap*)

declare *jmm.typeof-addr-thread-id2-addr-addr2thread-id* [*simp del*]

lemmas *jmm'-heap = jmm-heap*

interpretation *jmm': heap*
addr2thread-id thread-id2addr
jmm-spurious-wakeups
jmm-empty jmm-allocate jmm-typeof-addr jmm.heap-read-typed P jmm-heap-write
P
for *P*
by(*rule jmm'-heap*)

declare *jmm'.typeof-addr-thread-id2-addr-addr2thread-id* [*simp del*]

8.16.4 Locale *heap-conf*

interpretation *jmm: heap-conf-base*
addr2thread-id thread-id2addr
jmm-spurious-wakeups
jmm-empty jmm-allocate jmm-typeof-addr jmm-heap-read jmm-heap-write jmm-hconf P
P
for *P* .

abbreviation (*input*) *jmm'-hconf* :: '*m prog* ⇒ '*addr JMM-heap* ⇒ *bool* (⊢- *jmm''* - √) [51,51] 50)
where *jmm'-hconf* == *jmm-hconf*

interpretation *jmm': heap-conf-base*
addr2thread-id thread-id2addr
jmm-spurious-wakeups
jmm-empty jmm-allocate jmm-typeof-addr jmm.heap-read-typed P jmm-heap-write jmm'-hconf P
P
for *P* .

abbreviation *jmm-heap-read-typeable* :: ('*addr* :: *addr*) *itself* ⇒ '*m prog* ⇒ *bool*
where *jmm-heap-read-typeable tytok P* ≡ *jmm.heap-read-typeable (jmm-hconf P* :: '*addr JMM-heap* ⇒ *bool*) *P*

abbreviation *jmm'-heap-read-typeable* :: ('*addr* :: *addr*) *itself* ⇒ '*m prog* ⇒ *bool*
where *jmm'-heap-read-typeable tytok P* ≡ *jmm'.heap-read-typeable TYPE('m) P (jmm-hconf P* :: '*addr JMM-heap* ⇒ *bool*) *P*

lemma *jmm-heap-read-typeable: jmm-heap-read-typeable tytok P*
by(*rule jmm.heap-read-typeableI*)(*simp add: jmm-heap-read-def*)

lemma *jmm'-heap-read-typeable: jmm'-heap-read-typeable tytok P*
by(*rule jmm'.heap-read-typeableI*)(*auto simp add: jmm.heap-read-typed-def jmm-heap-read-def dest: jmm'.addr-loc-type-fun*)

lemma *jmm-heap-conf*:
heap-conf addr2thread-id thread-id2addr jmm-empty jmm-allocate jmm-typeof-addr jmm-heap-write
(jmm-hconf P) P
proof
show *P* ⊢ *jmm jmm-empty* √
by(*simp add: jmm-hconf-def*)
next
fix *h a hT*

```

assume jmm-typeof-addr  $h\ a = \lfloor hT \rfloor\ P \vdash_{jmm} h\ \surd$ 
thus is-htype  $P\ hT$  by(auto simp add: jmm-hconf-def jmm-heap-ops-defs intro: ranI)
next
fix  $h'\ h\ hT\ a$ 
assume  $(h', a) \in \text{jmm-allocate } h\ hT\ P \vdash_{jmm} h\ \surd\ \text{is-htype } P\ hT$ 
thus  $P \vdash_{jmm} h'\ \surd$ 
by(fastforce simp add: jmm-hconf-def jmm-heap-ops-defs ran-def split: if-split-asm)
next
fix  $h\ a\ al\ v\ h'\ T$ 
assume jmm-heap-write  $h\ a\ al\ v\ h'\ P \vdash_{jmm} h\ \surd$ 
and jmm.addr-loc-type  $P\ h\ a\ al\ T$  and  $P, h \vdash_{jmm} v : \leq T$ 
thus  $P \vdash_{jmm} h'\ \surd$  by(cases) simp
qed

```

```

interpretation jmm: heap-conf
  addr2thread-id thread-id2addr
  jmm-spurious-wakeups
  jmm-empty jmm-allocate jmm-typeof-addr jmm-heap-read jmm-heap-write jmm-hconf P
  P
for  $P$ 
by(rule jmm-heap-conf)

```

lemmas $\text{jmm}'\text{-heap-conf} = \text{jmm-heap-conf}$

```

interpretation jmm': heap-conf
  addr2thread-id thread-id2addr
  jmm-spurious-wakeups
  jmm-empty jmm-allocate jmm-typeof-addr jmm.heap-read-typed P jmm-heap-write jmm'-hconf P
  P
for  $P$ 
by(rule jmm'-heap-conf)

```

8.16.5 Locale *heap-progress*

```

lemma jmm-heap-progress:
  heap-progress addr2thread-id thread-id2addr jmm-empty jmm-allocate jmm-typeof-addr jmm-heap-read
  jmm-heap-write (jmm-hconf P) P
proof
fix  $h\ a\ al\ T$ 
assume  $P \vdash_{jmm} h\ \surd$ 
and al: jmm.addr-loc-type  $P\ h\ a\ al\ T$ 
show  $\exists v. \text{jmm-heap-read } h\ a\ al\ v \wedge P, h \vdash_{jmm} v : \leq T$ 
using jmm.defval-conf[of P h T] unfolding jmm-heap-ops-defs by blast
next
fix  $h\ a\ al\ T\ v$ 
assume jmm.addr-loc-type  $P\ h\ a\ al\ T$ 
show  $\exists h'. \text{jmm-heap-write } h\ a\ al\ v\ h'$ 
by(auto intro: jmm-heap-write.intros)
qed

```

```

interpretation jmm: heap-progress
  addr2thread-id thread-id2addr
  jmm-spurious-wakeups
  jmm-empty jmm-allocate jmm-typeof-addr jmm-heap-read jmm-heap-write jmm-hconf P

```

P
for *P*
by(*rule jmm-heap-progress*)

lemma *jmm'-heap-progress*:

heap-progress addr2thread-id thread-id2addr jmm-empty jmm-allocate jmm-typeof-addr (jmm.heap-read-typed
P) jmm-heap-write (jmm'-hconf P) P

proof

fix *h a al T*
assume *P ⊢ jmm' h √*
and *al: jmm'.addr-loc-type P h a al T*
thus $\exists v. jmm.heap-read-typed P h a al v \wedge P, h \vdash jmm' v : \leq T$
unfolding *jmm.heap-read-typed-def jmm-heap-read-def*
by(*auto dest: jmm'.addr-loc-type-fun intro: jmm'.defval-conf*)

next

fix *h a al T v*
assume *jmm'.addr-loc-type P h a al T*
and *P, h ⊢ jmm' v : ≤ T*
thus $\exists h'. jmm-heap-write h a al v h'$
by(*auto intro: jmm-heap-write.intros*)

qed

interpretation *jmm'*: *heap-progress*

addr2thread-id thread-id2addr

jmm-spurious-wakeups

jmm-empty jmm-allocate jmm-typeof-addr jmm.heap-read-typed P jmm-heap-write jmm'-hconf P

P

for *P*

by(*rule jmm'-heap-progress*)

8.16.6 Locale *heap-conf-read*

lemma *jmm'-heap-conf-read*:

heap-conf-read addr2thread-id thread-id2addr jmm-empty jmm-allocate jmm-typeof-addr (jmm.heap-read-typed
P) jmm-heap-write (jmm'-hconf P) P

by(*rule jmm.heap-conf-read-heap-read-typed*)

interpretation *jmm'*: *heap-conf-read*

addr2thread-id thread-id2addr

jmm-spurious-wakeups

jmm-empty jmm-allocate jmm-typeof-addr jmm.heap-read-typed P jmm-heap-write jmm'-hconf P

P

for *P*

by(*rule jmm'-heap-conf-read*)

interpretation *jmm'*: *heap-typesafe*

addr2thread-id thread-id2addr

jmm-spurious-wakeups

jmm-empty jmm-allocate jmm-typeof-addr jmm.heap-read-typed P jmm-heap-write jmm'-hconf P

P

for *P*

..

8.16.7 Locale *allocated-heap*

lemma *jmm-allocated-heap*:

allocated-heap addr2thread-id thread-id2addr jmm-empty jmm-allocate jmm-typeof-addr jmm-heap-write jmm-allocated P

proof

show *jmm-allocated jmm-empty = {}* **by**(*auto simp add: jmm-heap-ops-defs*)

next

fix *h' a h hT*

assume $(h', a) \in \text{jmm-allocate } h \text{ } hT$

thus $\text{jmm-allocated } h' = \text{insert } a (\text{jmm-allocated } h) \wedge a \notin \text{jmm-allocated } h$

by(*auto simp add: jmm-heap-ops-defs split: if-split-asm*)

next

fix *h a al v h'*

assume *jmm-heap-write h a al v h'*

thus $\text{jmm-allocated } h' = \text{jmm-allocated } h$ **by** *cases simp*

qed

interpretation *jmm: allocated-heap*

addr2thread-id thread-id2addr

jmm-spurious-wakeups

jmm-empty jmm-allocate jmm-typeof-addr jmm-heap-read jmm-heap-write

jmm-allocated

P

for *P*

by(*rule jmm-allocated-heap*)

lemmas *jmm'-allocated-heap = jmm-allocated-heap*

interpretation *jmm': allocated-heap*

addr2thread-id thread-id2addr

jmm-spurious-wakeups

jmm-empty jmm-allocate jmm-typeof-addr jmm.heap-read-typed P jmm-heap-write

jmm-allocated

P

for *P*

by(*rule jmm'-allocated-heap*)

8.16.8 Syntax translations

notation *jmm'.external-WT'* $(\langle -, - \vdash \text{jmm}'' (\text{--}'(-)) \rangle : \rightarrow [50, 0, 0, 0, 50] \ 60)$

abbreviation *jmm'-red-external* ::

'm prog \Rightarrow 'addr thread-id \Rightarrow 'addr JMM-heap \Rightarrow 'addr \Rightarrow mname \Rightarrow 'addr val list

\Rightarrow ('addr :: addr, 'addr thread-id, 'addr JMM-heap) external-thread-action

\Rightarrow 'addr extCallRet \Rightarrow 'addr JMM-heap \Rightarrow bool

where *jmm'-red-external P \equiv jmm'.red-external (TYPE('m)) P P*

abbreviation *jmm'-red-external-syntax* ::

'm prog \Rightarrow 'addr thread-id \Rightarrow 'addr \Rightarrow mname \Rightarrow 'addr val list \Rightarrow 'addr JMM-heap

\Rightarrow ('addr :: addr, 'addr thread-id, 'addr JMM-heap) external-thread-action

\Rightarrow 'addr extCallRet \Rightarrow 'addr JMM-heap \Rightarrow bool

$(\langle -, - \vdash \text{jmm}'' (\langle \langle \text{--}'(-) \rangle, / - \rangle) \rangle \dashrightarrow \text{ext} (\langle \langle (-), / (-) \rangle \rangle) \rangle [50, 0, 0, 0, 0, 0, 0, 0, 0] \ 51)$

where

P, t $\vdash \text{jmm}' \langle a \cdot M(vs), h \rangle -ta \rightarrow \text{ext} \langle va, h' \rangle \equiv \text{jmm}'\text{-red-external } P \ t \ h \ a \ M \ vs \ ta \ va \ h'$

abbreviation *jmm'-red-external-aggr* ::

'm prog ⇒ 'addr thread-id ⇒ 'addr ⇒ mname ⇒ 'addr val list ⇒ 'addr JMM-heap
 ⇒ (('addr :: addr, 'addr thread-id, 'addr JMM-heap) external-thread-action × 'addr extCallRet ×
 'addr JMM-heap) set

where *jmm'-red-external-aggr* P ≡ *jmm'.red-external-aggr* TYPE('m) P P

abbreviation *jmm'-heap-copy-loc* ::

'm prog ⇒ 'addr ⇒ 'addr ⇒ addr-loc ⇒ 'addr JMM-heap
 ⇒ ('addr :: addr, 'addr thread-id) obs-event list ⇒ 'addr JMM-heap ⇒ bool

where *jmm'-heap-copy-loc* ≡ *jmm'.heap-copy-loc* TYPE('m)

abbreviation *jmm'-heap-copies* ::

'm prog ⇒ 'addr ⇒ 'addr ⇒ addr-loc list ⇒ 'addr JMM-heap
 ⇒ ('addr :: addr, 'addr thread-id) obs-event list ⇒ 'addr JMM-heap ⇒ bool

where *jmm'-heap-copies* ≡ *jmm'.heap-copies* TYPE('m)

abbreviation *jmm'-heap-clone* ::

'm prog ⇒ 'addr JMM-heap ⇒ 'addr ⇒ 'addr JMM-heap
 ⇒ (('addr :: addr, 'addr thread-id) obs-event list × 'addr) option ⇒ bool

where *jmm'-heap-clone* P ≡ *jmm'.heap-clone* TYPE('m) P P

end

8.17 Compiler correctness for the JMM

theory *JMM-Compiler* **imports**

JMM-J

JMM-JVM

../Compiler/Correctness

../Framework/FWBisimLift

begin

lemma *action-loc-aux-compP* [*simp*]: *action-loc-aux* (compP f P) = *action-loc-aux* P
by(*auto* 4 4 *elim!*: *action-loc-aux-cases*)

lemma *action-loc-compP*: *action-loc* (compP f P) = *action-loc* P
by *simp*

lemma *is-volatile-compP* [*simp*]: *is-volatile* (compP f P) = *is-volatile* P

proof(*rule ext*)

fix *hT*

show *is-volatile* (compP f P) *hT* = *is-volatile* P *hT*

by(*cases hT*) *simp-all*

qed

lemma *saction-compP* [*simp*]: *saction* (compP f P) = *saction* P
by(*simp add: saction.simps fun-eq-iff*)

lemma *sactions-compP* [*simp*]: *sactions* (compP f P) = *sactions* P
by(*rule ext*)(*simp only: sactions-def, simp*)

lemma *addr-locs-compP* [*simp*]: *addr-locs* (compP f P) = *addr-locs* P

by(*rule ext*)(*case-tac x, simp-all*)

lemma *synchronizes-with-compP* [*simp*]: *synchronizes-with* (*compP f P*) = *synchronizes-with P*
by(*simp add: synchronizes-with.simps fun-eq-iff*)

lemma *sync-order-compP* [*simp*]: *sync-order* (*compP f P*) = *sync-order P*
by(*simp add: sync-order-def fun-eq-iff*)

lemma *sync-with-compP* [*simp*]: *sync-with* (*compP f P*) = *sync-with P*
by(*simp add: sync-with-def fun-eq-iff*)

lemma *po-sw-compP* [*simp*]: *po-sw* (*compP f P*) = *po-sw P*
by(*simp add: po-sw-def fun-eq-iff*)

lemma *happens-before-compP*: *happens-before* (*compP f P*) = *happens-before P*
by *simp*

lemma *addr-loc-default-compP* [*simp*]: *addr-loc-default* (*compP f P*) = *addr-loc-default P*

proof(*intro ext*)

fix *hT al*

show *addr-loc-default* (*compP f P*) *hT al* = *addr-loc-default P hT al*

by(*cases (P, hT, al) rule: addr-loc-default.cases*) *simp-all*

qed

lemma *value-written-aux-compP* [*simp*]: *value-written-aux* (*compP f P*) = *value-written-aux P*

proof(*intro ext*)

fix *a al*

show *value-written-aux* (*compP f P*) *a al* = *value-written-aux P a al*

by(*cases (P, a, al) rule: value-written-aux.cases*)(*simp-all add: value-written-aux.simps*)

qed

lemma *value-written-compP* [*simp*]: *value-written* (*compP f P*) = *value-written P*
by(*simp add: fun-eq-iff value-written.simps*)

lemma *is-write-seen-compP* [*simp*]: *is-write-seen* (*compP f P*) = *is-write-seen P*
by(*simp add: fun-eq-iff is-write-seen-def*)

lemma *justification-well-formed-compP* [*simp*]:
justification-well-formed (*compP f P*) = *justification-well-formed P*
by(*simp add: fun-eq-iff justification-well-formed-def*)

lemma *happens-before-committed-compP* [*simp*]:
happens-before-committed (*compP f P*) = *happens-before-committed P*
by(*simp add: fun-eq-iff happens-before-committed-def*)

lemma *happens-before-committed-weak-compP* [*simp*]:
happens-before-committed-weak (*compP f P*) = *happens-before-committed-weak P*
by(*simp add: fun-eq-iff happens-before-committed-weak-def*)

lemma *sync-order-committed-compP* [*simp*]:
sync-order-committed (*compP f P*) = *sync-order-committed P*
by(*simp add: fun-eq-iff sync-order-committed-def*)

lemma *value-written-committed-compP* [*simp*]:

value-written-committed (*compP f P*) = *value-written-committed P*
by(*simp add: fun-eq-iff value-written-committed-def*)

lemma *uncommitted-reads-see-hb-compP* [*simp*]:
uncommitted-reads-see-hb (*compP f P*) = *uncommitted-reads-see-hb P*
by(*simp add: fun-eq-iff uncommitted-reads-see-hb-def*)

lemma *external-actions-committed-compP* [*simp*]:
external-actions-committed (*compP f P*) = *external-actions-committed P*
by(*simp add: fun-eq-iff external-actions-committed-def*)

lemma *is-justified-by-compP* [*simp*]: *is-justified-by* (*compP f P*) = *is-justified-by P*
by(*simp add: fun-eq-iff is-justified-by.simps*)

lemma *is-weakly-justified-by-compP* [*simp*]: *is-weakly-justified-by* (*compP f P*) = *is-weakly-justified-by P*
by(*simp add: fun-eq-iff is-weakly-justified-by.simps*)

lemma *legal-execution-compP*: *legal-execution* (*compP f P*) = *legal-execution P*
by(*simp add: fun-eq-iff gen-legal-execution.simps*)

lemma *weakly-legal-execution-compP*: *weakly-legal-execution* (*compP f P*) = *weakly-legal-execution P*
by(*simp add: fun-eq-iff gen-legal-execution.simps*)

lemma *most-recent-write-for-compP* [*simp*]:
most-recent-write-for (*compP f P*) = *most-recent-write-for P*
by(*simp add: fun-eq-iff most-recent-write-for.simps*)

lemma *sequentially-consistent-compP* [*simp*]:
sequentially-consistent (*compP f P*) = *sequentially-consistent P*
by(*simp add: sequentially-consistent-def split-beta*)

lemma *conflict-compP* [*simp*]: *non-volatile-conflict* (*compP f P*) = *non-volatile-conflict P*
by(*simp add: fun-eq-iff non-volatile-conflict-def*)

lemma *correctly-synchronized-compP* [*simp*]:
correctly-synchronized (*compP f P*) = *correctly-synchronized P*
by(*simp add: fun-eq-iff correctly-synchronized-def*)

lemma (**in** *heap-base*) *heap-read-typed-compP* [*simp*]:
heap-read-typed (*compP f P*) = *heap-read-typed P*
by(*intro ext*)(*simp add: heap-read-typed-def*)

context *J-JVM-heap-conf-base* **begin**

definition *if-bisimJ2JVM* ::

((*'addr, 'thread-id, status × 'addr expr × 'addr locals, 'heap, 'addr*) *state*,
(*'addr, 'thread-id, status × 'addr option × 'addr frame list, 'heap, 'addr*) *state*) *bisim*

where

if-bisimJ2JVM =

FWbisimulation-base.mbisim red-red0.init-fin-bisim red-red0.init-fin-bisim-wait \circ_B
FWbisimulation-base.mbisim red0-Red1'.init-fin-bisim red0-Red1'.init-fin-bisim-wait \circ_B
if-mbisim-Red1'-Red1 \circ_B
FWbisimulation-base.mbisim Red1-execd.init-fin-bisim Red1-execd.init-fin-bisim-wait

definition *if-tlsimJ2JVM* ::

(*'thread-id* × (*'addr*, *'thread-id*, *status* × *'addr expr* × *'addr locals*,
'heap, *'addr*, (*'addr*, *'thread-id*) *obs-event action*) *thread-action*,
'thread-id × (*'addr*, *'thread-id*, *status* × *'addr jvm-thread-state*,
'heap, *'addr*, (*'addr*, *'thread-id*) *obs-event action*) *thread-action*) *bisim*

where

if-tlsimJ2JVM =
FWbisimulation-base.mta-bisim red-red0.init-fin-bisim ◦_B
FWbisimulation-base.mta-bisim red0-Red1'.init-fin-bisim ◦_B (=) ◦_B
FWbisimulation-base.mta-bisim Red1-execd.init-fin-bisim

end

sublocale *J-JVM-conf-read* < *red-mthr*: *if-τ multithreaded-wf final-expr mred P convert-RA τ MOVE*
P

by(*unfold-locales*)

sublocale *J-JVM-conf-read* < *execd-mthr*:

if-τ multithreaded-wf
JVM-final
mexecd (compP2 (compP1 P))
convert-RA
τ MOVE2 (compP2 (compP1 P))

by(*unfold-locales*)

context *J-JVM-conf-read* **begin**

theorem *if-bisimJ2JVM-weak-bisim*:

assumes *wf*: *wf-J-prog P*

shows *delay-bisimulation-diverge-final*

(*red-mthr.mthr.if.redT P*) (*execd-mthr.mthr.if.redT (J2JVM P)*) *if-bisimJ2JVM if-tlsimJ2JVM*
red-mthr.if.mτmove execd-mthr.if.mτmove red-mthr.mthr.if.mfinal execd-mthr.mthr.if.mfinal

apply (*simp only: if-bisimJ2JVM-def if-tlsimJ2JVM-def J2JVM-def o-apply*)

apply(*rule delay-bisimulation-diverge-final-compose*)

apply(*rule FWdelay-bisimulation-diverge.mthr-delay-bisimulation-diverge-final*)

apply(*rule FWdelay-bisimulation-diverge.init-fin-FWdelay-bisimulation-diverge*)

apply(*rule red-red0-FWbisim[OF wf-prog-wwf-prog[OF wf]]*)

apply(*rule delay-bisimulation-diverge-final-compose*)

apply(*rule FWdelay-bisimulation-diverge.mthr-delay-bisimulation-diverge-final*)

apply(*rule FWdelay-bisimulation-diverge.init-fin-FWdelay-bisimulation-diverge*)

apply(*rule red0-Red1'-FWweak-bisim[OF wf]*)

apply(*rule delay-bisimulation-diverge-final-compose*)

apply(*rule delay-bisimulation-diverge-final.intro*)

apply(*rule bisimulation-into-delay.delay-bisimulation*)

apply(*rule if-Red1'-Red1-bisim-into-weak[OF compP1-pres-wf[OF wf]]*)

apply(*rule bisimulation-final.delay-bisimulation-final-base*)

apply(*rule if-Red1'-Red1-bisimulation-final[OF compP1-pres-wf[OF wf]]*)

apply(*rule FWdelay-bisimulation-diverge.mthr-delay-bisimulation-diverge-final*)

apply(*rule FWdelay-bisimulation-diverge.init-fin-FWdelay-bisimulation-diverge*)

apply(*rule Red1-exec1-FWwbisim[OF compP1-pres-wf[OF wf]]*)

done

lemma *if-bisimJ2JVM-start*:

```

assumes wf: wf-J-prog P
and wf-start: wf-start-state P C M vs
shows if-bisimJ2JVM (init-fin-lift-state Running (J-start-state P C M vs))
                (init-fin-lift-state Running (JVM-start-state (J2JVM P) C M vs))
using assms
unfolding if-bisimJ2JVM-def J2JVM-def o-apply
apply(intro bisim-composeI)
  apply(rule FWbisimulation-base.init-fin-lift-state-mbisimI)
  apply(erule (1) bisim-J-J0-start[OF wf-prog-wwf-prog])
  apply(rule FWbisimulation-base.init-fin-lift-state-mbisimI)
  apply(erule (1) bisim-J0-J1-start)
  apply(erule if-bisim-J1-J1-start[OF compP1-pres-wf])
  apply simp
apply(rule FWbisimulation-base.init-fin-lift-state-mbisimI)
apply(erule bisim-J1-JVM-start[OF compP1-pres-wf])
apply simp
done

lemma red-Runs-eq-mexecd-Runs:
  fixes C M vs
  defines s: s  $\equiv$  init-fin-lift-state Running (J-start-state P C M vs)
  and comps: cs  $\equiv$  init-fin-lift-state Running (JVM-start-state (J2JVM P) C M vs)
  assumes wf: wf-J-prog P
  and wf-start: wf-start-state P C M vs
  shows red-mthr.mthr.if. $\mathcal{E}$  P s = execd-mthr.mthr.if. $\mathcal{E}$  (J2JVM P) cs
proof –
  from wf wf-start have bisim: if-bisimJ2JVM s cs
    unfolding s comps by(rule if-bisimJ2JVM-start)

  interpret divfin: delay-bisimulation-diverge-final
    red-mthr.mthr.if.redT P
    execd-mthr.mthr.if.redT (J2JVM P)
    if-bisimJ2JVM
    if-tlsimJ2JVM
    red-mthr.if.m $\tau$ move
    execd-mthr.if.m $\tau$ move
    red-mthr.mthr.if.mfinal
    execd-mthr.mthr.if.mfinal
  using wf by(rule if-bisimJ2JVM-weak-bisim)

  show ?thesis (is ?lhs = ?rhs)
proof(intro equalityI subsetI)
  fix E
  assume E  $\in$  ?lhs
  then obtain E' where E: E = lconcat (lmap ( $\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \{ta\}_o)$ ) (llist-of-tl $\text{list}$  E'))
    and E': red-mthr.if.mthr. $\tau$ Runs s E'
    unfolding red-mthr.if. $\mathcal{E}$ -conv-Runs by blast
  from divfin.simulation- $\tau$ Runs1[OF bisim E']
  obtain E'' where E'': execd-mthr.if.mthr. $\tau$ Runs cs E''
    and tlsim: tllist-all2 if-tlsimJ2JVM (option.rel-option if-bisimJ2JVM) E' E''
    unfolding J2JVM-def o-apply by blast
  let ?E = lconcat (lmap ( $\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \{ta\}_o)$ ) (llist-of-tl $\text{list}$  E''))
  from tlsim have llist-all2 if-tlsimJ2JVM (llist-of-tl $\text{list}$  E') (llist-of-tl $\text{list}$  E'')

```

by(rule *tllist-all2D-llist-all2-llist-of-tllist*)
hence *llist-all2* (=) (*lmap* ($\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \{\{ta\}_o\}) (\text{llist-of-tllist } E')$)
(*lmap* ($\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \{\{ta\}_o\}) (\text{llist-of-tllist } E'')$))
unfolding *llist-all2-lmap1 llist-all2-lmap2*
by(rule *llist-all2-mono*)(*auto simp add: if-tlsimJ2JVM-def FWbisimulation-base.mta-bisim-def*
ta-bisim-def)
hence $?E = E$ **unfolding** *llist.rel-eq E* **by** *simp*
also from E'' **have** $?E \in ?rhs$ **unfolding** *J2JVM-def o-apply execd-mthr.if.E-conv-Runs* **by** *blast*
finally (*subst*) **show** $E \in ?rhs$.
next
fix E
assume $E \in ?rhs$
then obtain E' **where** $E = \text{lconcat } (\text{map } (\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \{\{ta\}_o\}) (\text{llist-of-tllist } E'))$
 $E')$
and E' : *execd-mthr.if.mthr. τ Runs cs E'*
unfolding *execd-mthr.if.E-conv-Runs J2JVM-def o-apply* **by** *blast*
from *divfin.simulation- τ Runs2[OF bisim, simplified J2JVM-def o-apply, OF E']*
obtain E'' **where** E'' : *red-mthr.if.mthr. τ Runs s E''*
and *tlsim: tllist-all2 if-tlsimJ2JVM (option.rel-option if-bisimJ2JVM) E'' E' by blast*
let $?E = \text{lconcat } (\text{map } (\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \{\{ta\}_o\}) (\text{llist-of-tllist } E'))$
 $E')$
from *tlsim* **have** *llist-all2 if-tlsimJ2JVM (llist-of-tllist E'') (llist-of-tllist E')*
by(rule *tllist-all2D-llist-all2-llist-of-tllist*)
hence *llist-all2* (=) (*lmap* ($\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \{\{ta\}_o\}) (\text{llist-of-tllist } E'')$)
(*lmap* ($\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \{\{ta\}_o\}) (\text{llist-of-tllist } E')$))
unfolding *llist-all2-lmap1 llist-all2-lmap2*
by(rule *llist-all2-mono*)(*auto simp add: if-tlsimJ2JVM-def FWbisimulation-base.mta-bisim-def*
ta-bisim-def)
hence $?E = E$ **unfolding** *llist.rel-eq E* **by** *simp*
also from E'' **have** $?E \in ?lhs$ **unfolding** *red-mthr.if.E-conv-Runs* **by** *blast*
finally (*subst*) **show** $E \in ?lhs$.
qed
qed

lemma *red-E-eq-mexecd-E*:

$\llbracket \text{wf-J-prog } P; \text{wf-start-state } P \ C \ M \ \text{vs} \rrbracket$
 $\implies J\text{-E } P \ C \ M \ \text{vs } \text{Running} = \text{JVMD-E } (J2JVM \ P) \ C \ M \ \text{vs } \text{Running}$
by(*simp only: red-Runs-eq-mexecd-Runs*)

theorem *J2JVM-jmm-correct*:

assumes *wf: wf-J-prog P*
and *wf-start: wf-start-state P C M vs*
shows *legal-execution P (J-E P C M vs Running) (E, ws) \longleftrightarrow*
legal-execution (J2JVM P) (JVMD-E (J2JVM P) C M vs Running) (E, ws)
by(*simp only: red-E-eq-mexecd-E[OF assms] J2JVM-def o-apply compP1-def compP2-def legal-execution-compP*)

theorem *J2JVM-jmm-correct-weak*:

assumes *wf: wf-J-prog P*
and *wf-start: wf-start-state P C M vs*
shows *weakly-legal-execution P (J-E P C M vs Running) (E, ws) \longleftrightarrow*
weakly-legal-execution (J2JVM P) (JVMD-E (J2JVM P) C M vs Running) (E, ws)
by(*simp only: red-E-eq-mexecd-E[OF assms] J2JVM-def o-apply compP1-def compP2-def weakly-legal-execution-compP*)

theorem *J2JVM-jmm-correctly-synchronized*:

assumes *wf: wf-J-prog P*

```

and wf-start: wf-start-state P C M vs
shows correctly-synchronized (J2JVM P) (JVMd- $\mathcal{E}$  (J2JVM P) C M vs Running)  $\longleftrightarrow$ 
       correctly-synchronized P (J- $\mathcal{E}$  P C M vs Running)
by(simp only: red- $\mathcal{E}$ -eq-mexecd- $\mathcal{E}$ [OF assms] J2JVM-def o-apply compP1-def compP2-def correctly-synchronized-com)
end
end

```

8.18 JMM heap implementation 2

```

theory JMM-Type2
imports
  ../Common/ExternalCallWF
  ../Common/ConformThreaded
  JMM-Heap
begin

```

8.18.1 Definitions

```

datatype addr = Address htype nat — heap type and sequence number

```

```

lemma rec-addr-conv-case-addr [simp]: rec-addr = case-addr
by(auto intro!: ext split: addr.split)

```

```

instantiation addr :: addr begin

```

```

definition hash-addr (a :: addr) = (case a of Address ht n  $\Rightarrow$  int n)

```

```

definition monitor-funfun-to-list (ls :: addr  $\Rightarrow$  f nat) = (SOME xs. set xs = {x. funfun-dom ls $ x })

```

```

instance

```

```

proof

```

```

  fix ls :: addr  $\Rightarrow$  f nat

```

```

  show set (monitor-funfun-to-list ls) = Collect (( $\$$ ) (funfun-dom ls))

```

```

    unfolding monitor-funfun-to-list-addr-def

```

```

    using finite-list[OF finite-funfun-dom, where ?f.1 = ls]

```

```

    by(rule someI-ex)

```

```

qed

```

```

end

```

```

primrec the-Address :: addr  $\Rightarrow$  htype  $\times$  nat

```

```

where the-Address (Address hT n) = (hT, n)

```

The JMM heap only stores which sequence numbers of a given *htype* have already been allocated.

```

type-synonym JMM-heap = htype  $\Rightarrow$  nat set

```

```

translations (type) JMM-heap <= (type) htype  $\Rightarrow$  nat set

```

```

definition jmm-allocate :: JMM-heap  $\Rightarrow$  htype  $\Rightarrow$  (JMM-heap  $\times$  addr) set

```

```

where jmm-allocate h hT = (let hhT = h hT in ( $\lambda$ n. (h(hT := insert n hhT), Address hT n))) ‘ (- hhT))

```

```

abbreviation jmm-empty :: JMM-heap where jmm-empty == ( $\lambda$ -. {})

```

```

definition jmm-typeof-addr :: 'm prog  $\Rightarrow$  JMM-heap  $\Rightarrow$  addr  $\rightarrow$  htype

```

where $jmm\text{-typeof-addr } P \ h = (\lambda hT. \text{ if is-htype } P \ hT \text{ then Some } hT \text{ else None}) \circ \text{fst} \circ \text{the-Address}$

definition $jmm\text{-typeof-addr}' :: 'm \text{ prog} \Rightarrow \text{addr} \rightarrow \text{hType}$

where $jmm\text{-typeof-addr}' \ P = (\lambda hT. \text{ if is-htype } P \ hT \text{ then Some } hT \text{ else None}) \circ \text{fst} \circ \text{the-Address}$

lemma $jmm\text{-typeof-addr}'\text{-conv-}jmm\text{-type-addr}: jmm\text{-typeof-addr}' \ P = jmm\text{-typeof-addr} \ P \ h$

by(*simp add: jmm-typeof-addr-def jmm-typeof-addr'-def*)

lemma $jmm\text{-typeof-addr}'\text{-conv-}jmm\text{-typeof-addr}: (\lambda -. jmm\text{-typeof-addr}' \ P) = jmm\text{-typeof-addr} \ P$

by(*simp add: jmm-typeof-addr-def jmm-typeof-addr'-def fun-eq-iff*)

lemma $jmm\text{-typeof-addr}\text{-conv-}jmm\text{-typeof-addr}' : jmm\text{-typeof-addr} = (\lambda P \ -. \ jmm\text{-typeof-addr}' \ P)$

by(*simp add: jmm-typeof-addr'\text{-conv-}jmm-typeof-addr*)

definition $jmm\text{-heap-read} :: JMM\text{-heap} \Rightarrow \text{addr} \Rightarrow \text{addr-loc} \Rightarrow \text{addr val} \Rightarrow \text{bool}$

where $jmm\text{-heap-read} \ h \ a \ ad \ v = \text{True}$

context

notes $[[\text{inductive-internals}]]$

begin

inductive $jmm\text{-heap-write} :: JMM\text{-heap} \Rightarrow \text{addr} \Rightarrow \text{addr-loc} \Rightarrow \text{addr val} \Rightarrow JMM\text{-heap} \Rightarrow \text{bool}$

where $jmm\text{-heap-write} \ h \ a \ ad \ v \ h$

end

definition $jmm\text{-hconf} :: JMM\text{-heap} \Rightarrow \text{bool}$

where $jmm\text{-hconf} \ h \longleftrightarrow \text{True}$

definition $jmm\text{-allocated} :: JMM\text{-heap} \Rightarrow \text{addr set}$

where $jmm\text{-allocated} \ h = \{\text{Address } CTn \ n \mid CTn \ n. \ n \in h \ CTn\}$

definition $jmm\text{-spurious-wakeups} :: \text{bool}$

where $jmm\text{-spurious-wakeups} = \text{True}$

lemmas $jmm\text{-heap-ops-defs} =$

$jmm\text{-allocate-def } jmm\text{-typeof-addr-def}$

$jmm\text{-heap-read-def } jmm\text{-heap-write-def}$

$jmm\text{-allocated-def } jmm\text{-spurious-wakeups-def}$

type-synonym $\text{thread-id} = \text{addr}$

abbreviation (*input*) $\text{addr2thread-id} :: \text{addr} \Rightarrow \text{thread-id}$

where $\text{addr2thread-id} \equiv \lambda x. \ x$

abbreviation (*input*) $\text{thread-id2addr} :: \text{thread-id} \Rightarrow \text{addr}$

where $\text{thread-id2addr} \equiv \lambda x. \ x$

interpretation $jmm: \text{heap-base}$

$\text{addr2thread-id } \text{thread-id2addr}$

$jmm\text{-spurious-wakeups}$

$jmm\text{-empty } jmm\text{-allocate } jmm\text{-typeof-addr } P \ jmm\text{-heap-read } jmm\text{-heap-write}$

for P

.

abbreviation $jmm\text{-}heaxt :: 'm\ prog \Rightarrow JMM\text{-}heap \Rightarrow JMM\text{-}heap \Rightarrow bool$ ($\langle \cdot \vdash \cdot \leq_{jmm} \cdot \rangle [51,51,51]$ 50)

where $jmm\text{-}heaxt \equiv jmm.heaxt\ TYPE('m)$

abbreviation $jmm\text{-}conf :: 'm\ prog \Rightarrow JMM\text{-}heap \Rightarrow addr\ val \Rightarrow ty \Rightarrow bool$

($\langle \cdot, \cdot \vdash_{jmm} \cdot : \leq \cdot \rangle [51,51,51,51]$ 50)

where $jmm\text{-}conf\ P \equiv jmm.conf\ TYPE('m)\ P\ P$

abbreviation $jmm\text{-}addr\text{-}loc\text{-}type :: 'm\ prog \Rightarrow JMM\text{-}heap \Rightarrow addr \Rightarrow addr\text{-}loc \Rightarrow ty \Rightarrow bool$

($\langle \cdot, \cdot \vdash_{jmm} \cdot @ \cdot : \cdot \rangle [50, 50, 50, 50, 50]$ 51)

where $jmm\text{-}addr\text{-}loc\text{-}type\ P \equiv jmm.addr\text{-}loc\text{-}type\ TYPE('m)\ P\ P$

abbreviation $jmm\text{-}confs :: 'm\ prog \Rightarrow JMM\text{-}heap \Rightarrow addr\ val\ list \Rightarrow ty\ list \Rightarrow bool$

($\langle \cdot, \cdot \vdash_{jmm} \cdot [:\leq] \cdot \rangle [51,51,51,51]$ 50)

where $jmm\text{-}confs\ P \equiv jmm.confs\ TYPE('m)\ P\ P$

abbreviation $jmm\text{-}tconf :: 'm\ prog \Rightarrow JMM\text{-}heap \Rightarrow addr \Rightarrow bool$ ($\langle \cdot, \cdot \vdash_{jmm} \cdot \sqrt{t} \rangle [51,51,51]$ 50)

where $jmm\text{-}tconf\ P \equiv jmm.tconf\ TYPE('m)\ P\ P$

interpretation jmm : *allocated-heap-base*

addr2thread-id thread-id2addr

jmm-spurious-wakeups

jmm-empty jmm-allocate jmm-typeof-addr P jmm-heap-read jmm-heap-write

jmm-allocated

for P

.

Now a variation of the JMM with a different read operation that permits to read only type-conformant values

abbreviation $jmm\text{-}heap\text{-}read\text{-}typed :: 'm\ prog \Rightarrow JMM\text{-}heap \Rightarrow addr \Rightarrow addr\text{-}loc \Rightarrow addr\ val \Rightarrow bool$

where $jmm\text{-}heap\text{-}read\text{-}typed\ P \equiv jmm.heap\text{-}read\text{-}typed\ TYPE('m)\ P\ P$

interpretation jmm' : *heap-base*

addr2thread-id thread-id2addr

jmm-spurious-wakeups

jmm-empty jmm-allocate jmm-typeof-addr P jmm-heap-read-typed P jmm-heap-write

for P .

abbreviation $jmm'\text{-}heaxt :: 'm\ prog \Rightarrow JMM\text{-}heap \Rightarrow JMM\text{-}heap \Rightarrow bool$ ($\langle \cdot \vdash \cdot \leq_{jmm''} \cdot \rangle [51,51,51]$ 50)

where $jmm'\text{-}heaxt \equiv jmm'.heaxt\ TYPE('m)$

abbreviation $jmm'\text{-}conf :: 'm\ prog \Rightarrow JMM\text{-}heap \Rightarrow addr\ val \Rightarrow ty \Rightarrow bool$

($\langle \cdot, \cdot \vdash_{jmm''} \cdot : \leq \cdot \rangle [51,51,51,51]$ 50)

where $jmm'\text{-}conf\ P \equiv jmm'.conf\ TYPE('m)\ P\ P$

abbreviation $jmm'\text{-}addr\text{-}loc\text{-}type :: 'm\ prog \Rightarrow JMM\text{-}heap \Rightarrow addr \Rightarrow addr\text{-}loc \Rightarrow ty \Rightarrow bool$

($\langle \cdot, \cdot \vdash_{jmm''} \cdot @ \cdot : \cdot \rangle [50, 50, 50, 50, 50]$ 51)

where $jmm'\text{-}addr\text{-}loc\text{-}type\ P \equiv jmm'.addr\text{-}loc\text{-}type\ TYPE('m)\ P\ P$

abbreviation $jmm'\text{-}confs :: 'm\ prog \Rightarrow JMM\text{-}heap \Rightarrow addr\ val\ list \Rightarrow ty\ list \Rightarrow bool$

($\langle \cdot, \cdot \vdash_{jmm''} \cdot [:\leq] \cdot \rangle [51,51,51,51]$ 50)

where $jmm'\text{-}confs\ P \equiv jmm'.confs\ TYPE('m)\ P\ P$

abbreviation $jmm'\text{-tconf} :: 'm \text{ prog} \Rightarrow \text{JMM-heap} \Rightarrow \text{addr} \Rightarrow \text{bool} (\langle -, - \rangle \vdash jmm'' - \sqrt{t} \rangle [51,51,51] 50)$
where $jmm'\text{-tconf } P \equiv jmm'.\text{tconf } \text{TYPE}('m) P P$

8.18.2 Heap locale interpretations

8.18.3 Locale *heap*

lemma $jmm\text{-heap}: \text{heap } \text{addr2thread-id } \text{thread-id2addr } jmm\text{-allocate } (jmm\text{-typeof-addr } P) jmm\text{-heap-write } P$

proof

fix $h' a h hT$

assume $(h', a) \in jmm\text{-allocate } h hT \text{ is-htype } P hT$

thus $jmm\text{-typeof-addr } P h' a = \lfloor hT \rfloor$

by($\text{auto simp add: } jmm\text{-heap-ops-defs}$)

next

fix $h hT h' a$

assume $(h', a) \in jmm\text{-allocate } h hT$

thus $P \vdash h \triangleleft_{jmm} h'$ **by**($\text{auto simp add: } jmm\text{-heap-ops-defs intro: } jmm.\text{heatI}$)

next

fix $h a al v h'$

assume $jmm\text{-heap-write } h a al v h'$

thus $P \vdash h \triangleleft_{jmm} h'$ **by** cases auto

qed simp

interpretation $jmm: \text{heap}$

$\text{addr2thread-id } \text{thread-id2addr}$

$jmm\text{-spurious-wakeups}$

$jmm\text{-empty } jmm\text{-allocate } jmm\text{-typeof-addr } P jmm\text{-heap-read } jmm\text{-heap-write}$

P

for P

by($\text{rule } jmm\text{-heap}$)

declare $jmm.\text{typeof-addr-thread-id2-addr-addr2thread-id}$ [simp del]

lemmas $jmm'\text{-heap} = jmm\text{-heap}$

interpretation $jmm': \text{heap}$

$\text{addr2thread-id } \text{thread-id2addr}$

$jmm\text{-spurious-wakeups}$

$jmm\text{-empty } jmm\text{-allocate } jmm\text{-typeof-addr } P jmm\text{-heap-read-typed } P jmm\text{-heap-write}$

P

for P

by($\text{rule } jmm'\text{-heap}$)

declare $jmm'.\text{typeof-addr-thread-id2-addr-addr2thread-id}$ [simp del]

lemma $jmm\text{-heap-read-typed-default-val}:$

$\text{heap-base}.\text{heap-read-typed } \text{typeof-addr } jmm\text{-heap-read } P h a al$

($\text{default-val } (\text{THE } T. \text{heap-base}.\text{addr-loc-type } \text{typeof-addr } P h a al T)$)

by($\text{rule } \text{heap-base}.\text{heap-read-typedI}$)($\text{simp-all add: } \text{heap-base}.\text{THE-addr-loc-type } jmm\text{-heap-read-def } \text{heap-base}.\text{defval-conf}$)

lemma $jmm\text{-allocate-Eps}:$

($\text{SOME } ha. ha \in jmm\text{-allocate } h hT$) = (h', a')

$\implies \text{jmm-allocate } h \ hT \neq \{\} \longrightarrow (h', a') \in \text{jmm-allocate } h \ hT$
by(*auto dest: jmm.allocate-Eps*)

lemma *jmm-allocate-eq-empty*: $\text{jmm-allocate } h \ hT = \{\} \longleftrightarrow h \ hT = \text{UNIV}$
by(*auto simp add: jmm-allocate-def*)

lemma *jmm-allocate-otherD*:
 $(h', a) \in \text{jmm-allocate } h \ hT \implies \forall hT'. hT' \neq hT \longrightarrow h' \ hT' = h \ hT'$
by(*auto simp add: jmm-allocate-def*)

lemma *jmm-start-heap-ok*: *jmm.start-heap-ok*
apply(*simp add: jmm.start-heap-ok-def jmm.start-heap-data-def initialization-list-def sys-xcpts-list-def jmm.create-initial-object-simps*)
apply(*split prod.split, clarify, clarsimp simp add: jmm.create-initial-object-simps jmm-allocate-eq-empty Thread-neq-sys-xcpts sys-xcpts-neqs dest!: jmm-allocate-Eps jmm-allocate-otherD*)
done

8.18.4 Locale *heap-conf*

interpretation *jmm*: *heap-conf-base*
addr2thread-id thread-id2addr
jmm-spurious-wakeups
jmm-empty jmm-allocate jmm-typeof-addr P jmm-heap-read jmm-heap-write jmm-hconf
P
for *P* .

abbreviation (*input*) *jmm'-hconf* :: *JMM-heap* \Rightarrow *bool*
where *jmm'-hconf* == *jmm-hconf*

interpretation *jmm'*: *heap-conf-base*
addr2thread-id thread-id2addr
jmm-spurious-wakeups
jmm-empty jmm-allocate jmm-typeof-addr P jmm-heap-read-typed P jmm-heap-write jmm'-hconf
P
for *P* .

abbreviation *jmm-heap-read-typeable* :: *'m prog* \Rightarrow *bool*
where *jmm-heap-read-typeable* *P* \equiv *jmm.heap-read-typeable* *TYPE('m)* *P jmm-hconf P*

abbreviation *jmm'-heap-read-typeable* :: *'m prog* \Rightarrow *bool*
where *jmm'-heap-read-typeable* *P* \equiv *jmm'.heap-read-typeable* *TYPE('m)* *P jmm-hconf P*

lemma *jmm-heap-read-typeable*: *jmm-heap-read-typeable* *P*
by(*rule jmm.heap-read-typeableI*)(*simp add: jmm-heap-read-def*)

lemma *jmm'-heap-read-typeable*: *jmm'-heap-read-typeable* *P*
by(*rule jmm'.heap-read-typeableI*)(*auto simp add: jmm-heap-read-def jmm.heap-read-typed-def dest: jmm'.addr-loc-type-fun*)

lemma *jmm-heap-conf*:
heap-conf *addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr P) jmm-heap-write jmm-hconf P*
by(*unfold-locale*)(*simp-all add: jmm-hconf-def jmm-heap-ops-defs split: if-split-asm*)

interpretation *jmm*: heap-conf
 addr2thread-id thread-id2addr
 jmm-spurious-wakeups
 jmm-empty jmm-allocate jmm-typeof-addr P jmm-heap-read jmm-heap-write jmm-hconf
 P
 for P
 by(rule jmm-heap-conf)

lemmas *jmm'*-heap-conf = jmm-heap-conf

interpretation *jmm'*: heap-conf
 addr2thread-id thread-id2addr
 jmm-spurious-wakeups
 jmm-empty jmm-allocate jmm-typeof-addr P jmm-heap-read-typed P jmm-heap-write *jmm'*-hconf
 P
 for P
 by(rule *jmm'*-heap-conf)

8.18.5 Locale heap-progress

lemma *jmm*-heap-progress:
 heap-progress addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr P) jmm-heap-read
 jmm-heap-write jmm-hconf P

proof
 fix h a al T
 assume jmm-hconf h
 and al: P,h ⊢ jmm a@al : T
 show ∃ v. jmm-heap-read h a al v ∧ P,h ⊢ jmm v :≤ T
 using jmm.defval-conf[of P P h T] **unfolding** jmm-heap-ops-defs **by** blast

next
 fix h a al T v
 assume P,h ⊢ jmm a@al : T
 show ∃ h'. jmm-heap-write h a al v h'
 by(auto intro: jmm-heap-write.intros)
qed

interpretation *jmm*: heap-progress
 addr2thread-id thread-id2addr
 jmm-spurious-wakeups
 jmm-empty jmm-allocate jmm-typeof-addr P jmm-heap-read jmm-heap-write jmm-hconf
 P
 for P
 by(rule jmm-heap-progress)

lemma *jmm'*-heap-progress:
 heap-progress addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr P) (jmm-heap-read-typed
 P) jmm-heap-write *jmm'*-hconf P

proof
 fix h a al T
 assume *jmm'*-hconf h
 and al: P,h ⊢ *jmm'* a@al : T
 thus ∃ v. jmm-heap-read-typed P h a al v ∧ P,h ⊢ *jmm'* v :≤ T
unfolding jmm-heap-read-def jmm.heap-read-typed-def
 by(blast dest: *jmm'*.addr-loc-type-fun intro: *jmm'*.defval-conf)+

```

next
  fix  $h a al T v$ 
  assume  $P, h \vdash jmm' a @ al : T$ 
  and  $P, h \vdash jmm' v : \leq T$ 
  thus  $\exists h'. jmm\text{-heap-write } h a al v h'$ 
  by(auto intro: jmm-heap-write.intros)
qed

```

```

interpretation  $jmm'$ : heap-progress
  addr2thread-id thread-id2addr
  jmm-spurious-wakeups
  jmm-empty jmm-allocate jmm-typeof-addr P jmm-heap-read-typed P jmm-heap-write jmm'-hconf
   $P$ 
  for  $P$ 
by(rule jmm'-heap-progress)

```

8.18.6 Locale *heap-conf-read*

```

lemma  $jmm'\text{-heap-conf-read}$ :
  heap-conf-read addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr P) (jmm-heap-read-typed
   $P) jmm\text{-heap-write } jmm'\text{-hconf } P$ 
by(rule jmm.heap-conf-read-heap-read-typed)

```

```

interpretation  $jmm'$ : heap-conf-read
  addr2thread-id thread-id2addr
  jmm-spurious-wakeups
  jmm-empty jmm-allocate jmm-typeof-addr P jmm-heap-read-typed P jmm-heap-write jmm'-hconf
   $P$ 
  for  $P$ 
by(rule jmm'-heap-conf-read)

```

```

interpretation  $jmm'$ : heap-typesafe
  addr2thread-id thread-id2addr
  jmm-spurious-wakeups
  jmm-empty jmm-allocate jmm-typeof-addr P jmm-heap-read-typed P jmm-heap-write jmm'-hconf
   $P$ 
  for  $P$ 
  ..

```

8.18.7 Locale *allocated-heap*

```

lemma  $jmm\text{-allocated-heap}$ :
  allocated-heap addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr P) jmm-heap-write
   $jmm\text{-allocated } P$ 
proof
  show  $jmm\text{-allocated } jmm\text{-empty} = \{\}$  by(simp add: jmm-allocated-def)
next
  fix  $h' a h hT$ 
  assume  $(h', a) \in jmm\text{-allocate } h hT$ 
  thus  $jmm\text{-allocated } h' = \text{insert } a (jmm\text{-allocated } h) \wedge a \notin jmm\text{-allocated } h$ 
  by(auto simp add: jmm-heap-ops-defs split: if-split-asm)
next
  fix  $h a al v h'$ 
  assume  $jmm\text{-heap-write } h a al v h'$ 

```

thus jmm -allocated $h' = jmm$ -allocated h by cases simp
qed

interpretation jmm : allocated-heap
 $addr2thread-id$ $thread-id2addr$
 jmm -spurious-wakeups
 jmm -empty jmm -allocate jmm -typeof-addr P jmm -heap-read jmm -heap-write
 jmm -allocated
 P
for P
by(rule jmm -allocated-heap)

lemmas jmm' -allocated-heap = jmm -allocated-heap

interpretation jmm' : allocated-heap
 $addr2thread-id$ $thread-id2addr$
 jmm -spurious-wakeups
 jmm -empty jmm -allocate jmm -typeof-addr P jmm -heap-read-typed P jmm -heap-write
 jmm -allocated
 P
for P
by(rule jmm' -allocated-heap)

8.18.8 Syntax translations

notation $jmm'.external-WT'$ ($\langle -, - \vdash jmm''$ ($--'(-)$) $\rangle : \rightarrow [50, 0, 0, 0, 50] 60$)

abbreviation jmm' -red-external ::
 $'m$ prog \Rightarrow thread-id \Rightarrow JMM-heap \Rightarrow addr \Rightarrow mname \Rightarrow addr val list
 \Rightarrow (addr, thread-id, JMM-heap) external-thread-action
 \Rightarrow addr extCallRet \Rightarrow JMM-heap \Rightarrow bool
where jmm' -red-external $P \equiv jmm'.red$ -external (TYPE('m)) P P

abbreviation jmm' -red-external-syntax ::
 $'m$ prog \Rightarrow thread-id \Rightarrow addr \Rightarrow mname \Rightarrow addr val list \Rightarrow JMM-heap
 \Rightarrow (addr, thread-id, JMM-heap) external-thread-action
 \Rightarrow addr extCallRet \Rightarrow JMM-heap \Rightarrow bool
($\langle -, - \vdash jmm''$ ($((--'(-)),/-)$) $\rangle \dashrightarrow ext$ ($((-),/(-))$) $\rangle [50, 0, 0, 0, 0, 0, 0, 0, 0] 51$)
where

$P, t \vdash jmm' \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle \equiv jmm'$ -red-external P t h a M vs ta va h'

abbreviation jmm' -red-external-aggr ::
 $'m$ prog \Rightarrow thread-id \Rightarrow addr \Rightarrow mname \Rightarrow addr val list \Rightarrow JMM-heap
 \Rightarrow ((addr, thread-id, JMM-heap) external-thread-action \times addr extCallRet \times JMM-heap) set
where jmm' -red-external-aggr $P \equiv jmm'.red$ -external-aggr TYPE('m) P P

abbreviation jmm' -heap-copy-loc ::
 $'m$ prog \Rightarrow addr \Rightarrow addr \Rightarrow addr-loc \Rightarrow JMM-heap
 \Rightarrow (addr, thread-id) obs-event list \Rightarrow JMM-heap \Rightarrow bool
where jmm' -heap-copy-loc $\equiv jmm'.heap$ -copy-loc TYPE('m)

abbreviation jmm' -heap-copies ::
 $'m$ prog \Rightarrow addr \Rightarrow addr \Rightarrow addr-loc list \Rightarrow JMM-heap
 \Rightarrow (addr, thread-id) obs-event list \Rightarrow JMM-heap \Rightarrow bool

where jmm' -heap-copies $\equiv jmm'.heap-copies \text{ TYPE}('m)$

abbreviation jmm' -heap-clone ::

$'m \text{ prog} \Rightarrow \text{JMM-heap} \Rightarrow \text{addr} \Rightarrow \text{JMM-heap}$
 $\Rightarrow ((\text{addr}, \text{thread-id}) \text{ obs-event list} \times \text{addr}) \text{ option} \Rightarrow \text{bool}$

where jmm' -heap-clone $P \equiv jmm'.heap-clone \text{ TYPE}('m) P P$

end

theory *JMM-Interp* **imports**

JMM-Compiler
 $\dots/J/Deadlocked$
 $\dots/BV/JVMDeadlocked$
JMM-Type2
DRF-J
DRF-JVM

begin

lemma jmm' -J-typesafe:

$J\text{-typesafe } \text{addr}2\text{thread-id } \text{thread-id}2\text{addr } jmm\text{-empty } jmm\text{-allocate } (jmm\text{-typeof-addr } P) (jmm\text{-heap-read-typed } P) jmm\text{-heap-write } jmm\text{-hconf } P$

by *unfold-locales*

lemma jmm' -JVM-typesafe:

$JVM\text{-typesafe } \text{addr}2\text{thread-id } \text{thread-id}2\text{addr } jmm\text{-empty } jmm\text{-allocate } (jmm\text{-typeof-addr } P) (jmm\text{-heap-read-typed } P) jmm\text{-heap-write } jmm\text{-hconf } P$

by *unfold-locales*

lemma $jmm\text{-typeof-addr-comp} P$ [*simp*]:

$jmm\text{-typeof-addr } (\text{comp} P f P) = jmm\text{-typeof-addr } P$

by (*simp add: jmm-typeof-addr-def fun-eq-iff*)

lemma $\text{comp} P2\text{-comp} P1\text{-conv}$ s:

$is\text{-type } (\text{comp} P2 (\text{comp} P1 P)) = is\text{-type } P$
 $is\text{-class } (\text{comp} P2 (\text{comp} P1 P)) = is\text{-class } P$
 $jmm'\text{-addr-loc-type } (\text{comp} P2 (\text{comp} P1 P)) = jmm'\text{-addr-loc-type } P$
 $jmm'\text{-conf } (\text{comp} P2 (\text{comp} P1 P)) = jmm'\text{-conf } P$

by (*simp-all add: compP2-def heap-base.compP-conf heap-base.compP-addr-loc-type fun-eq-iff split: addr-loc.splits*)

lemma jmm' -J-JVM-conf-read:

$J\text{-JVM-conf-read } \text{addr}2\text{thread-id } \text{thread-id}2\text{addr } jmm\text{-empty } jmm\text{-allocate } (jmm\text{-typeof-addr } P) (jmm\text{-heap-read-typed } P) jmm\text{-heap-write } jmm\text{-hconf } P$

apply (*rule J-JVM-conf-read.intro*)

apply (*rule J1-JVM-conf-read.intro*)

apply (*rule JVM-conf-read.intro*)

prefer 2

apply (*rule JVM-heap-conf.intro*)

apply (*rule JVM-heap-conf-base'.intro*)

apply (*unfold compP2-def compP1-def compP-heap compP-heap-conf compP-heap-conf-read jmm-typeof-addr-comp*)

apply *unfold-locales*

done

lemma $jmm\text{-J-allocated-progress}$:

J-allocated-progress *addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr P)*
jmm-heap-read jmm-heap-write jmm-hconf jmm-allocated P
by *unfold-locales*

lemma *jmm'-J-allocated-progress*:

J-allocated-progress *addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr P)*
(jmm-heap-read-typed P) jmm-heap-write jmm-hconf jmm-allocated P
by (*unfold-locales*)

lemma *jmm'-JVM-allocated-progress*:

JVM-allocated-progress *addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr P)*
jmm-heap-read jmm-heap-write jmm-hconf jmm-allocated P
by *unfold-locales*

lemma *jmm'-JVM-allocated-progress*:

JVM-allocated-progress *addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr P)*
(jmm-heap-read-typed P) jmm-heap-write jmm-hconf jmm-allocated P
by (*unfold-locales*)

end

8.19 Specialize type safety for JMM heap implementation 2

theory *JMM-Typesafe2*

imports

JMM-Type2

JMM-Common

begin

interpretation *jmm: heap'*

addr2thread-id thread-id2addr

jmm-spurious-wakeups

jmm-empty jmm-allocate jmm-typeof-addr' P jmm-heap-read jmm-heap-write

for *P*

by (*rule heap'.intro*)(*unfold jmm-typeof-addr'-conv-jmm-typeof-addr, unfold-locales*)

abbreviation *jmm-addr-loc-type' :: 'm prog \Rightarrow addr \Rightarrow addr-loc \Rightarrow ty \Rightarrow bool ($\leftarrow \vdash_{jmm} \text{-@-} : \rightarrow$ [50, 50, 50, 50] 51)*

where *jmm-addr-loc-type' P \equiv jmm.addr-loc-type TYPE('m) P P*

lemma *jmm-addr-loc-type-conv-jmm-addr-loc-type'* [*simp, heap-independent*]:

jmm-addr-loc-type P h = jmm-addr-loc-type' P

by (*metis jmm-typeof-addr'-conv-jmm-typeof-addr heap-base'.addr-loc-type-conv-addr-loc-type*)

abbreviation *jmm-conf' :: 'm prog \Rightarrow addr val \Rightarrow ty \Rightarrow bool ($\leftarrow \vdash_{jmm} \text{-} : \leq \rightarrow$ [51, 51, 51] 50)*

where *jmm-conf' P \equiv jmm.conf TYPE('m) P P*

lemma *jmm-conf-conv-jmm-conf'* [*simp, heap-independent*]:

jmm-conf P h = jmm-conf' P

by (*metis jmm-typeof-addr'-conv-jmm-typeof-addr heap-base'.conf-conv-conf*)

lemma *jmm-heap''*: *heap'' addr2thread-id thread-id2addr jmm-allocate (jmm-typeof-addr' P) jmm-heap-write P*

by(*unfold-locales*)(*auto simp add: jmm-typeof-addr'-def jmm-allocate-def split: if-split-asm*)

interpretation *jmm: heap''*

addr2thread-id thread-id2addr

jmm-spurious-wakeups

jmm-empty jmm-allocate jmm-typeof-addr' P jmm-heap-read jmm-heap-write

for *P*

by(*rule jmm-heap''*)

interpretation *jmm': heap''*

addr2thread-id thread-id2addr

jmm-spurious-wakeups

jmm-empty jmm-allocate jmm-typeof-addr' P jmm-heap-read-typed P jmm-heap-write

for *P*

by(*rule jmm-heap''*)

abbreviation *jmm-wf-start-state* :: '*m prog* \Rightarrow *cname* \Rightarrow *mname* \Rightarrow *addr val list* \Rightarrow *bool*

where *jmm-wf-start-state P* \equiv *jmm.wf-start-state TYPE('m) P P*

abbreviation *if-heap-read-typed* ::

(*'x* \Rightarrow *bool*) \Rightarrow (*'l, 't, 'x, 'heap, 'w, ('addr* :: *addr, 'thread-id)* *obs-event*) *semantics*

\Rightarrow (*'addr* \Rightarrow *htype option*)

\Rightarrow '*m prog* \Rightarrow (*'l, 't, status* \times *'x, 'heap, 'w, ('addr, 'thread-id)* *obs-event action*) *semantics*

where

\bigwedge *final. if-heap-read-typed final r typeof-addr P t xh ta x'h' \equiv*

multithreaded-base.init-fin final r t xh ta x'h' \wedge

(\forall *ad al v T. NormalAction (ReadMem ad al v) \in set $\{ta\}_o \longrightarrow$ heap-base'.addr-loc-type TYPE('heap)*

typeof-addr P ad al T \longrightarrow heap-base'.conf TYPE('heap) typeof-addr P v T)

lemma *if-mthr-Runs-heap-read-typedI*:

fixes *final and r* :: (*'addr, 't, 'x, 'heap, 'w, ('addr* :: *addr, 'thread-id)* *obs-event*) *semantics*

assumes *trsys.Runs (multithreaded-base.redT (final-thread.init-fin-final final) (multithreaded-base.init-fin final r) (map NormalAction \circ convert-RA)) s ξ*

(**is** *trsys.Runs ?redT - -*)

and \bigwedge *ad al v T. \llbracket NormalAction (ReadMem ad al v) \in lset (lconcat (lmap (llist-of \circ obs-a \circ snd) ξ)); heap-base'.addr-loc-type TYPE('heap) typeof-addr P ad al T $\rrbracket \Longrightarrow$ heap-base'.conf TYPE('heap) typeof-addr P v T*

(**is** \bigwedge *ad al v T. \llbracket ?obs ξ ad al v; ?adal ad al T $\rrbracket \Longrightarrow$?conf v T*)

shows *trsys.Runs (multithreaded-base.redT (final-thread.init-fin-final final) (if-heap-read-typed final r typeof-addr P) (map NormalAction \circ convert-RA)) s ξ*

(**is** *trsys.Runs ?redT' - -*)

using *assms*

proof(*coinduction arbitrary: s ξ rule: trsys.Runs.coinduct[consumes 1, case-names Runs, case-conclusion Runs Stuck Step]*)

case (*Runs s ξ*)

let *?read = $\lambda\xi. (\forall$ ad al v T. ?obs ξ ad al v \longrightarrow ?adal ad al T \longrightarrow ?conf v T)*

note *read = Runs(2)*

from *Runs(1) show ?case*

proof(*cases rule: trsys.Runs.cases[consumes 1, case-names Stuck Step]*)

case (*Stuck S*)

{ **fix** *tta s'*

from $\langle \neg ?redT S tta s' \rangle$ **have** $\neg ?redT' S tta s'$

by(*rule contrapos-nn*)(*fastforce simp add: multithreaded-base.redT.simps*) }

hence $?Stuck$ using $\langle \xi = LNil \rangle$ unfolding $\langle s = S \rangle$ by *blast*
 thus $?thesis ..$
 next
 case $(Step\ S\ s'\ tta\ tta)$
 from $\langle \xi = LCons\ tta\ tta \rangle$ read
 have $read1: \bigwedge ad\ al\ v\ T. \llbracket NormalAction\ (ReadMem\ ad\ al\ v) \in set\ \{snd\ tta\}_o; ?adal\ ad\ al\ T \rrbracket$
 $\implies ?conf\ v\ T$
 and $read2: ?read\ tta$ by $(auto\ simp\ add: o-def)$
 from $\langle ?redT\ S\ tta\ s' \rangle$ read1
 have $?redT'\ S\ tta\ s'$ by $(fastforce\ simp\ add: multithreaded-base.redT.simps)$
 hence $?Step$ using $Step\ read2\ \langle s = S \rangle$ by *blast*
 thus $?thesis ..$
 qed
 qed
lemma *if-mthr-Runs-heap-read-typedD*:
 fixes $final$ and $r :: ('addr, 't, 'x, 'heap, 'w, ('addr :: addr, 'thread-id)\ obs-event)$ semantics
 assumes $Runs'$: $trsys.Runs\ (multithreaded-base.redT\ (final-thread.init-fin-final\ final)\ (if-heap-read-typed\ final\ r\ typeof-addr\ P))\ (map\ NormalAction\ \circ\ convert-RA))\ s\ \xi$
 (is $?Runs'\ s\ \xi$)
 and $stuck: \bigwedge tta\ s'\ tta\ s''. \llbracket$
 $multithreaded-base.RedT\ (final-thread.init-fin-final\ final)\ (if-heap-read-typed\ final\ r\ typeof-addr\ P)$
 $(map\ NormalAction\ \circ\ convert-RA)\ s\ tta\ s';$
 $multithreaded-base.redT\ (final-thread.init-fin-final\ final)\ (multithreaded-base.init-fin\ final\ r)\ (map$
 $NormalAction\ \circ\ convert-RA)\ s'\ tta\ s'' \rrbracket$
 $\implies \exists tta\ s''. multithreaded-base.redT\ (final-thread.init-fin-final\ final)\ (if-heap-read-typed\ final\ r\ typeof-addr$
 $P)\ (map\ NormalAction\ \circ\ convert-RA)\ s'\ tta\ s''$
 (is $\bigwedge tta\ s'\ tta\ s''. \llbracket ?RedT'\ s\ tta\ s'; ?redT\ s'\ tta\ s'' \rrbracket \implies \exists tta\ s''. ?redT'\ s'\ tta\ s''$)
 shows $trsys.Runs\ (multithreaded-base.redT\ (final-thread.init-fin-final\ final)\ (multithreaded-base.init-fin\ final\ r)\ (map\ NormalAction\ \circ\ convert-RA))\ s\ \xi$
 (is $?Runs\ s\ \xi$)
proof –
 define s' where $s' = s$
 with $Runs'$ have $\exists tta. ?RedT'\ s\ tta\ s' \wedge ?Runs'\ s'\ \xi$
 by $(auto\ simp\ add: multithreaded-base.RedT-def\ o-def)$
 thus $?Runs\ s'\ \xi$
proof $(coinduct\ rule: trsys.Runs.coinduct[consumes\ 1, case-names\ Runs, case-conclusion\ Runs\ Stuck\ Step])$
 case $(Runs\ s'\ \xi)$
 then obtain tta where $RedT'$: $?RedT'\ s\ tta\ s'$
 and $Runs'$: $?Runs'\ s'\ \xi$ by *blast*
 from $Runs'$ show $?case$
proof $(cases\ rule: trsys.Runs.cases[consumes\ 1, case-names\ Stuck\ Step])$
 case $(Stuck\ S)$
 have $\bigwedge tta\ s''. \neg ?redT\ s'\ tta\ s''$
proof
 fix $tta\ s''$
 assume $?redT\ s'\ tta\ s''$
 from $stuck[OF\ RedT'\ this]$
 obtain $tta\ s''$ where $?redT'\ s'\ tta\ s''$ by *blast*
 with $Stuck(3)[of\ tta\ s'']$ show *False*
 unfolding $\langle s' = S \rangle$ by *contradiction*
 qed
 with $Stuck(1-2)$ have $?Stuck$ by *simp*

```

thus ?thesis by(rule disjI1)
next
  case (Step S s''  $\xi'$  tta)
  note Step = Step(2-)[folded  $\langle s' = S \rangle$ ]
  from  $\langle ?redT' s' tta s'' \rangle$  have ?redT s' tta s''
    by(fastforce simp add: multithreaded-base.redT.simps)
  moreover from RedT'  $\langle ?redT' s' tta s'' \rangle$ 
  have ?RedT' s (ttas @ [tta]) s''
    unfolding multithreaded-base.RedT-def by(rule rtrancl3p-step)
  ultimately have ?Step using  $\langle \xi = LCons tta \xi' \rangle$   $\langle ?Runs' s'' \xi' \rangle$  by blast
  thus ?thesis by(rule disjI2)
qed
qed
qed

```

lemma heap-copy-loc-heap-read-typed:

```

  heap-base.heap-copy-loc (heap-base.heap-read-typed ( $\lambda-$  :: 'heap. typeof-addr) heap-read P) heap-write
  a a' al h obs h'  $\longleftrightarrow$ 
  heap-base.heap-copy-loc heap-read heap-write a a' al h obs h'  $\wedge$ 
  ( $\forall ad al v T. ReadMem ad al v \in set obs \longrightarrow heap-base'.addr-loc-type TYPE('heap) typeof-addr P ad$ 
   $al T \longrightarrow heap-base'.conf TYPE('heap) typeof-addr P v T)$ 
by(auto elim!: heap-base.heap-copy-loc.cases intro!: heap-base.heap-copy-loc.intros dest: heap-base.heap-read-typed-i
  heap-base.heap-read-typed-typed intro: heap-base.heap-read-typedI simp add: heap-base'.addr-loc-type-conv-addr-loc-t
  heap-base'.conf-conv-conf)

```

lemma heap-copies-heap-read-typed:

```

  heap-base.heap-copies (heap-base.heap-read-typed ( $\lambda-$  :: 'heap. typeof-addr) heap-read P) heap-write a
  a' als h obs h'  $\longleftrightarrow$ 
  heap-base.heap-copies heap-read heap-write a a' als h obs h'  $\wedge$ 
  ( $\forall ad al v T. ReadMem ad al v \in set obs \longrightarrow heap-base'.addr-loc-type TYPE('heap) typeof-addr P ad$ 
   $al T \longrightarrow heap-base'.conf TYPE('heap) typeof-addr P v T)$ 
  (is ?lhs  $\longleftrightarrow$  ?rhs)

```

proof

assume ?lhs **thus** ?rhs

by(induct rule: heap-base.heap-copies.induct[consumes 1])(auto intro!: heap-base.heap-copies.intros
 simp add: heap-copy-loc-heap-read-typed)

next

assume ?rhs **thus** ?lhs

by(rule conjE)(induct rule: heap-base.heap-copies.induct[consumes 1], auto intro!: heap-base.heap-copies.intros
 simp add: heap-copy-loc-heap-read-typed)

qed

lemma heap-clone-heap-read-typed:

```

  heap-base.heap-clone allocate ( $\lambda-$  :: 'heap. typeof-addr) (heap-base.heap-read-typed ( $\lambda-$  :: 'heap. typeof-addr)
  heap-read P) heap-write P a h h' obs  $\longleftrightarrow$ 
  heap-base.heap-clone allocate ( $\lambda-$  :: 'heap. typeof-addr) heap-read heap-write P a h h' obs  $\wedge$ 
  ( $\forall ad al v T obs' a'. obs = [(obs', a')] \longrightarrow ReadMem ad al v \in set obs' \longrightarrow heap-base'.addr-loc-type$ 
   $TYPE('heap) typeof-addr P ad al T \longrightarrow heap-base'.conf TYPE('heap) typeof-addr P v T)$ 
by(auto elim!: heap-base.heap-clone.cases intro: heap-base.heap-clone.intros simp add: heap-copies-heap-read-typed)

```

lemma red-external-heap-read-typed:

```

  heap-base.red-external addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate ( $\lambda-$  ::
  'heap. typeof-addr) (heap-base.heap-read-typed ( $\lambda-$  :: 'heap. typeof-addr) heap-read P) heap-write P t
  h a M vs ta va h'  $\longleftrightarrow$ 

```

heap-base.red-external addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate ($\lambda-$:: *'heap. typeof-addr*) *heap-read heap-write P t h a M vs ta va h' \wedge*
 $(\forall ad\ al\ v\ T\ obs'\ a'.\ ReadMem\ ad\ al\ v \in set\ \{\{ta\}\}_o \longrightarrow heap-base'.addr-loc-type\ TYPE('heap)$
 $typeof-addr\ P\ ad\ al\ T \longrightarrow heap-base'.conf\ TYPE('heap)\ typeof-addr\ P\ v\ T)$
by(*auto elim!*: *heap-base.red-external.cases intro: heap-base.red-external.intros simp add: heap-clone-heap-read-typed*)

lemma *red-external-aggr-heap-read-typed*:

$(ta, va, h') \in heap-base.red-external-aggr\ addr2thread-id\ thread-id2addr\ spurious-wakeups\ empty-heap$
 $allocate\ (\lambda-\ ::\ 'heap.\ typeof-addr)\ (heap-base.heap-read-typed\ (\lambda-\ ::\ 'heap.\ typeof-addr)\ heap-read\ P)$
 $heap-write\ P\ t\ h\ a\ M\ vs \longleftrightarrow$

$(ta, va, h') \in heap-base.red-external-aggr\ addr2thread-id\ thread-id2addr\ spurious-wakeups\ empty-heap$
 $allocate\ (\lambda-\ ::\ 'heap.\ typeof-addr)\ heap-read\ heap-write\ P\ t\ h\ a\ M\ vs \wedge$

$(\forall ad\ al\ v\ T\ obs'\ a'.\ ReadMem\ ad\ al\ v \in set\ \{\{ta\}\}_o \longrightarrow heap-base'.addr-loc-type\ TYPE('heap)$
 $typeof-addr\ P\ ad\ al\ T \longrightarrow heap-base'.conf\ TYPE('heap)\ typeof-addr\ P\ v\ T)$

by(*auto simp add: heap-base.red-external-aggr-def heap-clone-heap-read-typed split del: if-split split: if-split-asm*)

lemma *jmm'-heap-copy-locI*:

$\exists obs\ h'.\ heap-base.heap-copy-loc\ (heap-base.heap-read-typed\ typeof-addr\ jmm-heap-read\ P)\ jmm-heap-write$
 $a\ a'\ al\ h\ obs\ h'$

by(*auto intro!*: *heap-base.heap-copy-loc.intros jmm-heap-read-typed-default-val intro: jmm-heap-write.intros*)

lemma *jmm'-heap-copiesI*:

$\exists obs \ ::\ (addr, 'thread-id)\ obs-event\ list.$

$\exists h'.\ heap-base.heap-copies\ (heap-base.heap-read-typed\ typeof-addr\ jmm-heap-read\ P)\ jmm-heap-write$
 $a\ a'\ als\ h\ obs\ h'$

proof(*induction als arbitrary: h*)

case *Nil*

thus *?case* **by**(*blast intro: heap-base.heap-copies.intros*)

next

case (*Cons al als*)

from *jmm'-heap-copy-locI*[*of typeof-addr P a a' al h*]

obtain *ob* :: (*addr, 'thread-id*) *obs-event list and h'*

where *heap-base.heap-copy-loc (heap-base.heap-read-typed typeof-addr jmm-heap-read P) jmm-heap-write*
 $a\ a'\ al\ h\ ob\ h'$

by *blast*

with *Cons.IH*[*of h'*] **show** *?case*

by(*auto 4 4 intro: heap-base.heap-copies.intros*)

qed

lemma *jmm'-heap-cloneI*:

fixes *obsa* :: ((*addr, 'thread-id*) *obs-event list \times addr*) *option*

assumes *heap-base.heap-clone allocate typeof-addr jmm-heap-read jmm-heap-write P h a h' obsa*

shows $\exists h'.\ \exists obsa \ ::\ ((addr, 'thread-id)\ obs-event\ list \times\ addr)\ option.$

heap-base.heap-clone allocate typeof-addr (heap-base.heap-read-typed typeof-addr jmm-heap-read
 $P)\ jmm-heap-write\ P\ h\ a\ h'\ obsa$

using *assms*

proof(*cases rule: heap-base.heap-clone.cases[consumes 1, case-names Fail Obj Arr]*)

case *Fail*

thus *?thesis* **by**(*blast intro: heap-base.heap-clone.intros*)

next

case (*Obj C h' a' FDTs obs h'*)

with *jmm'-heap-copiesI*[of *typeof-addr P a a' map* ($\lambda((F, D), Tfm). CField D F$) *FDTs h'*]
show *?thesis* **by**(*blast intro: heap-base.heap-clone.intros*)
next
case (*Arr T n h' a' FDTs obs h''*)
with *jmm'-heap-copiesI*[of *typeof-addr P a a' map* ($\lambda((F, D), Tfm). CField D F$) *FDTs @ map*
ACell [0..<n]]
show *?thesis* **by**(*blast intro: heap-base.heap-clone.intros*)
qed

lemma *jmm'-red-externalI*:

$\bigwedge final.$
 $\llbracket heap-base.red-external\ addr2thread-id\ thread-id2addr\ spurious-wakeups\ empty-heap\ allocate\ typeof-addr\ jmm-heap-read\ jmm-heap-write\ P\ t\ h\ a\ M\ vs\ ta\ va\ h';$
 $final-thread.actions-ok\ final\ s\ t\ ta \rrbracket$
 $\implies \exists ta\ va\ h'. heap-base.red-external\ addr2thread-id\ thread-id2addr\ spurious-wakeups\ empty-heap$
 $allocate\ typeof-addr\ (heap-base.heap-read-typed\ typeof-addr\ jmm-heap-read\ P)\ jmm-heap-write\ P\ t\ h\ a$
 $M\ vs\ ta\ va\ h' \wedge final-thread.actions-ok\ final\ s\ t\ ta$
proof(*erule heap-base.red-external.cases, goal-cases*)
case 19
thus *?case apply* –
apply(*drule jmm'-heap-cloneI, clarify*)
apply(*rename-tac obsa', case-tac obsa'*)
by(*auto 4 4 intro: heap-base.red-external.intros simp add: final-thread.actions-ok-iff simp del:*
split-paired-Ex)
next
case 20
thus *?case apply* –
apply(*drule jmm'-heap-cloneI, clarify*)
apply(*rename-tac obsa', case-tac obsa'*)
by(*auto 4 4 intro: heap-base.red-external.intros simp add: final-thread.actions-ok-iff simp del:*
split-paired-Ex)
qed(*blast intro: heap-base.red-external.intros*)+

lemma *red-external-aggr-heap-read-typedI*:

$\bigwedge final.$
 $\llbracket (ta, vah') \in heap-base.red-external-aggr\ addr2thread-id\ thread-id2addr\ spurious-wakeups\ empty-heap$
 $allocate\ typeof-addr\ jmm-heap-read\ jmm-heap-write\ P\ t\ h\ a\ M\ vs;$
 $final-thread.actions-ok\ final\ s\ t\ ta$
 \rrbracket
 $\implies \exists ta\ vah'. (ta, vah') \in heap-base.red-external-aggr\ addr2thread-id\ thread-id2addr\ spurious-wakeups$
 $empty-heap\ allocate\ typeof-addr\ (heap-base.heap-read-typed\ typeof-addr\ jmm-heap-read\ P)\ jmm-heap-write$
 $P\ t\ h\ a\ M\ vs \wedge final-thread.actions-ok\ final\ s\ t\ ta$
apply(*simp add: heap-base.red-external-aggr-def split-beta split del: if-split split: if-split-asm del: split-paired-Ex*)
apply(*auto simp del: split-paired-Ex*)
apply(*drule jmm'-heap-cloneI*)
apply(*clarify*)
apply(*rename-tac obsa, case-tac obsa*)
apply(*force simp add: final-thread.actions-ok-iff del: disjCI intro: disjI1 disjI2 simp del: split-paired-Ex*)
apply(*force simp add: final-thread.actions-ok-iff del: disjCI intro: disjI1 disjI2 simp del: split-paired-Ex*)
apply(*drule jmm'-heap-cloneI*)
apply *clarify*
apply(*rename-tac obsa, case-tac obsa*)
apply(*force simp add: final-thread.actions-ok-iff del: disjCI intro: disjI1 disjI2 simp del: split-paired-Ex*)
apply(*force simp add: final-thread.actions-ok-iff del: disjCI intro: disjI1 disjI2 simp del: split-paired-Ex*)

done

end

8.20 JMM type safety for source code

theory *JMM-J-Typesafe* **imports**

JMM-Typesafe2

DRF-J

begin

locale *J-allocated-heap-conf'* =

h: *J-heap-conf*

addr2thread-id thread-id2addr

spurious-wakeups

empty-heap allocate λ -. *typeof-addr heap-read heap-write hconf*

P

+

h: *J-allocated-heap*

addr2thread-id thread-id2addr

spurious-wakeups

empty-heap allocate λ -. *typeof-addr heap-read heap-write*

allocated

P

+

heap''

addr2thread-id thread-id2addr

spurious-wakeups

empty-heap allocate typeof-addr heap-read heap-write

P

for *addr2thread-id* :: ('*addr* :: *addr*) \Rightarrow '*thread-id*

and *thread-id2addr* :: '*thread-id* \Rightarrow '*addr*

and *spurious-wakeups* :: *bool*

and *empty-heap* :: '*heap*

and *allocate* :: '*heap* \Rightarrow *h*type \Rightarrow ('*heap* \times '*addr*) *set*

and *typeof-addr* :: '*addr* \rightarrow *h*type

and *heap-read* :: '*heap* \Rightarrow '*addr* \Rightarrow *addr-loc* \Rightarrow '*addr val* \Rightarrow *bool*

and *heap-write* :: '*heap* \Rightarrow '*addr* \Rightarrow *addr-loc* \Rightarrow '*addr val* \Rightarrow '*heap* \Rightarrow *bool*

and *hconf* :: '*heap* \Rightarrow *bool*

and *allocated* :: '*heap* \Rightarrow '*addr set*

and *P* :: '*addr J-prog*

sublocale *J-allocated-heap-conf'* < *h*: *J-allocated-heap-conf*

addr2thread-id thread-id2addr

spurious-wakeups

empty-heap allocate λ -. *typeof-addr heap-read heap-write hconf allocated*

P

by(*unfold-locales*)

context *J-allocated-heap-conf'* **begin**

lemma *red-New-type-match*:

\llbracket *h.red' P t e s ta e' s'*; *NewHeapElem ad CTn* \in *set* $\{\{ta\}_0\}$; *typeof-addr ad* \neq *None* \rrbracket

\Rightarrow *typeof-addr* *ad* = $\lfloor C\text{Tn} \rfloor$
and *reds-New-type-match*:
 $\llbracket h.\text{reds}' P t \text{ es } s \text{ ta } \text{es}' s'; \text{NewHeapElem } ad \ C\text{Tn} \in \text{set } \{\text{ta}\}_o; \text{typeof-addr } ad \neq \text{None} \rrbracket$
 \Rightarrow *typeof-addr* *ad* = $\lfloor C\text{Tn} \rfloor$
by(*induct rule*: *h.red-reds.inducts*)(*auto dest*: *allocate-typeof-addr-SomeD red-external-New-type-match*)

lemma *mred-known-addr-typing'*:

assumes *wf*: *wf-J-prog P*

and *ok*: *h.start-heap-ok*

shows *known-addr-typing' addr2thread-id thread-id2addr empty-heap allocate typeof-addr heap-write allocated h.J-known-addr final-expr (h.mred P) ($\lambda t x h. \exists ET. h.\text{sconf-type-ok } ET t x h$) P*

proof –

interpret *known-addr-typing*

addr2thread-id thread-id2addr

spurious-wakeups

empty-heap allocate $\lambda\cdot$. *typeof-addr heap-read heap-write*

allocated h.J-known-addr

final-expr h.mred P $\lambda t x h. \exists ET. h.\text{sconf-type-ok } ET t x h$

P

using *assms* **by**(*rule h.mred-known-addr-typing*)

show *?thesis* **by** *unfold-locales(auto dest: red-New-type-match)*

qed

lemma *J-legal-read-value-typeable*:

assumes *wf*: *wf-J-prog P*

and *wf-start*: *h.wf-start-state P C M vs*

and *legal*: *weakly-legal-execution P (h.J- \mathcal{E} P C M vs status) (E, ws)*

and *a*: *enat a < llength E*

and *read*: *action-obs E a = NormalAction (ReadMem ad al v)*

shows $\exists T. P \vdash ad@al : T \wedge P \vdash v : \leq T$

proof –

note *wf*

moreover from *wf-start* **have** *h.start-heap-ok* **by** *cases*

moreover from *wf wf-start*

have *ts-ok* ($\lambda t x h. \exists ET. h.\text{sconf-type-ok } ET t x h$) (*thr (h.J-start-state P C M vs)*) *h.start-heap*

by(*rule h.J-start-state-sconf-type-ok*)

moreover from *wf* **have** *wf-syscls P* **by**(*rule wf-prog-wf-syscls*)

ultimately show *?thesis* **using** *legal a read*

by(*rule known-addr-typing'.weakly-legal-read-value-typeable[OF mred-known-addr-typing']*)

qed

end

8.20.1 Specific part for JMM implementation 2

abbreviation *jmm-J- \mathcal{E}*

$:: \text{addr } J\text{-prog} \Rightarrow \text{cname} \Rightarrow \text{mname} \Rightarrow \text{addr val list} \Rightarrow \text{status} \Rightarrow (\text{addr} \times (\text{addr}, \text{addr}) \text{ obs-event action}) \text{ llist set}$

where

jmm-J- \mathcal{E} P \equiv

J-heap-base.J- \mathcal{E} addr2thread-id thread-id2addr jmm-spurious-wakeups jmm-empty jmm-allocate (jmm-typeof-addr P) jmm-heap-read jmm-heap-write P

abbreviation $jmm'-J-\mathcal{E}$

$:: \text{addr } J\text{-prog} \Rightarrow \text{cname} \Rightarrow \text{mname} \Rightarrow \text{addr val list} \Rightarrow \text{status} \Rightarrow (\text{addr} \times (\text{addr}, \text{addr}) \text{ obs-event action}) \text{ llist set}$

where

$jmm'-J-\mathcal{E} P \equiv$
 $J\text{-heap-base.}J-\mathcal{E} \text{ addr2thread-id thread-id2addr jmm-spurious-wakeups jmm-empty jmm-allocate (jmm-typeof-addr } P)$
 $(jmm\text{-heap-read-typed } P) \text{ jmm-heap-write } P$

lemma $jmm\text{-}J\text{-heap-conf}$:

$J\text{-heap-conf addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr } P) \text{ jmm-heap-write } jmm\text{-hconf } P$

by(unfold-locales)

lemma $jmm\text{-}J\text{-allocated-heap-conf}$: $J\text{-allocated-heap-conf addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr } P) \text{ jmm-heap-write } jmm\text{-hconf } jmm\text{-allocated } P$

by(unfold-locales)

lemma $jmm\text{-}J\text{-allocated-heap-conf}'$:

$J\text{-allocated-heap-conf}' \text{ addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr}' P) \text{ jmm-heap-write } jmm\text{-hconf } jmm\text{-allocated } P$

apply($\text{rule } J\text{-allocated-heap-conf}'.\text{intro}$)

apply($\text{unfold } jmm\text{-typeof-addr}'\text{-conv-}jmm\text{-typeof-addr}$)

apply(unfold-locales)

done

lemma $\text{red-heap-read-typedD}$:

$J\text{-heap-base.red}' \text{ addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda - :: 'heap.\text{typeof-addr}) \text{ (heap-base.heap-read-typed } (\lambda - :: 'heap.\text{typeof-addr}) \text{ heap-read } P) \text{ heap-write } P \text{ t e s ta } e' s' \longleftrightarrow$

$J\text{-heap-base.red}' \text{ addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda - :: 'heap.\text{typeof-addr}) \text{ heap-read heap-write } P \text{ t e s ta } e' s' \wedge$

$(\forall \text{ ad al v T. ReadMem ad al v } \in \text{ set } \{\{ta\}_o \longrightarrow \text{heap-base}'.\text{addr-loc-type TYPE('heap) typeof-addr } P \text{ ad al } T \longrightarrow \text{heap-base}'.\text{conf TYPE('heap) typeof-addr } P \text{ v } T)$

(is $?lhs1 \longleftrightarrow ?rhs1a \wedge ?rhs1b)$

and $\text{reds-heap-read-typedD}$:

$J\text{-heap-base.reds}' \text{ addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda - :: 'heap.\text{typeof-addr}) \text{ (heap-base.heap-read-typed } (\lambda - :: 'heap.\text{typeof-addr}) \text{ heap-read } P) \text{ heap-write } P \text{ t e s s ta } es' s' \longleftrightarrow$

$J\text{-heap-base.reds}' \text{ addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda - :: 'heap.\text{typeof-addr}) \text{ heap-read heap-write } P \text{ t e s s ta } es' s' \wedge$

$(\forall \text{ ad al v T. ReadMem ad al v } \in \text{ set } \{\{ta\}_o \longrightarrow \text{heap-base}'.\text{addr-loc-type TYPE('heap) typeof-addr } P \text{ ad al } T \longrightarrow \text{heap-base}'.\text{conf TYPE('heap) typeof-addr } P \text{ v } T)$

(is $?lhs2 \longleftrightarrow ?rhs2a \wedge ?rhs2b)$

proof –

have $(?lhs1 \longrightarrow ?rhs1a \wedge ?rhs1b) \wedge (?lhs2 \longrightarrow ?rhs2a \wedge ?rhs2b)$

apply($\text{induct rule: } J\text{-heap-base.red-reds.induct}$)

prefer 50

apply($\text{subst (asm) red-external-heap-read-typed}$)

apply($\text{fastforce intro!: } J\text{-heap-base.red-reds.RedCallExternal simp add: convert-extTA-def}$)

prefer 49

apply(*fastforce dest: J-heap-base.red-reds.RedCall*)

apply(*auto intro: J-heap-base.red-reds.intros dest: heap-base.heap-read-typed-into-heap-read heap-base.heap-read-dest: heap-base'.addr-loc-type-conv-addr-loc-type[THEN fun-cong, THEN fun-cong, THEN fun-cong, THEN iffD2] heap-base'.conf-conv-conf[THEN fun-cong, THEN fun-cong, THEN iffD1]*)

done

moreover have ($?rhs1a \longrightarrow ?rhs1b \longrightarrow ?lhs1$) \wedge ($?rhs2a \longrightarrow ?rhs2b \longrightarrow ?lhs2$)

apply(*induct rule: J-heap-base.red-reds.induct*)

prefer 50

apply *simp*

apply(*intro strip*)

apply(*erule (1) J-heap-base.red-reds.RedCallExternal*)

apply(*subst red-external-heap-read-typed, erule conjI*)

apply(*blast+*)[4]

prefer 49

apply(*fastforce dest: J-heap-base.red-reds.RedCall*)

apply(*auto intro: J-heap-base.red-reds.intros intro!: heap-base.heap-read-typedI dest: heap-base'.addr-loc-type-conv-fun-cong, THEN fun-cong, THEN fun-cong, THEN iffD1] intro: heap-base'.conf-conv-conf[THEN fun-cong, THEN fun-cong, THEN iffD2]*)

done

ultimately show $?lhs1 \longleftrightarrow ?rhs1a \wedge ?rhs1b$ $?lhs2 \longleftrightarrow ?rhs2a \wedge ?rhs2b$ **by** *blast+*
qed

lemma *if-mred-heap-read-typedD*:

multithreaded-base.init-fin final-expr (J-heap-base.mred addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate ($\lambda- :: 'heap.typeof-addr$) (heap-base.heap-read-typed ($\lambda- :: 'heap.typeof-addr$) heap-read P) heap-write P) t xh ta x'h' \longleftrightarrow

if-heap-read-typed final-expr (J-heap-base.mred addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate ($\lambda- :: 'heap.typeof-addr$) heap-read heap-write P) typeof-addr P t xh ta x'h'

unfolding *multithreaded-base.init-fin.simps*

by(*subst red-heap-read-typedD*) *fastforce*

lemma *J- \mathcal{E} -heap-read-typedI*:

$\llbracket E \in J\text{-heap-base.}J\text{-}\mathcal{E} \text{ addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda- :: 'heap.typeof-addr) \text{ heap-read heap-write } P \ C \ M \ vs \ status;$

$\bigwedge ad \ al \ v \ T. \llbracket NormalAction (ReadMem \ ad \ al \ v) \in snd \ 'lset \ E; \text{heap-base'.addr-loc-type } TYPE('heap) \text{typeof-addr } P \ ad \ al \ T \rrbracket \implies \text{heap-base'.conf } TYPE('heap) \text{typeof-addr } P \ v \ T \rrbracket$

$\implies E \in J\text{-heap-base.}J\text{-}\mathcal{E} \text{ addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda- :: 'heap.typeof-addr) \text{ (heap-base.heap-read-typed } (\lambda- :: 'heap.typeof-addr) \text{ heap-read } P) \text{ heap-write } P \ C \ M \ vs \ status$

apply(*erule imageE, hypsubst*)

apply(*rule imageI*)

apply(*erule multithreaded-base. \mathcal{E} .cases, hypsubst*)

apply(*rule multithreaded-base. \mathcal{E} .intros*)

apply(*subst if-mred-heap-read-typedD[abs-def]*)

apply(*erule if-mthr-Runs-heap-read-typedI*)

apply(*auto simp add: image-Un lset-lmap[symmetric] lmap-lconcat llist.map-comp o-def split-def simp del: lset-lmap*)

done

lemma *jmm'-redI*:

$\llbracket J\text{-heap-base.red}' \text{ addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr}$


```

jmm-heap-read jmm-heap-write P t e s ta e' s';
  final-thread.actions-ok (final-thread.init-fin-final final-expr) S t ta ]
  ⇒ ∃ ta e' s'. J-heap-base.red' addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate
  typeof-addr (heap-base.heap-read-typed typeof-addr jmm-heap-read P) jmm-heap-write P t e s ta e' s'
  ∧ final-thread.actions-ok (final-thread.init-fin-final final-expr) S t ta
  (is [ ?red'; ?aok ] ⇒ ?concl)
  and jmm'-redsI:
  [ J-heap-base.reds' addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr
  jmm-heap-read jmm-heap-write P t e s s ta es' s';
  final-thread.actions-ok (final-thread.init-fin-final final-expr) S t ta ]
  ⇒ ∃ ta es' s'. J-heap-base.reds' addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate
  typeof-addr (heap-base.heap-read-typed typeof-addr jmm-heap-read P) jmm-heap-write P t e s s ta es' s'
  ∧
  final-thread.actions-ok (final-thread.init-fin-final final-expr) S t ta
  (is [ ?reds'; ?aoks ] ⇒ ?concls)
proof -
note [simp del] = split-paired-Ex
  and [simp add] = final-thread.actions-ok-iff heap-base.THE-addr-loc-type heap-base.defval-conf
  and [intro] = jmm-heap-read-typed-default-val

let ?v = λh a al. default-val (THE T. heap-base.addr-loc-type typeof-addr P h a al T)

have (?red' → ?aok → ?concl) ∧ (?reds' → ?aoks → ?concls)
proof(induct rule: J-heap-base.red-reds.induct)
  case (23 h a T n i v l)
  thus ?case by(auto 4 6 intro: J-heap-base.red-reds.RedAAcc[where v=?v h a (ACell (nat (sint
  i)))]))
  next
  case (35 h a D F v l)
  thus ?case by(auto 4 5 intro: J-heap-base.red-reds.RedFAcc[where v=?v h a (CField D F)])
  next
  case RedCASSucceed: (45 h a D F v v' h')
  thus ?case
  proof(cases v = ?v h a (CField D F))
    case True
    with RedCASSucceed show ?thesis
    by(fastforce intro: J-heap-base.red-reds.RedCASSucceed[where v=?v h a (CField D F)])
  next
  case False
  with RedCASSucceed show ?thesis
  by(fastforce intro: J-heap-base.red-reds.RedCASFail[where v''=?v h a (CField D F)])
  qed
next
  case RedCASFail: (46 h a D F v'' v v' l)
  thus ?case
  proof(cases v = ?v h a (CField D F))
    case True
    with RedCASFail show ?thesis
    by(fastforce intro: J-heap-base.red-reds.RedCASSucceed[where v=?v h a (CField D F)] jmm-heap-write.intros)
  next
  case False
  with RedCASFail show ?thesis
  by(fastforce intro: J-heap-base.red-reds.RedCASFail[where v''=?v h a (CField D F)])
  qed

```

```

next
  case (50 s a hU M Ts T D vs ta va h' ta' e' s')
  thus ?case
    apply clarify
    apply (drule jmm'-red-externalI, simp)
    apply (auto 4 4 intro: J-heap-base.red-reds.RedCallExternal)
    done
next
  case (52 e h l V vo ta e' h' l' T)
  thus ?case
    by (clarify) (iprover intro: J-heap-base.red-reds.BlockRed)
qed (blast intro: J-heap-base.red-reds.intros)+
thus [ [ ?red'; ?aok ]  $\implies$  ?concl and [ [ ?reds'; ?aoks ]  $\implies$  ?concls by blast+
qed

```

lemma *if-mred-heap-read-not-stuck:*

```

[ [ multithreaded-base.init-fin final-expr (J-heap-base.mred addr2thread-id thread-id2addr spurious-wakeups
empty-heap allocate typeof-addr jmm-heap-read jmm-heap-write P) t xh ta x'h';
  final-thread.actions-ok (final-thread.init-fin-final final-expr) s t ta ]
 $\implies$ 
 $\exists$  ta x'h'. multithreaded-base.init-fin final-expr (J-heap-base.mred addr2thread-id thread-id2addr spu-
rious-wakeups empty-heap allocate typeof-addr (heap-base.heap-read-typed typeof-addr jmm-heap-read
P) jmm-heap-write P) t xh ta x'h'  $\wedge$  final-thread.actions-ok (final-thread.init-fin-final final-expr) s t ta
apply (erule multithreaded-base.init-fin.cases)
apply hypsubst
apply clarify
apply (drule jmm'-redI)
apply (simp add: final-thread.actions-ok-iff)
apply clarify
apply (subst (2) split-paired-Ex)
apply (subst (2) split-paired-Ex)
apply (subst (2) split-paired-Ex)
apply (rule exI conjI)+
apply (rule multithreaded-base.init-fin.intros)
apply (simp)
apply (simp add: final-thread.actions-ok-iff)
apply (blast intro: multithreaded-base.init-fin.intros)
apply (blast intro: multithreaded-base.init-fin.intros)
done

```

lemma *if-mredT-heap-read-not-stuck:*

```

multithreaded-base.redT (final-thread.init-fin-final final-expr) (multithreaded-base.init-fin final-expr
(J-heap-base.mred addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr jmm-heap-read
jmm-heap-write P)) convert-RA' s tta s'
 $\implies$   $\exists$  tta s'. multithreaded-base.redT (final-thread.init-fin-final final-expr) (multithreaded-base.init-fin
final-expr (J-heap-base.mred addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr
(heap-base.heap-read-typed typeof-addr jmm-heap-read P) jmm-heap-write P)) convert-RA' s tta s'
apply (erule multithreaded-base.redT.cases)
apply hypsubst
apply (drule (1) if-mred-heap-read-not-stuck)
apply (erule exE)+
apply (rename-tac ta' x'h')
apply (insert redT-updWs-total)
apply (erule-tac x=t in meta-allE)

```

```

apply(erule-tac  $x=wset\ s$  in meta-allE)
apply(erule-tac  $x=\{ta\}_w$  in meta-allE)
apply clarsimp
apply(rule exI)+
apply(auto intro!: multithreaded-base.redT.intros)[1]
apply hypsubst
apply(rule exI conjI)+
apply(rule multithreaded-base.redT.redT-acquire)
apply assumption+
done

```

lemma *J- \mathcal{E} -heap-read-typedD*:

$E \in J\text{-heap-base.}J\text{-}\mathcal{E}\text{ addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate }(\lambda\text{-. typeof-addr})$
 $(\text{heap-base.heap-read-typed }(\lambda\text{-. typeof-addr})\text{ jmm-heap-read }P)\text{ jmm-heap-write }P\ C\ M\ \text{vs status}$
 $\implies E \in J\text{-heap-base.}J\text{-}\mathcal{E}\text{ addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate }(\lambda\text{-. typeof-addr})\text{ jmm-heap-read jmm-heap-write }P\ C\ M\ \text{vs status}$

```

apply(erule imageE, hypsubst)
apply(rule imageI)
apply(erule multithreaded-base. $\mathcal{E}$ .cases, hypsubst)
apply(rule multithreaded-base. $\mathcal{E}$ .intros)
apply(subst (asm) if-mred-heap-read-typedD[abs-def])
apply(erule if-mthr-Runs-heap-read-typedD)
apply(erule if-mredT-heap-read-not-stuck[where typeof-addr= $\lambda\text{-. typeof-addr}$ , unfolded if-mred-heap-read-typedD[abs-def]])
done

```

lemma *J- \mathcal{E} -typesafe-subset*: $\text{jmm}'\text{-}J\text{-}\mathcal{E}\ P\ C\ M\ \text{vs status} \subseteq \text{jmm}\text{-}J\text{-}\mathcal{E}\ P\ C\ M\ \text{vs status}$

unfolding $\text{jmm-typeof-addr-def}[abs-def]$
by(rule subsetI)(erule *J- \mathcal{E} -heap-read-typedD*)

lemma *J-legal-typesafe1*:

assumes wfP : $wf\text{-}J\text{-prog }P$
and ok : $\text{jmm}\text{-}wf\text{-start-state }P\ C\ M\ \text{vs}$
and $legal$: $\text{legal-execution }P\ (\text{jmm}\text{-}J\text{-}\mathcal{E}\ P\ C\ M\ \text{vs status})\ (E, ws)$
shows $\text{legal-execution }P\ (\text{jmm}'\text{-}J\text{-}\mathcal{E}\ P\ C\ M\ \text{vs status})\ (E, ws)$

proof –

```

let  $\mathcal{E}$  =  $\text{jmm}\text{-}J\text{-}\mathcal{E}\ P\ C\ M\ \text{vs status}$ 
let  $\mathcal{E}'$  =  $\text{jmm}'\text{-}J\text{-}\mathcal{E}\ P\ C\ M\ \text{vs status}$ 
from  $legal$  obtain  $J$ 
  where  $justified$ :  $P \vdash (E, ws)\ \text{justified-by }J$ 
  and  $range$ :  $\text{range }(\text{justifying-exec} \circ J) \subseteq \mathcal{E}$ 
  and  $E$ :  $E \in \mathcal{E}$  and  $wf$ :  $P \vdash (E, ws) \checkmark$  by(auto simp add: gen-legal-execution.simps)
let  $?J = J(0 := (\text{committed} = \{\}), \text{justifying-exec} = \text{justifying-exec } (J\ 1), \text{justifying-ws} = \text{justifying-ws } (J\ 1), \text{action-translation} = id)$ 

```

from wfP **have** $wf\text{-sys}$: $wf\text{-syscls }P$ **by**(rule $wf\text{-prog}\text{-}wf\text{-syscls}$)

from $justified$ **have** $P \vdash (\text{justifying-exec } (J\ 1), \text{justifying-ws } (J\ 1)) \checkmark$

by(simp add: justification-well-formed-def)

with $justified$ **have** $P \vdash (E, ws)\ \text{justified-by }?J$ **by**(rule drop-0th-justifying-exec)

moreover **have** $\text{range }(\text{justifying-exec} \circ ?J) \subseteq \mathcal{E}'$

proof

fix ξ

assume $\xi \in \text{range }(\text{justifying-exec} \circ ?J)$

then **obtain** n **where** $\xi = \text{justifying-exec } (?J\ n)$ **by** auto

**then obtain n where ξ : $\xi = \text{justifying-exec } (J \ n)$ and n : $n > 0$ by (auto split: if-split-asm)
from range ξ have $\xi \in ?\mathcal{E}$ by auto
thus $\xi \in ?\mathcal{E}'$ unfolding $\text{jmm-typeof-addr'-conv-jmm-type-addr}[\text{symmetric, abs-def}]$
proof (rule $J\text{-}\mathcal{E}\text{-heap-read-typedI}$)
fix $ad \ al \ v \ T$
assume $read$: $\text{NormalAction } (\text{ReadMem } ad \ al \ v) \in \text{snd } ' \text{lset } \xi$
and $adal$: $P \vdash \text{jmm } ad@al : T$
from $read$ obtain a where a : $\text{enat } a < \text{llength } \xi \text{ action-obs } \xi \ a = \text{NormalAction } (\text{ReadMem } ad \ al \ v)$
unfolding lset-conv-lnth by (auto simp add: action-obs-def)
with $J\text{-allocated-heap-conf'}.mred-known-addr-typing'[OF \text{jmm-}J\text{-allocated-heap-conf' } wfP \ \text{jmm-start-heap-ok}]$
 $J\text{-heap-conf}.J\text{-start-state-sconf-type-ok}[OF \ \text{jmm-}J\text{-heap-conf } wfP \ ok]$
 $wf\text{-sys is-justified-by-imp-is-weakly-justified-by}[OF \ \text{justified } wf]$ range n
have $\exists T. P \vdash \text{jmm } ad@al : T \wedge P \vdash \text{jmm } v : \leq T$
unfolding $\text{jmm-typeof-addr'-conv-jmm-type-addr}[\text{symmetric, abs-def}] \ \xi$
by (rule $\text{known-addr-typing'}.read\text{-value-typeable-justifying}$)
thus $P \vdash \text{jmm } v : \leq T$ using $adal$
by (auto dest: $\text{jmm.addr-loc-type-fun}[\text{unfolded } \text{jmm-typeof-addr-conv-jmm-typeof-addr'}, \text{unfolded } \text{heap-base'}.addr\text{-loc-type-conv-addr-loc-type}]$)
qed
qed
moreover from E have $E \in ?\mathcal{E}'$
unfolding $\text{jmm-typeof-addr'-conv-jmm-type-addr}[\text{symmetric, abs-def}]$
proof (rule $J\text{-}\mathcal{E}\text{-heap-read-typedI}$)
fix $ad \ al \ v \ T$
assume $read$: $\text{NormalAction } (\text{ReadMem } ad \ al \ v) \in \text{snd } ' \text{lset } E$
and $adal$: $P \vdash \text{jmm } ad@al : T$
from $read$ obtain a where a : $\text{enat } a < \text{llength } E \text{ action-obs } E \ a = \text{NormalAction } (\text{ReadMem } ad \ al \ v)$
unfolding lset-conv-lnth by (auto simp add: action-obs-def)
with $\text{jmm-}J\text{-allocated-heap-conf' } wfP \ ok \ \text{legal-imp-weakly-legal-execution}[OF \ \text{legal}]$
have $\exists T. P \vdash \text{jmm } ad@al : T \wedge P \vdash \text{jmm } v : \leq T$
unfolding $\text{jmm-typeof-addr'-conv-jmm-type-addr}[\text{symmetric, abs-def}]$
by (rule $J\text{-allocated-heap-conf'}.J\text{-legal-read-value-typeable}$)
thus $P \vdash \text{jmm } v : \leq T$ using $adal$
by (auto dest: $\text{jmm.addr-loc-type-fun}[\text{unfolded } \text{jmm-typeof-addr-conv-jmm-typeof-addr'}, \text{unfolded } \text{heap-base'}.addr\text{-loc-type-conv-addr-loc-type}]$)
qed
ultimately show $?thesis$ using wf unfolding $\text{gen-legal-execution.simps}$ by blast
qed**

lemma $J\text{-weakly-legal-typesafe1}$:

assumes wfP : $wf\text{-}J\text{-prog } P$

and ok : $\text{jmm-wf-start-state } P \ C \ M \ vs$

and $legal$: $\text{weakly-legal-execution } P \ (\text{jmm-}J\text{-}\mathcal{E} \ P \ C \ M \ vs \ \text{status}) \ (E, \ ws)$

shows $\text{weakly-legal-execution } P \ (\text{jmm}'\text{-}J\text{-}\mathcal{E} \ P \ C \ M \ vs \ \text{status}) \ (E, \ ws)$

proof –

let $?\mathcal{E} = \text{jmm-}J\text{-}\mathcal{E} \ P \ C \ M \ vs \ \text{status}$

let $?\mathcal{E}' = \text{jmm}'\text{-}J\text{-}\mathcal{E} \ P \ C \ M \ vs \ \text{status}$

from $legal$ obtain J

where justified : $P \vdash (E, \ ws) \ \text{weakly-justified-by } J$

and range : $\text{range } (\text{justifying-exec } \circ J) \subseteq ?\mathcal{E}$

and E : $E \in ?\mathcal{E}$ and wf : $P \vdash (E, \ ws) \ \checkmark$ by (auto simp add: $\text{gen-legal-execution.simps}$)

let $?J = J(0 := \{\}, \text{justifying-exec} = \text{justifying-exec } (J \ 1), \text{justifying-ws} = \text{justifying-ws}$

($J\ 1$), $action\text{-}translation = id$))

```

from wfP have wf-sys: wf-syscls P by(rule wf-prog-wf-syscls)

from justified have P ⊢ (justifying-exec (J 1), justifying-ws (J 1)) √
  by(simp add: justification-well-formed-def)
with justified have P ⊢ (E, ws) weakly-justified-by ?J by(rule drop-0th-weakly-justifying-exec)
moreover have range (justifying-exec ∘ ?J) ⊆ ?E'
proof
  fix ξ
  assume ξ ∈ range (justifying-exec ∘ ?J)
  then obtain n where ξ = justifying-exec (?J n) by auto
  then obtain n where ξ: ξ = justifying-exec (J n) and n: n > 0 by(auto split: if-split-asm)
  from range ξ have ξ ∈ ?E by auto
  thus ξ ∈ ?E' unfolding jmm-typeof-addr'-conv-jmm-type-addr[symmetric, abs-def]
  proof(rule J-E-heap-read-typedI)
    fix ad al v T
    assume read: NormalAction (ReadMem ad al v) ∈ snd ' lset ξ
    and adal: P ⊢ jmm ad@al : T
    from read obtain a where a: enat a < llength ξ action-obs ξ a = NormalAction (ReadMem ad
al v)
    unfolding lset-conv-lnth by(auto simp add: action-obs-def)
    with J-allocated-heap-conf'.mred-known-addr-typing'[OF jmm-J-allocated-heap-conf' wfP jmm-start-heap-ok]
      J-heap-conf'.J-start-state-sconf-type-ok[OF jmm-J-heap-conf wfP ok]
      wf-sys justified range n
    have ∃ T. P ⊢ jmm ad@al : T ∧ P ⊢ jmm v :≤ T
    unfolding jmm-typeof-addr'-conv-jmm-type-addr[symmetric, abs-def] ξ
    by(rule known-addr-typing'.read-value-typeable-justifying)
    thus P ⊢ jmm v :≤ T using adal
    by(auto dest: jmm.addr-loc-type-fun[unfolded jmm-typeof-addr-conv-jmm-typeof-addr', unfolded
heap-base'.addr-loc-type-conv-addr-loc-type])
    qed
  qed
moreover from E have E ∈ ?E'
  unfolding jmm-typeof-addr'-conv-jmm-type-addr[symmetric, abs-def]
proof(rule J-E-heap-read-typedI)
  fix ad al v T
  assume read: NormalAction (ReadMem ad al v) ∈ snd ' lset E
  and adal: P ⊢ jmm ad@al : T
  from read obtain a where a: enat a < llength E action-obs E a = NormalAction (ReadMem ad
al v)
  unfolding lset-conv-lnth by(auto simp add: action-obs-def)
  with jmm-J-allocated-heap-conf' wfP ok legal
  have ∃ T. P ⊢ jmm ad@al : T ∧ P ⊢ jmm v :≤ T
  unfolding jmm-typeof-addr'-conv-jmm-type-addr[symmetric, abs-def]
  by(rule J-allocated-heap-conf'.J-legal-read-value-typeable)
  thus P ⊢ jmm v :≤ T using adal
  by(auto dest: jmm.addr-loc-type-fun[unfolded jmm-typeof-addr-conv-jmm-typeof-addr', unfolded
heap-base'.addr-loc-type-conv-addr-loc-type])
  qed
  ultimately show ?thesis using wf unfolding gen-legal-execution.simps by blast
qed

```

lemma J-legal-typesafe2:

assumes *legal*: *legal-execution* P (*jmm'-J- \mathcal{E}* P C M *vs status*) (E , ws)
shows *legal-execution* P (*jmm-J- \mathcal{E}* P C M *vs status*) (E , ws)
proof –
let $?E = \text{jmm-J-}\mathcal{E}$ P C M *vs status*
let $?E' = \text{jmm'-J-}\mathcal{E}$ P C M *vs status*
from *legal* **obtain** J
where *justified*: $P \vdash (E, ws)$ *justified-by* J
and *range*: $\text{range}(\text{justifying-exec} \circ J) \subseteq ?E'$
and $E: E \in ?E'$ **and** *wf*: $P \vdash (E, ws) \checkmark$ **by**(*auto simp add: gen-legal-execution.simps*)
from *range* E **have** $\text{range}(\text{justifying-exec} \circ J) \subseteq ?E$ $E \in ?E$
using *J- \mathcal{E} -typesafe-subset*[*of P status C M vs*] **by** *blast+*
with *justified wf*
show *?thesis* **by**(*auto simp add: gen-legal-execution.simps*)
qed

lemma *J-weakly-legal-typesafe2*:
assumes *legal*: *weakly-legal-execution* P (*jmm'-J- \mathcal{E}* P C M *vs status*) (E , ws)
shows *weakly-legal-execution* P (*jmm-J- \mathcal{E}* P C M *vs status*) (E , ws)
proof –
let $?E = \text{jmm-J-}\mathcal{E}$ P C M *vs status*
let $?E' = \text{jmm'-J-}\mathcal{E}$ P C M *vs status*
from *legal* **obtain** J
where *justified*: $P \vdash (E, ws)$ *weakly-justified-by* J
and *range*: $\text{range}(\text{justifying-exec} \circ J) \subseteq ?E'$
and $E: E \in ?E'$ **and** *wf*: $P \vdash (E, ws) \checkmark$ **by**(*auto simp add: gen-legal-execution.simps*)
from *range* E **have** $\text{range}(\text{justifying-exec} \circ J) \subseteq ?E$ $E \in ?E$
using *J- \mathcal{E} -typesafe-subset*[*of P status C M vs*] **by** *blast+*
with *justified wf*
show *?thesis* **by**(*auto simp add: gen-legal-execution.simps*)
qed

theorem *J-weakly-legal-typesafe*:
assumes *wf-J-prog* P
and *jmm-wf-start-state* P C M *vs*
shows *weakly-legal-execution* P (*jmm-J- \mathcal{E}* P C M *vs status*) = *weakly-legal-execution* P (*jmm'-J- \mathcal{E}* P C M *vs status*)
apply(*rule ext iffI*)
apply(*clarify, erule J-weakly-legal-typesafe1*[*OF assms*])
apply(*clarify, erule J-weakly-legal-typesafe2*)
done

theorem *J-legal-typesafe*:
assumes *wf-J-prog* P
and *jmm-wf-start-state* P C M *vs*
shows *legal-execution* P (*jmm-J- \mathcal{E}* P C M *vs status*) = *legal-execution* P (*jmm'-J- \mathcal{E}* P C M *vs status*)
apply(*rule ext iffI*)
apply(*clarify, erule J-legal-typesafe1*[*OF assms*])
apply(*clarify, erule J-legal-typesafe2*)
done

end

8.21 JMM type safety for bytecode

```

theory JMM-JVM-Typesafe
imports
  JMM-Typesafe2
  DRF-JVM
begin

locale JVM-allocated-heap-conf' =
  h: JVM-heap-conf
    addr2thread-id thread-id2addr
    spurious-wakeups
    empty-heap allocate λ-. typeof-addr heap-read heap-write hconf
    P
  +
  h: JVM-allocated-heap
    addr2thread-id thread-id2addr
    spurious-wakeups
    empty-heap allocate λ-. typeof-addr heap-read heap-write
    allocated
    P
  +
  heap''
    addr2thread-id thread-id2addr
    spurious-wakeups
    empty-heap allocate typeof-addr heap-read heap-write
    P
for addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool
and allocated :: 'heap ⇒ 'addr set
and P :: 'addr jvm-prog

sublocale JVM-allocated-heap-conf' < h: JVM-allocated-heap-conf
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate λ-. typeof-addr heap-read heap-write hconf allocated
  P
by(unfold-locales)

context JVM-allocated-heap-conf' begin

lemma exec-instr-New-type-match:
  [[ (ta, s') ∈ h.exec-instr i P t h stk loc C M pc frs; NewHeapElem ad CTn ∈ set {ta}o; typeof-addr
ad ≠ None ]]
  ⇒ typeof-addr ad = [ CTn ]
by(cases i)(auto split: if-split-asm prod.split-asm dest: allocate-typeof-addr-SomeD red-external-aggr-New-type-match)

```

lemma *mexecd-New-type-match*:

[[*h.mexecd* *P* *t* (*xcpfrs*, *h*) *ta* (*xcpfrs'*, *h'*); *NewHeapElem* *ad* *CTn* ∈ *set* $\{ta\}_o$; *typeof-addr* *ad* ≠ *None*]]

⇒ *typeof-addr* *ad* = [*CTn*]

apply(*cases* *xcpfrs*)

apply(*cases* *xcpfrs'*)

apply(*simp* *add*: *split-beta*)

apply(*erule* *h.jvmd-NormalE*)

apply(*cases* *fst* *xcpfrs*)

apply(*auto* 4 3 *dest*: *exec-instr-New-type-match*)

done

lemma *mexecd-known-addr-typing'*:

assumes *wf*: *wf-jvm-prog*_Φ *P*

and *ok*: *h.start-heap-ok*

shows *known-addr-typing'* *addr2thread-id* *thread-id2addr* *empty-heap* *allocate* *typeof-addr* *heap-write* *allocated* *h.jvm-known-addr* *JVM-final* (*h.mexecd* *P*) (λ *t* (*xcp*, *frs*) *h*. *h.correct-state* Φ *t* (*xcp*, *h*, *frs*)) *P*

proof –

interpret *known-addr-typing*

addr2thread-id *thread-id2addr*

spurious-wakeups

empty-heap *allocate* λ-. *typeof-addr* *heap-read* *heap-write*

allocated *h.jvm-known-addr*

JVM-final *h.mexecd* *P* λ *t* (*xcp*, *frs*) *h*. *h.correct-state* Φ *t* (*xcp*, *h*, *frs*)

P

using *assms* **by**(*rule* *h.mexecd-known-addr-typing*)

show *?thesis* **by**(*unfold-locales*)(*erule* *mexecd-New-type-match*)

qed

lemma *JVM-weakly-legal-read-value-typeable*:

assumes *wf*: *wf-jvm-prog*_Φ *P*

and *wf-start*: *h.wf-start-state* *P* *C* *M* *vs*

and *legal*: *weakly-legal-execution* *P* (*h.JVMd-ℰ* *P* *C* *M* *vs* *status*) (*E*, *ws*)

and *a*: *enat* *a* < *llength* *E*

and *read*: *action-obs* *E* *a* = *NormalAction* (*ReadMem* *ad* *al* *v*)

shows ∃ *T*. *P* ⊢ *ad@al* : *T* ∧ *P* ⊢ *v* : ≤ *T*

proof –

note *wf*

moreover from *wf-start* **have** *h.start-heap-ok* **by** *cases*

moreover from *wf* *wf-start*

have *ts-ok* (λ *t* (*xcp*, *frs*) *h*. *h.correct-state* Φ *t* (*xcp*, *h*, *frs*)) (*thr* (*h.JVM-start-state* *P* *C* *M* *vs*))

h.start-heap

using *h.correct-jvm-state-initial*[*OF* *wf* *wf-start*]

by(*simp* *add*: *h.correct-jvm-state-def* *h.start-state-def* *split-beta*)

moreover from *wf* **obtain** *wf-md* **where** *wf'*: *wf-prog* *wf-md* *P* **by**(*blast* *dest*: *wt-jvm-progD*)

hence *wf-syscls* *P* **by**(*rule* *wf-prog-wf-syscls*)

ultimately show *?thesis* **using** *legal* *a* *read*

by(*rule* *known-addr-typing'.weakly-legal-read-value-typeable*[*OF* *mexecd-known-addr-typing'*])

qed

end

abbreviation *jmm-JVMd- \mathcal{E}*

$:: \text{addr } jvm\text{-prog} \Rightarrow \text{cname} \Rightarrow \text{mname} \Rightarrow \text{addr val list} \Rightarrow \text{status} \Rightarrow (\text{addr} \times (\text{addr}, \text{addr}) \text{ obs-event action}) \text{ llist set}$

where

$jmm\text{-JVMd-}\mathcal{E} \ P \equiv$
 $JVM\text{-heap-base.JVMd-}\mathcal{E} \ \text{addr}2\text{thread-id} \ \text{thread-id}2\text{addr} \ jmm\text{-spurious-wakeups} \ jmm\text{-empty} \ jmm\text{-allocate}$
 $(jmm\text{-typeof-addr } P) \ jmm\text{-heap-read} \ jmm\text{-heap-write } P$

abbreviation *jmm'-JVMd- \mathcal{E}*

$:: \text{addr } jvm\text{-prog} \Rightarrow \text{cname} \Rightarrow \text{mname} \Rightarrow \text{addr val list} \Rightarrow \text{status} \Rightarrow (\text{addr} \times (\text{addr}, \text{addr}) \text{ obs-event action}) \text{ llist set}$

where

$jmm'\text{-JVMd-}\mathcal{E} \ P \equiv$
 $JVM\text{-heap-base.JVMd-}\mathcal{E} \ \text{addr}2\text{thread-id} \ \text{thread-id}2\text{addr} \ jmm\text{-spurious-wakeups} \ jmm\text{-empty} \ jmm\text{-allocate}$
 $(jmm\text{-typeof-addr } P) \ (jmm\text{-heap-read-typed } P) \ jmm\text{-heap-write } P$

abbreviation *jmm-JVM-start-state*

$:: \text{addr } jvm\text{-prog} \Rightarrow \text{cname} \Rightarrow \text{mname} \Rightarrow \text{addr val list} \Rightarrow (\text{addr}, \text{thread-id}, \text{addr } jvm\text{-thread-state}, \text{JMM-heap}, \text{addr}) \text{ state}$

where $jmm\text{-JVM-start-state} \equiv JVM\text{-heap-base.JVM-start-state} \ \text{addr}2\text{thread-id} \ jmm\text{-empty} \ jmm\text{-allocate}$

lemma *jmm-JVM-heap-conf:*

$JVM\text{-heap-conf} \ \text{addr}2\text{thread-id} \ \text{thread-id}2\text{addr} \ jmm\text{-empty} \ jmm\text{-allocate} \ (jmm\text{-typeof-addr } P) \ jmm\text{-heap-write}$
 $jmm\text{-hconf } P$

by(*unfold-locales*)

lemma *jmm-JVMd-allocated-heap-conf':*

$JVM\text{-allocated-heap-conf}' \ \text{addr}2\text{thread-id} \ \text{thread-id}2\text{addr} \ jmm\text{-empty} \ jmm\text{-allocate} \ (jmm\text{-typeof-addr}'$
 $P) \ jmm\text{-heap-write} \ jmm\text{-hconf} \ jmm\text{-allocated } P$

apply(*rule JVM-allocated-heap-conf'.intro*)

apply(*unfold jmm-typeof-addr'-conv-jmm-typeof-addr*)

apply(*unfold-locales*)

done

lemma *exec-instr-heap-read-typed:*

$(ta, xcphfrs') \in JVM\text{-heap-base.exec-instr} \ \text{addr}2\text{thread-id} \ \text{thread-id}2\text{addr} \ \text{spurious-wakeups} \ \text{empty-heap}$
 $\text{allocate} \ (\lambda\text{-} :: 'heap. \text{typeof-addr}) \ (\text{heap-base.heap-read-typed} \ (\lambda\text{-} :: 'heap. \text{typeof-addr}) \ \text{heap-read } P)$
 $\text{heap-write } i \ P \ t \ h \ \text{stk} \ \text{loc} \ C \ M \ \text{pc} \ \text{frs} \ \longleftrightarrow$

$(ta, xcphfrs') \in JVM\text{-heap-base.exec-instr} \ \text{addr}2\text{thread-id} \ \text{thread-id}2\text{addr} \ \text{spurious-wakeups} \ \text{empty-heap}$
 $\text{allocate} \ (\lambda\text{-} :: 'heap. \text{typeof-addr}) \ \text{heap-read} \ \text{heap-write } i \ P \ t \ h \ \text{stk} \ \text{loc} \ C \ M \ \text{pc} \ \text{frs} \ \wedge$

$(\forall \text{ad} \ \text{al} \ v \ T. \ \text{ReadMem} \ \text{ad} \ \text{al} \ v \in \text{set } \{\!|ta|\!\}_o \longrightarrow \text{heap-base'.addr-loc-type } \text{TYPE}('heap) \ \text{typeof-addr}$
 $P \ \text{ad} \ \text{al} \ T \longrightarrow \text{heap-base'.conf } \text{TYPE}('heap) \ \text{typeof-addr } P \ v \ T)$

apply(*cases i*)

apply(*simp-all add: JVM-heap-base.exec-instr.simps split-beta cong: conj-cong*)

apply(*auto dest: heap-base.heap-read-typed-into-heap-read del: disjCI*)[5]

apply(*blast dest: heap-base.heap-read-typed-typed heap-base'.addr-loc-type-conv-addr-loc-type*[*THEN fun-cong, THEN fun-cong, THEN fun-cong, THEN iffD2*] *heap-base'.conf-conv-conf*[*THEN fun-cong, THEN fun-cong, THEN iffD1*])

apply(*auto dest: heap-base'.addr-loc-type-conv-addr-loc-type*[*THEN fun-cong, THEN fun-cong, THEN fun-cong, THEN iffD1*] *intro: heap-base'.conf-conv-conf*[*THEN fun-cong, THEN fun-cong, THEN iffD2*] *heap-base.heap-read-typedI*)[1]

apply(blast dest: heap-base.heap-read-typed-typed heap-base'.addr-loc-type-conv-addr-loc-type[THEN fun-cong, THEN fun-cong, THEN fun-cong, THEN iffD2] heap-base'.conf-conv-conf[THEN fun-cong, THEN fun-cong, THEN iffD1])
apply(auto dest: heap-base'.addr-loc-type-conv-addr-loc-type[THEN fun-cong, THEN fun-cong, THEN fun-cong, THEN iffD1] intro: heap-base'.conf-conv-conf[THEN fun-cong, THEN fun-cong, THEN iffD2] heap-base.heap-read-typedI)[1]
apply(blast dest: heap-base.heap-read-typed-typed heap-base'.addr-loc-type-conv-addr-loc-type[THEN fun-cong, THEN fun-cong, THEN fun-cong, THEN iffD2] heap-base'.conf-conv-conf[THEN fun-cong, THEN fun-cong, THEN iffD1])
subgoal by(auto dest: heap-base.heap-read-typed-into-heap-read)
apply(blast dest: heap-base.heap-read-typed-typed heap-base'.addr-loc-type-conv-addr-loc-type[THEN fun-cong, THEN fun-cong, THEN fun-cong, THEN iffD2] heap-base'.conf-conv-conf[THEN fun-cong, THEN fun-cong, THEN iffD1])
subgoal by(auto dest: heap-base'.addr-loc-type-conv-addr-loc-type[THEN fun-cong, THEN fun-cong, THEN fun-cong, THEN iffD1] intro: heap-base'.conf-conv-conf[THEN fun-cong, THEN fun-cong, THEN iffD2] heap-base.heap-read-typedI)[1]
subgoal by(auto 4 3 dest: heap-base.heap-read-typed-into-heap-read intro: heap-base.heap-read-typedI simp add: heap-base'.conf-conv-conf heap-base'.addr-loc-type-conv-addr-loc-type)
apply(subst red-external-aggr-heap-read-typed)
apply(fastforce)
apply auto
done

lemma exec-heap-read-typed:

$(ta, xcphfrs') \in JVM\text{-heap-base.exec addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda \cdot :: 'heap. \text{typeof-addr}) (heap\text{-base.heap-read-typed } (\lambda \cdot. \text{typeof-addr}) heap\text{-read } P) heap\text{-write } P t xcphfrs \longleftrightarrow$
 $(ta, xcphfrs') \in JVM\text{-heap-base.exec addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda \cdot :: 'heap. \text{typeof-addr}) heap\text{-read heap-write } P t xcphfrs \wedge$
 $(\forall ad\ al\ v\ T. \text{ReadMem } ad\ al\ v \in \text{set } \{\{ta\}\}_o \longrightarrow heap\text{-base'.addr-loc-type TYPE('heap) typeof-addr } P\ ad\ al\ T \longrightarrow heap\text{-base'.conf TYPE('heap) typeof-addr } P\ v\ T)$
apply(cases xcphfrs)
apply(cases fst xcphfrs)
apply(case-tac [!] snd (snd xcphfrs))
apply(auto simp add: JVM-heap-base.exec.simps exec-instr-heap-read-typed)
done

lemma exec-1-d-heap-read-typed:

$JVM\text{-heap-base.exec-1-d addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda \cdot :: 'heap. \text{typeof-addr}) (heap\text{-base.heap-read-typed } (\lambda \cdot. \text{typeof-addr}) heap\text{-read } P) heap\text{-write } P t (\text{Normal } xcphfrs) ta (\text{Normal } xcphfrs') \longleftrightarrow$
 $JVM\text{-heap-base.exec-1-d addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda \cdot :: 'heap. \text{typeof-addr}) heap\text{-read heap-write } P t (\text{Normal } xcphfrs) ta (\text{Normal } xcphfrs') \wedge$
 $(\forall ad\ al\ v\ T. \text{ReadMem } ad\ al\ v \in \text{set } \{\{ta\}\}_o \longrightarrow heap\text{-base'.addr-loc-type TYPE('heap) typeof-addr } P\ ad\ al\ T \longrightarrow heap\text{-base'.conf TYPE('heap) typeof-addr } P\ v\ T)$
by(auto elim!: JVM-heap-base.jvmd-NormalE intro: JVM-heap-base.exec-1-d-NormalI simp add: exec-heap-read-typed JVM-heap-base.exec-d-def)

lemma mexecd-heap-read-typed:

$JVM\text{-heap-base.mexecd addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda \cdot :: 'heap. \text{typeof-addr}) (heap\text{-base.heap-read-typed } (\lambda \cdot :: 'heap. \text{typeof-addr}) heap\text{-read } P) heap\text{-write } P t xcprsh ta xcprsh' \longleftrightarrow$
 $JVM\text{-heap-base.mexecd addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda \cdot ::$

'heap. typeof-addr) heap-read heap-write P t $xcpfrsh$ ta $xcpfrsh'$ \wedge
 $(\forall ad\ al\ v\ T. ReadMem\ ad\ al\ v \in set\ \{\!|ta|\!\}_o \longrightarrow heap\text{-base}'.addr\text{-loc}\text{-type}\ TYPE('heap)\ \text{typeof}\text{-addr}$
 $P\ ad\ al\ T \longrightarrow heap\text{-base}'.conf\ TYPE('heap)\ \text{typeof}\text{-addr}\ P\ v\ T)$
by(simp add: split-beta exec-1-d-heap-read-typed)

lemma *if-mexecd-heap-read-typed*:

multithreaded-base.init-fin JVM-final (JVM-heap-base.mexecd addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate $(\lambda - :: 'heap.\ \text{typeof}\text{-addr})$ *(heap-base.heap-read-typed* $(\lambda - :: 'heap.\ \text{typeof}\text{-addr})$ *heap-read* P) *heap-write* P) *t xh ta x'h' \longleftrightarrow*

if-heap-read-typed JVM-final (JVM-heap-base.mexecd addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate $(\lambda - :: 'heap.\ \text{typeof}\text{-addr})$ *heap-read heap-write* P) *typeof-addr* P *t xh ta x'h'*

unfolding *multithreaded-base.init-fin.simps*

by(subst mexecd-heap-read-typed) fastforce

lemma *JVMd- \mathcal{E} -heap-read-typedI*:

$\llbracket E \in JVM\text{-heap}\text{-base}.JVMd\text{-}\mathcal{E}\ \text{addr}2\text{thread}\text{-id}\ \text{thread}\text{-id}2\text{addr}\ \text{spurious}\text{-wakeups}\ \text{empty}\text{-heap}\ \text{allocate}$
 $(\lambda - :: 'heap.\ \text{typeof}\text{-addr})\ \text{heap}\text{-read}\ \text{heap}\text{-write}\ P\ C\ M\ \text{vs}\ \text{status};$

$\bigwedge ad\ al\ v\ T. \llbracket NormalAction\ (ReadMem\ ad\ al\ v) \in snd\ 'lset\ E; heap\text{-base}'.addr\text{-loc}\text{-type}\ TYPE('heap)$
 $\text{typeof}\text{-addr}\ P\ ad\ al\ T \rrbracket \implies heap\text{-base}'.conf\ TYPE('heap)\ \text{typeof}\text{-addr}\ P\ v\ T \rrbracket$

$\implies E \in JVM\text{-heap}\text{-base}.JVMd\text{-}\mathcal{E}\ \text{addr}2\text{thread}\text{-id}\ \text{thread}\text{-id}2\text{addr}\ \text{spurious}\text{-wakeups}\ \text{empty}\text{-heap}\ \text{allocate}$
 $(\lambda - :: 'heap.\ \text{typeof}\text{-addr})\ (heap\text{-base}.heap\text{-read}\text{-typed}\ (\lambda - :: 'heap.\ \text{typeof}\text{-addr})\ \text{heap}\text{-read}\ P)\ \text{heap}\text{-write}$
 $P\ C\ M\ \text{vs}\ \text{status}$

apply(erule imageE, hypsubst)

apply(rule imageI)

apply(erule multithreaded-base. \mathcal{E} .cases, hypsubst)

apply(rule multithreaded-base. \mathcal{E} .intros)

apply(subst if-mexecd-heap-read-typed[abs-def])

apply(erule if-mthr-Runs-heap-read-typedI)

apply(auto simp add: image-Un lset-lmap[symmetric] lmap-lconcat llist.map-comp o-def split-def simp
del: lset-lmap)

done

lemma *jmm'-exec-instrI*:

$\llbracket (ta, xcpfrs) \in JVM\text{-heap}\text{-base}.exec\text{-instr}\ \text{addr}2\text{thread}\text{-id}\ \text{thread}\text{-id}2\text{addr}\ \text{spurious}\text{-wakeups}\ \text{empty}\text{-heap}$
 $\text{allocate}\ \text{typeof}\text{-addr}\ \text{jmm}\text{-heap}\text{-read}\ \text{jmm}\text{-heap}\text{-write}\ i\ P\ t\ h\ \text{stk}\ \text{loc}\ C\ M\ \text{pc}\ \text{frs};$

$\text{final}\text{-thread}.actions\text{-ok}\ (\text{final}\text{-thread}.init\text{-fin}\text{-final}\ JVM\text{-final})\ s\ t\ ta \rrbracket$

$\implies \exists ta\ xcpfrs. (ta, xcpfrs) \in JVM\text{-heap}\text{-base}.exec\text{-instr}\ \text{addr}2\text{thread}\text{-id}\ \text{thread}\text{-id}2\text{addr}\ \text{spuri}$
 $ous\text{-wakeups}\ \text{empty}\text{-heap}\ \text{allocate}\ \text{typeof}\text{-addr}\ (heap\text{-base}.heap\text{-read}\text{-typed}\ \text{typeof}\text{-addr}\ \text{jmm}\text{-heap}\text{-read}\ P)$
 $\text{jmm}\text{-heap}\text{-write}\ i\ P\ t\ h\ \text{stk}\ \text{loc}\ C\ M\ \text{pc}\ \text{frs} \wedge \text{final}\text{-thread}.actions\text{-ok}\ (\text{final}\text{-thread}.init\text{-fin}\text{-final}\ JVM\text{-final})$
 $s\ t\ ta$

apply(cases i)

apply(auto simp add: JVM-heap-base.exec-instr.simps split-beta final-thread.actions-ok-iff intro!: jmm-heap-read-typed-default-
rev-image-eqI simp del: split-paired-Ex split: if-split-asm)

subgoal for $F\ D$

apply(cases hd (tl stk) = (default-val (THE T. heap-base.addr-loc-type typeof-addr P h (the-Addr
(hd (tl (tl stk)))))) (CField D F) T)))

subgoal by(auto simp add: JVM-heap-base.exec-instr.simps split-beta final-thread.actions-ok-iff in-
tro!: jmm-heap-read-typed-default-val rev-image-eqI simp del: split-paired-Ex split: if-split-asm del: dis-
jCI intro!: disjI1 exI)

subgoal by(auto simp add: JVM-heap-base.exec-instr.simps split-beta final-thread.actions-ok-iff in-
tro!: jmm-heap-read-typed-default-val rev-image-eqI simp del: split-paired-Ex split: if-split-asm del: dis-
jCI intro!: disjI2 exI)

done

subgoal for $F\ D$

apply(cases $hd (tl\ stk) = (default\text{-}val (THE\ T.\ heap\text{-}base.\ addr\text{-}loc\text{-}type\ typeof\text{-}addr\ P\ h (the\text{-}Addr\ (hd\ (tl\ (tl\ stk)))) (CField\ D\ F)\ T)))$)

subgoal by(auto simp add: JVM-heap-base.exec-instr.simps split-beta final-thread.actions-ok-iff intro!: jmm-heap-read-typed-default-val rev-image-eqI simp del: split-paired-Ex split: if-split-asm del: disjCI intro!: disjI1 exI jmm-heap-write.intros)

subgoal by(rule exI conjI disjI2 refl jmm-heap-read-typed-default-val|assumption)+ auto

done

subgoal by(drule red-external-aggr-heap-read-typedI)(fastforce simp add: final-thread.actions-ok-iff simp del: split-paired-Ex)+

done

lemma $jmm'\text{-}execI$:

$\llbracket (ta, xcphfrs') \in JVM\text{-}heap\text{-}base.\ exec\ addr2thread\text{-}id\ thread\text{-}id2addr\ spurious\text{-}wakeups\ empty\text{-}heap\ allocate\ typeof\text{-}addr\ jmm\text{-}heap\text{-}read\ jmm\text{-}heap\text{-}write\ P\ t\ xcphfrs;$

$final\text{-}thread.\ actions\text{-}ok (final\text{-}thread.\ init\text{-}fin\text{-}final\ JVM\text{-}final) s\ t\ ta \rrbracket$

$\implies \exists ta\ xcphfrs'. (ta, xcphfrs') \in JVM\text{-}heap\text{-}base.\ exec\ addr2thread\text{-}id\ thread\text{-}id2addr\ spurious\text{-}wakeups\ empty\text{-}heap\ allocate\ typeof\text{-}addr (heap\text{-}base.\ heap\text{-}read\text{-}typed\ typeof\text{-}addr\ jmm\text{-}heap\text{-}read\ P) jmm\text{-}heap\text{-}write\ P\ t\ xcphfrs \wedge final\text{-}thread.\ actions\text{-}ok (final\text{-}thread.\ init\text{-}fin\text{-}final\ JVM\text{-}final) s\ t\ ta$

apply(cases xcphfrs)

apply(cases snd (snd xcphfrs))

apply(simp add: JVM-heap-base.exec.simps)

apply(cases fst xcphfrs)

apply(fastforce simp add: JVM-heap-base.exec.simps dest!: jmm'\text{-}exec\text{-}instrI)+

done

lemma $jmm'\text{-}execdI$:

$\llbracket JVM\text{-}heap\text{-}base.\ exec\text{-}1\text{-}d\ addr2thread\text{-}id\ thread\text{-}id2addr\ spurious\text{-}wakeups\ empty\text{-}heap\ allocate\ typeof\text{-}addr\ jmm\text{-}heap\text{-}read\ jmm\text{-}heap\text{-}write\ P\ t (Normal\ xcphfrs)\ ta (Normal\ xcphfrs');$

$final\text{-}thread.\ actions\text{-}ok (final\text{-}thread.\ init\text{-}fin\text{-}final\ JVM\text{-}final) s\ t\ ta \rrbracket$

$\implies \exists ta\ xcphfrs'. JVM\text{-}heap\text{-}base.\ exec\text{-}1\text{-}d\ addr2thread\text{-}id\ thread\text{-}id2addr\ spurious\text{-}wakeups\ empty\text{-}heap\ allocate\ typeof\text{-}addr (heap\text{-}base.\ heap\text{-}read\text{-}typed\ typeof\text{-}addr\ jmm\text{-}heap\text{-}read\ P) jmm\text{-}heap\text{-}write\ P\ t (Normal\ xcphfrs)\ ta (Normal\ xcphfrs') \wedge final\text{-}thread.\ actions\text{-}ok (final\text{-}thread.\ init\text{-}fin\text{-}final\ JVM\text{-}final) s\ t\ ta$

apply(erule JVM-heap-base.jvmd-NormalE)

apply(drule (1) jmm'\text{-}execI)

apply(force intro: JVM-heap-base.exec-1-d-NormalI simp add: JVM-heap-base.exec-d-def)

done

lemma $jmm'\text{-}mexecdI$:

$\llbracket JVM\text{-}heap\text{-}base.\ mexecd\ addr2thread\text{-}id\ thread\text{-}id2addr\ spurious\text{-}wakeups\ empty\text{-}heap\ allocate\ typeof\text{-}addr\ jmm\text{-}heap\text{-}read\ jmm\text{-}heap\text{-}write\ P\ t\ xcpfrsh\ ta\ xcpfrsh';$

$final\text{-}thread.\ actions\text{-}ok (final\text{-}thread.\ init\text{-}fin\text{-}final\ JVM\text{-}final) s\ t\ ta \rrbracket$

$\implies \exists ta\ xcpfrsh'. JVM\text{-}heap\text{-}base.\ mexecd\ addr2thread\text{-}id\ thread\text{-}id2addr\ spurious\text{-}wakeups\ empty\text{-}heap\ allocate\ typeof\text{-}addr (heap\text{-}base.\ heap\text{-}read\text{-}typed\ typeof\text{-}addr\ jmm\text{-}heap\text{-}read\ P) jmm\text{-}heap\text{-}write\ P\ t\ xcpfrsh\ ta\ xcpfrsh' \wedge final\text{-}thread.\ actions\text{-}ok (final\text{-}thread.\ init\text{-}fin\text{-}final\ JVM\text{-}final) s\ t\ ta$

by(simp add: split-beta)(drule (1) jmm'\text{-}execdI, auto 4 10)

lemma $if\text{-}mexecd\text{-}heap\text{-}read\text{-}not\text{-}stuck$:

$\llbracket multithreaded\text{-}base.\ init\text{-}fin\ JVM\text{-}final (JVM\text{-}heap\text{-}base.\ mexecd\ addr2thread\text{-}id\ thread\text{-}id2addr\ spurious\text{-}wakeups\ empty\text{-}heap\ allocate\ typeof\text{-}addr\ jmm\text{-}heap\text{-}read\ jmm\text{-}heap\text{-}write\ P) t\ xh\ ta\ x'h';$

$final\text{-}thread.\ actions\text{-}ok (final\text{-}thread.\ init\text{-}fin\text{-}final\ JVM\text{-}final) s\ t\ ta \rrbracket$

$\implies \exists ta\ x'h'. multithreaded\text{-}base.\ init\text{-}fin\ JVM\text{-}final (JVM\text{-}heap\text{-}base.\ mexecd\ addr2thread\text{-}id\ thread\text{-}id2addr\ spurious\text{-}wakeups\ empty\text{-}heap\ allocate\ typeof\text{-}addr (heap\text{-}base.\ heap\text{-}read\text{-}typed\ typeof\text{-}addr\ jmm\text{-}heap\text{-}read\ P) jmm\text{-}heap\text{-}write\ P) t\ xh\ ta\ x'h' \wedge final\text{-}thread.\ actions\text{-}ok (final\text{-}thread.\ init\text{-}fin\text{-}final\ JVM\text{-}final) s\ t\ ta$

```

apply(erule multithreaded-base.init-fin.cases)
apply hypsubst
apply(drule jmm'-mexecdI)
  apply(simp add: final-thread.actions-ok-iff)
apply clarify
apply(subst (2) split-paired-Ex)
apply(subst (2) split-paired-Ex)
apply(subst (2) split-paired-Ex)
apply(rule exI conjI)+
  apply(rule multithreaded-base.init-fin.intros)
  apply simp
apply(simp add: final-thread.actions-ok-iff)
apply(blast intro: multithreaded-base.init-fin.intros)
apply(blast intro: multithreaded-base.init-fin.intros)
done

```

lemma *if-mExecd-heap-read-not-stuck*:

multithreaded-base.redT (final-thread.init-fin-final JVM-final) (multithreaded-base.init-fin JVM-final (JVM-heap-base.mexecd addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr jmm-heap-read jmm-heap-write P)) convert-RA' s tta s'

$\implies \exists tta s'. \text{multithreaded-base.redT (final-thread.init-fin-final JVM-final) (multithreaded-base.init-fin JVM-final (JVM-heap-base.mexecd addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr (heap-base.heap-read-typed typeof-addr jmm-heap-read P) jmm-heap-write P)) convert-RA' s tta s'}$

```

apply(erule multithreaded-base.redT.cases)
apply hypsubst
thm if-mexecd-heap-read-not-stuck
apply(drule (1) if-mexecd-heap-read-not-stuck)
apply(erule exE)+
apply(rename-tac ta' x'h')
apply(insert redT-updWs-total)
apply(erule-tac x=t in meta-allE)
apply(erule-tac x=wset s in meta-allE)
apply(erule-tac x={ta'}_w in meta-allE)
apply clarsimp
apply(rule exI)+
apply(auto intro!: multithreaded-base.redT.intros)[1]
apply hypsubst
apply(rule exI conjI)+
apply(rule multithreaded-base.redT.redT-acquire)
apply assumption+
done

```

lemma *JVM-legal-typesafe1*:

assumes *wfP*: wf-jvm-prog P

and *ok*: jmm-wf-start-state P C M vs

and *legal*: legal-execution P (jmm-JVMd- \mathcal{E} P C M vs status) (E, ws)

shows legal-execution P (jmm'-JVMd- \mathcal{E} P C M vs status) (E, ws)

proof –

let ? \mathcal{E} = jmm-JVMd- \mathcal{E} P C M vs status

let ? \mathcal{E}' = jmm'-JVMd- \mathcal{E} P C M vs status

from legal **obtain** J

where justified: $P \vdash (E, ws)$ justified-by J

and range: range (justifying-exec \circ J) \subseteq ? \mathcal{E}

and $E: E \in ?\mathcal{E}$ **and** $wf: P \vdash (E, ws) \surd$ **by**(*auto simp add: gen-legal-execution.simps*)
let $?J = J(0 := \{\text{committed} = \{\}, \text{justifying-exec} = \text{justifying-exec } (J \ 1), \text{justifying-ws} = \text{justifying-ws } (J \ 1), \text{action-translation} = \text{id}\})$

from wfP **obtain** Φ **where** $\Phi: wf\text{-jvm-prog}_{\Phi} P$ **by**(*auto simp add: wf-jvm-prog-def*)
hence $wf\text{-sys}: wf\text{-syscls } P$ **by**(*auto dest: wt-jvm-progD intro: wf-prog-wf-syscls*)

from *justified* **have** $P \vdash (\text{justifying-exec } (J \ 1), \text{justifying-ws } (J \ 1)) \surd$ **by**(*simp add: justification-well-formed-def*)
with *justified* **have** $P \vdash (E, ws)$ *justified-by* $?J$ **by**(*rule drop-0th-justifying-exec*)
moreover **have** $\text{range } (\text{justifying-exec} \circ ?J) \subseteq ?\mathcal{E}'$

proof
fix ξ
assume $\xi \in \text{range } (\text{justifying-exec} \circ ?J)$
then **obtain** n **where** $\xi = \text{justifying-exec } (?J \ n)$ **by** *auto*
then **obtain** n **where** $\xi: \xi = \text{justifying-exec } (J \ n)$ **and** $n: n > 0$ **by**(*auto split: if-split-asm*)
from $\text{range } \xi$ **have** $\xi \in ?\mathcal{E}$ **by** *auto*
thus $\xi \in ?\mathcal{E}'$ **unfolding** $\text{jmm-typeof-addr}'\text{-conv-jmm-type-addr}[\text{symmetric}, \text{abs-def}]$
proof(*rule JVMd- \mathcal{E} -heap-read-typedI*)
fix $ad \ al \ v \ T$
assume $\text{read}: \text{NormalAction } (\text{ReadMem } ad \ al \ v) \in \text{snd } \text{'lset } \xi$
and $\text{adal}: P \vdash \text{jmm } ad@al : T$
from read **obtain** a **where** $a: \text{enat } a < \text{llength } \xi$ $\text{action-obs } \xi \ a = \text{NormalAction } (\text{ReadMem } ad \ al \ v)$
unfolding lset-conv-lnth **by**(*auto simp add: action-obs-def*)

have $\text{ts-ok } (\lambda t \ (xcp, \text{frs}) \ h. \text{JVM-heap-conf-base.correct-state } \text{addr2thread-id } \text{jmm-empty } \text{jmm-allocate } (\lambda\text{-. } \text{jmm-typeof-addr}' \ P) \ \text{jmm-hconf } P \ \Phi \ t \ (xcp, \text{h}, \text{frs})) \ (\text{thr } (\text{jmm-JVM-start-state } P \ C \ M \ \text{vs})) \ \text{jmm.start-heap}$

using $\text{JVM-heap-conf.correct-jvm-state-initial}[\text{OF } \text{jmm-JVM-heap-conf } \Phi \ \text{ok}]$
by(*simp add: JVM-heap-conf-base.correct-jvm-state-def jmm-typeof-addr}'\text{-conv-jmm-typeof-addr-heap-base.start-state-def split-beta*)
with $\text{JVM-allocated-heap-conf}'.\text{mexecd-known-addr-typing}'[\text{OF } \text{jmm-JVMd-allocated-heap-conf}' \ \Phi \ \text{jmm-start-heap-ok}]$
have $\exists T. P \vdash \text{jmm } ad@al : T \wedge P \vdash \text{jmm } v : \leq T$
using $wf\text{-sys is-justified-by-imp-is-weakly-justified-by}[\text{OF } \text{justified } wf] \ \text{range } n \ a$
unfolding $\text{jmm-typeof-addr}'\text{-conv-jmm-type-addr}[\text{symmetric}, \text{abs-def}] \ \xi$
by(*rule known-addr-typing'.read-value-typeable-justifying*)
thus $P \vdash \text{jmm } v : \leq T$ **using** adal
by(*auto dest: jmm.addr-loc-type-fun[unfolded jmm-typeof-addr-conv-jmm-typeof-addr]', unfolded heap-base'.addr-loc-type-conv-addr-loc-type]*)
qed
qed
moreover **from** E **have** $E \in ?\mathcal{E}'$
unfolding $\text{jmm-typeof-addr}'\text{-conv-jmm-type-addr}[\text{symmetric}, \text{abs-def}]$
proof(*rule JVMd- \mathcal{E} -heap-read-typedI*)
fix $ad \ al \ v \ T$
assume $\text{read}: \text{NormalAction } (\text{ReadMem } ad \ al \ v) \in \text{snd } \text{'lset } E$
and $\text{adal}: P \vdash \text{jmm } ad@al : T$
from read **obtain** a **where** $a: \text{enat } a < \text{llength } E$ $\text{action-obs } E \ a = \text{NormalAction } (\text{ReadMem } ad \ al \ v)$
unfolding lset-conv-lnth **by**(*auto simp add: action-obs-def*)
with $\text{jmm-JVMd-allocated-heap-conf}' \ \Phi \ \text{ok}$ $\text{legal-imp-weakly-legal-execution}[\text{OF } \text{legal}]$

have $\exists T. P \vdash_{jmm} ad@al : T \wedge P \vdash_{jmm} v : \leq T$
unfolding $jmm\text{-typeof-addr}'\text{-conv-jmm-typeof-addr}$ [*symmetric, abs-def*]
by(*rule JVM-allocated-heap-conf'.JVM-weakly-legal-read-value-typeable*)
thus $P \vdash_{jmm} v : \leq T$ **using** *adal*
by(*auto dest: jmm.addr-loc-type-fun[unfolded jmm-typeof-addr-conv-jmm-typeof-addr]', unfolded heap-base'.addr-loc-type-conv-addr-loc-type]*)
qed
ultimately show *?thesis* **using** *wf* **unfolding** *gen-legal-execution.simps* **by** *blast*
qed

lemma *JVM-weakly-legal-typesafe1*:

assumes *wfP*: *wf-jvm-prog P*

and *ok*: *jmm-wf-start-state P C M vs*

and *legal*: *weakly-legal-execution P (jmm-JVMd- \mathcal{E} P C M vs status) (E, ws)*

shows *weakly-legal-execution P (jmm'-JVMd- \mathcal{E} P C M vs status) (E, ws)*

proof –

let $?E = jmm\text{-JVMd-}\mathcal{E} P C M vs status$

let $?E' = jmm'\text{-JVMd-}\mathcal{E} P C M vs status$

from *legal* **obtain** *J*

where *justified*: $P \vdash (E, ws)$ *weakly-justified-by J*

and *range*: $range (justifying-exec \circ J) \subseteq ?E$

and *E*: $E \in ?E$ **and** *wf*: $P \vdash (E, ws) \checkmark$ **by**(*auto simp add: gen-legal-execution.simps*)

let $?J = J(0 := \{\}, committed = \{\}, justifying-exec = justifying-exec (J 1), justifying-ws = justifying-ws (J 1), action-translation = id)$

from *wfP* **obtain** Φ **where** Φ : *wf-jvm-prog Φ P* **by**(*auto simp add: wf-jvm-prog-def*)

hence *wf-sys*: *wf-syscls P* **by**(*auto dest: wt-jvm-progD intro: wf-prog-wf-syscls*)

from *justified* **have** $P \vdash (justifying-exec (J 1), justifying-ws (J 1)) \checkmark$ **by**(*simp add: justification-well-formed-def*)

with *justified* **have** $P \vdash (E, ws)$ *weakly-justified-by ?J* **by**(*rule drop-0th-weakly-justifying-exec*)

moreover **have** $range (justifying-exec \circ ?J) \subseteq ?E'$

proof

fix ξ

assume $\xi \in range (justifying-exec \circ ?J)$

then **obtain** *n* **where** $\xi = justifying-exec (?J n)$ **by** *auto*

then **obtain** *n* **where** ξ : $\xi = justifying-exec (J n)$ **and** *n*: $n > 0$ **by**(*auto split: if-split-asm*)

from *range* ξ **have** $\xi \in ?E$ **by** *auto*

thus $\xi \in ?E'$ **unfolding** $jmm\text{-typeof-addr}'\text{-conv-jmm-type-addr}$ [*symmetric, abs-def*]

proof(*rule JVMd- \mathcal{E} -heap-read-typedI*)

fix *ad al v T*

assume *read*: *NormalAction (ReadMem ad al v) \in snd ' lset ξ*

and *adal*: $P \vdash_{jmm} ad@al : T$

from *read* **obtain** *a* **where** *a*: $enat a < llength \xi$ *action-obs* $\xi a = NormalAction (ReadMem ad al v)$

unfolding *lset-conv-lnth* **by**(*auto simp add: action-obs-def*)

have *ts-ok* ($\lambda t (xcp, frs) h. JVM\text{-heap-conf-base.correct-state addr2thread-id jmm-empty jmm-allocate} (\lambda-. jmm\text{-typeof-addr}' P) jmm\text{-hconf} P \Phi t (xcp, h, frs)) (thr (jmm\text{-JVM-start-state} P C M vs)) jmm.start\text{-heap}$)

using *JVM-heap-conf.correct-jvm-state-initial*[*OF jmm-JVM-heap-conf Φok*]

by(*simp add: JVM-heap-conf-base.correct-jvm-state-def jmm-typeof-addr}'-conv-jmm-typeof-addr heap-base.start-state-def split-beta*)

with *JVM-allocated-heap-conf'.mexecd-known-addr-typing'*[*OF jmm-JVMd-allocated-heap-conf'*
 Φ *jmm-start-heap-ok*]
have $\exists T. P \vdash_{jmm} ad@al : T \wedge P \vdash_{jmm} v : \leq T$
using *wf-sys justified range n a*
unfolding *jmm-typeof-addr'-conv-jmm-type-addr*[*symmetric, abs-def*] ξ
by(*rule known-addr-typing'.read-value-typeable-justifying*)
thus $P \vdash_{jmm} v : \leq T$ **using** *adal*
by(*auto dest: jmm.addr-loc-type-fun[unfolded jmm-typeof-addr-conv-jmm-typeof-addr', unfolded heap-base'.addr-loc-type-conv-addr-loc-type]*)
qed
qed
moreover from *E* **have** $E \in ?\mathcal{E}'$
unfolding *jmm-typeof-addr'-conv-jmm-type-addr*[*symmetric, abs-def*]
proof(*rule JVMd- \mathcal{E} -heap-read-typedI*)
fix *ad al v T*
assume *read: NormalAction (ReadMem ad al v) \in snd ' lset E*
and *adal: P \vdash_{jmm} ad@al : T*
from *read* **obtain** *a* **where** *a: enat a < llength E action-obs E a = NormalAction (ReadMem ad al v)*
unfolding *lset-conv-lnth* **by**(*auto simp add: action-obs-def*)
with *jmm-JVMd-allocated-heap-conf' Φ ok legal*
have $\exists T. P \vdash_{jmm} ad@al : T \wedge P \vdash_{jmm} v : \leq T$
unfolding *jmm-typeof-addr'-conv-jmm-typeof-addr*[*symmetric, abs-def*]
by(*rule JVM-allocated-heap-conf'.JVM-weakly-legal-read-value-typeable*)
thus $P \vdash_{jmm} v : \leq T$ **using** *adal*
by(*auto dest: jmm.addr-loc-type-fun[unfolded jmm-typeof-addr-conv-jmm-typeof-addr', unfolded heap-base'.addr-loc-type-conv-addr-loc-type]*)
qed
ultimately show *?thesis* **using** *wf unfolding gen-legal-execution.simps* **by** *blast*
qed

lemma *JVMd- \mathcal{E} -heap-read-typedD*:

$E \in \text{JVM-heap-base.JVMd-}\mathcal{E} \text{ addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate}$
 $(\lambda-. \text{typeof-addr}) (\text{heap-base.heap-read-typed } (\lambda-. \text{typeof-addr}) \text{ jmm-heap-read } P) \text{ jmm-heap-write } P \text{ } C \text{ } M \text{ vs status}$

$\implies E \in \text{JVM-heap-base.JVMd-}\mathcal{E} \text{ addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate}$
 $(\lambda-. \text{typeof-addr}) \text{ jmm-heap-read } \text{ jmm-heap-write } P \text{ } C \text{ } M \text{ vs status}$

apply(*erule imageE, hypsubst*)

apply(*rule imageI*)

apply(*erule multithreaded-base. \mathcal{E} .cases, hypsubst*)

apply(*rule multithreaded-base. \mathcal{E} .intros*)

apply(*subst (asm) if-mexecd-heap-read-typed[abs-def]*)

apply(*erule if-mthr-Runs-heap-read-typedD*)

apply(*erule if-mExecd-heap-read-not-stuck[where typeof-addr= $\lambda-. \text{typeof-addr}$, unfolded if-mexecd-heap-read-typed]*)
done

lemma *JVMd- \mathcal{E} -typesafe-subset: jmm'-JVMd- \mathcal{E} P C M vs status \subseteq jmm-JVMd- \mathcal{E} P C M vs status*

unfolding *jmm-typeof-addr-def*[*abs-def*]

by(*rule subsetI*)(*erule JVMd- \mathcal{E} -heap-read-typedD*)

lemma *JVMd-legal-typesafe2*:

assumes *legal: legal-execution P (jmm'-JVMd- \mathcal{E} P C M vs status) (E, ws)*

shows *legal-execution P (jmm-JVMd- \mathcal{E} P C M vs status) (E, ws)*

proof –


```

let ? $\mathcal{E}$  = jmm-JVMd- $\mathcal{E}$  P C M vs status
let ? $\mathcal{E}'$  = jmm'-JVMd- $\mathcal{E}$  P C M vs status
from legal obtain J
  where justified:  $P \vdash (E, ws)$  justified-by J
  and range:  $\text{range}(\text{justifying-exec} \circ J) \subseteq ?\mathcal{E}'$ 
  and E:  $E \in ?\mathcal{E}'$  and wf:  $P \vdash (E, ws) \checkmark$  by(auto simp add: gen-legal-execution.simps)
from range E have  $\text{range}(\text{justifying-exec} \circ J) \subseteq ?\mathcal{E}$   $E \in ?\mathcal{E}$ 
  using JVMd- $\mathcal{E}$ -typesafe-subset[of P status C M vs] by blast+
with justified wf
show ?thesis by(auto simp add: gen-legal-execution.simps)
qed

```

theorem *JVMd-weakly-legal-typesafe2*:

```

assumes legal: weakly-legal-execution P (jmm'-JVMd- $\mathcal{E}$  P C M vs status) (E, ws)
shows weakly-legal-execution P (jmm-JVMd- $\mathcal{E}$  P C M vs status) (E, ws)

```

proof –

```

let ? $\mathcal{E}$  = jmm-JVMd- $\mathcal{E}$  P C M vs status
let ? $\mathcal{E}'$  = jmm'-JVMd- $\mathcal{E}$  P C M vs status
from legal obtain J
  where justified:  $P \vdash (E, ws)$  weakly-justified-by J
  and range:  $\text{range}(\text{justifying-exec} \circ J) \subseteq ?\mathcal{E}'$ 
  and E:  $E \in ?\mathcal{E}'$  and wf:  $P \vdash (E, ws) \checkmark$  by(auto simp add: gen-legal-execution.simps)
from range E have  $\text{range}(\text{justifying-exec} \circ J) \subseteq ?\mathcal{E}$   $E \in ?\mathcal{E}$ 
  using JVMd- $\mathcal{E}$ -typesafe-subset[of P status C M vs] by blast+
with justified wf
show ?thesis by(auto simp add: gen-legal-execution.simps)
qed

```

theorem *JVMd-weakly-legal-typesafe*:

```

assumes wf-jvm-prog P
and jmm-wf-start-state P C M vs
shows weakly-legal-execution P (jmm-JVMd- $\mathcal{E}$  P C M vs status) = weakly-legal-execution P (jmm'-JVMd- $\mathcal{E}$  P C M vs status)
apply(rule ext iffI)+
apply(clarify, erule JVM-weakly-legal-typesafe1[OF assms])
apply(clarify, erule JVMd-weakly-legal-typesafe2)
done

```

theorem *JVMd-legal-typesafe*:

```

assumes wf-jvm-prog P
and jmm-wf-start-state P C M vs
shows legal-execution P (jmm-JVMd- $\mathcal{E}$  P C M vs status) = legal-execution P (jmm'-JVMd- $\mathcal{E}$  P C M vs status)
apply(rule ext iffI)+
apply(clarify, erule JVM-legal-typesafe1[OF assms])
apply(clarify, erule JVMd-legal-typesafe2)
done

```

end

8.22 Compiler correctness for JMM heap implementation 2

theory *JMM-Compiler-Type2*

imports

JMM-Compiler
JMM-J-Typesafe
JMM-JVM-Typesafe
JMM-Interp

begin**theorem** *J2JVM-jmm-correct*:**assumes** *wf*: *wf-J-prog P***and** *wf-start*: *jmm-wf-start-state P C M vs***shows** *legal-execution P (jmm-J- \mathcal{E} P C M vs Running) (E, ws) \longleftrightarrow* *legal-execution (J2JVM P) (jmm-JVMd- \mathcal{E} (J2JVM P) C M vs Running) (E, ws)***using** *JVMd-legal-typesafe[OF wt-J2JVM[OF wf], of C M vs Running, symmetric] wf-start***by**(*simp only: J-legal-typesafe[OF assms] J-JVM-conf-read.red- \mathcal{E} -eq-mexecd- \mathcal{E} [OF jmm'-J-JVM-conf-read assms] J2JVM-def o-apply compP1-def compP2-def legal-execution-compP heap-base.wf-start-state-compP jmm-typeof-addr-compP heap-base.heap-read-typed-compP*)**theorem** *J2JVM-jmm-correct-weak*:**assumes** *wf*: *wf-J-prog P***and** *wf-start*: *jmm-wf-start-state P C M vs***shows** *weakly-legal-execution P (jmm-J- \mathcal{E} P C M vs Running) (E, ws) \longleftrightarrow* *weakly-legal-execution (J2JVM P) (jmm-JVMd- \mathcal{E} (J2JVM P) C M vs Running) (E, ws)***using** *JVMd-weakly-legal-typesafe[OF wt-J2JVM[OF wf], of C M vs Running, symmetric] wf-start***by**(*simp only: J-weakly-legal-typesafe[OF assms] J-JVM-conf-read.red- \mathcal{E} -eq-mexecd- \mathcal{E} [OF jmm'-J-JVM-conf-read assms] J2JVM-def o-apply compP1-def compP2-def weakly-legal-execution-compP heap-base.wf-start-state-compP jmm-typeof-addr-compP heap-base.heap-read-typed-compP*)**theorem** *J2JVM-jmm-correctly-synchronized*:**assumes** *wf*: *wf-J-prog P***and** *wf-start*: *jmm-wf-start-state P C M vs***and** *ka*: $\bigcup (ka\text{-Val } 'set\ vs) \subseteq set\ jmm.start\ addrs$ **shows** *correctly-synchronized (J2JVM P) (jmm-JVMd- \mathcal{E} (J2JVM P) C M vs Running) \longleftrightarrow* *correctly-synchronized P (jmm-J- \mathcal{E} P C M vs Running)***(is ?lhs \longleftrightarrow ?rhs)****proof****assume** *?lhs***show** *?rhs unfolding correctly-synchronized-def***proof**(*intro strip*)**fix** *E ws a a'***assume** *E*: *E \in jmm-J- \mathcal{E} P C M vs Running***and** *wf-exec*: *P \vdash (E, ws) \checkmark* **and** *sc*: *sequentially-consistent P (E, ws)***and** *actions*: *a \in actions E a' \in actions E***and** *conflict*: *P, E \vdash a \dagger a'***from** *E wf-exec sc***have** *legal-execution P (jmm-J- \mathcal{E} P C M vs Running) (E, ws)***by**(*rule sc-legal.SC-is-legal[OF J-allocated-progress.J-sc-legal[OF jmm-J-allocated-progress wf jmm-heap-read-typeable wf-start ka]]*)**hence** *legal-execution (J2JVM P) (jmm-JVMd- \mathcal{E} (J2JVM P) C M vs Running) (E, ws)***by**(*simp only: J2JVM-jmm-correct[OF wf wf-start]*)**hence** *E \in jmm-JVMd- \mathcal{E} (J2JVM P) C M vs Running J2JVM P \vdash (E, ws) \checkmark* **by**(*simp-all add: gen-legal-execution.simps*)**moreover from sc have** *sequentially-consistent (J2JVM P) (E, ws)*

```

  by(simp add: J2JVM-def compP2-def)
moreover from conflict have J2JVM P,E ⊢ a † a'
  by(simp add: J2JVM-def compP2-def)
ultimately have J2JVM P,E ⊢ a ≤hb a' ∨ J2JVM P,E ⊢ a' ≤hb a
  using ‹?lhs› actions by(auto simp add: correctly-synchronized-def)
thus P,E ⊢ a ≤hb a' ∨ P,E ⊢ a' ≤hb a
  by(simp add: J2JVM-def compP2-def)
qed
next
assume ?rhs
show ?lhs unfolding correctly-synchronized-def
proof(intro strip)
  fix E ws a a'
  assume E: E ∈ jmm-JVMd-ℰ (J2JVM P) C M vs Running
    and wf-exec: J2JVM P ⊢ (E, ws) √
    and sc: sequentially-consistent (J2JVM P) (E, ws)
    and actions: a ∈ actions E a' ∈ actions E
    and conflict: J2JVM P,E ⊢ a † a'

  from wf have wf-jvm-prog (J2JVM P) by(rule wt-J2JVM)
  then obtain Φ where wf': wf-jvm-progΦ (J2JVM P)
    by(auto simp add: wf-jvm-prog-def)
  from wf-start have wf-start': jmm-wf-start-state (J2JVM P) C M vs
    by(simp add: J2JVM-def compP2-def heap-base.wf-start-state-compP)
  from E wf-exec sc
  have legal-execution (J2JVM P) (jmm-JVMd-ℰ (J2JVM P) C M vs Running) (E, ws)
  by(rule sc-legal.SC-is-legal[OF JVM-allocated-progress.JVM-sc-legal[OF jmm-JVM-allocated-progress
wf' jmm-heap-read-typeable wf-start' ka]])
  hence legal-execution P (jmm-J-ℰ P C M vs Running) (E, ws)
    by(simp only: J2JVM-jmm-correct[OF wf wf-start'])
  hence E ∈ jmm-J-ℰ P C M vs Running P ⊢ (E, ws) √
    by(simp-all add: gen-legal-execution.simps)
  moreover from sc have sequentially-consistent P (E, ws)
    by(simp add: J2JVM-def compP2-def)
  moreover from conflict have P,E ⊢ a † a'
    by(simp add: J2JVM-def compP2-def)
  ultimately have P,E ⊢ a ≤hb a' ∨ P,E ⊢ a' ≤hb a
    using ‹?rhs› actions by(auto simp add: correctly-synchronized-def)
  thus J2JVM P,E ⊢ a ≤hb a' ∨ J2JVM P,E ⊢ a' ≤hb a
    by(simp add: J2JVM-def compP2-def)
qed
qed
end

```

theory JMM

imports

JMM-DRF

SC-Legal

DRF-J

DRF-JVM

JMM-Type

JMM-Interp

1560

```
JMM-Typesafe  
JMM-J-Typesafe  
JMM-JVM-Typesafe  
JMM-Compiler-Type2  
begin  
  
end  
theory MM-Main  
imports  
  SC  
  SC-Interp  
  SC-Collections  
  JMM  
begin  
  
end
```

Chapter 9

Schedulers

9.1 Refinement for multithreaded states

```
theory State-Refinement
imports
  ../Framework/FWSemantics
  ../Common/StartConfig
begin

type-synonym
  ('l,'t,'m,'m-t,'m-w,'s-i) state-refine = ('l,'t) locks × ('m-t × 'm) × 'm-w × 's-i

locale state-refine-base =
  fixes final :: 'x ⇒ bool
  and r :: 't ⇒ ('x × 'm) ⇒ (('l,'t,'x,'m,'w,'o) thread-action × 'x × 'm) Predicate.pred
  and convert-RA :: 'l released-locks ⇒ 'o list
  and thr-α :: 'm-t ⇒ ('l,'t,'x) thread-info
  and thr-invar :: 'm-t ⇒ bool
  and ws-α :: 'm-w ⇒ ('w,'t) wait-sets
  and ws-invar :: 'm-w ⇒ bool
  and is-α :: 's-i ⇒ 't interrupts
  and is-invar :: 's-i ⇒ bool
begin

fun state-α :: ('l,'t,'m,'m-t,'m-w, 's-i) state-refine ⇒ ('l,'t,'x,'m,'w) state
where state-α (ls, (ts, m), ws, is) = (ls, (thr-α ts, m), ws-α ws, is-α is)

lemma state-α-conv [simp]:
  locks (state-α s) = locks s
  thr (state-α s) = thr-α (thr s)
  shr (state-α s) = shr s
  wset (state-α s) = ws-α (wset s)
  interrupts (state-α s) = is-α (interrupts s)
by(case-tac [!]) s auto

inductive state-invar :: ('l,'t,'m,'m-t,'m-w,'s-i) state-refine ⇒ bool
where [| thr-invar ts; ws-invar ws; is-invar is |] ⇒ state-invar (ls, (ts, m), ws, is)

inductive-simps state-invar-simps [simp]:
  state-invar (ls, (ts, m), ws, is)
```

```

lemma state-invarD [simp]:
  assumes state-invar s
  shows thr-invar (thr s) ws-invar (wset s) is-invar (interrupts s)
using assms by(case-tac [!] s) auto

```

end

```

sublocale state-refine-base <  $\alpha$ : final-thread final .

```

```

sublocale state-refine-base <  $\alpha$ :
  multithreaded-base
  final
   $\lambda t\ xm\ ta\ x'm'.$  Predicate.eval (r t xm) (ta, x'm')
.

```

definition (*in heap-base*) *start-state-refine* ::

```

  'm-t  $\Rightarrow$  ('thread-id  $\Rightarrow$  ('x  $\times$  'addr released-locks)  $\Rightarrow$  'm-t  $\Rightarrow$  'm-t)  $\Rightarrow$  'm-w  $\Rightarrow$  's-i
   $\Rightarrow$  (cname  $\Rightarrow$  mname  $\Rightarrow$  ty list  $\Rightarrow$  ty  $\Rightarrow$  'md  $\Rightarrow$  'addr val list  $\Rightarrow$  'x)  $\Rightarrow$  'md prog  $\Rightarrow$  cname  $\Rightarrow$  mname
   $\Rightarrow$  'addr val list
   $\Rightarrow$  ('addr, 'thread-id, 'heap, 'm-t, 'm-w, 's-i) state-refine

```

where

```

   $\wedge$  is-empty.
  start-state-refine thr-empty thr-update ws-empty is-empty f P C M vs =
  (let (D, Ts, T, m) = method P C M
    in ( $K\$$  None, (thr-update start-tid (f D M Ts T (the m) vs, no-wait-locks) thr-empty, start-heap),
    ws-empty, is-empty))

```

definition *Jinja-output* ::

```

  's  $\Rightarrow$  'thread-id  $\Rightarrow$  ('addr, 'thread-id, 'x, 'heap, 'addr, ('addr, 'thread-id) obs-event) thread-action
   $\Rightarrow$  ('thread-id  $\times$  ('addr, 'thread-id) obs-event list) option

```

where *Jinja-output* σ *t ta* = (if $\{ta\}_o = []$ then None else Some (*t, \{ta\}_o*))

lemmas [*code*] =

```

  heap-base.start-state-refine-def

```

end

9.2 Abstract scheduler

theory *Scheduler*

imports

```

  State-Refinement
  ../Framework/FWProgressAux
  ../Framework/FWLTS

  ../Basic/JT-ICF

```

begin

Define an unfold operation that puts everything into one function to avoid duplicate evaluation.

definition *unfold-tllist'* :: ('a \Rightarrow 'b \times 'a + 'c) \Rightarrow 'a \Rightarrow ('b, 'c) *tllist*

where [*code del*]:

unfold-tllist' f =
unfold-tllist ($\lambda a. \exists c. f a = \text{Inr } c$) (*projr* \circ *f*) (*fst* \circ *projl* \circ *f*) (*snd* \circ *projl* \circ *f*)

lemma *unfold-tllist' [code]*:

unfold-tllist' f a =
 (*case f a of Inr c* \Rightarrow *TNil c* | *Inl (b, a')* \Rightarrow *TCons b (unfold-tllist' f a')*)
by(*rule tllist.expand*)(*auto simp add: unfold-tllist'-def split: sum.split-asm*)

type-synonym

(*'l, 't, 'x, 'm, 'w, 'o, 'm-t, 'm-w, 's-i, 's*) *scheduler* =
 '*s* \Rightarrow (*'l, 't, 'm, 'm-t, 'm-w, 's-i*) *state-refine* \Rightarrow (*'t* \times ((*'l, 't, 'x, 'm, 'w, 'o*) *thread-action* \times '*x* \times '*m*) *option*
 \times '*s*) *option*

locale *scheduler-spec-base* =

state-refine-base
final r convert-RA
thr- α thr-invar
ws- α ws-invar
is- α is-invar
for *final* :: '*x* \Rightarrow *bool*
and *r* :: '*t* \Rightarrow ('*x* \times '*m*) \Rightarrow ((*'l, 't, 'x, 'm, 'w, 'o*) *thread-action* \times '*x* \times '*m*) *Predicate.pred*
and *convert-RA* :: '*l* *released-locks* \Rightarrow '*o* *list*
and *schedule* :: ('*l, 't, 'x, 'm, 'w, 'o, 'm-t, 'm-w, 's-i, 's*) *scheduler*
and *σ -invar* :: '*s* \Rightarrow '*t* *set* \Rightarrow *bool*
and *thr- α* :: '*m-t* \Rightarrow ('*l, 't, 'x*) *thread-info*
and *thr-invar* :: '*m-t* \Rightarrow *bool*
and *ws- α* :: '*m-w* \Rightarrow ('*w, 't*) *wait-sets*
and *ws-invar* :: '*m-w* \Rightarrow *bool*
and *is- α* :: '*s-i* \Rightarrow '*t* *interrupts*
and *is-invar* :: '*s-i* \Rightarrow *bool*

locale *scheduler-spec* =

scheduler-spec-base
final r convert-RA
schedule σ -invar
thr- α thr-invar
ws- α ws-invar
is- α is-invar
for *final* :: '*x* \Rightarrow *bool*
and *r* :: '*t* \Rightarrow ('*x* \times '*m*) \Rightarrow ((*'l, 't, 'x, 'm, 'w, 'o*) *thread-action* \times '*x* \times '*m*) *Predicate.pred*
and *convert-RA* :: '*l* *released-locks* \Rightarrow '*o* *list*
and *schedule* :: ('*l, 't, 'x, 'm, 'w, 'o, 'm-t, 'm-w, 's-i, 's*) *scheduler*
and *σ -invar* :: '*s* \Rightarrow '*t* *set* \Rightarrow *bool*
and *thr- α* :: '*m-t* \Rightarrow ('*l, 't, 'x*) *thread-info*
and *thr-invar* :: '*m-t* \Rightarrow *bool*
and *ws- α* :: '*m-w* \Rightarrow ('*w, 't*) *wait-sets*
and *ws-invar* :: '*m-w* \Rightarrow *bool*
and *is- α* :: '*s-i* \Rightarrow '*t* *interrupts*
and *is-invar* :: '*s-i* \Rightarrow *bool*

+

fixes *invariant* :: ('*l, 't, 'x, 'm, 'w*) *state set*

assumes *schedule-NoneD*:

\llbracket *schedule σ s = None; state-invar s; σ -invar σ (dom (thr- α (thr s))); state- α s \in invariant \rrbracket*

$\implies \alpha.\text{active-threads}(\text{state-}\alpha s) = \{\}$
and *schedule-Some-NoneD*:
 $\llbracket \text{schedule } \sigma s = \llbracket (t, \text{None}, \sigma') \rrbracket; \text{state-invar } s; \sigma\text{-invar } \sigma (\text{dom } (\text{thr-}\alpha (\text{thr } s))); \text{state-}\alpha s \in \text{invariant} \rrbracket$
 $\implies \exists x \text{ ln } n. \text{thr-}\alpha (\text{thr } s) t = \llbracket (x, \text{ln}) \rrbracket \wedge \text{ln } \$ n > 0 \wedge \neg \text{waiting } (\text{ws-}\alpha (\text{wset } s) t) \wedge \text{may-acquire-all}(\text{locks } s) t \text{ ln}$
and *schedule-Some-SomeD*:
 $\llbracket \text{schedule } \sigma s = \llbracket (t, \llbracket (ta, x', m') \rrbracket, \sigma') \rrbracket; \text{state-invar } s; \sigma\text{-invar } \sigma (\text{dom } (\text{thr-}\alpha (\text{thr } s))); \text{state-}\alpha s \in \text{invariant} \rrbracket$
 $\implies \exists x. \text{thr-}\alpha (\text{thr } s) t = \llbracket (x, \text{no-wait-locks}) \rrbracket \wedge \text{Predicate.eval } (r t (x, \text{shr } s)) (ta, x', m') \wedge \alpha.\text{actions-ok}(\text{state-}\alpha s) t ta$
and *schedule-invar-None*:
 $\llbracket \text{schedule } \sigma s = \llbracket (t, \text{None}, \sigma') \rrbracket; \text{state-invar } s; \sigma\text{-invar } \sigma (\text{dom } (\text{thr-}\alpha (\text{thr } s))); \text{state-}\alpha s \in \text{invariant} \rrbracket$
 $\implies \sigma\text{-invar } \sigma' (\text{dom } (\text{thr-}\alpha (\text{thr } s)))$
and *schedule-invar-Some*:
 $\llbracket \text{schedule } \sigma s = \llbracket (t, \llbracket (ta, x', m') \rrbracket, \sigma') \rrbracket; \text{state-invar } s; \sigma\text{-invar } \sigma (\text{dom } (\text{thr-}\alpha (\text{thr } s))); \text{state-}\alpha s \in \text{invariant} \rrbracket$
 $\implies \sigma\text{-invar } \sigma' (\text{dom } (\text{thr-}\alpha (\text{thr } s)) \cup \{t. \exists x m. \text{NewThread } t x m \in \text{set } \{\llbracket ta \rrbracket_t\}\})$

locale *pick-wakeup-spec-base* =
state-refine-base
final *r* *convert-RA*
thr-}\alpha *thr-invar*
ws-}\alpha *ws-invar*
is-}\alpha *is-invar*
for *final* :: 'x \Rightarrow bool
and *r* :: 't \Rightarrow ('x \times 'm) \Rightarrow (('l, 't, 'x, 'm, 'w, 'o) *thread-action* \times 'x \times 'm) *Predicate.pred*
and *convert-RA* :: 'l *released-locks* \Rightarrow 'o *list*
and *pick-wakeup* :: 's \Rightarrow 't \Rightarrow 'w \Rightarrow 'm-w \Rightarrow 't *option*
and *\sigma-invar* :: 's \Rightarrow 't *set* \Rightarrow bool
and *thr-}\alpha* :: 'm-t \Rightarrow ('l, 't, 'x) *thread-info*
and *thr-invar* :: 'm-t \Rightarrow bool
and *ws-}\alpha* :: 'm-w \Rightarrow ('w, 't) *wait-sets*
and *ws-invar* :: 'm-w \Rightarrow bool
and *is-}\alpha* :: 's-i \Rightarrow 't *interrupts*
and *is-invar* :: 's-i \Rightarrow bool

locale *pick-wakeup-spec* =
pick-wakeup-spec-base
final *r* *convert-RA*
pick-wakeup *\sigma-invar*
thr-}\alpha *thr-invar*
ws-}\alpha *ws-invar*
is-}\alpha *is-invar*
for *final* :: 'x \Rightarrow bool
and *r* :: 't \Rightarrow ('x \times 'm) \Rightarrow (('l, 't, 'x, 'm, 'w, 'o) *thread-action* \times 'x \times 'm) *Predicate.pred*
and *convert-RA* :: 'l *released-locks* \Rightarrow 'o *list*
and *pick-wakeup* :: 's \Rightarrow 't \Rightarrow 'w \Rightarrow 'm-w \Rightarrow 't *option*
and *\sigma-invar* :: 's \Rightarrow 't *set* \Rightarrow bool
and *thr-}\alpha* :: 'm-t \Rightarrow ('l, 't, 'x) *thread-info*
and *thr-invar* :: 'm-t \Rightarrow bool
and *ws-}\alpha* :: 'm-w \Rightarrow ('w, 't) *wait-sets*
and *ws-invar* :: 'm-w \Rightarrow bool


```

and is- $\alpha$  :: 's-i  $\Rightarrow$  't interrupts
and is-invar :: 's-i  $\Rightarrow$  bool
+
assumes pick-wakeup-NoneD:
[[ pick-wakeup  $\sigma$  t w ws = None; ws-invar ws;  $\sigma$ -invar  $\sigma$  T; dom (ws- $\alpha$  ws)  $\subseteq$  T; t  $\in$  T ]]
 $\Rightarrow$  InWS w  $\notin$  ran (ws- $\alpha$  ws)
and pick-wakeup-SomeD:
[[ pick-wakeup  $\sigma$  t w ws = [t']; ws-invar ws;  $\sigma$ -invar  $\sigma$  T; dom (ws- $\alpha$  ws)  $\subseteq$  T; t  $\in$  T ]]
 $\Rightarrow$  ws- $\alpha$  ws t' = [InWS w]

```

```

locale scheduler-base-aux =
  state-refine-base
  final r convert-RA
  thr- $\alpha$  thr-invar
  ws- $\alpha$  ws-invar
  is- $\alpha$  is-invar
for final :: 'x  $\Rightarrow$  bool
and r :: 't  $\Rightarrow$  ('x  $\times$  'm)  $\Rightarrow$  (('l,'t,'x,'m,'w,'o) thread-action  $\times$  'x  $\times$  'm) Predicate.pred
and convert-RA :: 'l released-locks  $\Rightarrow$  'o list
and thr- $\alpha$  :: 'm-t  $\Rightarrow$  ('l,'t,'x) thread-info
and thr-invar :: 'm-t  $\Rightarrow$  bool
and thr-lookup :: 't  $\Rightarrow$  'm-t  $\rightarrow$  ('x  $\times$  'l released-locks)
and thr-update :: 't  $\Rightarrow$  'x  $\times$  'l released-locks  $\Rightarrow$  'm-t  $\Rightarrow$  'm-t
and ws- $\alpha$  :: 'm-w  $\Rightarrow$  ('w,'t) wait-sets
and ws-invar :: 'm-w  $\Rightarrow$  bool
and ws-lookup :: 't  $\Rightarrow$  'm-w  $\rightarrow$  'w wait-set-status
and is- $\alpha$  :: 's-i  $\Rightarrow$  't interrupts
and is-invar :: 's-i  $\Rightarrow$  bool
and is-memb :: 't  $\Rightarrow$  's-i  $\Rightarrow$  bool
and is-ins :: 't  $\Rightarrow$  's-i  $\Rightarrow$  's-i
and is-delete :: 't  $\Rightarrow$  's-i  $\Rightarrow$  's-i
begin

```

```

definition free-thread-id :: 'm-t  $\Rightarrow$  't  $\Rightarrow$  bool
where free-thread-id ts t  $\longleftrightarrow$  thr-lookup t ts = None

```

```

fun redT-updT :: 'm-t  $\Rightarrow$  ('t,'x,'m) new-thread-action  $\Rightarrow$  'm-t
where
  redT-updT ts (NewThread t' x m) = thr-update t' (x, no-wait-locks) ts
| redT-updT ts - = ts

```

```

definition redT-updTs :: 'm-t  $\Rightarrow$  ('t,'x,'m) new-thread-action list  $\Rightarrow$  'm-t
where redT-updTs = foldl redT-updT

```

```

primrec thread-ok :: 'm-t  $\Rightarrow$  ('t,'x,'m) new-thread-action  $\Rightarrow$  bool
where
  thread-ok ts (NewThread t x m) = free-thread-id ts t
| thread-ok ts (ThreadExists t b) = (b  $\neq$  free-thread-id ts t)

```

We use *local.redT-updT* in *thread-ok* instead of *redT-updT'* like in theory *JinjaThreads.FWThread*. This fixes 'x in the ('t, 'x, 'm) *new-thread-action list* type, but avoids *undefined*, which raises an exception during execution in the generated code.

```

primrec thread-oks :: 'm-t  $\Rightarrow$  ('t,'x,'m) new-thread-action list  $\Rightarrow$  bool
where

```

$thread\text{-}oks\ ts\ [] = True$
 $| thread\text{-}oks\ ts\ (ta\#\ tas) = (thread\text{-}ok\ ts\ ta \wedge thread\text{-}oks\ (redT\text{-}updT\ ts\ ta)\ tas)$

definition $wset\text{-}actions\text{-}ok :: 'm\text{-}w \Rightarrow 't \Rightarrow ('t, 'w)\ \text{wait-set-action list} \Rightarrow bool$
where

$wset\text{-}actions\text{-}ok\ ws\ t\ was \longleftrightarrow$
 $ws\text{-}lookup\ t\ ws =$
 (if $Notified \in set\ was$ then $[PostWS\ WSNotified]$
 else if $WokenUp \in set\ was$ then $[PostWS\ WSWokenUp]$
 else $None$)

primrec $cond\text{-}action\text{-}ok :: ('l, 't, 'm, 'm\text{-}t, 'm\text{-}w, 's\text{-}i)\ \text{state-refine} \Rightarrow 't \Rightarrow 't\ \text{conditional-action} \Rightarrow bool$
where

$\bigwedge ln.\ cond\text{-}action\text{-}ok\ s\ t\ (Join\ T) =$
 (case $thr\text{-}lookup\ T\ (thr\ s)$
 of $None \Rightarrow True$
 $| [(x, ln)] \Rightarrow t \neq T \wedge final\ x \wedge ln = no\text{-}wait\text{-}locks \wedge ws\text{-}lookup\ T\ (wset\ s) = None$)
 $| cond\text{-}action\text{-}ok\ s\ t\ Yield = True$

definition $cond\text{-}action\text{-}oks :: ('l, 't, 'm, 'm\text{-}t, 'm\text{-}w, 's\text{-}i)\ \text{state-refine} \Rightarrow 't \Rightarrow 't\ \text{conditional-action list} \Rightarrow bool$

where

$cond\text{-}action\text{-}oks\ s\ t\ cts = list\text{-}all\ (cond\text{-}action\text{-}ok\ s\ t)\ cts$

primrec $redT\text{-}updI :: 's\text{-}i \Rightarrow 't\ \text{interrupt-action} \Rightarrow 's\text{-}i$
where

$redT\text{-}updI\ is\ (Interrupt\ t) = is\text{-}ins\ t\ is$
 $| redT\text{-}updI\ is\ (ClearInterrupt\ t) = is\text{-}delete\ t\ is$
 $| redT\text{-}updI\ is\ (IsInterrupted\ t\ b) = is$

primrec $redT\text{-}updIs :: 's\text{-}i \Rightarrow 't\ \text{interrupt-action list} \Rightarrow 's\text{-}i$
where

$redT\text{-}updIs\ is\ [] = is$
 $| redT\text{-}updIs\ is\ (ia\ \#\ ias) = redT\text{-}updIs\ (redT\text{-}updI\ is\ ia)\ ias$

primrec $interrupt\text{-}action\text{-}ok :: 's\text{-}i \Rightarrow 't\ \text{interrupt-action} \Rightarrow bool$
where

$interrupt\text{-}action\text{-}ok\ is\ (Interrupt\ t) = True$
 $| interrupt\text{-}action\text{-}ok\ is\ (ClearInterrupt\ t) = True$
 $| interrupt\text{-}action\text{-}ok\ is\ (IsInterrupted\ t\ b) = (b = (is\text{-}memb\ t\ is))$

primrec $interrupt\text{-}actions\text{-}ok :: 's\text{-}i \Rightarrow 't\ \text{interrupt-action list} \Rightarrow bool$
where

$interrupt\text{-}actions\text{-}ok\ is\ [] = True$
 $| interrupt\text{-}actions\text{-}ok\ is\ (ia\ \#\ ias) \longleftrightarrow interrupt\text{-}action\text{-}ok\ is\ ia \wedge interrupt\text{-}actions\text{-}ok\ (redT\text{-}updI\ is\ ia)\ ias$

definition $actions\text{-}ok :: ('l, 't, 'm, 'm\text{-}t, 'm\text{-}w, 's\text{-}i)\ \text{state-refine} \Rightarrow 't \Rightarrow ('l, 't, 'x, 'm, 'w, 'o')\ \text{thread-action} \Rightarrow bool$

where

$actions\text{-}ok\ s\ t\ ta \longleftrightarrow$
 $lock\text{-}ok\text{-}las\ (locks\ s)\ t\ \{\!\!|ta\!\!\}_l \wedge$
 $thread\text{-}oks\ (thr\ s)\ \{\!\!|ta\!\!\}_t \wedge$
 $cond\text{-}action\text{-}oks\ s\ t\ \{\!\!|ta\!\!\}_c \wedge$

wset-actions-ok (*wset s*) *t* $\{ta\}_w \wedge$
interrupt-actions-ok (*interrupts s*) $\{ta\}_i$

end

locale *scheduler-base* =

scheduler-base-aux

final r convert-RA

thr- α thr-invar thr-lookup thr-update

ws- α ws-invar ws-lookup

is- α is-invar is-memb is-ins is-delete

+

scheduler-spec-base

final r convert-RA

schedule σ -invar

thr- α thr-invar

ws- α ws-invar

is- α is-invar

+

pick-wakeup-spec-base

final r convert-RA

pick-wakeup σ -invar

thr- α thr-invar

ws- α ws-invar

is- α is-invar

for *final* :: '*x* \Rightarrow *bool*

and *r* :: '*t* \Rightarrow ('*x* \times '*m*) \Rightarrow (('l, '*t*, '*x*, '*m*, '*w*, '*o*) *thread-action* \times '*x* \times '*m*) *Predicate.pred*

and *convert-RA* :: '*l* *released-locks* \Rightarrow '*o* *list*

and *schedule* :: ('l, '*t*, '*x*, '*m*, '*w*, '*o*, '*m-t*, '*m-w*, '*s-i*, '*s*) *scheduler*

and *output* :: '*s* \Rightarrow '*t* \Rightarrow ('l, '*t*, '*x*, '*m*, '*w*, '*o*) *thread-action* \Rightarrow '*q* *option*

and *pick-wakeup* :: '*s* \Rightarrow '*t* \Rightarrow '*w* \Rightarrow '*m-w* \Rightarrow '*t* *option*

and *σ -invar* :: '*s* \Rightarrow '*t* *set* \Rightarrow *bool*

and *thr- α* :: '*m-t* \Rightarrow ('l, '*t*, '*x*) *thread-info*

and *thr-invar* :: '*m-t* \Rightarrow *bool*

and *thr-lookup* :: '*t* \Rightarrow '*m-t* \rightarrow ('*x* \times '*l* *released-locks*)

and *thr-update* :: '*t* \Rightarrow '*x* \times '*l* *released-locks* \Rightarrow '*m-t* \Rightarrow '*m-t*

and *ws- α* :: '*m-w* \Rightarrow ('*w*, '*t*) *wait-sets*

and *ws-invar* :: '*m-w* \Rightarrow *bool*

and *ws-lookup* :: '*t* \Rightarrow '*m-w* \rightarrow '*w* *wait-set-status*

and *ws-update* :: '*t* \Rightarrow '*w* *wait-set-status* \Rightarrow '*m-w* \Rightarrow '*m-w*

and *ws-delete* :: '*t* \Rightarrow '*m-w* \Rightarrow '*m-w*

and *ws-iterate* :: '*m-w* \Rightarrow ('*t* \times '*w* *wait-set-status*, '*m-w*) *set-iterator*

and *is- α* :: '*s-i* \Rightarrow '*t* *interrupts*

and *is-invar* :: '*s-i* \Rightarrow *bool*

and *is-memb* :: '*t* \Rightarrow '*s-i* \Rightarrow *bool*

and *is-ins* :: '*t* \Rightarrow '*s-i* \Rightarrow '*s-i*

and *is-delete* :: '*t* \Rightarrow '*s-i* \Rightarrow '*s-i*

begin

primrec *exec-updW* :: '*s* \Rightarrow '*t* \Rightarrow '*m-w* \Rightarrow ('*t*, '*w*) *wait-set-action* \Rightarrow '*m-w*

where

exec-updW σ *t* *ws* (*Notify w*) =

(*case pick-wakeup* σ *t* *w* *ws*

of None \Rightarrow *ws*

$|$ *Some* $t \Rightarrow$ *ws-update* t (*PostWS WSNotified*) ws)
 $|$ *exec-updW* σ t ws (*NotifyAll* w) =
ws-iterate ws ($\lambda-. \text{True}$) ($\lambda(t, w')$ ws' . *if* $w' = \text{InWS } w$ *then* *ws-update* t (*PostWS WSNotified*) ws'
else ws')
 ws
 $|$ *exec-updW* σ t ws (*Suspend* w) = *ws-update* t (*InWS* w) ws
 $|$ *exec-updW* σ t ws (*WakeUp* t') =
(*case* *ws-lookup* t' ws *of* [*InWS* w] \Rightarrow *ws-update* t' (*PostWS WSWokenUp*) ws $|$ $- \Rightarrow$ ws)
 $|$ *exec-updW* σ t ws *Notified* = *ws-delete* t ws
 $|$ *exec-updW* σ t ws *WokenUp* = *ws-delete* t ws

definition *exec-updWs* :: $'s \Rightarrow 't \Rightarrow 'm-w \Rightarrow ('t, 'w)$ *wait-set-action list* $\Rightarrow 'm-w$
where *exec-updWs* σ t = *foldl* (*exec-updW* σ t)

definition *exec-upd* ::

$'s \Rightarrow ('l, 't, 'm, 'm-t, 'm-w, 's-i)$ *state-refine* $\Rightarrow 't \Rightarrow ('l, 't, 'x, 'm, 'w, 'o)$ *thread-action* $\Rightarrow 'x \Rightarrow 'm$
 $\Rightarrow ('l, 't, 'm, 'm-t, 'm-w, 's-i)$ *state-refine*

where [*simp*]:

exec-upd σ s t x' m' =
(*redT-updLs* (*locks* s) t $\{ta\}_l$,
(*thr-update* t (x' , *redT-updLns* (*locks* s) t (*snd* (*the* (*thr-lookup* t (*thr* s)))) $\{ta\}_l$) (*redT-updTs* (*thr*
 s) $\{ta\}_t$), m'),
exec-updWs σ t (*wset* s) $\{ta\}_w$, *redT-updIs* (*interrupts* s) $\{ta\}_i$)

definition *execT* ::

$'s \Rightarrow ('l, 't, 'm, 'm-t, 'm-w, 's-i)$ *state-refine*
 $\Rightarrow ('s \times 't \times ('l, 't, 'x, 'm, 'w, 'o)$ *thread-action* $\times ('l, 't, 'm, 'm-t, 'm-w, 's-i)$ *state-refine*) *option*

where

execT σ s =
(*do* {
(t , $tax'm'$, σ') \leftarrow *schedule* σ s ;
case $tax'm'$ *of*
None \Rightarrow
(*let* (x , ln) = *the* (*thr-lookup* t (*thr* s));
 ta = ($K\$$ \square , \square , \square , \square , \square , *convert-RA* ln);
 s' = (*acquire-all* (*locks* s) t ln , (*thr-update* t (x , *no-wait-locks*) (*thr* s), *shr* s), *wset* s , *interrupts*
 s)
in [(σ', t, ta, s')])
 $|$ [(ta, x', m')] \Rightarrow [$(\sigma', t, ta, \text{exec-upd } \sigma$ s t ta x' $m')$]
})

primrec *exec-step* ::

$'s \times ('l, 't, 'm, 'm-t, 'm-w, 's-i)$ *state-refine* \Rightarrow
 $('s \times 't \times ('l, 't, 'x, 'm, 'w, 'o)$ *thread-action*) $\times 's \times ('l, 't, 'm, 'm-t, 'm-w, 's-i)$ *state-refine* + $('l, 't, 'm, 'm-t, 'm-w, 's-i)$
state-refine

where

exec-step (σ , s) =
(*case* *execT* σ s *of*
None \Rightarrow *Inr* s
 $|$ *Some* ($\sigma', t, ta, s')$ \Rightarrow *Inl* ($(\sigma, t, ta), \sigma', s')$)

declare *exec-step.simps* [*simp del*]

definition *exec-aux* ::

's × ('l,'t,'m,'m-t,'m-w,'s-i) state-refine
 ⇒ ('s × 't × ('l,'t,'x,'m,'w,'o) thread-action, ('l,'t,'m,'m-t,'m-w,'s-i) state-refine) tllist

where

exec-aux σ s = unfold-tllist' exec-step σ s

definition exec :: 's ⇒ ('l,'t,'m,'m-t,'m-w,'s-i) state-refine ⇒ ('q, ('l,'t,'m,'m-t,'m-w,'s-i) state-refine)
 tllist

where

exec σ s = tmap the id (tfilter undefined (λq. q ≠ None) (tmap (λ(σ, t, ta). output σ t ta) id
 (exec-aux (σ, s))))

end

Implement pick-wakeup by map-sel'

definition pick-wakeup-via-sel ::

('m-w ⇒ ('t ⇒ 'w wait-set-status ⇒ bool) → 't × 'w wait-set-status)
 ⇒ 's ⇒ 't ⇒ 'w ⇒ 'm-w ⇒ 't option

where pick-wakeup-via-sel ws-sel σ t w ws = map-option fst (ws-sel ws (λt w'. w' = InWS w))

lemma pick-wakeup-spec-via-sel:

assumes sel: map-sel' ws-α ws-invar ws-sel

shows pick-wakeup-spec (pick-wakeup-via-sel (λs P. ws-sel s (λ(k,v). P k v))) σ-invar ws-α ws-invar

proof –

interpret ws: map-sel' ws-α ws-invar ws-sel by(rule sel)

show ?thesis

by(unfold-locales)(auto simp add: pick-wakeup-via-sel-def ran-def dest: ws.sel'-noneD ws.sel'-SomeD)

qed

locale scheduler-ext-base =

scheduler-base-aux

final r convert-RA

thr-α thr-invar thr-lookup thr-update

ws-α ws-invar ws-lookup

is-α is-invar is-memb is-ins is-delete

for final :: 'x ⇒ bool

and r :: 't ⇒ ('x × 'm) ⇒ (('l,'t,'x,'m,'w,'o) thread-action × 'x × 'm) Predicate.pred

and convert-RA :: 'l released-locks ⇒ 'o list

and thr-α :: 'm-t ⇒ ('l,'t,'x) thread-info

and thr-invar :: 'm-t ⇒ bool

and thr-lookup :: 't ⇒ 'm-t → ('x × 'l released-locks)

and thr-update :: 't ⇒ 'x × 'l released-locks ⇒ 'm-t ⇒ 'm-t

and thr-iterate :: 'm-t ⇒ ('t × ('x × 'l released-locks), 's-t) set-iterator

and ws-α :: 'm-w ⇒ ('w,'t) wait-sets

and ws-invar :: 'm-w ⇒ bool

and ws-lookup :: 't ⇒ 'm-w → 'w wait-set-status

and ws-update :: 't ⇒ 'w wait-set-status ⇒ 'm-w ⇒ 'm-w

and ws-sel :: 'm-w ⇒ ('t × 'w wait-set-status ⇒ bool) → ('t × 'w wait-set-status)

and is-α :: 's-i ⇒ 't interrupts

and is-invar :: 's-i ⇒ bool

and is-memb :: 't ⇒ 's-i ⇒ bool

and is-ins :: 't ⇒ 's-i ⇒ 's-i

and is-delete :: 't ⇒ 's-i ⇒ 's-i

+

fixes thr'-α :: 's-t ⇒ 't set

```

and thr'-invar :: 's-t ⇒ bool
and thr'-empty :: unit ⇒ 's-t
and thr'-ins-dj :: 't ⇒ 's-t ⇒ 's-t
begin

abbreviation pick-wakeup :: 's ⇒ 't ⇒ 'w ⇒ 'm-w ⇒ 't option
where pick-wakeup ≡ pick-wakeup-via-sel (λs P. ws-sel s (λ(k,v). P k v))

fun active-threads :: ('l,'t,'m,'m-t,'m-w,'s-i) state-refine ⇒ 's-t
where
  active-threads (ls, (ts, m), ws, is) =
    thr-iterate ts (λ-. True)
      (λ(t, (x, ln)) ts'. if ln = no-wait-locks
        then if Predicate.holds
          (do {
            (ta, -) ← r t (x, m);
            Predicate.if-pred (actions-ok (ls, (ts, m), ws, is) t ta)
          })
          then thr'-ins-dj t ts'
          else ts'
        else if ¬ waiting (ws-lookup t ws) ∧ may-acquire-all ls t ln then thr'-ins-dj t ts' else
ts')
      (thr'-empty ())

end

locale scheduler-aux =
  scheduler-base-aux
  final r convert-RA
  thr-α thr-invar thr-lookup thr-update
  ws-α ws-invar ws-lookup
  is-α is-invar is-memb is-ins is-delete
+
  thr: finite-map thr-α thr-invar +
  thr: map-lookup thr-α thr-invar thr-lookup +
  thr: map-update thr-α thr-invar thr-update +
  ws: map ws-α ws-invar +
  ws: map-lookup ws-α ws-invar ws-lookup +
  is: set is-α is-invar +
  is: set-memb is-α is-invar is-memb +
  is: set-ins is-α is-invar is-ins +
  is: set-delete is-α is-invar is-delete
for final :: 'x ⇒ bool
and r :: 't ⇒ ('x × 'm) ⇒ (('l,'t,'x,'m,'w,'o) thread-action × 'x × 'm) Predicate.pred
and convert-RA :: 'l released-locks ⇒ 'o list
and thr-α :: 'm-t ⇒ ('l,'t,'x) thread-info
and thr-invar :: 'm-t ⇒ bool
and thr-lookup :: 't ⇒ 'm-t → ('x × 'l released-locks)
and thr-update :: 't ⇒ 'x × 'l released-locks ⇒ 'm-t ⇒ 'm-t
and ws-α :: 'm-w ⇒ ('w,'t) wait-sets
and ws-invar :: 'm-w ⇒ bool
and ws-lookup :: 't ⇒ 'm-w → 'w wait-set-status
and is-α :: 's-i ⇒ 't interrupts
and is-invar :: 's-i ⇒ bool

```

and *is-memb* :: 't ⇒ 's-i ⇒ bool
and *is-ins* :: 't ⇒ 's-i ⇒ 's-i
and *is-delete* :: 't ⇒ 's-i ⇒ 's-i
begin

lemma *free-thread-id-correct* [*simp*]:

thr-invar ts ⇒ *free-thread-id ts* = *FWThread.free-thread-id (thr-α ts)*
by(*auto simp add: free-thread-id-def fun-eq-iff thr.lookup-correct intro: free-thread-id.intros*)

lemma *redT-updT-correct* [*simp*]:

assumes *thr-invar ts*
shows *thr-α (redT-updT ts nta)* = *FWThread.redT-updT (thr-α ts) nta*
and *thr-invar (redT-updT ts nta)*
by(*case-tac [!] nta*)(*simp-all add: thr.update-correct assms*)

lemma *redT-updT_s-correct* [*simp*]:

assumes *thr-invar ts*
shows *thr-α (redT-updT_s ts ntas)* = *FWThread.redT-updT_s (thr-α ts) ntas*
and *thr-invar (redT-updT_s ts ntas)*
using *assms*
by(*induct ntas arbitrary: ts*)(*simp-all add: redT-updT_s-def*)

lemma *thread-ok-correct* [*simp*]:

thr-invar ts ⇒ *thread-ok ts nta* ⇔ *FWThread.thread-ok (thr-α ts) nta*
by(*cases nta simp-all*)

lemma *thread-oks-correct* [*simp*]:

thr-invar ts ⇒ *thread-oks ts ntas* ⇔ *FWThread.thread-oks (thr-α ts) ntas*
by(*induct ntas arbitrary: ts simp-all*)

lemma *wset-actions-ok-correct* [*simp*]:

ws-invar ws ⇒ *wset-actions-ok ws t was* ⇔ *FWWait.wset-actions-ok (ws-α ws) t was*
by(*simp add: wset-actions-ok-def FWWait.wset-actions-ok-def ws.lookup-correct*)

lemma *cond-action-ok-correct* [*simp*]:

state-invar s ⇒ *cond-action-ok s t cta* ⇔ *α.cond-action-ok (state-α s) t cta*
by(*cases s,cases cta*)(*auto simp add: thr.lookup-correct ws.lookup-correct*)

lemma *cond-action-oks-correct* [*simp*]:

assumes *state-invar s*
shows *cond-action-oks s t ctas* ⇔ *α.cond-action-oks (state-α s) t ctas*
by(*induct ctas*)(*simp-all add: cond-action-oks-def assms*)

lemma *redT-updI-correct* [*simp*]:

assumes *is-invar is*
shows *is-α (redT-updI is ia)* = *FWInterrupt.redT-updI (is-α is) ia*
and *is-invar (redT-updI is ia)*
using *assms*
by(*case-tac [!] ia*)(*auto simp add: is.ins-correct is.delete-correct*)

lemma *redT-updI_s-correct* [*simp*]:

assumes *is-invar is*
shows *is-α (redT-updI_s is ias)* = *FWInterrupt.redT-updI_s (is-α is) ias*
and *is-invar (redT-updI_s is ias)*

using *assms*

by(*induct ias arbitrary: is*)(*auto*)

lemma *interrupt-action-ok-correct* [*simp*]:

is-invar is \implies *interrupt-action-ok is ia* \iff *FWInterrupt.interrupt-action-ok (is- α is) ia*

by(*cases ia*)(*auto simp add: is.memb-correct*)

lemma *interrupt-actions-ok-correct* [*simp*]:

is-invar is \implies *interrupt-actions-ok is ias* \iff *FWInterrupt.interrupt-actions-ok (is- α is) ias*

by(*induct ias arbitrary:is*) *simp-all*

lemma *actions-ok-correct* [*simp*]:

state-invar s \implies *actions-ok s t ta* \iff *α .actions-ok (state- α s) t ta*

by(*auto simp add: actions-ok-def*)

end

locale *scheduler* =

scheduler-base

final r convert-RA

schedule output pick-wakeup σ -invar

thr- α thr-invar thr-lookup thr-update

ws- α ws-invar ws-lookup ws-update ws-delete ws-iterate

is- α is-invar is-memb is-ins is-delete

+

scheduler-aux

final r convert-RA

thr- α thr-invar thr-lookup thr-update

ws- α ws-invar ws-lookup

is- α is-invar is-memb is-ins is-delete

+

scheduler-spec

final r convert-RA

schedule σ -invar

thr- α thr-invar

ws- α ws-invar

is- α is-invar

invariant

+

pick-wakeup-spec

final r convert-RA

pick-wakeup σ -invar

thr- α thr-invar

ws- α ws-invar

is- α is-invar

+

ws: map-update ws- α ws-invar ws-update +

ws: map-delete ws- α ws-invar ws-delete +

ws: map-iteratei ws- α ws-invar ws-iterate

for *final* :: '*x* \Rightarrow *bool*

and *r* :: '*t* \Rightarrow ('*x* \times '*m*) \Rightarrow (('l,'t,'x,'m,'w,'o) *thread-action* \times '*x* \times '*m*) *Predicate.pred*

and *convert-RA* :: '*l* *released-locks* \Rightarrow '*o* *list*

and *schedule* :: ('l,'t,'x,'m,'w,'o,'m-t,'m-w,'s-i,'s) *scheduler*

and *output* :: '*s* \Rightarrow '*t* \Rightarrow ('l,'t,'x,'m,'w,'o) *thread-action* \Rightarrow '*q* *option*


```

and pick-wakeup :: 's ⇒ 't ⇒ 'w ⇒ 'm-w ⇒ 't option
and σ-invar :: 's ⇒ 't set ⇒ bool
and thr-α :: 'm-t ⇒ ('l,'t,'x) thread-info
and thr-invar :: 'm-t ⇒ bool
and thr-lookup :: 't ⇒ 'm-t → ('x × 'l released-locks)
and thr-update :: 't ⇒ 'x × 'l released-locks ⇒ 'm-t ⇒ 'm-t
and ws-α :: 'm-w ⇒ ('w,'t) wait-sets
and ws-invar :: 'm-w ⇒ bool
and ws-lookup :: 't ⇒ 'm-w → 'w wait-set-status
and ws-update :: 't ⇒ 'w wait-set-status ⇒ 'm-w ⇒ 'm-w
and ws-delete :: 't ⇒ 'm-w ⇒ 'm-w
and ws-iterate :: 'm-w ⇒ ('t × 'w wait-set-status, 'm-w) set-iterator
and is-α :: 's-i ⇒ 't interrupts
and is-invar :: 's-i ⇒ bool
and is-memb :: 't ⇒ 's-i ⇒ bool
and is-ins :: 't ⇒ 's-i ⇒ 's-i
and is-delete :: 't ⇒ 's-i ⇒ 's-i
and invariant :: ('l,'t,'x,'m,'w) state set
+
assumes invariant: invariant3p α.redT invariant
begin

```

lemma *exec-updW-correct*:

```

assumes invar: ws-invar ws σ-invar σ T dom (ws-α ws) ⊆ T t ∈ T
shows redT-updW t (ws-α ws) wa (ws-α (exec-updW σ t ws wa)) (is ?thesis1)
and ws-invar (exec-updW σ t ws wa) (is ?thesis2)

```

proof –

```

from invar have ?thesis1 ∧ ?thesis2
proof(cases wa)
case [simp]: (Notify w)
show ?thesis
proof(cases pick-wakeup σ t w ws)
case (Some t')
hence ws-α ws t' = [InWS w] using invar by(rule pick-wakeup-SomeD)
with Some show ?thesis using invar by(auto simp add: ws.update-correct)
next
case None
hence InWS w ∉ ran (ws-α ws) using invar by(rule pick-wakeup-NoneD)
with None show ?thesis using invar by(auto simp add: ran-def)
qed
next
case [simp]: (NotifyAll w)
let ?f = λ(t, w') ws'. if w' = InWS w then ws-update t (PostWS WSNotified) ws' else ws'
let ?I = λT ws'. (∀ k. if k ∉ T ∧ ws-α ws k = [InWS w] then ws-α ws' k = [PostWS WSNotified]
else ws-α ws' k = ws-α ws k) ∧ ws-invar ws'
from invar have ?I (dom (ws-α ws)) ws by(auto simp add: ws.lookup-correct)
with ⟨ws-invar ws⟩ have ?I {} (ws-iterate ws (λ-. True) ?f ws)
proof(rule ws.iterate-rule-P[where I=?I])
fix t w' T ws'
assume t: t ∈ T and w': ws-α ws t = [w']
and T: T ⊆ dom (ws-α ws) and I: ?I T ws'
{ fix t'
assume t' ∉ T - {t} ws-α ws t' = [InWS w]
with t I w' invar have ws-α (?f (t, w') ws') t' = [PostWS WSNotified]

```

```

    by(auto)(simp-all add: ws.update-correct) }
  moreover {
    fix t'
    assume t' ∈ T - {t} ∨ ws-α ws t' ≠ [InWS w]
    with t I w' invar have ws-α (?f (t,w') ws') t' = ws-α ws t'
    by(auto simp add: ws.update-correct) }
  moreover
  have ws-invar (?f (t, w') ws') using I by(simp add: ws.update-correct)
  ultimately show ?I (T - {t}) (?f (t, w') ws') by safe simp
qed
hence ws-α (ws-iterate ws (λ-. True) ?f ws) = (λt. if ws-α ws t = [InWS w] then [PostWS
WSNotified] else ws-α ws t)
and ws-invar (ws-iterate ws (λ-. True) ?f ws) by(simp-all add: fun-eq-iff)
thus ?thesis by simp
next
case WakeUp thus ?thesis using assms
by(auto simp add: ws.lookup-correct ws.update-correct split: wait-set-status.split)
qed(simp-all add: ws.update-correct ws.delete-correct map-upd-eq-restrict)
thus ?thesis1 ?thesis2 by simp-all
qed

```

lemma *exec-updWs-correct*:

```

  assumes ws-invar ws σ-invar σ T dom (ws-α ws) ⊆ T t ∈ T
  shows redT-updWs t (ws-α ws) was (ws-α (exec-updWs σ t ws was)) (is ?thesis1)
  and ws-invar (exec-updWs σ t ws was) (is ?thesis2)
proof -
  from ⟨ws-invar ws⟩ ⟨dom (ws-α ws) ⊆ T⟩
  have ?thesis1 ∧ ?thesis2
  proof(induct was arbitrary: ws)
    case Nil thus ?case by(auto simp add: exec-updWs-def redT-updWs-def)
  next
    case (Cons wa was)
    let ?ws' = exec-updW σ t ws wa
    from ⟨ws-invar ws⟩ ⟨σ-invar σ T⟩ ⟨dom (ws-α ws) ⊆ T⟩ ⟨t ∈ T⟩
    have invar': ws-invar ?ws' and red: redT-updW t (ws-α ws) wa (ws-α ?ws')
    by(rule exec-updW-correct)+
    have dom (ws-α ?ws') ⊆ T
    proof
      fix t' assume t' ∈ dom (ws-α ?ws')
      with red have t' ∈ dom (ws-α ws) ∨ t = t'
      by(auto dest!: redT-updW-Some-otherD split: wait-set-status.split-asm)
      with ⟨dom (ws-α ws) ⊆ T⟩ ⟨t ∈ T⟩ show t' ∈ T by auto
    qed
    with invar' have redT-updWs t (ws-α ?ws') was (ws-α (exec-updWs σ t ?ws' was)) ∧ ws-invar
    (exec-updWs σ t ?ws' was)
    by(rule Cons.hyps)
    thus ?case using red
    by(auto simp add: exec-updWs-def redT-updWs-def intro: rtrancl3p-step-converse)
  qed
  thus ?thesis1 ?thesis2 by simp-all
qed

```

lemma *exec-upd-correct*:

```

  assumes state-invar s σ-invar σ (dom (thr-α (thr s))) t ∈ (dom (thr-α (thr s)))

```

and *wset-thread-ok* (*ws- α* (*wset s*)) (*thr- α* (*thr s*))
shows *redT-upd* (*state- α* *s*) *t ta x' m'* (*state- α* (*exec-upd* σ *s t ta x' m'*))
and *state-invar* (*exec-upd* σ *s t ta x' m'*)
using *assms unfolding wset-thread-ok-conv-dom*
by(*auto simp add: thr.update-correct thr.lookup-correct intro: exec-updWs-correct*)

lemma *execT-None*:

assumes *invar: state-invar s σ -invar σ (dom (thr- α (thr s))) state- α s \in invariant*
and *exec: execT σ s = None*
shows α .*active-threads* (*state- α* *s*) = {}
using *assms*
by(*cases schedule σ s*)(*fastforce simp add: execT-def thr.lookup-correct dest: schedule-Some-NoneD schedule-NoneD*)**+**

lemma *execT-Some*:

assumes *invar: state-invar s σ -invar σ (dom (thr- α (thr s))) state- α s \in invariant*
and *wstok: wset-thread-ok* (*ws- α* (*wset s*)) (*thr- α* (*thr s*))
and *exec: execT σ s = [(σ' , *t*, *ta*, *s'*)]*
shows α .*redT* (*state- α* *s*) (*t*, *ta*) (*state- α* *s'*) (**is** *?thesis1*)
and *state-invar s' (is ?thesis2)*
and *σ -invar σ' (dom (thr- α (thr s')) (is ?thesis3)*
proof –
note [*simp del*] = *redT-upd-simps exec-upd-def*

have *?thesis1 \wedge ?thesis2 \wedge ?thesis3*

proof(*cases fst (snd (the (schedule σ s)))*)

case *None*

with *exec invar have schedule: schedule σ s = [(*t*, *None*, σ')]*

and *ta: ta = (K\$ [], [], [], [], [], convert-RA (snd (the (thr- α (thr s) t))))*

and *s': s' = (acquire-all (locks s) t (snd (the (thr- α (thr s) t))), (thr-update t (fst (the (thr- α (thr s) t)), no-wait-locks) (thr s), shr s), wset s, interrupts s)*

by(*auto simp add: execT-def bind-eq-Some-conv thr.lookup-correct split-beta split del: option.split-asm*)
from *schedule-Some-NoneD[OF schedule invar]*

obtain *x ln n where t: thr- α (thr s) t = [(*x*, *ln*)]*

and $0 < \text{ln } \$ n \neg$ *waiting* (*ws- α* (*wset s*) *t*) *may-acquire-all* (*locks s*) *t ln* **by** *blast*

hence *?thesis1* **using** *ta s' invar* **by**(*auto intro: α .redT.redT-acquire simp add: thr.update-correct*)

moreover from *invar s' have ?thesis2* **by**(*simp add: thr.update-correct*)

moreover from *t s' invar have dom (thr- α (thr s')) = dom (thr- α (thr s))* **by**(*auto simp add: thr.update-correct*)

hence *?thesis3* **using** *invar schedule* **by**(*auto intro: schedule-invar-None*)

ultimately show *?thesis* **by** *simp*

next

case (*Some t α m*)

with *exec invar obtain x' m'*

where *schedule: schedule σ s = [(*t*, [(*ta*, *x'*, *m'*)], σ')]*

and *s': s' = exec-upd σ s t ta x' m'*

by(*cases t α m*)(*fastforce simp add: execT-def bind-eq-Some-conv split del: option.split-asm*)
from *schedule-Some-SomeD[OF schedule invar]*

obtain *x where t: thr- α (thr s) t = [(*x*, no-wait-locks)]*

and *Predicate.eval (r t (x, shr s)) (ta, x', m')*

and *aok: α .actions-ok (state- α s) t ta* **by** *blast*

with *s' have ?thesis1* **using** *invar wstok*

by(*fastforce intro: α .redT.intros exec-upd-correct*)

moreover from *invar s' t wstok* **have** *?thesis2* **by**(*auto intro: exec-upd-correct*)
moreover {
 from *schedule invar*
 have σ -*invar* σ' ($\text{dom } (\text{thr-}\alpha \text{ (thr } s)) \cup \{t. \exists x m. \text{NewThread } t x m \in \text{set } \{\text{ta}\}_t\}$)
 by(*rule schedule-invar-Some*)
 also have $\text{dom } (\text{thr-}\alpha \text{ (thr } s)) \cup \{t. \exists x m. \text{NewThread } t x m \in \text{set } \{\text{ta}\}_t\} = \text{dom } (\text{thr-}\alpha \text{ (thr } s'))$
 using *invar s' aok t*
 by(*auto simp add: exec-upd-def thr.lookup-correct thr.update-correct simp del: split-paired-Ex*)(*fastforce*
dest: redT-updTs-new-thread intro: redT-updTs-Some1 redT-updTs-new-thread-ts simp del: split-paired-Ex)
 finally have σ -*invar* σ' ($\text{dom } (\text{thr-}\alpha \text{ (thr } s'))$) . }
 ultimately show *?thesis* **by** *simp*
qed
thus *?thesis1 ?thesis2 ?thesis3* **by** *simp-all*
qed

lemma *exec-step-into-redT*:

assumes *invar: state-invar s* σ -*invar* σ ($\text{dom } (\text{thr-}\alpha \text{ (thr } s))$) *state- α s* \in *invariant*
and *wstok: wset-thread-ok* ($\text{ws-}\alpha$ (*wset s*)) ($\text{thr-}\alpha$ (*thr s*))
and *exec: exec-step* (σ , *s*) = *Inl* ($(\sigma'', t, \text{ta}), \sigma', s'$)
shows α .*redT* (*state- α s*) (*t, ta*) (*state- α s'*) $\sigma'' = \sigma$
and *state-invar s'* σ -*invar* σ' ($\text{dom } (\text{thr-}\alpha \text{ (thr } s'))$) *state- α s'* \in *invariant*

proof –

from *exec* **have** *execT*: *execT* σ *s* = $\lfloor (\sigma', t, \text{ta}, s') \rfloor$
and *q*: $\sigma'' = \sigma$ **by**(*auto simp add: exec-step.simps split-beta*)
from *invar wstok execT* **show** *red*: α .*redT* (*state- α s*) (*t, ta*) (*state- α s'*)
and *invar'*: *state-invar s'* σ -*invar* σ' ($\text{dom } (\text{thr-}\alpha \text{ (thr } s'))$) $\sigma'' = \sigma$
by(*rule execT-Some*)+(*rule q*)
from *invariant red* $\langle \text{state-}\alpha \text{ } s \in \text{invariant} \rangle$
show *state- α s'* \in *invariant* **by**(*rule invariant3pD*)

qed

lemma *exec-step-InrD*:

assumes *state-invar s* σ -*invar* σ ($\text{dom } (\text{thr-}\alpha \text{ (thr } s))$) *state- α s* \in *invariant*
and *exec-step* (σ , *s*) = *Inr* *s'*
shows α .*active-threads* (*state- α s*) = {}
and *s' = s*

using *assms*

by(*auto simp add: exec-step-def dest: execT-None*)

lemma (*in multithreaded-base*) *red-in-active-threads*:

assumes *s* \rightarrow *ta* \rightarrow *s'*
shows *t* \in *active-threads s*

using *assms*

by *cases*(*auto intro: active-threads.intros*)

lemma *exec-aux-into-Runs*:

assumes *state-invar s* σ -*invar* σ ($\text{dom } (\text{thr-}\alpha \text{ (thr } s))$) *state- α s* \in *invariant*
and *wset-thread-ok* ($\text{ws-}\alpha$ (*wset s*)) ($\text{thr-}\alpha$ (*thr s*))
shows α .*mthr.Runs* (*state- α s*) (*lmap snd (llist-of-tllist (exec-aux* (σ , *s*)))) (**is** *?thesis1*)
and *tfinite* (*exec-aux* (σ , *s*)) \implies *state-invar* (*terminal* (*exec-aux* (σ , *s*))) (**is** \implies *?thesis2*)

proof –

from *assms* **show** *?thesis1*
proof(*coinduction arbitrary: σ s*)
 case (*Runs* σ *s*)

```

note invar =  $\langle \text{state-invar } s \rangle \langle \sigma\text{-invar } \sigma \text{ (dom (thr-}\alpha \text{ (thr } s)) \rangle \langle \text{state-}\alpha \text{ } s \in \text{invariant} \rangle$ 
  and wstok =  $\langle \text{wset-thread-ok (ws-}\alpha \text{ (wset } s)) \text{ (thr-}\alpha \text{ (thr } s)) \rangle$ 
show ?case
proof(cases exec-aux ( $\sigma$ ,  $s$ ))
  case (TNil  $s'$ )
  hence  $\alpha.\text{active-threads (state-}\alpha \text{ } s) = \{\}$   $s' = s$ 
  by(auto simp add: exec-aux-def unfold-tllist' split: sum.split-asm dest: exec-step-InrD[OF invar])
  hence ?Stuck using TNil by(auto dest: \alpha.red-in-active-threads)
  thus ?thesis ..
next
case (TCons  $\sigma tta tlls'$ )
then obtain  $t ta \sigma' s' \sigma''$ 
  where [simp]:  $\sigma tta = (\sigma'', t, ta)$ 
  and [simp]:  $tlls' = \text{exec-aux } (\sigma', s')$ 
  and step:  $\text{exec-step } (\sigma, s) = \text{Inl } ((\sigma'', t, ta), \sigma', s')$ 
  unfolding exec-aux-def by(subst (asm) (2) unfold-tllist')(fastforce split: sum.split-asm)
from invar wstok step
have redT:  $\alpha.\text{redT (state-}\alpha \text{ } s) (t, ta) (state-}\alpha \text{ } s')$ 
  and [simp]:  $\sigma'' = \sigma$ 
  and invar':  $\text{state-invar } s' \sigma\text{-invar } \sigma' \text{ (dom (thr-}\alpha \text{ (thr } s')) \text{ state-}\alpha \text{ } s' \in \text{invariant}$ 
  by(rule exec-step-into-redT)+
from wstok \alpha.redT-preserves-wset-thread-ok[OF redT]
have wset-thread-ok (ws-}\alpha \text{ (wset } s')) \text{ (thr-}\alpha \text{ (thr } s')) by simp
with invar' redT TCons have ?Step by(auto simp del: split-paired-Ex)
  thus ?thesis ..
qed
qed
next
assume tfinite ( $\text{exec-aux } (\sigma, s)$ )
thus ?thesis2 using assms
proof(induct exec-aux ( $\sigma$ ,  $s$ ) arbitrary: \sigma s rule: tfinite-induct)
  case TNil thus ?case
  by(auto simp add: exec-aux-def unfold-tllist' split-beta split: sum.split-asm dest: exec-step-InrD)
next
case (TCons  $\sigma tta tlls$ )
from  $\langle \text{TCons } \sigma tta tlls = \text{exec-aux } (\sigma, s) \rangle$ 
obtain  $\sigma'' t ta \sigma' s'$ 
  where [simp]:  $\sigma tta = (\sigma'', t, ta)$ 
  and tlls:  $tlls = \text{exec-aux } (\sigma', s')$ 
  and step:  $\text{exec-step } (\sigma, s) = \text{Inl } ((\sigma'', t, ta), \sigma', s')$ 
  unfolding exec-aux-def by(subst (asm) (2) unfold-tllist')(fastforce split: sum.split-asm)
note tlls moreover
from  $\langle \text{state-invar } s \rangle \langle \sigma\text{-invar } \sigma \text{ (dom (thr-}\alpha \text{ (thr } s)) \rangle \langle \text{state-}\alpha \text{ } s \in \text{invariant} \rangle \langle \text{wset-thread-ok (ws-}\alpha$ 
 $(\text{wset } s)) \text{ (thr-}\alpha \text{ (thr } s)) \rangle \text{ step}$ 
have [simp]:  $\sigma'' = \sigma$ 
  and invar':  $\text{state-invar } s' \sigma\text{-invar } \sigma' \text{ (dom (thr-}\alpha \text{ (thr } s')) \text{ state-}\alpha \text{ } s' \in \text{invariant}$ 
  and redT:  $\alpha.\text{redT (state-}\alpha \text{ } s) (t, ta) (state-}\alpha \text{ } s')$ 
  by(rule exec-step-into-redT)+
note invar' moreover
from  $\alpha.\text{redT-preserves-wset-thread-ok[OF redT]} \langle \text{wset-thread-ok (ws-}\alpha \text{ (wset } s)) \text{ (thr-}\alpha \text{ (thr } s)) \rangle$ 
have  $\text{wset-thread-ok (ws-}\alpha \text{ (wset } s')) \text{ (thr-}\alpha \text{ (thr } s'))$  by simp
ultimately have  $\text{state-invar (terminal (exec-aux } (\sigma', s')))$  by(rule TCons)
with  $\langle \text{TCons } \sigma tta tlls = \text{exec-aux } (\sigma, s) \rangle$ [symmetric]
show ?case unfolding tlls by simp

```

qed
qed

end

```

locale scheduler-ext-aux =
  scheduler-ext-base
  final r convert-RA
  thr- $\alpha$  thr-invar thr-lookup thr-update thr-iterate
  ws- $\alpha$  ws-invar ws-lookup ws-update ws-sel
  is- $\alpha$  is-invar is-memb is-ins is-delete
  thr'- $\alpha$  thr'-invar thr'-empty thr'-ins-dj
+
  scheduler-aux
  final r convert-RA
  thr- $\alpha$  thr-invar thr-lookup thr-update
  ws- $\alpha$  ws-invar ws-lookup
  is- $\alpha$  is-invar is-memb is-ins is-delete
+
  thr: map-iteratei thr- $\alpha$  thr-invar thr-iterate +
  ws: map-update ws- $\alpha$  ws-invar ws-update +
  ws: map-sel' ws- $\alpha$  ws-invar ws-sel +
  thr': finite-set thr'- $\alpha$  thr'-invar +
  thr': set-empty thr'- $\alpha$  thr'-invar thr'-empty +
  thr': set-ins-dj thr'- $\alpha$  thr'-invar thr'-ins-dj
for final :: 'x  $\Rightarrow$  bool
and r :: 't  $\Rightarrow$  ('x  $\times$  'm)  $\Rightarrow$  (('l,'t,'x,'m,'w,'o) thread-action  $\times$  'x  $\times$  'm) Predicate.pred
and convert-RA :: 'l released-locks  $\Rightarrow$  'o list
and thr- $\alpha$  :: 'm-t  $\Rightarrow$  ('l,'t,'x) thread-info
and thr-invar :: 'm-t  $\Rightarrow$  bool
and thr-lookup :: 't  $\Rightarrow$  'm-t  $\rightarrow$  ('x  $\times$  'l released-locks)
and thr-update :: 't  $\Rightarrow$  'x  $\times$  'l released-locks  $\Rightarrow$  'm-t  $\Rightarrow$  'm-t
and thr-iterate :: 'm-t  $\Rightarrow$  ('t  $\times$  ('x  $\times$  'l released-locks), 's-t) set-iterator
and ws- $\alpha$  :: 'm-w  $\Rightarrow$  ('w,'t) wait-sets
and ws-invar :: 'm-w  $\Rightarrow$  bool
and ws-lookup :: 't  $\Rightarrow$  'm-w  $\rightarrow$  'w wait-set-status
and ws-update :: 't  $\Rightarrow$  'w wait-set-status  $\Rightarrow$  'm-w  $\Rightarrow$  'm-w
and ws-sel :: 'm-w  $\Rightarrow$  (('t  $\times$  'w wait-set-status)  $\Rightarrow$  bool)  $\rightarrow$  ('t  $\times$  'w wait-set-status)
and is- $\alpha$  :: 's-i  $\Rightarrow$  't interrupts
and is-invar :: 's-i  $\Rightarrow$  bool
and is-memb :: 't  $\Rightarrow$  's-i  $\Rightarrow$  bool
and is-ins :: 't  $\Rightarrow$  's-i  $\Rightarrow$  's-i
and is-delete :: 't  $\Rightarrow$  's-i  $\Rightarrow$  's-i
and thr'- $\alpha$  :: 's-t  $\Rightarrow$  't set
and thr'-invar :: 's-t  $\Rightarrow$  bool
and thr'-empty :: unit  $\Rightarrow$  's-t
and thr'-ins-dj :: 't  $\Rightarrow$  's-t  $\Rightarrow$  's-t

```

begin

lemma active-threads-correct [simp]:

assumes state-invar s

shows thr'- α (active-threads s) = α .active-threads (state- α s) (**is** ?thesis1)

and thr'-invar (active-threads s) (**is** ?thesis2)

proof –

obtain $ls\ ts\ m\ ws\ is$ **where** $s = (ls, (ts, m), ws, is)$ **by**(cases s) *fastforce*
let $?f = \lambda(t, (x, ln))\ TS.$ *if* $ln = no-wait-locks$
 then if $Predicate.holds$ (do { $(ta, -) \leftarrow r\ t\ (x, m); Predicate.if-pred$ ($actions-ok$ ($ls, (ts, m),$
 $ws, is)$ $t\ ta$) })
 then $thr'-ins-dj\ t\ TS$ *else* TS
 else if $\neg waiting$ ($ws-lookup\ t\ ws$) $\wedge may-acquire-all\ ls\ t\ ln$ *then* $thr'-ins-dj\ t\ TS$ *else* TS
let $?I = \lambda T\ TS.$ $thr'-invar\ TS \wedge thr'-\alpha\ TS \subseteq dom\ (thr-\alpha\ ts) - T \wedge (\forall t. t \notin T \longrightarrow t \in thr'-\alpha\ TS$
 $\longleftrightarrow t \in \alpha.active-threads\ (state-\alpha\ s))$

from $assms\ s$ **have** $thr-invar\ ts$ **by** *simp*
moreover **have** $?I\ (dom\ (thr-\alpha\ ts))\ (thr'-empty\ ())$
 by(*auto simp add: thr'.empty-correct s elim: $\alpha.active-threads.cases$*)
ultimately **have** $?I\ \{\}$ (*thr-iterate* $ts\ (\lambda-. True)\ ?f\ (thr'-empty\ ())$)
proof(*rule thr.iterate-rule-P[where $I=?I$]*)

fix $t\ xln\ T\ TS$
assume $tT: t \in T$
 and $tst: thr-\alpha\ ts\ t = \lfloor xln \rfloor$
 and $Tdom: T \subseteq dom\ (thr-\alpha\ ts)$
 and $I: ?I\ T\ TS$
obtain $x\ ln$ **where** $xln: xln = (x, ln)$ **by**(cases xln)
from $tT\ I$ **have** $t: t \notin thr'-\alpha\ TS$ **by** *blast*

from I **have** $invar: thr'-invar\ TS ..$
hence $thr'-invar\ (?f\ (t, xln)\ TS)$ **using** t
 unfolding xln **by**(*auto simp add: thr'.ins-dj-correct*)
moreover **from** I **have** $thr'-\alpha\ TS \subseteq dom\ (thr-\alpha\ ts) - T$ **by** *blast*
hence $thr'-\alpha\ (?f\ (t, xln)\ TS) \subseteq dom\ (thr-\alpha\ ts) - (T - \{t\})$
 using $invar\ tst\ t$ **by**(*auto simp add: xln thr'.ins-dj-correct*)

moreover

{
 fix t'
 assume $t': t' \notin T - \{t\}$
 have $t' \in thr'-\alpha\ (?f\ (t, xln)\ TS) \longleftrightarrow t' \in \alpha.active-threads\ (state-\alpha\ s)$ (**is** $?lhs \longleftrightarrow ?rhs$)
 proof(cases $t' = t$)

case $True$

show $?thesis$

proof

assume $?lhs$

with $True\ xln\ invar\ tst\ \langle state-invar\ s \rangle\ t$ **show** $?rhs$

by(*fastforce simp add: holds-eq thr'.ins-dj-correct s split-beta ws.lookup-correct split: if-split-asm*)

elim!: bindE if-predE intro: $\alpha.active-threads.intros$)

next

assume $?rhs$

with $True\ xln\ invar\ tst\ \langle state-invar\ s \rangle\ t$ **show** $?lhs$

by(*fastforce elim!: $\alpha.active-threads.cases simp add: holds-eq s thr'.ins-dj-correct ws.lookup-correct$*)

elim!: bindE if-predE intro: bindI if-predI)

qed

next

case $False$

with t' **have** $t' \notin T$ **by** *simp*

with I **have** $t' \in thr'-\alpha\ TS \longleftrightarrow t' \in \alpha.active-threads\ (state-\alpha\ s)$ **by** *blast*

thus $?thesis$ **using** $xln\ False\ invar\ t$ **by**(*auto simp add: thr'.ins-dj-correct*)

qed

}

```

    ultimately show ?I (T - {t}) (?f (t, xln) TS) by blast
  qed
  thus ?thesis1 ?thesis2 by(auto simp add: s)
qed

end

locale scheduler-ext =
  scheduler-ext-aux
  final r convert-RA
  thr- $\alpha$  thr-invar thr-lookup thr-update thr-iterate
  ws- $\alpha$  ws-invar ws-lookup ws-update ws-sel
  is- $\alpha$  is-invar is-memb is-ins is-delete
  thr'- $\alpha$  thr'-invar thr'-empty thr'-ins-dj
+
  scheduler-spec
  final r convert-RA
  schedule  $\sigma$ -invar
  thr- $\alpha$  thr-invar
  ws- $\alpha$  ws-invar
  is- $\alpha$  is-invar
  invariant
+
  ws: map-delete ws- $\alpha$  ws-invar ws-delete +
  ws: map-iteratei ws- $\alpha$  ws-invar ws-iterate
  for final :: 'x  $\Rightarrow$  bool
  and r :: 't  $\Rightarrow$  ('x  $\times$  'm)  $\Rightarrow$  (('l,'t,'x,'m,'w,'o) thread-action  $\times$  'x  $\times$  'm) Predicate.pred
  and convert-RA :: 'l released-locks  $\Rightarrow$  'o list
  and schedule :: ('l,'t,'x,'m,'w,'o,'m-t,'m-w,'s-i,'s) scheduler
  and output :: 's  $\Rightarrow$  't  $\Rightarrow$  ('l,'t,'x,'m,'w,'o) thread-action  $\Rightarrow$  'q option
  and  $\sigma$ -invar :: 's  $\Rightarrow$  't set  $\Rightarrow$  bool
  and thr- $\alpha$  :: 'm-t  $\Rightarrow$  ('l,'t,'x) thread-info
  and thr-invar :: 'm-t  $\Rightarrow$  bool
  and thr-lookup :: 't  $\Rightarrow$  'm-t  $\rightarrow$  ('x  $\times$  'l released-locks)
  and thr-update :: 't  $\Rightarrow$  'x  $\times$  'l released-locks  $\Rightarrow$  'm-t  $\Rightarrow$  'm-t
  and thr-iterate :: 'm-t  $\Rightarrow$  ('t  $\times$  ('x  $\times$  'l released-locks), 's-t) set-iterator
  and ws- $\alpha$  :: 'm-w  $\Rightarrow$  ('w,'t) wait-sets
  and ws-invar :: 'm-w  $\Rightarrow$  bool
  and ws-empty :: unit  $\Rightarrow$  'm-w
  and ws-lookup :: 't  $\Rightarrow$  'm-w  $\rightarrow$  'w wait-set-status
  and ws-update :: 't  $\Rightarrow$  'w wait-set-status  $\Rightarrow$  'm-w  $\Rightarrow$  'm-w
  and ws-delete :: 't  $\Rightarrow$  'm-w  $\Rightarrow$  'm-w
  and ws-iterate :: 'm-w  $\Rightarrow$  ('t  $\times$  'w wait-set-status, 'm-w) set-iterator
  and ws-sel :: 'm-w  $\Rightarrow$  ('t  $\times$  'w wait-set-status  $\Rightarrow$  bool)  $\rightarrow$  ('t  $\times$  'w wait-set-status)
  and is- $\alpha$  :: 's-i  $\Rightarrow$  't interrupts
  and is-invar :: 's-i  $\Rightarrow$  bool
  and is-memb :: 't  $\Rightarrow$  's-i  $\Rightarrow$  bool
  and is-ins :: 't  $\Rightarrow$  's-i  $\Rightarrow$  's-i
  and is-delete :: 't  $\Rightarrow$  's-i  $\Rightarrow$  's-i
  and thr'- $\alpha$  :: 's-t  $\Rightarrow$  't set
  and thr'-invar :: 's-t  $\Rightarrow$  bool
  and thr'-empty :: unit  $\Rightarrow$  's-t
  and thr'-ins-dj :: 't  $\Rightarrow$  's-t  $\Rightarrow$  's-t
  and invariant :: ('l,'t,'x,'m,'w) state set

```


+
assumes *invariant*: *invariant3p* α .*redT* *invariant*

sublocale *scheduler-ext* <
pick-wakeup-spec
final r convert-RA
pick-wakeup σ -*invar*
thr- α *thr-invar*
ws- α *ws-invar*
by(*rule pick-wakeup-spec-via-sel*)(*unfold-locales*)

sublocale *scheduler-ext* <
scheduler
final r convert-RA
schedule output pick-wakeup σ -*invar*
thr- α *thr-invar* *thr-lookup* *thr-update*
ws- α *ws-invar* *ws-lookup* *ws-update* *ws-delete* *ws-iterate*
is- α *is-invar* *is-memb* *is-ins* *is-delete*
invariant
by(*unfold-locales*)(*rule invariant*)

9.2.1 Schedulers for deterministic small-step semantics

The default code equations for *Predicate.the* impose the type class constraint *eq* on the predicate elements. For the semantics, which contains the heap, there might be no such instance, so we use new constants for which other code equations can be used. These do not add the type class constraint, but may fail more often with non-uniqueness exception.

definition *singleton2* **where** [*simp*]: *singleton2* = *Predicate.singleton*

definition *the-only2* **where** [*simp*]: *the-only2* = *Predicate.the-only*

definition *the2* **where** [*simp*]: *the2* = *Predicate.the*

context *multithreaded-base* **begin**

definition *step-thread* ::

$((l, t, x, m, w, o) \text{ thread-action} \Rightarrow 's) \Rightarrow (l, t, x, m, w) \text{ state} \Rightarrow 't$
 $\Rightarrow ('t \times ((l, t, x, m, w, o) \text{ thread-action} \times 'x \times 'm) \text{ option} \times 's) \text{ option}$

where

$\wedge \text{ln. step-thread update-state } s \ t =$
(case thr s t of
 [*(x, ln)*] \Rightarrow
if ln = no-wait-locks then
if $\exists ta \ x' \ m'. t \vdash (x, shr \ s) -ta \rightarrow (x', m') \wedge \text{actions-ok } s \ t \ ta$ then
let
 $(ta, x', m') = \text{THE } (ta, x', m'). t \vdash (x, shr \ s) -ta \rightarrow (x', m') \wedge \text{actions-ok } s \ t \ ta$
in
 [*(t, [(ta, x', m')], update-state ta)*]
else
None
else if may-acquire-all (locks s) t ln $\wedge \neg \text{waiting (wset s t)}$ then
 [*(t, None, update-state (K\\$ [], [], [], [], [], convert-RA ln))*]
else
None
 | *None* \Rightarrow *None*)

lemma *step-thread-NoneD*:

step-thread update-state s t = None \implies t \notin active-threads s

unfolding *step-thread-def*

by(*fastforce simp add: split-beta elim!: active-threads.cases split: if-split-asm*)

lemma *inactive-step-thread-eq-NoneI*:

t \notin active-threads s \implies step-thread update-state s t = None

unfolding *step-thread-def*

by(*fastforce simp add: split-beta split: if-split-asm intro: active-threads.intros*)

lemma *step-thread-eq-None-conv*:

step-thread update-state s t = None \iff t \notin active-threads s

by(*blast dest: step-thread-NoneD intro: inactive-step-thread-eq-NoneI*)

lemma *step-thread-eq-Some-activeD*:

step-thread update-state s t = $\lfloor (t', \text{taxm}\sigma') \rfloor$

\implies t' = t \wedge t \in active-threads s

unfolding *step-thread-def*

by(*fastforce split: if-split-asm simp add: split-beta intro: active-threads.intros*)

declare *actions-ok-iff* [*simp del*]

declare *actions-ok.cases* [*rule del*]

lemma *step-thread-Some-NoneD*:

step-thread update-state s t' = $\lfloor (t, \text{None}, \sigma') \rfloor$

$\implies \exists x \ln n. \text{thr } s t = \lfloor (x, \ln) \rfloor \wedge \ln \$ n > 0 \wedge \neg \text{waiting } (\text{wset } s t) \wedge \text{may-acquire-all } (\text{locks } s) t \ln$

$\wedge \sigma' = \text{update-state } (K\$ \lfloor \rfloor, \lfloor \rfloor, \lfloor \rfloor, \lfloor \rfloor, \lfloor \rfloor, \text{convert-RA } \ln)$

unfolding *step-thread-def*

by(*auto split: if-split-asm simp add: split-beta elim!: neq-no-wait-locksE*)

lemma *step-thread-Some-SomeD*:

\llbracket deterministic I; step-thread update-state s t' = $\lfloor (t, \lfloor (ta, x', m') \rfloor, \sigma') \rfloor$; s \in I \rrbracket

$\implies \exists x. \text{thr } s t = \lfloor (x, \text{no-wait-locks}) \rfloor \wedge t \vdash \langle x, \text{shr } s \rangle -ta \rightarrow \langle x', m' \rangle \wedge \text{actions-ok } s t ta \wedge \sigma' = \text{update-state } ta$

unfolding *step-thread-def*

by(*auto simp add: split-beta deterministic-THE split: if-split-asm*)

end

context *scheduler-base-aux* **begin**

definition *step-thread* ::

((l', t', x', m', w', o) thread-action \Rightarrow 's) \Rightarrow (l', t', m', m-t, m-w, s-i) state-refine \Rightarrow 't \Rightarrow

('t \times ((l', t', x', m', w', o) thread-action \times 'x \times 'm) option \times 's) option

where

$\wedge \ln. \text{step-thread update-state s t =$

(case thr-lookup t (thr s) of

$\lfloor (x, \ln) \rfloor \Rightarrow$

if ln = no-wait-locks then

let

reds = do {

(ta, x', m') \leftarrow r t (x, shr s);

if actions-ok s t ta then Predicate.single (ta, x', m') else bot

```

}
in
  if Predicate.holds (reds  $\gg$  ( $\lambda$ -. Predicate.single ())) then
    let
      (ta, x', m') = the2 reds
    in
      [(t, [(ta, x', m'), update-state ta])
    else
      None
  else if may-acquire-all (locks s) t ln  $\wedge$   $\neg$  waiting (ws-lookup t (wset s)) then
    [(t, None, update-state (K$ [], [], [], [], [], convert-RA ln))]
  else
    None
| None  $\Rightarrow$  None)

```

end

context scheduler-aux begin

lemma deterministic-THE2:

```

assumes  $\alpha$ .deterministic I
and tst: thr- $\alpha$  (thr s) t = [(x, no-wait-locks)]
and red: Predicate.eval (r t (x, shr s)) (ta, x', m')
and aok:  $\alpha$ .actions-ok (state- $\alpha$  s) t ta
and I: state- $\alpha$  s  $\in$  I

```

shows Predicate.the (r t (x, shr s) \gg (λ (ta, x', m'). if α .actions-ok (state- α s) t ta then Predicate.single (ta, x', m') else bot)) = (ta, x', m')

proof -

```

show ?thesis unfolding the-def
  apply(rule the-equality)
  apply(rule bindI[OF red])
  apply(simp add: singleI aok)
  apply(erule bindE)
  apply(clarsimp split: if-split-asm)
  apply(drule (1)  $\alpha$ .deterministicD[OF  $\langle$  $\alpha$ .deterministic I $\rangle$ , where s=state- $\alpha$  s, simplified, OF red
- tst aok])
  apply(rule I)
  apply simp
done
qed

```

lemma step-thread-correct:

```

assumes det:  $\alpha$ .deterministic I
and invar:  $\sigma$ -invar  $\sigma$  (dom (thr- $\alpha$  (thr s))) state-invar s state- $\alpha$  s  $\in$  I
shows

```

map-option (apsnd (apsnd σ - α)) (step-thread update-state s t) = α .step-thread (σ - α \circ update-state) (state- α s) t (is ?thesis1)

```

and ( $\bigwedge$ ta. FWThread.thread-oks (thr- $\alpha$  (thr s))  $\{ta\}_t \implies \sigma$ -invar (update-state ta) (dom (thr- $\alpha$  (thr s))  $\cup$  {t.  $\exists$  x m. NewThread t x m  $\in$  set  $\{ta\}_t$ })  $\implies$  case-option True ( $\lambda$ (t, t $\alpha$ m,  $\sigma$ ).  $\sigma$ -invar  $\sigma$  (case t $\alpha$ m of None  $\implies$  dom (thr- $\alpha$  (thr s)) | Some (ta, x', m')  $\implies$  dom (thr- $\alpha$  (thr s))  $\cup$  {t.  $\exists$  x m. NewThread t x m  $\in$  set  $\{ta\}_t$ })) (step-thread update-state s t)

```

```

(is ( $\bigwedge$ ta. ?tso ta  $\implies$  ?inv ta)  $\implies$  ?thesis2)

```

proof -

```

have ?thesis1  $\wedge$  (( $\forall$  ta. ?tso ta  $\longrightarrow$  ?inv ta)  $\longrightarrow$  ?thesis2)

```

```

proof(cases step-thread update-state s t)
  case None
  with invar show ?thesis
    apply (auto simp add: thr.lookup-correct  $\alpha$ .step-thread-def step-thread-def ws.lookup-correct
      split-beta holds-eq split: if-split-asm cong del: image-cong-simp)
    apply metis
    apply metis
    done
  next
  case (Some a)
  then obtain t'  $\text{ta} \text{m}$   $\sigma'$ 
    where rrs: step-thread update-state s t = [(t',  $\text{ta} \text{m}$ ,  $\sigma'$ )] by(cases a) auto
  show ?thesis
  proof(cases  $\text{ta} \text{m}$ )
    case None
    with rrs invar have ?thesis1
      by(auto simp add: thr.lookup-correct ws.lookup-correct  $\alpha$ .step-thread-def step-thread-def split-beta
        split: if-split-asm)
    moreover {
      let ?ta = (K$ [], [], [], [], [], convert-RA (snd (the (thr-lookup t (thr s))))))
      assume ?tso ?ta  $\longrightarrow$  ?inv ?ta
      hence ?thesis2 using None rrs
      by(auto simp add: thr.lookup-correct ws.lookup-correct  $\alpha$ .step-thread-def step-thread-def split-beta
        split: if-split-asm) }
    ultimately show ?thesis by blast
  next
  case (Some a)
  with rrs obtain ta x' m'
    where rrs: step-thread update-state s t = [(t', [(ta, x', m')],  $\sigma'$ )]
    by(cases a) fastforce
  with invar have ?thesis1
    by (auto simp add: thr.lookup-correct ws.lookup-correct  $\alpha$ .step-thread-def step-thread-def
      split-beta  $\alpha$ .deterministic-THE [OF det, where s=state- $\alpha$  s, simplified]
      deterministic-THE2[OF det] holds-eq split: if-split-asm
      cong del: image-cong-simp) blast+
  moreover {
    assume ?tso ta  $\longrightarrow$  ?inv ta
    hence ?thesis2 using rrs invar
    by(auto simp add: thr.lookup-correct ws.lookup-correct  $\alpha$ .step-thread-def step-thread-def split-beta
       $\alpha$ .deterministic-THE[OF det, where s=state- $\alpha$  s, simplified] deterministic-THE2[OF det] holds-eq
      split: if-split-asm)(auto simp add:  $\alpha$ .actions-ok-iff)
  }
  ultimately show ?thesis by blast
  qed
  qed
  thus ?thesis1 ( $\wedge$ ta. ?tso ta  $\implies$  ?inv ta)  $\implies$  ?thesis2 by blast+
  qed

```

lemma step-thread-eq-None-conv:

```

assumes det:  $\alpha$ .deterministic I
and invar: state-invar s state- $\alpha$  s  $\in$  I
shows step-thread update-state s t = None  $\iff$  t  $\notin$   $\alpha$ .active-threads (state- $\alpha$  s)
using assms step-thread-correct(1)[OF det - invar(1), of  $\lambda$ - . True, of id update-state t]
by(simp add: map-option.id  $\alpha$ .step-thread-eq-None-conv)

```

lemma *step-thread-Some-NoneD*:

assumes *det*: α .deterministic *I*
and *step*: *step-thread update-state s t' = [(t, None, σ')]*
and *invar*: *state-invar s state- α s $\in I$*
shows $\exists x \ln n. \text{thr-}\alpha (\text{thr } s) t = [(x, \ln)] \wedge \ln \$ n > 0 \wedge \neg \text{waiting} (\text{ws-}\alpha (\text{wset } s) t) \wedge \text{may-acquire-all}$
(locks s) t ln $\wedge \sigma' = \text{update-state} (K\$ [], [], [], [], [], \text{convert-RA } \ln)$
using *assms step-thread-correct(1)[OF det - invar(1), of λ - . True, of id update-state t]*
by(*fastforce simp add: map-option.id dest: α .step-thread-Some-NoneD[OF sym]*)

lemma *step-thread-Some-SomeD*:

assumes *det*: α .deterministic *I*
and *step*: *step-thread update-state s t' = [(t, [(ta, x', m \wedge)], σ')]*
and *invar*: *state-invar s state- α s $\in I$*
shows $\exists x. \text{thr-}\alpha (\text{thr } s) t = [(x, \text{no-wait-locks})] \wedge \text{Predicate.eval} (r \ t \ (x, \text{shr } s)) (ta, x', m \wedge) \wedge$
actions-ok s t ta $\wedge \sigma' = \text{update-state } ta$
using *assms step-thread-correct(1)[OF det - invar(1), of λ - . True, of id update-state t]*
by(*auto simp add: map-option.id dest: α .step-thread-Some-SomeD[OF det sym]*)

end

9.2.2 Code Generator setup

lemmas [*code*] =

scheduler-base-aux.free-thread-id-def
scheduler-base-aux.redT-updT.simps
scheduler-base-aux.redT-updTs-def
scheduler-base-aux.thread-ok.simps
scheduler-base-aux.thread-oks.simps
scheduler-base-aux.wset-actions-ok-def
scheduler-base-aux.cond-action-ok.simps
scheduler-base-aux.cond-action-oks-def
scheduler-base-aux.redT-updI.simps
scheduler-base-aux.redT-updIs.simps
scheduler-base-aux.interrupt-action-ok.simps
scheduler-base-aux.interrupt-actions-ok.simps
scheduler-base-aux.actions-ok-def
scheduler-base-aux.step-thread-def

lemmas [*code*] =

scheduler-base.exec-updW.simps
scheduler-base.exec-updWs-def
scheduler-base.exec-upd-def
scheduler-base.execT-def
scheduler-base.exec-step.simps
scheduler-base.exec-aux-def
scheduler-base.exec-def

lemmas [*code*] =

scheduler-ext-base.active-threads.simps

lemma *singleton2-code* [*code*]:

singleton2 ddefault (Predicate.Seq f) =
(case f () of

```

    Predicate.Empty ⇒ dfault ()
  | Predicate.Insert x P ⇒
    if Predicate.is-empty P then x else Code.abort (STR "singleton2 not unique") (λ-. singleton2 dfault
(Predicate.Seq f))
  | Predicate.Join P xq ⇒
    if Predicate.is-empty P then
      the-only2 dfault xq
    else if Predicate.null xq then singleton2 dfault P else Code.abort (STR "singleton2 not unique")
(λ-. singleton2 dfault (Predicate.Seq f)))
unfolding singleton2-def the-only2-def
by(auto simp only: singleton-code Code.abort-def split: seq.split if-split)

```

lemma *the-only2-code* [code]:

```

the-only2 dfault Predicate.Empty = Code.abort (STR "the-only2 empty") dfault
the-only2 dfault (Predicate.Insert x P) =
  (if Predicate.is-empty P then x else Code.abort (STR "the-only2 not unique") (λ-. the-only2 dfault
(Predicate.Insert x P)))
the-only2 dfault (Predicate.Join P xq) =
  (if Predicate.is-empty P then
    the-only2 dfault xq
  else if Predicate.null xq then
    singleton2 dfault P
  else
    Code.abort (STR "the-only2 not unique") (λ-. the-only2 dfault (Predicate.Join P xq)))
unfolding singleton2-def the-only2-def by simp-all

```

lemma *the2-eq* [code]:

```

the2 A = singleton2 (λx. Code.abort (STR "not-unique") (λ-. the2 A)) A
unfolding the2-def singleton2-def by(rule the-eq)

```

end

9.3 Random scheduler

theory *Random-Scheduler*

imports

Scheduler

begin

type-synonym *random-scheduler* = *Random.seed*

abbreviation (*input*)

random-scheduler-invar :: *random-scheduler* ⇒ 't set ⇒ bool

where *random-scheduler-invar* ≡ λ- -. True

locale *random-scheduler-base* =

scheduler-ext-base

final *r* *convert-RA*

thr-α *thr-invar* *thr-lookup* *thr-update* *thr-iterate*

ws-α *ws-invar* *ws-lookup* *ws-update* *ws-sel*

is-α *is-invar* *is-memb* *is-ins* *is-delete*

thr'-α *thr'-invar* *thr'-empty* *thr'-ins-dj*

for *final* :: 'x ⇒ bool

```

and  $r :: 't \Rightarrow ('x \times 'm) \Rightarrow (('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \times 'x \times 'm) \text{ Predicate.pred}$ 
and  $\text{convert-RA} :: 'l \text{ released-locks} \Rightarrow 'o \text{ list}$ 
and  $\text{output} :: \text{random-scheduler} \Rightarrow 't \Rightarrow ('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \Rightarrow 'q \text{ option}$ 
and  $\text{thr-}\alpha :: 'm\text{-}t \Rightarrow ('l, 't, 'x) \text{ thread-info}$ 
and  $\text{thr-invar} :: 'm\text{-}t \Rightarrow \text{bool}$ 
and  $\text{thr-lookup} :: 't \Rightarrow 'm\text{-}t \rightarrow ('x \times 'l \text{ released-locks})$ 
and  $\text{thr-update} :: 't \Rightarrow 'x \times 'l \text{ released-locks} \Rightarrow 'm\text{-}t \Rightarrow 'm\text{-}t$ 
and  $\text{thr-iterate} :: 'm\text{-}t \Rightarrow ('t \times ('x \times 'l \text{ released-locks}), 's\text{-}t) \text{ set-iterator}$ 
and  $\text{ws-}\alpha :: 'm\text{-}w \Rightarrow ('w, 't) \text{ wait-sets}$ 
and  $\text{ws-invar} :: 'm\text{-}w \Rightarrow \text{bool}$ 
and  $\text{ws-lookup} :: 't \Rightarrow 'm\text{-}w \rightarrow 'w \text{ wait-set-status}$ 
and  $\text{ws-update} :: 't \Rightarrow 'w \text{ wait-set-status} \Rightarrow 'm\text{-}w \Rightarrow 'm\text{-}w$ 
and  $\text{ws-delete} :: 't \Rightarrow 'm\text{-}w \Rightarrow 'm\text{-}w$ 
and  $\text{ws-iterate} :: 'm\text{-}w \Rightarrow ('t \times 'w \text{ wait-set-status}, 'm\text{-}w) \text{ set-iterator}$ 
and  $\text{ws-sel} :: 'm\text{-}w \Rightarrow ('t \times 'w \text{ wait-set-status} \Rightarrow \text{bool}) \rightarrow ('t \times 'w \text{ wait-set-status})$ 
and  $\text{is-}\alpha :: 's\text{-}i \Rightarrow 't \text{ interrupts}$ 
and  $\text{is-invar} :: 's\text{-}i \Rightarrow \text{bool}$ 
and  $\text{is-memb} :: 't \Rightarrow 's\text{-}i \Rightarrow \text{bool}$ 
and  $\text{is-ins} :: 't \Rightarrow 's\text{-}i \Rightarrow 's\text{-}i$ 
and  $\text{is-delete} :: 't \Rightarrow 's\text{-}i \Rightarrow 's\text{-}i$ 
and  $\text{thr}'\text{-}\alpha :: 's\text{-}t \Rightarrow 't \text{ set}$ 
and  $\text{thr}'\text{-invar} :: 's\text{-}t \Rightarrow \text{bool}$ 
and  $\text{thr}'\text{-empty} :: \text{unit} \Rightarrow 's\text{-}t$ 
and  $\text{thr}'\text{-ins-dj} :: 't \Rightarrow 's\text{-}t \Rightarrow 's\text{-}t$ 
+
fixes  $\text{thr}'\text{-to-list} :: 's\text{-}t \Rightarrow 't \text{ list}$ 
begin

```

```

definition  $\text{next-thread} :: \text{random-scheduler} \Rightarrow 's\text{-}t \Rightarrow ('t \times \text{random-scheduler}) \text{ option}$ 
where
   $\text{next-thread seed active} =$ 
   $(\text{let } ts = \text{thr}'\text{-to-list active}$ 
   $\text{in if } ts = [] \text{ then None else Some (Random.select (thr}'\text{-to-list active) seed)})$ 

```

```

definition  $\text{random-scheduler} :: ('l, 't, 'x, 'm, 'w, 'o, 'm\text{-}t, 'm\text{-}w, 's\text{-}i, \text{random-scheduler}) \text{ scheduler}$ 
where
   $\text{random-scheduler seed } s =$ 
   $(\text{do } \{$ 
   $\quad (t, \text{seed}') \leftarrow \text{next-thread seed (active-threads } s);$ 
   $\quad \text{step-thread } (\lambda t a. \text{seed}') s t$ 
   $\})$ 

```

end

```

locale  $\text{random-scheduler} =$ 
   $\text{random-scheduler-base}$ 
   $\text{final } r \text{ convert-RA } \text{output}$ 
   $\text{thr-}\alpha \text{ thr-invar } \text{thr-lookup } \text{thr-update } \text{thr-iterate}$ 
   $\text{ws-}\alpha \text{ ws-invar } \text{ws-lookup } \text{ws-update } \text{ws-delete } \text{ws-iterate } \text{ws-sel}$ 
   $\text{is-}\alpha \text{ is-invar } \text{is-memb } \text{is-ins } \text{is-delete}$ 
   $\text{thr}'\text{-}\alpha \text{ thr}'\text{-invar } \text{thr}'\text{-empty } \text{thr}'\text{-ins-dj } \text{thr}'\text{-to-list}$ 
+
   $\text{scheduler-ext-aux}$ 
   $\text{final } r \text{ convert-RA}$ 

```

```

  thr- $\alpha$  thr-invar thr-lookup thr-update thr-iterate
  ws- $\alpha$  ws-invar ws-lookup ws-update ws-sel
  is- $\alpha$  is-invar is-memb is-ins is-delete
  thr'- $\alpha$  thr'-invar thr'-empty thr'-ins-dj
+
ws: map-delete ws- $\alpha$  ws-invar ws-delete +
ws: map-iteratei ws- $\alpha$  ws-invar ws-iterate +
thr': set-to-list thr'- $\alpha$  thr'-invar thr'-to-list
for final :: 'x  $\Rightarrow$  bool
and r :: 't  $\Rightarrow$  ('x  $\times$  'm)  $\Rightarrow$  (('l,'t,'x,'m,'w,'o) thread-action  $\times$  'x  $\times$  'm) Predicate.pred
and convert-RA :: 'l released-locks  $\Rightarrow$  'o list
and output :: random-scheduler  $\Rightarrow$  't  $\Rightarrow$  ('l,'t,'x,'m,'w,'o) thread-action  $\Rightarrow$  'q option
and thr- $\alpha$  :: 'm-t  $\Rightarrow$  ('l,'t,'x) thread-info
and thr-invar :: 'm-t  $\Rightarrow$  bool
and thr-lookup :: 't  $\Rightarrow$  'm-t  $\rightarrow$  ('x  $\times$  'l released-locks)
and thr-update :: 't  $\Rightarrow$  'x  $\times$  'l released-locks  $\Rightarrow$  'm-t  $\Rightarrow$  'm-t
and thr-iterate :: 'm-t  $\Rightarrow$  ('t  $\times$  ('x  $\times$  'l released-locks), 's-t) set-iterator
and ws- $\alpha$  :: 'm-w  $\Rightarrow$  ('w,'t) wait-sets
and ws-invar :: 'm-w  $\Rightarrow$  bool
and ws-lookup :: 't  $\Rightarrow$  'm-w  $\rightarrow$  'w wait-set-status
and ws-update :: 't  $\Rightarrow$  'w wait-set-status  $\Rightarrow$  'm-w  $\Rightarrow$  'm-w
and ws-delete :: 't  $\Rightarrow$  'm-w  $\Rightarrow$  'm-w
and ws-iterate :: 'm-w  $\Rightarrow$  ('t  $\times$  'w wait-set-status, 'm-w) set-iterator
and ws-sel :: 'm-w  $\Rightarrow$  ('t  $\times$  'w wait-set-status  $\Rightarrow$  bool)  $\rightarrow$  ('t  $\times$  'w wait-set-status)
and is- $\alpha$  :: 's-i  $\Rightarrow$  't interrupts
and is-invar :: 's-i  $\Rightarrow$  bool
and is-memb :: 't  $\Rightarrow$  's-i  $\Rightarrow$  bool
and is-ins :: 't  $\Rightarrow$  's-i  $\Rightarrow$  's-i
and is-delete :: 't  $\Rightarrow$  's-i  $\Rightarrow$  's-i
and thr'- $\alpha$  :: 's-t  $\Rightarrow$  't set
and thr'-invar :: 's-t  $\Rightarrow$  bool
and thr'-empty :: unit  $\Rightarrow$  's-t
and thr'-ins-dj :: 't  $\Rightarrow$  's-t  $\Rightarrow$  's-t
and thr'-to-list :: 's-t  $\Rightarrow$  't list
begin

```

lemma next-thread-eq-None-iff:

```

  assumes thr'-invar active random-scheduler-invar seed T
  shows next-thread seed active = None  $\longleftrightarrow$  thr'- $\alpha$  active = {}
using thr'.to-list-correct[OF assms(1)]
by(auto simp add: next-thread-def neq-Nil-conv)

```

lemma next-thread-eq-SomeD:

```

  assumes next-thread seed active = Some (t, seed')
  and thr'-invar active random-scheduler-invar seed T
  shows t  $\in$  thr'- $\alpha$  active
using assms
by(auto simp add: next-thread-def thr'.to-list-correct split: if-split-asm dest: select[of - seed])

```

lemma random-scheduler-spec:

```

  assumes det:  $\alpha$ .deterministic I
  shows scheduler-spec final r random-scheduler random-scheduler-invar thr- $\alpha$  thr-invar ws- $\alpha$  ws-invar
  is- $\alpha$  is-invar I
proof

```



```

fix  $\sigma$   $s$ 
assume  $rr$ : random-scheduler  $\sigma$   $s = \text{None}$ 
  and  $invar$ : random-scheduler-invar  $\sigma$  ( $\text{dom } (thr-\alpha (thr\ s))$ ) state-invar  $s$   $state-\alpha$   $s \in I$ 
from  $invar(2)$  have  $thr'-invar$  (active-threads  $s$ ) by(rule active-threads-correct)
thus  $\alpha$ .active-threads ( $state-\alpha$   $s$ ) =  $\{\}$  using  $rr$   $invar$ 
  by(auto simp add: random-scheduler-def Option-bind-eq-None-conv next-thread-eq-None-iff step-thread-eq-None-conv[OF det] dest: next-thread-eq-SomeD)
next
  fix  $\sigma$   $s$   $t$   $\sigma'$ 
assume  $rr$ : random-scheduler  $\sigma$   $s = \lfloor(t, \text{None}, \sigma')\rfloor$ 
  and  $invar$ : random-scheduler-invar  $\sigma$  ( $\text{dom } (thr-\alpha (thr\ s))$ ) state-invar  $s$   $state-\alpha$   $s \in I$ 
thus  $\exists x\ ln\ n.$   $thr-\alpha (thr\ s)\ t = \lfloor(x, ln)\rfloor \wedge 0 < ln\ \$\ n \wedge \neg\ waiting\ (ws-\alpha\ (wset\ s)\ t) \wedge may-acquire-all$ 
(locks  $s$ )  $t\ ln$ 
  by(fastforce simp add: random-scheduler-def bind-eq-Some-conv dest: step-thread-Some-NoneD[OF det])
next
  fix  $\sigma$   $s$   $t$   $x'$   $m'$   $\sigma'$ 
assume  $rr$ : random-scheduler  $\sigma$   $s = \lfloor(t, \lfloor(ta, x', m')\rfloor, \sigma')\rfloor$ 
  and  $invar$ : random-scheduler-invar  $\sigma$  ( $\text{dom } (thr-\alpha (thr\ s))$ ) state-invar  $s$   $state-\alpha$   $s \in I$ 
thus  $\exists x.$   $thr-\alpha (thr\ s)\ t = \lfloor(x, no-wait-locks)\rfloor \wedge Predicate.eval\ (r\ t\ (x, shr\ s))\ (ta, x', m')$   $\wedge$ 
 $\alpha$ .actions-ok ( $state-\alpha$   $s$ )  $t\ ta$ 
  by(auto simp add: random-scheduler-def bind-eq-Some-conv dest: step-thread-Some-SomeD[OF det])
qed simp-all

```

end

```

sublocale random-scheduler-base <
  scheduler-base
  final  $r$  convert-RA
  random-scheduler output pick-wakeup-via-sel ( $\lambda s\ P.$   $ws-sel\ s\ (\lambda(k,v).$   $P\ k\ v)$ ) random-scheduler-invar
  thr-\alpha thr-invar thr-lookup thr-update
  ws-\alpha ws-invar ws-lookup ws-update ws-delete ws-iterate
  is-\alpha is-invar is-memb is-ins is-delete
for  $n0$  .

```

```

sublocale random-scheduler <
  pick-wakeup-spec
  final  $r$  convert-RA
  pick-wakeup-via-sel ( $\lambda s\ P.$   $ws-sel\ s\ (\lambda(k,v).$   $P\ k\ v)$ ) random-scheduler-invar
  thr-\alpha thr-invar
  ws-\alpha ws-invar
  is-\alpha is-invar
by(rule pick-wakeup-spec-via-sel)(unfold-locales)

```

context *random-scheduler* **begin**

lemma *random-scheduler-scheduler*:

assumes det : α .*deterministic* I

shows

scheduler

final r *convert-RA*

random-scheduler (*pick-wakeup-via-sel* ($\lambda s\ P.$ $ws-sel\ s\ (\lambda(k,v).$ $P\ k\ v)$)) *random-scheduler-invar*

thr-\alpha *thr-invar* *thr-lookup* *thr-update*

ws-\alpha *ws-invar* *ws-lookup* *ws-update* *ws-delete* *ws-iterate*

```

    is- $\alpha$  is-invar is-memb is-ins is-delete
  I
proof —
interpret scheduler-spec
  final r convert-RA
  random-scheduler random-scheduler-invar
  thr- $\alpha$  thr-invar
  ws- $\alpha$  ws-invar
  is- $\alpha$  is-invar
  I
using det by(rule random-scheduler-spec)

  show ?thesis by(unfold-locales)(rule  $\alpha$ .deterministic-invariant3p[OF det])
qed

end

```

9.3.1 Code generator setup

```

lemmas [code] =
  random-scheduler-base.next-thread-def
  random-scheduler-base.random-scheduler-def

end

```

9.4 Round robin scheduler

```

theory Round-Robin
imports
  Scheduler
begin

```

A concrete scheduler must pick one possible reduction step from the small-step semantics for individual threads. Currently, this is only possible if there is only one such by using *Predicate.the*.

9.4.1 Concrete schedulers

9.4.2 Round-robin schedulers

```

type-synonym 'queue round-robin = 'queue  $\times$  nat
  — Waiting queue of threads and remaining number of steps of the first thread until it has to return
  resources

```

```

primrec enqueue-new-thread :: 't list  $\Rightarrow$  ('t,'x,'m) new-thread-action  $\Rightarrow$  't list
where
  enqueue-new-thread queue (NewThread t x m) = queue @ [t]
| enqueue-new-thread queue (ThreadExists t b) = queue

```

```

definition enqueue-new-threads :: 't list  $\Rightarrow$  ('t,'x,'m) new-thread-action list  $\Rightarrow$  't list
where
  enqueue-new-threads = foldl enqueue-new-thread

```

primrec *round-robin-update-state* :: $\text{nat} \Rightarrow 't \text{ list round-robin} \Rightarrow 't \Rightarrow ('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \Rightarrow 't \text{ list round-robin}$

where

round-robin-update-state $n0$ (*queue*, n) t $ta =$
 (let $queue' = \text{enqueue-new-threads } queue \ \{ta\}_t$
 in if $n = 0 \vee \text{Yield} \in \text{set } \{ta\}_c$ then (*rotate1* $queue'$, $n0$) else ($queue'$, $n - 1$))

context *multithreaded-base* **begin**

abbreviation *round-robin-step* :: $\text{nat} \Rightarrow 't \text{ list round-robin} \Rightarrow ('l, 't, 'x, 'm, 'w) \text{ state} \Rightarrow 't \Rightarrow ('t \times ((('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \times 'x \times 'm) \text{ option} \times 't \text{ list round-robin}) \text{ option}$

where

round-robin-step $n0$ σ s $t \equiv \text{step-thread } (\text{round-robin-update-state } n0 \ \sigma \ t) \ s \ t$

partial-function (*option*) *round-robin-reschedule* :: $'t \Rightarrow$

$'t \text{ list} \Rightarrow \text{nat} \Rightarrow ('l, 't, 'x, 'm, 'w) \text{ state} \Rightarrow ('t \times ((('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \times 'x \times 'm) \text{ option} \times 't \text{ list round-robin}) \text{ option}$

where

round-robin-reschedule $t0$ *queue* $n0$ $s =$
 (let
 $t = \text{hd } queue;$
 $queue' = \text{tl } queue$
 in
 if $t = t0$ then
 None
 else
 case *round-robin-step* $n0$ ($t \# queue'$, $n0$) s t of
 None $\Rightarrow \text{round-robin-reschedule } t0$ ($queue' \ @ [t]$) $n0$ s
 | $[ttaxm\sigma] \Rightarrow [ttaxm\sigma]$)

fun *round-robin* :: $\text{nat} \Rightarrow 't \text{ list round-robin} \Rightarrow ('l, 't, 'x, 'm, 'w) \text{ state} \Rightarrow ('t \times ((('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \times 'x \times 'm) \text{ option} \times 't \text{ list round-robin}) \text{ option}$

where

round-robin $n0$ ($[], n$) $s = \text{None}$
 | *round-robin* $n0$ ($t \# queue$, n) $s =$
 (case *round-robin-step* $n0$ ($t \# queue$, n) s t of
 $[ttaxm\sigma] \Rightarrow [ttaxm\sigma]$
 | None $\Rightarrow \text{round-robin-reschedule } t$ ($queue \ @ [t]$) $n0$ s)

end

primrec *round-robin-invar* :: $'t \text{ list round-robin} \Rightarrow 't \text{ set} \Rightarrow \text{bool}$

where *round-robin-invar* (*queue*, n) $T \longleftrightarrow \text{set } queue = T \wedge \text{distinct } queue$

lemma *set-enqueue-new-thread*:

$\text{set } (\text{enqueue-new-thread } queue \ nta) = \text{set } queue \cup \{t. \exists x \ m. \ nta = \text{NewThread } t \ x \ m\}$

by(cases nta) *auto*

lemma *set-enqueue-new-threads*:

$\text{set } (\text{enqueue-new-threads } queue \ nta) = \text{set } queue \cup \{t. \exists x \ m. \ \text{NewThread } t \ x \ m \in \text{set } nta\}$

apply(*induct* $ntas$ *arbitrary*: *queue*)

apply(*auto simp add*: *enqueue-new-threads-def set-enqueue-new-thread*)

done

lemma *enqueue-new-thread-eq-Nil* [*simp*]:

enqueue-new-thread queue nta = [] \longleftrightarrow queue = [] \wedge ($\exists t b. nta = \text{ThreadExists } t b$)

by(*cases nta*) *simp-all*

lemma *enqueue-new-threads-eq-Nil* [*simp*]:

*enqueue-new-threads queue ntas = [] \longleftrightarrow queue = [] \wedge set ntas \subseteq {*ThreadExists t b* | *t b. True*}*

apply(*induct ntas arbitrary: queue*)

apply(*auto simp add: enqueue-new-threads-def*)

done

lemma *distinct-enqueue-new-threads*:

fixes *ts* :: (*'l,'t,'x*) *thread-info*

and *ntas* :: (*'t,'x,'m*) *new-thread-action list*

assumes *thread-oks ts ntas set queue = dom ts distinct queue*

shows *distinct (enqueue-new-threads queue ntas)*

using *assms*

proof(*induct ntas arbitrary: ts queue*)

case Nil **thus** ?*case* **by**(*simp add: enqueue-new-threads-def*)

next

case (*Cons nt ntas*)

from $\langle \text{thread-oks } ts \ (nt \# \ ntas) \rangle$

have *thread-ok ts nt* **and** *thread-oks (redT-updT ts nt) ntas* **by** *simp-all*

from $\langle \text{thread-ok } ts \ nt \rangle$ $\langle \text{set } queue = \text{dom } ts \rangle$ $\langle \text{distinct } queue \rangle$

have *set (enqueue-new-thread queue nt) = dom (redT-updT ts nt) \wedge distinct (enqueue-new-thread queue nt)*

by(*cases nt*)(*auto*)

with $\langle \text{thread-oks } (redT-updT \ ts \ nt) \ ntas \rangle$

have *distinct (enqueue-new-threads (enqueue-new-thread queue nt) ntas)*

by(*blast intro: Cons.hyps*)

thus ?*case* **by**(*simp add: enqueue-new-threads-def*)

qed

lemma *round-robin-reschedule-induct* [*consumes 1, case-names head rotate*]:

assumes *major: t0 \in set queue*

and *head: $\bigwedge queue. P (t0 \# queue)$*

and *rotate: $\bigwedge queue \ t. [\![t \neq t0; t0 \in set \ queue; P (queue \ @ \ [t])]\!] \implies P (t \# queue)$*

shows *P queue*

using *major*

proof(*induct n \equiv length (takeWhile ($\lambda x. x \neq t0$) queue) arbitrary: queue*)

case 0

then obtain *queue'* **where** *queue = t0 $\#$ queue'*

by(*cases queue*)(*auto split: if-split-asm*)

thus ?*case* **by**(*simp add: head*)

next

case (*Suc n*)

then obtain *t queue'* **where** [*simp*]: *queue = t $\#$ queue'*

and *t: t \neq t0* **and** *n: n = length (takeWhile ($\lambda x. x \neq t0$) queue')*

and *t0: t0 \in set queue'*

by(*cases queue*)(*auto split: if-split-asm*)

from *n t0* **have** *n = length (takeWhile ($\lambda x. x \neq t0$) (queue' @ [t]))* **by**(*simp*)

moreover from *t0* **have** *t0 \in set (queue' @ [t])* **by** *simp*

ultimately have *P (queue' @ [t])* **by**(*rule Suc.hyps*)

with *t t0* **show** ?*case* **by**(*simp add: rotate*)

qed

context *multithreaded-base* **begin**

declare *actions-ok-iff* [*simp del*]

declare *actions-ok.cases* [*rule del*]

lemma *round-robin-step-invar-None*:

$\llbracket \text{round-robin-step } n0 \ \sigma \ s \ t' = \llbracket (t, \text{None}, \sigma') \rrbracket; \text{round-robin-invar } \sigma \ (\text{dom } (\text{thr } s)) \rrbracket$
 $\implies \text{round-robin-invar } \sigma' \ (\text{dom } (\text{thr } s))$

by(*cases* σ)(*auto dest: step-thread-Some-NoneD simp add: set-enqueue-new-threads distinct-enqueue-new-threads*)

lemma *round-robin-step-invar-Some*:

$\llbracket \text{deterministic } I; \text{round-robin-step } n0 \ \sigma \ s \ t' = \llbracket (t, \llbracket (ta, x', m') \rrbracket, \sigma') \rrbracket; \text{round-robin-invar } \sigma \ (\text{dom } (\text{thr } s)); s \in I \rrbracket$

$\implies \text{round-robin-invar } \sigma' \ (\text{dom } (\text{thr } s) \cup \{t. \exists x m. \text{NewThread } t \ x \ m \in \text{set } \{ta\}_t\})$

apply(*cases* σ)

apply *clarsimp*

apply(*frule* (1) *step-thread-Some-SomeD*)

apply(*auto split: if-split-asm simp add: split-beta set-enqueue-new-threads deterministic-THE*)

apply(*auto simp add: actions-ok-iff distinct-enqueue-new-threads*)

done

lemma *round-robin-reschedule-Cons*:

round-robin-reschedule $t0 \ (t0 \ \# \ \text{queue}) \ n0 \ s = \text{None}$
 $t \neq t0 \implies \text{round-robin-reschedule } t0 \ (t \ \# \ \text{queue}) \ n0 \ s =$
 (*case* *round-robin-step* $n0 \ (t \ \# \ \text{queue}, \ n0) \ s \ t$ *of*
 $\text{None} \Rightarrow \text{round-robin-reschedule } t0 \ (\text{queue} \ @ \ [t]) \ n0 \ s$
 $| \ \text{Some } ttaxm\sigma \Rightarrow \text{Some } ttaxm\sigma$)

by(*simp-all add: round-robin-reschedule.simps*)

lemma *round-robin-reschedule-NoneD*:

assumes *rrr*: *round-robin-reschedule* $t0 \ \text{queue} \ n0 \ s = \text{None}$

and *t0*: $t0 \in \text{set } \text{queue}$

shows *set* (*takeWhile* ($\lambda t'. t' \neq t0$) *queue*) \cap *active-threads* $s = \{\}$

using *t0 rrr*

proof(*induct queue rule: round-robin-reschedule-induct*)

case (*head queue*)

thus *?case* **by** *simp*

next

case (*rotate queue t*)

from $\langle \text{round-robin-reschedule } t0 \ (t \ \# \ \text{queue}) \ n0 \ s = \text{None} \rangle \langle t \neq t0 \rangle$

have *round-robin-step* $n0 \ (t \ \# \ \text{queue}, \ n0) \ s \ t = \text{None}$

and *round-robin-reschedule* $t0 \ (\text{queue} \ @ \ [t]) \ n0 \ s = \text{None}$

by(*simp-all add: round-robin-reschedule-Cons*)

from *this*(1) **have** $t \notin \text{active-threads } s$ **by**(*rule step-thread-NoneD*)

moreover from $\langle \text{round-robin-reschedule } t0 \ (\text{queue} \ @ \ [t]) \ n0 \ s = \text{None} \rangle$

have *set* (*takeWhile* ($\lambda t'. t' \neq t0$) (*queue* $@$ $[t]$)) \cap *active-threads* $s = \{\}$

by(*rule rotate.hyps*)

moreover have *takeWhile* ($\lambda t'. t' \neq t0$) (*queue* $@$ $[t]$) = *takeWhile* ($\lambda t'. t' \neq t0$) *queue*

using $\langle t0 \in \text{set } \text{queue} \rangle$ **by** *simp*

ultimately show *?case* **using** $\langle t \neq t0 \rangle$ **by** *simp*

qed

lemma *round-robin-reschedule-Some-NoneD*:

```

assumes rrr: round-robin-reschedule t0 queue n0 s = [(t, None, σ')]
and t0: t0 ∈ set queue
shows ∃ x ln n. thr s t = [(x, ln)] ∧ ln $ n > 0 ∧ ¬ waiting (wset s t) ∧ may-acquire-all (locks s)
t ln
using t0 rrr
proof(induct queue rule: round-robin-reschedule-induct)
  case head thus ?case by(simp add: round-robin-reschedule-Cons)
next
  case (rotate queue t')
  show ?case
  proof(cases round-robin-step n0 (t' # queue, n0) s t')
    case None
    with ⟨round-robin-reschedule t0 (t' # queue) n0 s = [(t, None, σ')]⟩ ⟨t' ≠ t0⟩
    have round-robin-reschedule t0 (queue @ [t']) n0 s = [(t, None, σ')]
      by(simp add: round-robin-reschedule-Cons)
    thus ?thesis by(rule rotate.hyps)
  next
  case (Some a)
  with ⟨round-robin-reschedule t0 (t' # queue) n0 s = [(t, None, σ')]⟩ ⟨t' ≠ t0⟩
  have round-robin-step n0 (t' # queue, n0) s t' = [(t, None, σ')]
    by(simp add: round-robin-reschedule-Cons)
  thus ?thesis by(blast dest: step-thread-Some-NoneD)
  qed
qed

```

lemma round-robin-reschedule-Some-SomeD:

```

assumes deterministic I
and rrr: round-robin-reschedule t0 queue n0 s = [(t, [(ta, x', m'), σ'])]
and t0: t0 ∈ set queue
and I: s ∈ I
shows ∃ x. thr s t = [(x, no-wait-locks)] ∧ t ⊢ ⟨x, shr s⟩ -ta→ ⟨x', m'⟩ ∧ actions-ok s t ta
using t0 rrr
proof(induct queue rule: round-robin-reschedule-induct)
  case head thus ?case by(simp add: round-robin-reschedule-Cons)
next
  case (rotate queue t')
  show ?case
  proof(cases round-robin-step n0 (t' # queue, n0) s t')
    case None
    with ⟨round-robin-reschedule t0 (t' # queue) n0 s = [(t, [(ta, x', m'), σ'])]⟩ ⟨t' ≠ t0⟩
    have round-robin-reschedule t0 (queue @ [t']) n0 s = [(t, [(ta, x', m'), σ'])]
      by(simp add: round-robin-reschedule-Cons)
    thus ?thesis by(rule rotate.hyps)
  next
  case (Some a)
  with ⟨round-robin-reschedule t0 (t' # queue) n0 s = [(t, [(ta, x', m'), σ'])]⟩ ⟨t' ≠ t0⟩
  have round-robin-step n0 (t' # queue, n0) s t' = [(t, [(ta, x', m'), σ'])]
    by(simp add: round-robin-reschedule-Cons)
  thus ?thesis using I by(blast dest: step-thread-Some-SomeD[OF ⟨deterministic I⟩])
  qed
qed

```

lemma round-robin-reschedule-invar-None:

```

assumes rrr: round-robin-reschedule t0 queue n0 s = [(t, None, σ')]

```

```

and invar: round-robin-invar (queue, n0) (dom (thr s))
and t0:  $t0 \in \text{set } \text{queue}$ 
shows round-robin-invar  $\sigma'$  (dom (thr s))
using t0 rrr invar
proof(induct queue rule: round-robin-reschedule-induct)
  case head thus ?case by(simp add: round-robin-reschedule-Cons)
next
  case (rotate queue t')
  show ?case
  proof(cases round-robin-step n0 (t' # queue, n0) s t')
    case None
    with  $\langle \text{round-robin-reschedule } t0 (t' \# \text{queue}) n0 s = \lfloor (t, \text{None}, \sigma') \rfloor \rangle \langle t' \neq t0 \rangle$ 
    have round-robin-reschedule t0 (queue @ [t']) n0 s =  $\lfloor (t, \text{None}, \sigma') \rfloor$ 
      by(simp add: round-robin-reschedule-Cons)
    moreover from  $\langle \text{round-robin-invar } (t' \# \text{queue}, n0) (\text{dom } (\text{thr } s)) \rangle$ 
    have round-robin-invar (queue @ [t'], n0) (dom (thr s)) by simp
    ultimately show ?thesis by(rule rotate.hyps)
  next
  case (Some a)
  with  $\langle \text{round-robin-reschedule } t0 (t' \# \text{queue}) n0 s = \lfloor (t, \text{None}, \sigma') \rfloor \rangle \langle t' \neq t0 \rangle$ 
  have round-robin-step n0 (t' # queue, n0) s t' =  $\lfloor (t, \text{None}, \sigma') \rfloor$ 
    by(simp add: round-robin-reschedule-Cons)
  thus ?thesis using  $\langle \text{round-robin-invar } (t' \# \text{queue}, n0) (\text{dom } (\text{thr } s)) \rangle$ 
    by(rule round-robin-step-invar-None)
  qed
qed

lemma round-robin-reschedule-invar-Some:
  assumes deterministic I
  and rrr: round-robin-reschedule t0 queue n0 s =  $\lfloor (t, \lfloor (ta, x', m') \rfloor, \sigma') \rfloor$ 
  and invar: round-robin-invar (queue, n0) (dom (thr s))
  and t0:  $t0 \in \text{set } \text{queue}$ 
  and s  $\in I$ 
  shows round-robin-invar  $\sigma'$  (dom (thr s)  $\cup \{t. \exists x m. \text{NewThread } t x m \in \text{set } \{ta\}_t\}$ )
using t0 rrr invar
proof(induct queue rule: round-robin-reschedule-induct)
  case head thus ?case by(simp add: round-robin-reschedule-Cons)
next
  case (rotate queue t')
  show ?case
  proof(cases round-robin-step n0 (t' # queue, n0) s t')
    case None
    with  $\langle \text{round-robin-reschedule } t0 (t' \# \text{queue}) n0 s = \lfloor (t, \lfloor (ta, x', m') \rfloor, \sigma') \rfloor \rangle \langle t' \neq t0 \rangle$ 
    have round-robin-reschedule t0 (queue @ [t']) n0 s =  $\lfloor (t, \lfloor (ta, x', m') \rfloor, \sigma') \rfloor$ 
      by(simp add: round-robin-reschedule-Cons)
    moreover from  $\langle \text{round-robin-invar } (t' \# \text{queue}, n0) (\text{dom } (\text{thr } s)) \rangle$ 
    have round-robin-invar (queue @ [t'], n0) (dom (thr s)) by simp
    ultimately show ?thesis by(rule rotate.hyps)
  next
  case (Some a)
  with  $\langle \text{round-robin-reschedule } t0 (t' \# \text{queue}) n0 s = \lfloor (t, \lfloor (ta, x', m') \rfloor, \sigma') \rfloor \rangle \langle t' \neq t0 \rangle$ 
  have round-robin-step n0 (t' # queue, n0) s t' =  $\lfloor (t, \lfloor (ta, x', m') \rfloor, \sigma') \rfloor$ 
    by(simp add: round-robin-reschedule-Cons)
  thus ?thesis using  $\langle \text{round-robin-invar } (t' \# \text{queue}, n0) (\text{dom } (\text{thr } s)) \rangle \langle s \in I \rangle$ 

```

by(rule round-robin-step-invar-Some[OF ‹deterministic I›])
 qed
 qed

lemma round-robin-NoneD:

assumes rr: round-robin n0 σ s = None
 and invar: round-robin-invar σ (dom (thr s))
 shows active-threads s = {}

proof –

obtain queue n where σ : $\sigma = (\text{queue}, n)$ by(cases σ)
 show ?thesis
 proof(cases queue)
 case Nil
 thus ?thesis using invar σ by(fastforce elim: active-threads.cases)
 next
 case (Cons t queue')
 with rr σ have round-robin-step n0 (t # queue', n) s t = None
 and round-robin-reschedule t (queue' @ [t]) n0 s = None by simp-all
 from ‹round-robin-step n0 (t # queue', n) s t = None›
 have t \notin active-threads s by(rule step-thread-NoneD)
 moreover from ‹round-robin-reschedule t (queue' @ [t]) n0 s = None›
 have set (takeWhile ($\lambda x. x \neq t$) (queue' @ [t])) \cap active-threads s = {}
 by(rule round-robin-reschedule-NoneD) simp
 moreover from invar σ Cons
 have takeWhile ($\lambda x. x \neq t$) (queue' @ [t]) = queue'
 by(subst takeWhile-append2) auto
 moreover from invar have active-threads s \subseteq set queue
 using σ by(auto elim: active-threads.cases)
 ultimately show ?thesis using Cons by auto
 qed
 qed

lemma round-robin-Some-NoneD:

assumes rr: round-robin n0 σ s = [(t, None, σ')]
 shows $\exists x \ln n. \text{thr } s \ t = [(x, \ln)] \wedge \ln \ \$ \ n > 0 \wedge \neg \text{waiting } (\text{wset } s \ t) \wedge \text{may-acquire-all } (\text{locks } s)$
 t ln

proof –

obtain queue n where σ : $\sigma = (\text{queue}, n)$ by(cases σ)
 with rr have queue $\neq []$ by clarsimp
 then obtain t' queue' where queue: queue = t' # queue'
 by(auto simp add: neq-Nil-conv)
 show ?thesis
 proof(cases round-robin-step n0 (t' # queue', n) s t')
 case (Some a)
 with rr queue σ have round-robin-step n0 (t' # queue', n) s t' = [(t, None, σ')] by simp
 thus ?thesis by(blast dest: step-thread-Some-NoneD)
 next
 case None
 with rr queue σ have round-robin-reschedule t' (queue' @ [t']) n0 s = [(t, None, σ')] by simp
 thus ?thesis by(rule round-robin-reschedule-Some-NoneD) simp
 qed
 qed

lemma round-robin-Some-SomeD:

assumes *deterministic I*
and *rr: round-robin n0 σ s = [(t, [(ta, x', m')], σ')]*
and *s ∈ I*
shows $\exists x. \text{thr } s \text{ t} = [(x, \text{no-wait-locks})] \wedge t \vdash \langle x, \text{shr } s \rangle \text{-ta} \rightarrow \langle x', m' \rangle \wedge \text{actions-ok } s \text{ t ta}$
proof –
obtain *queue n where $\sigma: \sigma = (\text{queue}, n)$ by(cases σ)*
with *rr have queue ≠ [] by clarsimp*
then obtain *t' queue' where queue: queue = t' # queue'*
by(*auto simp add: neq-Nil-conv*)
show *?thesis*
proof(*cases round-robin-step n0 (t' # queue', n) s t'*)
case (*Some a*)
with *rr queue σ have round-robin-step n0 (t' # queue', n) s t' = [(t, [(ta, x', m')], σ')]* **by** *simp*
thus *?thesis using $\langle s \in I \rangle$ by(blast dest: step-thread-Some-SomeD[OF \langle deterministic I \rangle])*
next
case *None*
with *rr queue σ have round-robin-reschedule t' (queue' @ [t']) n0 s = [(t, [(ta, x', m')], σ')]* **by** *simp*
thus *?thesis by(rule round-robin-reschedule-Some-SomeD[OF \langle deterministic I \rangle])(simp-all add: $\langle s \in I \rangle$)*
qed
qed

lemma *round-robin-invar-None:*

assumes *rr: round-robin n0 σ s = [(t, None, σ')]*
and *invar: round-robin-invar σ (dom (thr s))*
shows *round-robin-invar σ' (dom (thr s))*
proof –
obtain *queue n where $\sigma: \sigma = (\text{queue}, n)$ by(cases σ)*
with *rr have queue ≠ [] by clarsimp*
then obtain *t' queue' where queue: queue = t' # queue'*
by(*auto simp add: neq-Nil-conv*)
show *?thesis*
proof(*cases round-robin-step n0 (t' # queue', n) s t'*)
case (*Some a*)
with *rr queue σ have round-robin-step n0 (t' # queue', n) s t' = [(t, None, σ')]* **by** *simp*
thus *?thesis using invar unfolding σ queue by(rule round-robin-step-invar-None)*
next
case *None*
with *rr queue σ have round-robin-reschedule t' (queue' @ [t']) n0 s = [(t, None, σ')]* **by** *simp*
moreover from *invar queue σ have round-robin-invar (queue' @ [t'], n0) (dom (thr s))* **by** *simp*
ultimately show *?thesis by(rule round-robin-reschedule-invar-None) simp*
qed
qed

lemma *round-robin-invar-Some:*

assumes *deterministic I*
and *rr: round-robin n0 σ s = [(t, [(ta, x', m')], σ')]*
and *invar: round-robin-invar σ (dom (thr s)) s ∈ I*
shows *round-robin-invar σ' (dom (thr s) ∪ {t. $\exists x m. \text{NewThread } t \ x \ m \in \text{set } \{ta\}_t$ })*
proof –
obtain *queue n where $\sigma: \sigma = (\text{queue}, n)$ by(cases σ)*
with *rr have queue ≠ [] by clarsimp*
then obtain *t' queue' where queue: queue = t' # queue'*

```

  by(auto simp add: neq-Nil-conv)
show ?thesis
proof(cases round-robin-step n0 (t' # queue', n) s t')
  case (Some a)
  with rr queue  $\sigma$  have round-robin-step n0 (t' # queue', n) s t' = [(t, [(ta, x', m'),  $\sigma'$ ])] by simp
  thus ?thesis using invar unfolding  $\sigma$  queue by(rule round-robin-step-invar-Some[OF  $\langle$ deterministic I $\rangle$ ])
next
  case None
  with rr queue  $\sigma$  have round-robin-reschedule t' (queue' @ [t']) n0 s = [(t, [(ta, x', m'),  $\sigma'$ ])] by simp
  moreover from invar queue  $\sigma$ 
  have round-robin-invar (queue' @ [t'], n0) (dom (thr s)) by simp
  ultimately show ?thesis by(rule round-robin-reschedule-invar-Some[OF  $\langle$ deterministic I $\rangle$ ])(simp-all add:  $\langle$ s  $\in$  I $\rangle$ )
qed
qed
end

```

```

locale round-robin-base =
  scheduler-base-aux
  final r convert-RA
  thr- $\alpha$  thr-invar thr-lookup thr-update
  ws- $\alpha$  ws-invar ws-lookup
  is- $\alpha$  is-invar is-memb is-ins is-delete
  for final :: 'x  $\Rightarrow$  bool
  and r :: 't  $\Rightarrow$  ('x  $\times$  'm)  $\Rightarrow$  (('l,'t,'x,'m,'w,'o) thread-action  $\times$  'x  $\times$  'm) Predicate.pred
  and convert-RA :: 'l released-locks  $\Rightarrow$  'o list
  and output :: 'queue round-robin  $\Rightarrow$  't  $\Rightarrow$  ('l,'t,'x,'m,'w,'o) thread-action  $\Rightarrow$  'q option
  and thr- $\alpha$  :: 'm-t  $\Rightarrow$  ('l,'t,'x) thread-info
  and thr-invar :: 'm-t  $\Rightarrow$  bool
  and thr-lookup :: 't  $\Rightarrow$  'm-t  $\rightarrow$  ('x  $\times$  'l released-locks)
  and thr-update :: 't  $\Rightarrow$  'x  $\times$  'l released-locks  $\Rightarrow$  'm-t  $\Rightarrow$  'm-t
  and ws- $\alpha$  :: 'm-w  $\Rightarrow$  ('w,'t) wait-sets
  and ws-invar :: 'm-w  $\Rightarrow$  bool
  and ws-lookup :: 't  $\Rightarrow$  'm-w  $\rightarrow$  'w wait-set-status
  and ws-update :: 't  $\Rightarrow$  'w wait-set-status  $\Rightarrow$  'm-w  $\Rightarrow$  'm-w
  and ws-delete :: 't  $\Rightarrow$  'm-w  $\Rightarrow$  'm-w
  and ws-iterate :: 'm-w  $\Rightarrow$  ('t  $\times$  'w wait-set-status, 'm-w) set-iterator
  and ws-sel :: 'm-w  $\Rightarrow$  ('t  $\times$  'w wait-set-status  $\Rightarrow$  bool)  $\rightarrow$  ('t  $\times$  'w wait-set-status)
  and is- $\alpha$  :: 's-i  $\Rightarrow$  't interrupts
  and is-invar :: 's-i  $\Rightarrow$  bool
  and is-memb :: 't  $\Rightarrow$  's-i  $\Rightarrow$  bool
  and is-ins :: 't  $\Rightarrow$  's-i  $\Rightarrow$  's-i
  and is-delete :: 't  $\Rightarrow$  's-i  $\Rightarrow$  's-i
  +
  fixes queue- $\alpha$  :: 'queue  $\Rightarrow$  't list
  and queue-invar :: 'queue  $\Rightarrow$  bool
  and queue-empty :: unit  $\Rightarrow$  'queue
  and queue-isEmpty :: 'queue  $\Rightarrow$  bool
  and queue-enqueue :: 't  $\Rightarrow$  'queue  $\Rightarrow$  'queue
  and queue-dequeue :: 'queue  $\Rightarrow$  't  $\times$  'queue
  and queue-push :: 't  $\Rightarrow$  'queue  $\Rightarrow$  'queue

```

begin

definition *queue-rotate1* :: 'queue \Rightarrow 'queue
where *queue-rotate1* = case-prod *queue-enqueue* \circ *queue-dequeue*

primrec *enqueue-new-thread* :: 'queue \Rightarrow ('t,'x,'m) new-thread-action \Rightarrow 'queue
where
enqueue-new-thread *ts* (NewThread *t x m*) = *queue-enqueue t ts*
| *enqueue-new-thread* *ts* (ThreadExists *t b*) = *ts*

definition *enqueue-new-threads* :: 'queue \Rightarrow ('t,'x,'m) new-thread-action list \Rightarrow 'queue
where
enqueue-new-threads = foldl *enqueue-new-thread*

primrec *round-robin-update-state* :: nat \Rightarrow 'queue round-robin \Rightarrow 't \Rightarrow ('l,'t,'x,'m,'w,'o) thread-action
 \Rightarrow 'queue round-robin

where
round-robin-update-state *n0* (*queue*, *n*) *t ta* =
(let *queue'* = *enqueue-new-threads queue* $\{\!|ta|\!\}_t$
in if *n* = 0 \vee Yield \in set $\{\!|ta|\!\}_c$ then (*queue-rotate1 queue'*, *n0*) else (*queue'*, *n* - 1))

abbreviation *round-robin-step* ::
nat \Rightarrow 'queue round-robin \Rightarrow ('l,'t,'m,'m-t,'m-w,'s-i) state-refine \Rightarrow 't
 \Rightarrow ('t \times (('l,'t,'x,'m,'w,'o) thread-action \times 'x \times 'm) option \times 'queue round-robin) option
where
round-robin-step *n0* σ *s t* \equiv step-thread (*round-robin-update-state* *n0* σ *t*) *s t*

partial-function (*option*) *round-robin-reschedule* ::
't \Rightarrow 'queue \Rightarrow nat \Rightarrow ('l,'t,'m,'m-t,'m-w,'s-i) state-refine
 \Rightarrow ('t \times (('l,'t,'x,'m,'w,'o) thread-action \times 'x \times 'm) option \times 'queue round-robin) option

where
round-robin-reschedule *t0 queue n0 s* =
(let
(*t*, *queue'*) = *queue-dequeue queue*
in
if *t* = *t0* then
None
else
case *round-robin-step* *n0* (*queue-push t queue'*, *n0*) *s t* of
None \Rightarrow *round-robin-reschedule t0* (*queue-enqueue t queue'*) *n0 s*
| [*ttaxm* σ] \Rightarrow [*ttaxm* σ])

primrec *round-robin* :: nat \Rightarrow ('l,'t,'x,'m,'w,'o,'m-t,'m-w,'s-i,'queue round-robin) scheduler
where

round-robin *n0* (*queue*, *n*) *s* =
(if *queue-isEmpty queue* then None
else
let
(*t*, *queue'*) = *queue-dequeue queue*
in
(case *round-robin-step* *n0* (*queue-push t queue'*, *n*) *s t* of
[*ttaxm* σ] \Rightarrow [*ttaxm* σ]
| None \Rightarrow *round-robin-reschedule t* (*queue-enqueue t queue'*) *n0 s*))

primrec *round-robin-invar* :: 'queue round-robin \Rightarrow 't set \Rightarrow bool
where *round-robin-invar* (queue, n) T \longleftrightarrow queue-invar queue \wedge Round-Robin.round-robin-invar (queue- α queue, n) T

definition *round-robin- α* :: 'queue round-robin \Rightarrow 't list round-robin
where *round-robin- α* = apfst queue- α

definition *round-robin-start* :: nat \Rightarrow 't \Rightarrow 'queue round-robin
where *round-robin-start* n0 t = (queue-enqueue t (queue-empty ()), n0)

lemma *round-robin-invar-correct*:
round-robin-invar σ T \Longrightarrow Round-Robin.round-robin-invar (round-robin- α σ) T
by(cases σ)(simp add: round-robin- α -def)

end

locale *round-robin* =
round-robin-base
final r convert-RA output
thr- α thr-invar thr-lookup thr-update
ws- α ws-invar ws-lookup ws-update ws-delete ws-iterate ws-sel
is- α is-invar is-memb is-ins is-delete
queue- α queue-invar queue-empty queue-isEmpty queue-enqueue queue-dequeue queue-push
+
scheduler-aux
final r convert-RA
thr- α thr-invar thr-lookup thr-update
ws- α ws-invar ws-lookup
is- α is-invar is-memb is-ins is-delete
+
ws: map-update ws- α ws-invar ws-update +
ws: map-delete ws- α ws-invar ws-delete +
ws: map-iteratei ws- α ws-invar ws-iterate +
ws: map-sel' ws- α ws-invar ws-sel +
queue: list queue- α queue-invar +
queue: list-empty queue- α queue-invar queue-empty +
queue: list-isEmpty queue- α queue-invar queue-isEmpty +
queue: list-enqueue queue- α queue-invar queue-enqueue +
queue: list-dequeue queue- α queue-invar queue-dequeue +
queue: list-push queue- α queue-invar queue-push
for *final* :: 'x \Rightarrow bool
and *r* :: 't \Rightarrow ('x \times 'm) \Rightarrow (('l,'t,'x,'m,'w,'o) thread-action \times 'x \times 'm) Predicate.pred
and *convert-RA* :: 'l released-locks \Rightarrow 'o list
and *output* :: 'queue round-robin \Rightarrow 't \Rightarrow ('l,'t,'x,'m,'w,'o) thread-action \Rightarrow 'q option
and *thr- α* :: 'm-t \Rightarrow ('l,'t,'x) thread-info
and *thr-invar* :: 'm-t \Rightarrow bool
and *thr-lookup* :: 't \Rightarrow 'm-t \rightarrow ('x \times 'l released-locks)
and *thr-update* :: 't \Rightarrow 'x \times 'l released-locks \Rightarrow 'm-t \Rightarrow 'm-t
and *ws- α* :: 'm-w \Rightarrow ('w,'t) wait-sets
and *ws-invar* :: 'm-w \Rightarrow bool
and *ws-lookup* :: 't \Rightarrow 'm-w \rightarrow 'w wait-set-status
and *ws-update* :: 't \Rightarrow 'w wait-set-status \Rightarrow 'm-w \Rightarrow 'm-w
and *ws-delete* :: 't \Rightarrow 'm-w \Rightarrow 'm-w
and *ws-iterate* :: 'm-w \Rightarrow ('t \times 'w wait-set-status, 'm-w) set-iterator

```

and ws-sel :: 'm-w ⇒ ('t × 'w wait-set-status ⇒ bool) → ('t × 'w wait-set-status)
and is-α :: 's-i ⇒ 't interrupts
and is-invar :: 's-i ⇒ bool
and is-memb :: 't ⇒ 's-i ⇒ bool
and is-ins :: 't ⇒ 's-i ⇒ 's-i
and is-delete :: 't ⇒ 's-i ⇒ 's-i
and queue-α :: 'queue ⇒ 't list
and queue-invar :: 'queue ⇒ bool
and queue-empty :: unit ⇒ 'queue
and queue-isEmpty :: 'queue ⇒ bool
and queue-enqueue :: 't ⇒ 'queue ⇒ 'queue
and queue-dequeue :: 'queue ⇒ 't × 'queue
and queue-push :: 't ⇒ 'queue ⇒ 'queue
begin

lemma queue-rotate1-correct:
  assumes queue-invar queue queue-α queue ≠ []
  shows queue-α (queue-rotate1 queue) = rotate1 (queue-α queue)
  and queue-invar (queue-rotate1 queue)
using assms
apply(auto simp add: queue-rotate1-def split-beta queue.dequeue-correct queue.enqueue-correct)
by(cases queue-α queue) simp-all

lemma enqueue-thread-correct:
  assumes queue-invar queue
  shows queue-α (enqueue-new-thread queue nta) = Round-Robin.enqueue-new-thread (queue-α queue)
  nta
  and queue-invar (enqueue-new-thread queue nta)
using assms
by(case-tac [!] nta)(simp-all add: queue.enqueue-correct)

lemma enqueue-threads-correct:
  assumes queue-invar queue
  shows queue-α (enqueue-new-threads queue ntas) = Round-Robin.enqueue-new-threads (queue-α queue) ntas
  and queue-invar (enqueue-new-threads queue ntas)
using assms
apply(induct ntas arbitrary: queue)
apply(simp-all add: enqueue-new-threads-def Round-Robin.enqueue-new-threads-def enqueue-thread-correct)
done

lemma round-robin-update-thread-correct:
  assumes round-robin-invar σ T t' ∈ T
  shows round-robin-α (round-robin-update-state n0 σ t ta) = Round-Robin.round-robin-update-state
  n0 (round-robin-α σ) t ta
using assms
apply(cases σ)
apply(auto simp add: round-robin-α-def queue-rotate1-correct enqueue-threads-correct del: conjI)
apply(subst (1 2) queue-rotate1-correct)
apply(auto simp add: enqueue-threads-correct)
done

lemma round-robin-step-correct:
  assumes det: α.deterministic I

```

and *invar*: *round-robin-invar* σ (*dom* (*thr- α* (*thr s*))) *state-invar* *s* *state- α* *s* $\in I$
shows
map-option (*apsnd* (*apsnd round-robin- α*)) (*round-robin-step* *n0* σ *s* *t*) =
 α .*round-robin-step* *n0* (*round-robin- α* σ) (*state- α* *s*) *t* (**is** *?thesis1*)
and *case-option* *True* ($\lambda(t, \text{taxm}, \sigma)$. *round-robin-invar* σ (*case taxm of None* \Rightarrow *dom* (*thr- α* (*thr s*)) |
Some (*ta*, *x'*, *m'*) \Rightarrow *dom* (*thr- α* (*thr s*)) \cup $\{t. \exists x m. \text{NewThread } t x m \in \text{set } \{ta\}_t\}$)) (*round-robin-step*
n0 σ *s* *t*)
(**is** *?thesis2*)
proof –
have *?thesis1* \wedge *?thesis2*
proof(*cases* *dom* (*thr- α* (*thr s*)) = $\{\}$)
case *True*
thus *?thesis* **using** *invar*
apply(*cases* σ)
apply(*auto* *dest*: *step-thread-Some-NoneD*[*OF det*] *step-thread-Some-SomeD*[*OF det*])
apply(*fastforce simp* *add*: α .*step-thread-eq-None-conv* *elim*: α .*active-threads.cases* *intro*: *sym*)
done
next
case *False*
then obtain *t'* **where** *t'*: *t' \in dom* (*thr- α* (*thr s*)) **by** *blast*
hence *?thesis1*
using *step-thread-correct*(1)[*of I round-robin-invar* σ *s round-robin- α round-robin-update-state*
n0 σ *t t*, *OF det invar*]
unfolding *o-def* **using** *invar*
by(*subst* (*asm*) *round-robin-update-thread-correct*) *auto*
moreover
{ **fix** *ta* :: (*'l*, *'t*, *'x*, *'m*, *'w*, *'o*) *thread-action*
assume *FWThread.thread-oks* (*thr- α* (*thr s*)) $\{ta\}_t$
moreover from *t'* *invar* **have** *queue- α* (*fst* σ) $\neq \square$ **by**(*cases* σ) *auto*
ultimately have *round-robin-invar* (*round-robin-update-state* *n0* σ *t ta*) (*dom* (*thr- α* (*thr s*)) \cup
 $\{t. \exists x m. \text{NewThread } t x m \in \text{set } \{ta\}_t\}$)
using *invar* *t'* **by**(*cases* σ)(*auto simp* *add*: *queue-rotate1-correct* *enqueue-threads-correct*
set-enqueue-new-threads *iff* *del*: *domIff* *intro*: *distinct-enqueue-new-threads*) **}**
from *step-thread-correct*(2)[*OF det*, *of round-robin-invar* σ *s round-robin-update-state* *n0* σ *t t*,
OF invar this]
have *?thesis2* **using** *t'* *invar* **by** *simp*
ultimately show *?thesis* **by** *blast*
qed
thus *?thesis1* *?thesis2* **by** *blast+*
qed

lemma *round-robin-reschedule-correct*:

assumes *det*: α .*deterministic* *I*
and *invar*: *round-robin-invar* (*queue*, *n*) (*dom* (*thr- α* (*thr s*))) *state-invar* *s* *state- α* *s* $\in I$
and *t0*: *t0* \in *set* (*queue- α* *queue*)
shows *map-option* (*apsnd* (*apsnd round-robin- α*)) (*round-robin-reschedule* *t0* *queue* *n0* *s*) =
 α .*round-robin-reschedule* *t0* (*queue- α* *queue*) *n0* (*state- α* *s*)
and *case-option* *True* ($\lambda(t, \text{taxm}, \sigma)$. *round-robin-invar* σ (*case taxm of None* \Rightarrow *dom* (*thr- α*
(*thr s*)) | *Some* (*ta*, *x'*, *m'*) \Rightarrow *dom* (*thr- α* (*thr s*)) \cup $\{t. \exists x m. \text{NewThread } t x m \in \text{set } \{ta\}_t\}$))
(*round-robin-reschedule* *t0* *queue* *n0* *s*)
using *t0 invar*
proof(*induct* *queue- α* *queue* *arbitrary*: *queue* *n* *rule*: *round-robin-reschedule-induct*)
case *head*
{ **case** 1 **thus** *?case* **using** *head*[*symmetric*]

by(subst round-robin-reschedule.simps)(subst α .round-robin-reschedule.simps, clarsimp simp add: split-beta queue.dequeue-correct)

next

case 2 thus ?case using head[symmetric]

by(subst round-robin-reschedule.simps)(clarsimp simp add: split-beta queue.dequeue-correct) }

next

case (rotate α queue' t)

obtain t' queue' **where** queue': queue-dequeue queue = (t', queue') **by**(cases queue-dequeue queue)

note [simp] = $\langle t \# \alpha$ queue' = queue- α queue \rangle [symmetric]

{ case 1

with queue' **have** [simp]: t' = t α queue' = queue- α queue' queue-invar queue' **by**(auto elim: queue.removeE)

from 1 queue' have invar': round-robin-invar (queue-push t queue', n0) (dom (thr- α (thr s)))

by(auto simp add: queue.push-correct)

show ?case

proof(cases round-robin-step n0 (queue-push t queue', n0) s t)

case Some thus ?thesis

using queue' $\langle t \neq t0 \rangle$ round-robin-step-correct[OF det invar' \langle state-invar s \rangle , of n0 t] invar' \langle state- α s $\in I \rangle$

by(subst round-robin-reschedule.simps)(subst α .round-robin-reschedule.simps, auto simp add: round-robin- α -def queue.push-correct)

next

case None

hence α None: α .round-robin-step n0 (queue- α (queue-push t queue'), n0) (state- α s) t = None

using round-robin-step-correct[OF det invar' \langle state-invar s \rangle , of n0 t] invar' \langle state- α s $\in I \rangle$

by(auto simp add: queue.push-correct round-robin- α -def)

have α queue' @ [t] = queue- α (queue-enqueue t queue') **by**(simp add: queue.enqueue-correct)

moreover from invar'

have round-robin-invar (queue-enqueue t queue', n0) (dom (thr- α (thr s)))

by(auto simp add: queue.enqueue-correct queue.push-correct)

ultimately

have map-option (apsnd (apsnd round-robin- α)) (round-robin-reschedule t0 (queue-enqueue t queue') n0 s) =

α .round-robin-reschedule t0 (queue- α (queue-enqueue t queue')) n0 (state- α s)

using \langle state-invar s \rangle \langle state- α s $\in I \rangle$ **by**(rule rotate.hyps)

thus ?thesis **using** None α None $\langle t \neq t0 \rangle$ invar' queue'

by(subst round-robin-reschedule.simps)(subst α .round-robin-reschedule.simps, auto simp add: queue.enqueue-correct queue.push-correct)

qed

next

case 2

with queue' **have** [simp]: t' = t α queue' = queue- α queue' queue-invar queue' **by**(auto elim: queue.removeE)

from 2 queue' have invar': round-robin-invar (queue-push t queue', n0) (dom (thr- α (thr s)))

by(auto simp add: queue.push-correct)

show ?case

proof(cases round-robin-step n0 (queue-push t queue', n0) s t)

case Some thus ?thesis

using queue' $\langle t \neq t0 \rangle$ round-robin-step-correct[OF det invar' \langle state-invar s \rangle , of n0 t] invar' \langle state- α s $\in I \rangle$

by(subst round-robin-reschedule.simps)(auto simp add: round-robin- α -def queue.push-correct)

next

case None

have α queue' @ [t] = queue- α (queue-enqueue t queue') **by**(simp add: queue.enqueue-correct)

```

moreover from invar'
have round-robin-invar (queue-enqueue t queue', n0) (dom (thr- $\alpha$  (thr s)))
  by(auto simp add: queue.enqueue-correct queue.push-correct)
ultimately
have case-option True ( $\lambda(t, taxm, \sigma). \text{round-robin-invar } \sigma \text{ (case-option (dom (thr-}\alpha \text{ (thr s))) } (\lambda(ta, x', m'). \text{dom (thr-}\alpha \text{ (thr s))} \cup \{t. \exists x m. \text{NewThread } t x m \in \text{set } \{\{ta\}_t\}) taxm})) \text{(round-robin-reschedule } t0 \text{ (queue-enqueue } t \text{ queue}') } n0 \text{ s)}$ )
  using  $\langle \text{state-invar } s \rangle \langle \text{state-}\alpha \text{ } s \in I \rangle$  by(rule rotate.hyps)
  thus ?thesis using None  $\langle t \neq t0 \rangle$  invar' queue'
  by(subst round-robin-reschedule.simps)(auto simp add: queue.enqueue-correct queue.push-correct)
qed
}
qed

```

lemma *round-robin-correct*:

assumes *det: α .deterministic I*

and *invar: round-robin-invar σ (dom (thr- α (thr s))) state-invar s state- α s \in I*

shows *map-option (apsnd (apsnd round-robin- α)) (round-robin n0 σ s) = α .round-robin n0 (round-robin- α σ) (state- α s)*

(**is** *?thesis1*)

and *case-option True* ($\lambda(t, taxm, \sigma). \text{round-robin-invar } \sigma \text{ (case } taxm \text{ of } None \Rightarrow \text{dom (thr-}\alpha \text{ (thr s))} \mid \text{Some (ta, x', m') } \Rightarrow \text{dom (thr-}\alpha \text{ (thr s))} \cup \{t. \exists x m. \text{NewThread } t x m \in \text{set } \{\{ta\}_t\}) \text{(round-robin } n0 \text{ } \sigma \text{ s)}$)

(**is** *?thesis2*)

proof –

obtain *queue n where $\sigma: \sigma = (\text{queue}, n)$* **by**(*cases σ*)

have *?thesis1 \wedge ?thesis2*

proof(*cases queue- α queue*)

case Nil **thus** *?thesis using invar σ*

by(*auto simp add: split-beta queue.isEmpty-correct round-robin- α -def*)

next

case (*Cons t α queue'*)

with *invar σ obtain queue'*

where [*simp*]: *queue-dequeue queue = (t, queue') α queue' = queue- α queue' queue-invar queue'*

by(*auto elim: queue.removeE*)

from *invar σ Cons* **have** *invar': round-robin-invar (queue-push t queue', n) (dom (thr- α (thr s)))*

by(*auto simp add: queue.push-correct*)

from *invar σ Cons* **have** *invar'': round-robin-invar (queue-enqueue t queue', n0) (dom (thr- α (thr s)))*

by(*auto simp add: queue.enqueue-correct*)

show *?thesis*

proof(*cases round-robin-step n0 (queue-push t queue', n) s t*)

case Some

with σ *Cons invar* **show** *?thesis*

using *round-robin-step-correct[OF det invar' $\langle \text{state-invar } s \rangle$, of n0 t]*

by(*auto simp add: queue.isEmpty-correct queue.push-correct round-robin- α -def*)

next

case None

from *invar σ Cons* **have** *t \in set (queue- α (queue-enqueue t queue'))*

by(*auto simp add: queue.enqueue-correct*)

from *round-robin-reschedule-correct[OF det invar'' $\langle \text{state-invar } s \rangle$, OF $\langle \text{state-}\alpha \text{ } s \in I \rangle$ this, of n0]* *None σ Cons invar*

round-robin-step-correct[OF det invar' $\langle \text{state-invar } s \rangle$, of n0 t]

show *?thesis* **by**(*auto simp add: queue.isEmpty-correct queue.push-correct round-robin- α -def*)

queue.enqueue-correct)

qed
 qed
 thus ?thesis1 ?thesis2 by simp-all
 qed

lemma round-robin-scheduler-spec:

assumes det: α .deterministic I
 shows scheduler-spec final r (round-robin n0) round-robin-invar thr- α thr-invar ws- α ws-invar is- α
 is-invar I

proof

fix σ s
 assume rr: round-robin n0 σ s = None
 and invar: round-robin-invar σ (dom (thr- α (thr s))) state-invar s state- α s \in I
 from round-robin-correct[OF det, OF invar, of n0] rr
 have α .round-robin n0 (round-robin- α σ) (state- α s) = None by simp
 moreover from invar have Round-Robin.round-robin-invar (round-robin- α σ) (dom (thr (state- α
 s)))

by(simp add: round-robin-invar-correct)

ultimately show α .active-threads (state- α s) = {} by(rule α .round-robin-NoneD)

next

fix σ s t σ'
 assume rr: round-robin n0 σ s = [(t, None, σ')]
 and invar: round-robin-invar σ (dom (thr- α (thr s))) state-invar s state- α s \in I
 from round-robin-correct[OF det, OF invar, of n0] rr
 have rr': α .round-robin n0 (round-robin- α σ) (state- α s) = [(t, None, round-robin- α σ')] by simp
 then show $\exists x$ ln n. thr- α (thr s) t = [(x, ln)] \wedge 0 < ln \$ n \wedge \neg waiting (ws- α (wset s) t) \wedge
 may-acquire-all (locks s) t ln

by(rule α .round-robin-Some-NoneD[where s=state- α s, unfolded state- α -conv])

next

fix σ s t ta x' m' σ'
 assume rr: round-robin n0 σ s = [(t, [(ta, x', m')], σ')]
 and invar: round-robin-invar σ (dom (thr- α (thr s))) state-invar s state- α s \in I
 from round-robin-correct[OF det, OF invar, of n0] rr
 have rr': α .round-robin n0 (round-robin- α σ) (state- α s) = [(t, [(ta, x', m')], round-robin- α σ')]

by simp

thus $\exists x$. thr- α (thr s) t = [(x, no-wait-locks)] \wedge Predicate.eval (r t (x, shr s)) (ta, x', m') \wedge
 α .actions-ok (state- α s) t ta

using <state- α s \in I> by(rule α .round-robin-Some-SomeD[OF det, where s=state- α s, unfolded
 state- α -conv])

next

fix σ s t σ'
 assume rr: round-robin n0 σ s = [(t, None, σ')]
 and invar: round-robin-invar σ (dom (thr- α (thr s))) state-invar s state- α s \in I
 from round-robin-correct[OF det, OF invar, of n0] rr
 show round-robin-invar σ' (dom (thr- α (thr s))) by simp

next

fix σ s t ta x' m' σ'
 assume rr: round-robin n0 σ s = [(t, [(ta, x', m')], σ')]
 and invar: round-robin-invar σ (dom (thr- α (thr s))) state-invar s state- α s \in I
 from round-robin-correct[OF det, OF invar, of n0] rr
 show round-robin-invar σ' (dom (thr- α (thr s)) \cup {t. $\exists x$ m. NewThread t x m \in set {ta}_t}) by

simp

qed

lemma *round-robin-start-invar*:

round-robin-invar (round-robin-start n0 t0) {t0}

by(*simp add: round-robin-start-def queue.empty-correct queue.enqueue-correct*)

end

sublocale *round-robin-base* <

scheduler-base

final r convert-RA

round-robin n0 output pick-wakeup-via-sel (λs P. ws-sel s (λ(k,v). P k v)) round-robin-invar

thr-α thr-invar thr-lookup thr-update

ws-α ws-invar ws-lookup ws-update ws-delete ws-iterate

is-α is-invar is-memb is-ins is-delete

for *n0* .

sublocale *round-robin* <

pick-wakeup-spec

final r convert-RA

pick-wakeup-via-sel (λs P. ws-sel s (λ(k,v). P k v)) round-robin-invar

thr-α thr-invar

ws-α ws-invar

is-α is-invar

by(*rule pick-wakeup-spec-via-sel*)(*unfold-locales*)

context *round-robin begin*

lemma *round-robin-scheduler*:

assumes *det: α.deterministic I*

shows

scheduler

final r convert-RA

(round-robin n0) (pick-wakeup-via-sel (λs P. ws-sel s (λ(k,v). P k v))) round-robin-invar

thr-α thr-invar thr-lookup thr-update

ws-α ws-invar ws-lookup ws-update ws-delete ws-iterate

is-α is-invar is-memb is-ins is-delete

I

proof –

interpret *scheduler-spec*

final r convert-RA

round-robin n0 round-robin-invar

thr-α thr-invar

ws-α ws-invar

is-α is-invar

I

using *det* **by**(*rule round-robin-scheduler-spec*)

show *?thesis* **by**(*unfold-locales*)(*rule α.deterministic-invariant3p[OF det]*)

qed

end

lemmas [*code*] =

round-robin-base.queue-rotate1-def

round-robin-base.enqueue-new-thread.simps
round-robin-base.enqueue-new-threads-def
round-robin-base.round-robin-update-state.simps
round-robin-base.round-robin-reschedule.simps
round-robin-base.round-robin.simps
round-robin-base.round-robin-start-def

end

theory *SC-Schedulers*

imports

Random-Scheduler
Round-Robin
../MM/SC-Collections

../Basic/JT-ICF

begin

abbreviation *sc-start-state-refine* ::

$'m-t \Rightarrow (\text{thread-id} \Rightarrow ('x \times \text{addr released-locks}) \Rightarrow 'm-t \Rightarrow 'm-t) \Rightarrow 'm-w \Rightarrow 's-i$
 $\Rightarrow (\text{cname} \Rightarrow \text{mname} \Rightarrow \text{ty list} \Rightarrow \text{ty} \Rightarrow 'md \Rightarrow \text{addr val list} \Rightarrow 'x) \Rightarrow 'md \text{ prog} \Rightarrow \text{cname} \Rightarrow \text{mname}$
 $\Rightarrow \text{addr val list}$
 $\Rightarrow (\text{addr}, \text{thread-id}, \text{heap}, 'm-t, 'm-w, 's-i) \text{ state-refine}$

where

$\bigwedge \text{is-empty.}$
 $\text{sc-start-state-refine thr-empty thr-update ws-empty is-empty } f P \equiv$
 $\text{heap-base.start-state-refine addr2thread-id sc-empty (sc-allocate } P) \text{ thr-empty thr-update ws-empty}$
 $\text{is-empty } f P$

abbreviation *sc-state- α* ::

$(l, 't :: \text{linorder}, 'm, ('t, 'x \times l \Rightarrow f \text{ nat}) \text{ rm}, ('t, 'w \text{ wait-set-status}) \text{ rm}, 't \text{ rs}) \text{ state-refine}$
 $\Rightarrow (l, 't, 'x, 'm, 'w) \text{ state}$

where *sc-state- α* \equiv *state-refine-base.state- α* *rm- α* *rm- α* *rs- α*

lemma *sc-state- α -sc-start-state-refine* [*simp*]:

$\text{sc-state-}\alpha \text{ (sc-start-state-refine (rm-empty } ()) \text{ rm-update (rm-empty } ()) \text{ (rs-empty } ()) \text{ } f P C M \text{ vs}) =$
 $\text{sc-start-state } f P C M \text{ vs}$

by (*simp add: heap-base.start-state-refine-def state-refine-base.state- α .simps split-beta sc.start-state-def rm-correct rs-correct*)

locale *sc-scheduler* =

scheduler
final r convert-RA
schedule output pick-wakeup σ -invar
rm- α rm-invar rm-lookup rm-update
rm- α rm-invar rm-lookup rm-update rm-delete rm-iteratei
rs- α rs-invar rs-memb rs-ins rs-delete
invariant

for *final* :: $'x \Rightarrow \text{bool}$

and *r* :: $'t \Rightarrow ('x \times 'm) \Rightarrow ((l, 't :: \text{linorder}, 'x, 'm, 'w, 'o) \text{ thread-action} \times 'x \times 'm) \text{ Predicate.pred}$

and *convert-RA* :: $l \text{ released-locks} \Rightarrow 'o \text{ list}$

and *schedule* :: $(l, 't, 'x, 'm, 'w, 'o, ('t, 'x \times l \Rightarrow f \text{ nat}) \text{ rm}, ('t, 'w \text{ wait-set-status}) \text{ rm}, 't \text{ rs}, 's) \text{ scheduler}$

and *output* :: $'s \Rightarrow 't \Rightarrow (l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \Rightarrow 'q \text{ option}$

and *pick-wakeup* :: $'s \Rightarrow 't \Rightarrow 'w \Rightarrow ('t, 'w \text{ wait-set-status}) \text{ RBT.rbt} \Rightarrow 't \text{ option}$

and σ -invar :: 's \Rightarrow 't set \Rightarrow bool
and invariant :: ('l,'t,'x,'m,'w) state set

locale *sc-round-robin-base* =
round-robin-base
final r *convert-RA* *output*
rm- α *rm-invar* *rm-lookup* *rm-update*
rm- α *rm-invar* *rm-lookup* *rm-update* *rm-delete* *rm-iteratei* *rm-sel*
rs- α *rs-invar* *rs-memb* *rs-ins* *rs-delete*
fifo- α *fifo-invar* *fifo-empty* *fifo-isEmpty* *fifo-enqueue* *fifo-dequeue* *fifo-push*
for *final* :: 'x \Rightarrow bool
and r :: 't \Rightarrow ('x \times 'm) \Rightarrow (('l,'t :: *linorder*, 'x,'m,'w,'o) *thread-action* \times 'x \times 'm) *Predicate.pred*
and *convert-RA* :: 'l *released-locks* \Rightarrow 'o list
and *output* :: 't *fifo round-robin* \Rightarrow 't \Rightarrow ('l,'t,'x,'m,'w,'o) *thread-action* \Rightarrow 'q *option*

locale *sc-round-robin* =
round-robin
final r *convert-RA* *output*
rm- α *rm-invar* *rm-lookup* *rm-update*
rm- α *rm-invar* *rm-lookup* *rm-update* *rm-delete* *rm-iteratei* *rm-sel*
rs- α *rs-invar* *rs-memb* *rs-ins* *rs-delete*
fifo- α *fifo-invar* *fifo-empty* *fifo-isEmpty* *fifo-enqueue* *fifo-dequeue* *fifo-push*
for *final* :: 'x \Rightarrow bool
and r :: 't \Rightarrow ('x \times 'm) \Rightarrow (('l,'t :: *linorder*, 'x,'m,'w,'o) *thread-action* \times 'x \times 'm) *Predicate.pred*
and *convert-RA* :: 'l *released-locks* \Rightarrow 'o list
and *output* :: 't *fifo round-robin* \Rightarrow 't \Rightarrow ('l,'t,'x,'m,'w,'o) *thread-action* \Rightarrow 'q *option*

sublocale *sc-round-robin* < *sc-round-robin-base* .

locale *sc-random-scheduler-base* =
random-scheduler-base
final r *convert-RA* *output*
rm- α *rm-invar* *rm-lookup* *rm-update* *rm-iteratei*
rm- α *rm-invar* *rm-lookup* *rm-update* *rm-delete* *rm-iteratei* *rm-sel*
rs- α *rs-invar* *rs-memb* *rs-ins* *rs-delete*
lsi- α *lsi-invar* *lsi-empty* *lsi-ins-dj* *lsi-to-list*
for *final* :: 'x \Rightarrow bool
and r :: 't \Rightarrow ('x \times 'm) \Rightarrow (('l,'t :: *linorder*, 'x,'m,'w,'o) *thread-action* \times 'x \times 'm) *Predicate.pred*
and *convert-RA* :: 'l *released-locks* \Rightarrow 'o list
and *output* :: *random-scheduler* \Rightarrow 't \Rightarrow ('l,'t,'x,'m,'w,'o) *thread-action* \Rightarrow 'q *option*

locale *sc-random-scheduler* =
random-scheduler
final r *convert-RA* *output*
rm- α *rm-invar* *rm-lookup* *rm-update* *rm-iteratei*
rm- α *rm-invar* *rm-lookup* *rm-update* *rm-delete* *rm-iteratei* *rm-sel*
rs- α *rs-invar* *rs-memb* *rs-ins* *rs-delete*
lsi- α *lsi-invar* *lsi-empty* *lsi-ins-dj* *lsi-to-list*
for *final* :: 'x \Rightarrow bool
and r :: 't \Rightarrow ('x \times 'm) \Rightarrow (('l,'t :: *linorder*, 'x,'m,'w,'o) *thread-action* \times 'x \times 'm) *Predicate.pred*
and *convert-RA* :: 'l *released-locks* \Rightarrow 'o list
and *output* :: *random-scheduler* \Rightarrow 't \Rightarrow ('l,'t,'x,'m,'w,'o) *thread-action* \Rightarrow 'q *option*

sublocale *sc-random-scheduler* < *sc-random-scheduler-base* .

No spurious wake-ups in generated code

```
overloading sc-spurious-wakeups  $\equiv$  sc-spurious-wakeups
begin
  definition sc-spurious-wakeups [code]: sc-spurious-wakeups  $\equiv$  False
end

end
```

9.5 Tabulation for lookup functions

```
theory TypeRelRefine
imports
  ../Common/TypeRel
  HOL-Library.AList-Mapping
begin
```

9.5.1 Auxiliary lemmata

```
lemma rtranclp-tranclpE:
  assumes  $r^{\hat{**}} x y$ 
  obtains (refl)  $x = y$ 
  | (trancl)  $r^{\hat{++}} x y$ 
using assms
by(cases)(blast dest: rtranclp-into-tranclp1)+
```

```
lemma map-of-map2: map-of (map ( $\lambda(k, v). (k, f k v)$ ) xs) k = map-option (f k) (map-of xs k)
by(induct xs) auto
```

```
lemma map-of-map-K: map-of (map ( $\lambda k. (k, c)$ ) xs) k = (if k  $\in$  set xs then Some c else None)
by(induct xs) auto
```

```
lift-definition map-values :: ( $'a \Rightarrow 'b \Rightarrow 'c$ )  $\Rightarrow$  ( $'a, 'b$ ) mapping  $\Rightarrow$  ( $'a, 'c$ ) mapping
is  $\lambda f m k. \text{map-option } (f k) (m k)$  .
```

```
lemma map-values-Mapping [simp]:
  map-values f (Mapping.Mapping m) = Mapping.Mapping ( $\lambda k. \text{map-option } (f k) (m k)$ )
by(rule map-values.abs-eq)
```

```
lemma map-Mapping: Mapping.map f g (Mapping.Mapping m) = Mapping.Mapping (map-option g  $\circ$ 
m  $\circ$  f)
by(rule map.abs-eq)
```

```
abbreviation subclst ::  $'m \text{ prog} \Rightarrow \text{cname} \Rightarrow \text{cname} \Rightarrow \text{bool}$ 
where subclst P  $\equiv$  (subcls1 P) $^{\hat{++}}$ 
```

9.5.2 Representation type for tabulated lookup functions

```
type-synonym
   $'m \text{ prog-impl}' =$ 
   $'m \text{ cdecl list} \times$ 
  (cname,  $'m \text{ class}$ ) mapping  $\times$ 
  (cname, cname set) mapping  $\times$ 
  (cname, (vname, cname  $\times$  ty  $\times$  fmod) mapping) mapping  $\times$ 
```

(*cname*, (*mname*, *cname* × *ty list* × *ty* × '*m option*) *mapping*) *mapping*)

lift-definition *tabulate-class* :: '*m cdecl list* ⇒ (*cname*, '*m class*) *mapping*
is *class* ∘ *Program* .

lift-definition *tabulate-subcls* :: '*m cdecl list* ⇒ (*cname*, *cname set*) *mapping*
is λ*P C*. if *is-class* (*Program P*) *C* then *Some* {*D*. *Program P* ⊢ *C* ≼* *D*} else *None* .

lift-definition *tabulate-sees-field* :: '*m cdecl list* ⇒ (*cname*, (*vname*, *cname* × *ty* × *fmod*) *mapping*)
mapping
is λ*P C*. if *is-class* (*Program P*) *C* then
 Some (λ*F*. if ∃ *T fm D*. *Program P* ⊢ *C* sees *F:T* (*fm*) in *D* then *Some* (*field* (*Program P*) *C*
 F) else *None*)
 else *None* .

lift-definition *tabulate-Method* :: '*m cdecl list* ⇒ (*cname*, (*mname*, *cname* × *ty list* × *ty* × '*m option*)
mapping) *mapping*
is λ*P C*. if *is-class* (*Program P*) *C* then
 Some (λ*M*. if ∃ *Ts T mthd D*. *Program P* ⊢ *C* sees *M:Ts→T=mthd* in *D* then *Some* (*method*
 (*Program P*) *C* *M*) else *None*)
 else *None* .

fun *wf-prog-impl'* :: '*m prog-impl'* ⇒ *bool*
where
 wf-prog-impl' (*P*, *c*, *s*, *f*, *m*) ↔
 c = *tabulate-class P* ∧
 s = *tabulate-subcls P* ∧
 f = *tabulate-sees-field P* ∧
 m = *tabulate-Method P*

9.5.3 Implementation type for tabulated lookup functions

typedef '*m prog-impl* = {*P* :: '*m prog-impl'*. *wf-prog-impl' P*}
morphisms *impl-of ProgRefine*
proof
 show ([], *Mapping.empty*, *Mapping.empty*, *Mapping.empty*, *Mapping.empty*) ∈ ?*prog-impl*
 apply *clarsimp*
 by *transfer* (*simp-all add: fun-eq-iff is-class-def rel-funI*)
qed

lemma *impl-of-ProgImpl [simp]*:
 wf-prog-impl' P fsm ⇒ *impl-of* (*ProgRefine P fsm*) = *P fsm*
by(*simp add: ProgRefine-inverse*)

definition *program* :: '*m prog-impl* ⇒ '*m prog*
where *program* = *Program* ∘ *fst* ∘ *impl-of*

code-datatype *program*

lemma *prog-impl-eq-iff*:
 Pi = *Pi'* ↔ *program Pi* = *program Pi'* **for** *Pi Pi'*
apply(*cases Pi*)
apply(*cases Pi'*)
apply(*auto simp add: ProgRefine-inverse program-def ProgRefine-inject*)

done

lemma *wf-prog-impl'-impl-of* [*simp, intro!*]:

wf-prog-impl' (impl-of Pi) for Pi

using *impl-of[of Pi]* **by** *simp*

lemma *ProgImpl-impl-of* [*simp, code abstype*]:

ProgRefine (impl-of Pi) = Pi for Pi

by(*rule impl-of-inverse*)

lemma *program-ProgRefine* [*simp*]: *wf-prog-impl' Psfm \implies program (ProgRefine Psfm) = Program (fst Psfm)*

by(*simp add: program-def*)

lemma *classes-program* [*code*]: *classes (program P) = fst (impl-of P)*

by(*simp add: program-def*)

lemma *class-program* [*code*]: *class (program Pi) = Mapping.lookup (fst (snd (impl-of Pi))) for Pi*

by(*cases Pi*)(*clarsimp simp add: tabulate-class-def lookup.rep-eq Mapping-inverse*)

9.5.4 Refining sub class and lookup functions to use precomputed mappings

declare *subcls'.equation* [*code del*]

lemma *subcls'-program* [*code*]:

subcls' (program Pi) C D \longleftrightarrow

C = D \vee

(case Mapping.lookup (fst (snd (snd (impl-of Pi)))) C of None \Rightarrow False

| Some m \Rightarrow D \in m) for Pi

apply(*cases Pi*)

apply(*clarsimp simp add: subcls'-def tabulate-subcls-def lookup.rep-eq Mapping-inverse*)

apply(*auto elim!: rtranclp-tranclpE dest: subcls-is-class intro: tranclp-into-rtranclp*)

done

lemma *subcls'-i-i-i-program* [*code*]:

subcls'-i-i-i P C D = (if subcls' P C D then Predicate.single () else bot)

by(*rule pred-eqI*)(*auto elim: subcls'-i-i-iE intro: subcls'-i-i-iI*)

lemma *subcls'-i-i-o-program* [*code*]:

subcls'-i-i-o (program Pi) C =

sup (Predicate.single C) (case Mapping.lookup (fst (snd (snd (impl-of Pi)))) C of None \Rightarrow bot | Some

m \Rightarrow pred-of-set m) for Pi

by(*cases Pi*)(*fastforce simp add: subcls'-i-i-o-def subcls'-def tabulate-subcls-def lookup.rep-eq Mapping-inverse*

intro!: pred-eqI split: if-split-asm elim: rtranclp-tranclpE dest: subcls-is-class intro: tranclp-into-rtranclp)

lemma *rtranclp-FioB-i-i-subcls1-i-i-o-code* [*code-unfold*]:

rtranclp-FioB-i-i (subcls1-i-i-o P) = subcls'-i-i-i P

by(*auto simp add: fun-eq-iff subcls1-i-i-o-def subcls'-def rtranclp-FioB-i-i-def subcls'-i-i-i-def*)

declare *Method.equation*[*code del*]

lemma *Method-program* [*code*]:

program Pi \vdash C sees M:Ts \rightarrow T=meth in D \longleftrightarrow

(case Mapping.lookup (snd (snd (snd (snd (impl-of Pi)))))) C of

None \Rightarrow False

| *Some* $m \Rightarrow$
 (case *Mapping.lookup* m M of
 None \Rightarrow *False*
 | *Some* $(D', Ts', T', meth') \Rightarrow Ts = Ts' \wedge T = T' \wedge meth = meth' \wedge D = D')$ **for** Pi
by(cases Pi)(*auto split: if-split-asm dest: sees-method-is-class simp add: tabulate-Method-def lookup.rep-eq Mapping-inverse*)

lemma *Method-i-i-i-o-o-o-program* [code]:
Method-i-i-i-o-o-o (program Pi) C $M =$
 (case *Mapping.lookup* (snd (snd (snd (snd (impl-of Pi)))))) C of
 None \Rightarrow *bot*
 | *Some* $m \Rightarrow$
 (case *Mapping.lookup* m M of
 None \Rightarrow *bot*
 | *Some* $(D, Ts, T, meth) \Rightarrow$ *Predicate.single* $(Ts, T, meth, D)$) **for** Pi
by(*auto simp add: Method-i-i-i-o-o-o-def Method-program intro!: pred-eqI*)

lemma *Method-i-i-i-o-o-i-program* [code]:
Method-i-i-i-o-o-i (program Pi) C M $D =$
 (case *Mapping.lookup* (snd (snd (snd (snd (impl-of Pi)))))) C of
 None \Rightarrow *bot*
 | *Some* $m \Rightarrow$
 (case *Mapping.lookup* m M of
 None \Rightarrow *bot*
 | *Some* $(D', Ts, T, meth) \Rightarrow$ if $D = D'$ then *Predicate.single* $(Ts, T, meth)$ else *bot*) **for** Pi
by(*auto simp add: Method-i-i-i-o-o-i-def Method-program intro!: pred-eqI*)

declare *sees-field.equation*[code del]

lemma *sees-field-program* [code]:
 program $Pi \vdash C$ sees $F:T$ (fd) in $D \longleftrightarrow$
 (case *Mapping.lookup* (fst (snd (snd (snd (impl-of Pi)))))) C of
 None \Rightarrow *False*
 | *Some* $m \Rightarrow$
 (case *Mapping.lookup* m F of
 None \Rightarrow *False*
 | *Some* $(D', T', fd') \Rightarrow T = T' \wedge fd = fd' \wedge D = D')$ **for** Pi
by(cases Pi)(*auto split: if-split-asm dest: has-visible-field[THEN has-field-is-class] simp add: tabulate-sees-field-def lookup.rep-eq Mapping-inverse*)

lemma *sees-field-i-i-i-o-o-o-program* [code]:
sees-field-i-i-i-o-o-o (program Pi) C $F =$
 (case *Mapping.lookup* (fst (snd (snd (snd (impl-of Pi)))))) C of
 None \Rightarrow *bot*
 | *Some* $m \Rightarrow$
 (case *Mapping.lookup* m F of
 None \Rightarrow *bot*
 | *Some* $(D, T, fd) \Rightarrow$ *Predicate.single* (T, fd, D)) **for** Pi
by(*auto simp add: sees-field-program sees-field-i-i-i-o-o-o-def intro: pred-eqI*)

lemma *sees-field-i-i-i-o-o-i-program* [code]:
sees-field-i-i-i-o-o-i (program Pi) C F $D =$
 (case *Mapping.lookup* (fst (snd (snd (snd (impl-of Pi)))))) C of
 None \Rightarrow *bot*

| *Some m* \Rightarrow
 (case *Mapping.lookup m F* of
 None \Rightarrow *bot*
 | *Some (D', T, fd)* \Rightarrow if *D = D'* then *Predicate.single(T, fd)* else *bot*) **for** *Pi*
by(*auto simp add: sees-field-program sees-field-i-i-o-o-i-def intro: pred-eqI*)

lemma *field-program* [*code*]:
field (program Pi) C F =
 (case *Mapping.lookup (fst (snd (snd (impl-of Pi))))*) *C* of
 None \Rightarrow *Code.abort (STR "not-unique")* (λ -. *Predicate.the bot*)
 | *Some m* \Rightarrow
 (case *Mapping.lookup m F* of
 None \Rightarrow *Code.abort (STR "not-unique")* (λ -. *Predicate.the bot*)
 | *Some (D', T, fd)* \Rightarrow (*D', T, fd*)) **for** *Pi*

unfolding *field-def*
by(*cases Pi*)(*fastforce simp add: Predicate.the-def tabulate-sees-field-def lookup.rep-eq Mapping-inverse split: if-split-asm intro: arg-cong[where f=The] dest: has-visible-field[THEN has-field-is-class] sees-field-fun*)

9.5.5 Implementation for precomputing mappings

definition *tabulate-program* :: '*m cdecl list* \Rightarrow '*m prog-impl*
where *tabulate-program P = ProgRefine (P, tabulate-class P, tabulate-subcls P, tabulate-sees-field P, tabulate-Method P)*

lemma *impl-of-tabulate-program* [*code abstract*]:
impl-of (tabulate-program P) = (P, tabulate-class P, tabulate-subcls P, tabulate-sees-field P, tabulate-Method P)
by(*simp add: tabulate-program-def*)

lemma *Program-code* [*code*]:
Program = program \circ tabulate-program
by(*simp add: program-def fun-eq-iff tabulate-program-def*)

class

lemma *tabulate-class-code* [*code*]:
tabulate-class = Mapping.of-alist
by *transfer (simp add: fun-eq-iff)*

subcls

inductive *subcls1'* :: '*m cdecl list* \Rightarrow *cname* \Rightarrow *cname* \Rightarrow *bool*
where
find: C \neq Object \Longrightarrow subcls1' ((C, D, rest) # P) C D
| *step: [C \neq Object; C \neq C'; subcls1' P C D] \Longrightarrow subcls1' ((C', D', rest) # P) C D*

code-pred
(*modes: i* \Rightarrow *i* \Rightarrow *o* \Rightarrow *bool*)
subcls1' .

lemma *subcls1-into-subcls1'*:
assumes *subcls1 (Program P) C D*
shows *subcls1' P C D*

proof –
from *assms obtain rest where map-of P C = [(D, rest)] C \neq Object* **by** *cases simp*

```

thus ?thesis by(induct P)(auto split: if-split-asm intro: subcls1'.intros)
qed

```

```

lemma subcls1'-into-subcls1:
  assumes subcls1' P C D
  shows subcls1 (Program P) C D
using assms
proof(induct)
  case find thus ?case by(auto intro: subcls1.intros)
next
  case step thus ?case by(auto elim!: subcls1.cases intro: subcls1.intros)
qed

```

```

lemma subcls1-eq-subcls1':
  subcls1 (Program P) = subcls1' P
by(auto simp add: fun-eq-iff intro: subcls1-into-subcls1' subcls1'-into-subcls1)

```

```

definition subcls'' :: 'm cdecl list  $\Rightarrow$  cname  $\Rightarrow$  cname  $\Rightarrow$  bool
where subcls'' P = (subcls1' P)^**

```

```

code-pred
  (modes: i  $\Rightarrow$  i  $\Rightarrow$  i  $\Rightarrow$  bool)
  [inductify]
  subcls'' .

```

```

lemma subcls''-eq-subcls: subcls'' P = subcls (Program P)
by(simp add: subcls''-def subcls1-eq-subcls1')

```

```

lemma subclst-snd-classD:
  assumes subclst (Program P) C D
  shows D  $\in$  fst ' snd ' set P
using assms
by(induct)(fastforce elim!: subcls1.cases dest!: map-of-SomeD intro: rev-image-eqI)+

```

```

definition check-acyclicity :: (cname, cname set) mapping  $\Rightarrow$  'm cdecl list  $\Rightarrow$  unit
where check-acyclicity - - = ()

```

```

definition cyclic-class-hierarchy :: unit
where [code del]: cyclic-class-hierarchy = ()

```

```

declare [[code abort: cyclic-class-hierarchy]]

```

```

lemma check-acyclicity-code:
  check-acyclicity mapping P =
  (let - =
    map ( $\lambda(C, D, -)$ .
      if C = Object then ()
      else
        (case Mapping.lookup mapping D of
          None  $\Rightarrow$  ()
          | Some Cs  $\Rightarrow$  if C  $\in$  Cs then cyclic-class-hierarchy else ()))
      P
    in ())
by simp

```

```

lemma tabulate-subcls-code [code]:
  tabulate-subcls P =
  (let cnames = map fst P;
      cnames' = map (fst ∘ snd) P;
      mapping = Mapping.tabulate cnames (λC. set (C # [D ← cnames'. subcls'' P C D]));
      - = check-acyclicity mapping P
    in mapping
  )
apply(auto simp add: tabulate-subcls-def Mapping.tabulate-def fun-eq-iff is-class-def o-def map-of-map2[simplified split-def] Mapping-inject)
apply(subst map-of-map2[simplified split-def])
apply(auto simp add: fun-eq-iff subcls''-eq-subcls map-of-map-K dest: subclst-snd-classD elim: rtranclp-tranclpE)[1]
apply(subst map-of-map2[simplified split-def])
apply(rule sym)
apply simp
apply(case-tac map-of P x)
apply auto
done

```

Fields

Problem: Does not terminate for cyclic class hierarchies! This problem already occurs in Jinja's well-formedness checker: *wf-cdecl* calls *wf-mdecl* before checking for acyclicity, but *wf-J-mdecl* involves the type judgements, which in turn requires *Fields* (via *sees-field*). Checking acyclicity before executing *Fields'* for tabulation is difficult because we would have to intertwine tabulation and well-formedness checking. Possible (local) solution: additional termination parameter (like memoisation for *rtranclp*) and list option as error return parameter.

inductive

```

Fields' :: 'm cdecl list ⇒ cname ⇒ ((vname × cname) × (ty × fmod)) list ⇒ bool
for P :: 'm cdecl list
where
  rec:
  [ map-of P C = Some(D,fs,ms); C ≠ Object; Fields' P D FDTs;
    FDTs' = map (λ(F,Tm). ((F,C),Tm)) fs @ FDTs ]
  ⇒ Fields' P C FDTs'
| Object:
  [ map-of P Object = Some(D,fs,ms); FDTs = map (λ(F,T). ((F,Object),T)) fs ]
  ⇒ Fields' P Object FDTs

```

lemma *Fields'-into-Fields*:

```

assumes Fields' P C FDTs
shows Program P ⊢ C has-fields FDTs
using assms
by induct(auto intro: Fields.intros)

```

lemma *Fields-into-Fields'*:

```

assumes Program P ⊢ C has-fields FDTs
shows Fields' P C FDTs
using assms
by induct(auto intro: Fields'.intros)

```

lemma *Fields'-eq-Fields*:

Fields' P = Fields (Program P)

by(*auto simp add: fun-eq-iff intro: Fields'-into-Fields Fields-into-Fields'*)

code-pred

(*modes: i ⇒ i ⇒ o ⇒ bool*)

Fields'.

definition *fields' :: 'm cdecl list ⇒ cname ⇒ ((vname × cname) × (ty × fmod)) list*

where *fields' P C = (if ∃ FDTs. Fields' P C FDTs then THE FDTs. Fields' P C FDTs else [])*

lemma *eval-Fields'-conv*:

Predicate.eval (Fields'-i-i-o P C) = Fields' P C

by(*auto intro: Fields'-i-i-oI elim: Fields'-i-i-oE intro!: ext*)

lemma *fields'-code [code]*:

fields' P C =

(let FDTs = Fields'-i-i-o P C in if Predicate.holds (FDTs ≧ (λ-. Predicate.single ())) then Predicate.the FDTs else [])

by(*auto simp add: fields'-def holds-eq Fields'-i-i-o-def intro: Fields'-i-i-oI Predicate.the-eqI[THEN sym]*)

lemma *The-Fields [simp]*:

P ⊢ C has-fields FDTs ⇒ The (Fields P C) = FDTs

by(*auto dest: has-fields-fun*)

lemma *tabulate-sees-field-code [code]*:

tabulate-sees-field P =

Mapping.tabulate (map fst P) (λC. Mapping.of-alist (map (λ((F, D), Tfm). (F, (D, Tfm))) (fields' P C)))

apply(*simp add: tabulate-sees-field-def tabulate-def is-class-def fields'-def Fields'-eq-Fields Mapping-inject*)

apply(*rule ext*)

apply *clarsimp*

apply(*rule conjI*)

apply(*clarsimp simp add: o-def*)

apply(*subst map-of-map2[unfolded split-def]*)

apply *simp*

apply *transfer*

apply(*rule conjI*)

apply *clarsimp*

apply(*rule ext*)

apply *clarsimp*

apply(*rule conjI*)

apply(*clarsimp simp add: sees-field-def Fields'-eq-Fields*)

apply(*drule (1) has-fields-fun, clarsimp*)

apply *clarify*

apply(*rule sym*)

apply(*rule ccontr*)

apply(*clarsimp simp add: sees-field-def Fields'-eq-Fields*)

apply *clarsimp*

apply(*rule ext*)

apply(*clarsimp simp add: sees-field-def*)

apply(*clarsimp simp add: o-def*)

apply(*subst map-of-map2[simplified split-def]*)

```

apply(rule sym)
apply(clarsimp)
apply(rule ccontr)
apply simp
done

```

Methods

Same termination problem as for *Fields'*

```

inductive Methods' :: 'm cdecl list  $\Rightarrow$  cname  $\Rightarrow$  (mname  $\times$  (ty list  $\times$  ty  $\times$  'm option)  $\times$  cname) list
 $\Rightarrow$  bool
  for P :: 'm cdecl list
where
  [| map-of P Object = Some(D,fs,ms); Mm = map ( $\lambda(M, rest).$  (M, (rest, Object))) ms |]
   $\Rightarrow$  Methods' P Object Mm
| [| map-of P C = Some(D,fs,ms); C  $\neq$  Object; Methods' P D Mm;
  Mm' = map ( $\lambda(M, rest).$  (M, (rest, C))) ms @ Mm |]
   $\Rightarrow$  Methods' P C Mm'

```

lemma *Methods'-into-Methods*:

```

  assumes Methods' P C Mm
  shows Program P  $\vdash$  C sees-methods (map-of Mm)
using assms
apply induct
apply(clarsimp simp add: o-def split-def)
apply(rule sees-methods-Object)
  apply fastforce
apply(rule ext)
apply(subst map-of-map2[unfolded split-def])
apply(simp add: o-def)

```

```

apply(rule sees-methods-rec)
  apply fastforce
  apply simp
  apply assumption
apply(clarsimp simp add: map-add-def map-of-map2)
done

```

lemma *Methods-into-Methods'*:

```

  assumes Program P  $\vdash$  C sees-methods Mm
  shows  $\exists Mm'. \textit{Methods' P C Mm'} \wedge Mm = \textit{map-of Mm'}$ 
using assms
by induct(auto intro: Methods'.intros simp add: map-of-map2 map-add-def)

```

code-pred

```

(modes: i  $\Rightarrow$  i  $\Rightarrow$  o  $\Rightarrow$  bool)
Methods'

```

.

definition *methods'* :: 'm cdecl list \Rightarrow cname \Rightarrow (mname \times (ty list \times ty \times 'm option) \times cname) list
where *methods' P C* = (*if* $\exists Mm. \textit{Methods' P C Mm}$ *then THE Mm. Methods' P C Mm* *else* [])

lemma *methods'-code* [*code*]:

```

methods' P C =

```

```

    (let Mm = Methods'-i-i-o P C
      in if Predicate.holds (Mm  $\gg$  ( $\lambda$ -. Predicate.single ())) then Predicate.the Mm else [])
unfolding methods'-def
by(auto simp add: holds-eq Methods'-i-i-o-def Predicate.the-def)

```

```

lemma Methods'-fun:
  assumes Methods' P C Mm
  shows Methods' P C Mm'  $\implies$  Mm = Mm'
using assms
apply(induct arbitrary: Mm')
  apply(fastforce elim: Methods'.cases)
apply(rotate-tac -1)
apply(erule Methods'.cases)
  apply(fastforce)
apply clarify
apply(simp)
done

```

```

lemma The-Methods' [simp]: Methods' P C Mm  $\implies$  The (Methods' P C) = Mm
by(auto dest: Methods'-fun)

```

```

lemma methods-def2 [simp]: Methods' P C Mm  $\implies$  methods' P C = Mm
by(auto simp add: methods'-def)

```

```

lemma tabulate-Method-code [code]:
  tabulate-Method P =
    Mapping.tabulate (map fst P) ( $\lambda$ C. Mapping.of-alist (map ( $\lambda$ (M, (rest, D)). (M, D, rest)) (methods'
    P C)))
apply(simp add: tabulate-Method-def tabulate-def o-def lookup.rep-eq Mapping-inject)
apply(rule ext)
apply clarsimp
apply(rule conjI)
  apply clarify
  apply(rule sym)
apply(subst map-of-map2[unfolded split-def])
apply(simp add: is-class-def)
apply transfer
apply(rule ext)
apply(simp add: map-of-map2)
apply(rule conjI)
  apply(clarsimp simp add: map-of-map2 Method-def)
  apply(drule Methods-into-Methods')
  apply clarsimp
  apply(simp add: split-def)
  apply(subst map-of-map2[unfolded split-def])
  apply simp
apply clarify
apply(clarsimp simp add: methods'-def)
apply(frule Methods'-into-Methods)
apply(clarsimp simp add: Method-def)
apply(simp add: split-def)
apply(subst map-of-map2[unfolded split-def])
apply(fastforce intro: ccontr)
apply clarify

```

```

apply(rule sym)
apply(simp add: map-of-eq-None-iff is-class-def)
apply(simp only: set-map[symmetric] map-map o-def fst-conv)
apply simp
done

```

Merge modules TypeRel, Decl and TypeRelRefine to avoid cyclic modules

```

code-identifier
code-module TypeRel  $\rightarrow$ 
  (SML) TypeRel and (Haskell) TypeRel and (OCaml) TypeRel
| code-module TypeRelRefine  $\rightarrow$ 
  (SML) TypeRel and (Haskell) TypeRel and (OCaml) TypeRel
| code-module Decl  $\rightarrow$ 
  (SML) TypeRel and (Haskell) TypeRel and (OCaml) TypeRel

```

```

ML-val  $\langle @\{code\} Program \rangle$ 

```

```

end

```

```

theory PCompilerRefine
imports
  TypeRelRefine
  ../Compiler/PCompiler
begin

```

9.5.6 compP

Applying the compiler to a tabulated program either compiles every method twice (once for the program itself and once for method lookup) or recomputes the class and method lookup tabulation from scratch. We follow the second approach.

```

fun compP-code' :: (cname  $\Rightarrow$  mname  $\Rightarrow$  ty list  $\Rightarrow$  ty  $\Rightarrow$  'a  $\Rightarrow$  'b)  $\Rightarrow$  'a prog-impl'  $\Rightarrow$  'b prog-impl'
where
  compP-code' f (P, Cs, s, F, m) =
    (let P' = map (compC f) P
     in (P', tabulate-class P', s, F, tabulate-Method P'))

```

```

definition compP-code :: (cname  $\Rightarrow$  mname  $\Rightarrow$  ty list  $\Rightarrow$  ty  $\Rightarrow$  'a  $\Rightarrow$  'b)  $\Rightarrow$  'a prog-impl'  $\Rightarrow$  'b prog-impl'
where compP-code f P = ProgRefine (compP-code' f (impl-of P))

```

```

declare compP.simps [simp del] compP.simps[symmetric, simp]

```

```

lemma compP-code-code [code abstract]:
  impl-of (compP-code f P) = compP-code' f (impl-of P)
apply(cases P)
apply(simp add: compP-code-def)
apply(subst ProgRefine-inverse)
apply(auto simp add: tabulate-subcls-def tabulate-sees-field-def Mapping-inject intro!: ext)
done

```

```

declare compP.simps [simp] compP.simps[symmetric, simp del]

```

```

lemma compP-program [code]:

```

```

  compP f (program P) = program (compP-code f P)
by(cases P)(clarsimp simp add: program-def compP-code-code)

```

Merge module names to avoid cycles in module dependency

code-identifier

```

code-module PCompiler  $\rightarrow$ 
  (SML) PCompiler and (OCaml) PCompiler and (Haskell) PCompiler
| code-module PCompilerRefine  $\rightarrow$ 
  (SML) PCompiler and (OCaml) PCompiler and (Haskell) PCompiler

```

```
ML-val  $\langle @\{code\ compP\} \rangle$ 
```

```
end
```

9.6 Executable semantics for J

```
theory J-Execute
```

```
imports
```

```
  SC-Schedulers
```

```
  ../J/Threaded
```

```
begin
```

```
interpretation sc:
```

```
  J-heap-base
```

```
  addr2thread-id
```

```
  thread-id2addr
```

```
  sc-spurious-wakeups
```

```
  sc-empty
```

```
  sc-allocate P
```

```
  sc-typeof-addr
```

```
  sc-heap-read
```

```
  sc-heap-write
```

```
for P .
```

```
abbreviation sc-red ::
```

```
((addr, thread-id, heap) external-thread-action  $\Rightarrow$  (addr, thread-id, 'o, heap) Jinja-thread-action)
```

```
 $\Rightarrow$  addr J-prog  $\Rightarrow$  thread-id  $\Rightarrow$  addr expr  $\Rightarrow$  heap  $\times$  addr locals
```

```
 $\Rightarrow$  (addr, thread-id, 'o, heap) Jinja-thread-action  $\Rightarrow$  addr expr  $\Rightarrow$  heap  $\times$  addr locals  $\Rightarrow$  bool
```

```
 $\langle \langle -, - \vdash_{sc} ((1 \langle -, - \rangle) \dashrightarrow / (1 \langle -, - \rangle)) \rangle [51, 51, 0, 0, 0, 0, 0, 0] 81 \rangle$ 
```

```
where
```

```
sc-red extTA P  $\equiv$  sc.red (TYPE(addr J-mb)) P extTA P
```

```
fun sc-red-i-i-i-i-i-i-i-Fii-i-oB-Fii-i-i-oB-i-i-i-i-i-o-o-o
```

```
where
```

```
sc-red-i-i-i-i-i-i-i-Fii-i-oB-Fii-i-i-oB-i-i-i-i-i-o-o-o P t ((e, xs), h) =
```

```
red-i-i-i-i-i-i-i-Fii-i-oB-Fii-i-i-oB-i-i-i-i-i-o-o-o
```

```
  addr2thread-id thread-id2addr sc-spurious-wakeups
```

```
  sc-empty (sc-allocate P) sc-typeof-addr sc-heap-read-i-i-i-i-o sc-heap-write-i-i-i-i-o
```

```
(extTA2J P) P t e (h, xs)
```

```
 $\gg$  ( $\lambda$ (ta, e, h, xs). Predicate.single (ta, (e, xs), h))
```

```
abbreviation sc-J-start-state-refine ::
```

```
addr J-prog  $\Rightarrow$  cname  $\Rightarrow$  mname  $\Rightarrow$  addr val list  $\Rightarrow$ 
```


$(addr, thread-id, heap, (thread-id, (addr\ expr \times addr\ locals) \times addr\ released-locks) rm, (thread-id, addr\ wait-set-status) rm, thread-id\ rs)$ *state-refine*

where

sc-J-start-state-refine \equiv

sc-start-state-refine

$(rm-empty\ ())\ rm-update\ (rm-empty\ ())\ (rs-empty\ ())$

$(\lambda C\ M\ Ts\ T\ (pns, body)\ vs.\ (blocks\ (this\ \# pns)\ (Class\ C\ \# Ts)\ (Null\ \# vs)\ body, Map.empty))$

lemma *eval-sc-red-i-i-i-i-i-Fii-i-oB-Fii-i-i-oB-i-i-i-i-i-o-o-o*:

$(\lambda t\ xm\ ta\ x'm'.\ Predicate.eval\ (sc-red-i-i-i-i-i-Fii-i-oB-Fii-i-i-oB-i-i-i-i-i-o-o-o\ P\ t\ xm)\ (ta, x'm'))$

$=$

$(\lambda t\ ((e, xs), h)\ ta\ ((e', xs'), h').\ extTA2J\ P, P, t \vdash_{sc} \langle e, (h, xs) \rangle - ta \rightarrow \langle e', (h', xs') \rangle)$

by (*auto elim!*: *red-i-i-i-i-i-Fii-i-oB-Fii-i-i-oB-i-i-i-i-i-o-o-oE intro!*: *red-i-i-i-i-i-Fii-i-oB-Fii-i-i-oB-i-i-i-i-i-o-o-oI*

ext SUP1-I simp add: eval-sc-heap-write-i-i-i-i-o eval-sc-heap-read-i-i-i-o)

lemma *sc-J-start-state-invar*: $(\lambda-. True)\ (sc-state-\alpha\ (sc-J-start-state-refine\ P\ C\ M\ vs))$

by *simp*

9.6.1 Round-robin scheduler

interpretation *J-rr*:

sc-round-robin-base

final-expr sc-red-i-i-i-i-i-Fii-i-oB-Fii-i-i-oB-i-i-i-i-i-o-o-o P convert-RA Jinja-output

for *P*

.

definition *sc-rr-J-start-state* :: $nat \Rightarrow 'm\ prog \Rightarrow thread-id\ fifo\ round-robin$

where *sc-rr-J-start-state* $n0\ P = J-rr.round-robin-start\ n0\ (sc-start-tid\ P)$

definition *exec-J-rr* ::

$nat \Rightarrow addr\ J-prog \Rightarrow cname \Rightarrow mname \Rightarrow addr\ val\ list \Rightarrow$

$(thread-id \times (addr, thread-id)\ obs-event\ list,$

$(addr, thread-id)\ locks \times ((thread-id, (addr\ expr \times addr\ locals) \times addr\ released-locks) rm \times heap)$

\times

$(thread-id, addr\ wait-set-status) rm \times thread-id\ rs)$ *tllist*

where

exec-J-rr $n0\ P\ C\ M\ vs = J-rr.exec\ P\ n0\ (sc-rr-J-start-state\ n0\ P)\ (sc-J-start-state-refine\ P\ C\ M\ vs)$

interpretation *J-rr*:

sc-round-robin

final-expr sc-red-i-i-i-i-i-Fii-i-oB-Fii-i-i-oB-i-i-i-i-i-o-o-o P convert-RA Jinja-output

for *P*

by (*unfold-locales*)

interpretation *J-rr*:

sc-scheduler

final-expr sc-red-i-i-i-i-i-Fii-i-oB-Fii-i-i-oB-i-i-i-i-i-o-o-o P convert-RA

J-rr.round-robin P n0 Jinja-output pick-wakeup-via-sel $(\lambda s\ P.\ rm-sel\ s\ (\lambda(k, v).\ P\ k\ v))\ J-rr.round-robin-invar$

UNIV

for *P n0*

unfolding *sc-scheduler-def*

apply (*rule J-rr.round-robin-scheduler*)

apply (*unfold eval-sc-red-i-i-i-i-i-Fii-i-oB-Fii-i-i-oB-i-i-i-i-i-o-o-o*)

apply (*rule sc.red-mthr-deterministic[OF sc-deterministic-heap-ops]*)

apply(*simp add: sc-spurious-wakeups*)
done

9.6.2 Random scheduler

interpretation *J-rnd*:

sc-random-scheduler-base

final-expr sc-red-i-i-i-i-i-i-i-Fii-i-oB-Fii-i-i-oB-i-i-i-i-i-o-o-o P convert-RA Jinja-output
for *P*

.

definition *sc-rnd-J-start-state* :: *Random.seed* \Rightarrow *random-scheduler*

where *sc-rnd-J-start-state seed* = *seed*

definition *exec-J-rnd* ::

Random.seed \Rightarrow *addr J-prog* \Rightarrow *cname* \Rightarrow *mname* \Rightarrow *addr val list* \Rightarrow

(*thread-id* \times (*addr, thread-id*) *obs-event list*,

(*addr, thread-id*) *locks* \times ((*thread-id, (addr expr* \times *addr locals*) \times *addr released-locks*) *rm* \times *heap*)

\times

(*thread-id, addr wait-set-status*) *rm* \times *thread-id rs*) *tllist*

where

exec-J-rnd seed P C M vs = *J-rnd.exec P (sc-rnd-J-start-state seed) (sc-J-start-state-refine P C M vs)*

interpretation *J-rnd*:

sc-random-scheduler

final-expr sc-red-i-i-i-i-i-i-i-Fii-i-oB-Fii-i-i-oB-i-i-i-i-i-o-o-o P convert-RA Jinja-output
for *P*

by(*unfold-locales*)

interpretation *J-rnd*:

sc-scheduler

final-expr sc-red-i-i-i-i-i-i-i-Fii-i-oB-Fii-i-i-oB-i-i-i-i-i-o-o-o P convert-RA

J-rnd.random-scheduler P Jinja-output pick-wakeup-via-sel ($\lambda s P. rm-sel s (\lambda(k,v). P k v)$) $\lambda - .$

True

UNIV

for *P*

unfolding *sc-scheduler-def*

apply(*rule J-rnd.random-scheduler-scheduler*)

apply(*unfold eval-sc-red-i-i-i-i-i-i-i-Fii-i-oB-Fii-i-i-oB-i-i-i-i-i-o-o-o*)

apply(*rule sc.red-mthr-deterministic[OF sc-deterministic-heap-ops]*)

apply(*simp add: sc-spurious-wakeups*)

done

ML-val $\langle @\{\text{code } exec-J-rr\} \rangle$

ML-val $\langle @\{\text{code } exec-J-rnd\} \rangle$

end

9.7 Executable semantics for the JVM

theory *ExternalCall-Execute*

imports

```

../Common/ExternalCall
../Basic/Set-without-equal
begin

```

9.7.1 Translated versions of external calls for the JVM

```

locale heap-execute = addr-base +
  constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
  and thread-id2addr :: 'thread-id ⇒ 'addr
  fixes spurious-wakeups :: bool
  and empty-heap :: 'heap
  and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
  and typeof-addr :: 'heap ⇒ 'addr ⇒ htype option
  and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val set
  and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap set

```

```

sublocale heap-execute < execute: heap-base
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr
  λh a ad v. v ∈ heap-read h a ad λh a ad v h'. h' ∈ heap-write h a ad v
.

```

```

context heap-execute begin

```

```

definition heap-copy-loc :: 'addr ⇒ 'addr ⇒ addr-loc ⇒ 'heap ⇒ (('addr, 'thread-id) obs-event list × 'heap) set

```

```

where [simp]:

```

```

  heap-copy-loc a a' al h = {(obs, h'). execute.heap-copy-loc a a' al h obs h'}

```

```

lemma heap-copy-loc-code:

```

```

  heap-copy-loc a a' al h =
    (do {
      v ← heap-read h a al;
      h' ← heap-write h a' al v;
      {[ReadMem a al v, WriteMem a' al v], h'})
    })

```

```

by(auto simp add: execute.heap-copy-loc.simps)

```

```

definition heap-copies :: 'addr ⇒ 'addr ⇒ addr-loc list ⇒ 'heap ⇒ (('addr, 'thread-id) obs-event list × 'heap) set

```

```

where [simp]: heap-copies a a' al h = {(obs, h'). execute.heap-copies a a' al h obs h'}

```

```

lemma heap-copies-code:

```

```

shows heap-copies-Nil:

```

```

  heap-copies a a' [] h = {([], h)}

```

```

and heap-copies-Cons:

```

```

  heap-copies a a' (al # als) h =

```

```

    (do {
      (ob, h') ← heap-copy-loc a a' al h;
      (obs, h'') ← heap-copies a a' als h';
      {(ob @ obs, h'')}
    })

```

```

by(fastforce elim!: execute.heap-copies-cases intro: execute.heap-copies.intros)+

```

definition *heap-clone* :: 'm prog ⇒ 'heap ⇒ 'addr ⇒ ('heap × (('addr, 'thread-id) obs-event list × 'addr) option) set

where [*simp*]: *heap-clone* P h a = {(h', obsa). *execute.heap-clone* P h a h' obsa}

lemma *heap-clone-code*:

heap-clone P h a =
 (case *typeof-addr* h a of
 | [*Class-type* C] ⇒
 let HA = *allocate* h (*Class-type* C)
 in if HA = {} then {(h, None)} else do {
 (h', a') ← HA;
 FDTs ← *set-of-pred* (*Fields-i-i-o* P C);
 (obs, h'') ← *heap-copies* a a' (map (λ((F, D), Tfm). *CField* D F) FDTs) h';
 {(h'', [(*NewHeapElem* a' (*Class-type* C) # obs, a')])}
 }
 | [*Array-type* T n] ⇒
 let HA = *allocate* h (*Array-type* T n)
 in if HA = {} then {(h, None)} else do {
 (h', a') ← HA;
 FDTs ← *set-of-pred* (*Fields-i-i-o* P *Object*);
 (obs, h'') ← *heap-copies* a a' (map (λ((F, D), Tfm). *CField* D F) FDTs @ map *ACell* [0..*n*])
 h';
 {(h'', [(*NewHeapElem* a' (*Array-type* T n) # obs, a')])}
 }
 | - ⇒ {})
by (*auto* 4 3 *elim!*: *execute.heap-clone.cases split: ty.splits*
prod.split-asm htype.splits intro: execute.heap-clone.intros
simp add: eval-Fields-conv split-beta prod-eq-iff)
 (*auto simp add: eval-Fields-conv Bex-def*)

definition *red-external-aggr* ::

'm prog ⇒ 'thread-id ⇒ 'addr ⇒ *mname* ⇒ 'addr val list ⇒ 'heap ⇒
 (('addr, 'thread-id, 'heap) *external-thread-action* × 'addr *extCallRet* × 'heap) set

where [*simp*]:

red-external-aggr P t a M vs h = *execute.red-external-aggr* P t a M vs h

lemma *red-external-aggr-code*:

red-external-aggr P t a M vs h =
 (if M = *wait* then
 let ad-t = *thread-id2addr* t
 in {(⊥Unlock→a, Lock→a, *IsInterrupted* t True, *ClearInterrupt* t, *ObsInterrupted* t⊥, *execute.RetEXC InterruptedException*, h),
 (⊥Suspend a, Unlock→a, Lock→a, *ReleaseAcquire*→a, *IsInterrupted* t False, *SyncUnlock* a⊥, *RetStaySame*, h),
 (⊥UnlockFail→a⊥, *execute.RetEXC IllegalMonitorState*, h),
 (⊥Notified⊥, *RetVal Unit*, h),
 (⊥WokenUp, *ClearInterrupt* t, *ObsInterrupted* t⊥, *execute.RetEXC InterruptedException*, h)} ∪
 (if *spurious-wakeups* then {(⊥Unlock→a, Lock→a, *ReleaseAcquire*→a, *IsInterrupted* t False, *SyncUnlock* a⊥, *RetVal Unit*, h)} else {})
 else if M = *notify* then
 {(⊥Notify a, Unlock→a, Lock→a⊥, *RetVal Unit*, h),
 (⊥UnlockFail→a⊥, *execute.RetEXC IllegalMonitorState*, h)}
 else if M = *notifyAll* then

```

    {({NotifyAll a, Unlock→a, Lock→a }, RetVal Unit, h),
     ({UnlockFail→a}, execute.RetEXC IllegalMonitorState, h)}
else if M = clone then
  do {
    (h', obsa) ← heap-clone P h a;
    {case obsa of None ⇒ (ε, execute.RetEXC OutOfMemory, h')
     | Some (obs, a') ⇒ ((K$ [], [], [], [], obs), RetVal (Addr a'), h')}
  }
else if M = hashCode then {(ε, RetVal (Intg (word-of-int (hash-addr a))), h)}
else if M = print then {({ExternalCall a M vs Unit}, RetVal Unit, h)}
else if M = currentThread then {(ε, RetVal (Addr (thread-id2addr t)), h)}
else if M = interrupted then
  {({IsInterrupted t True, ClearInterrupt t, ObsInterrupted t}, RetVal (Bool True), h),
   ({IsInterrupted t False}, RetVal (Bool False), h)}
else if M = yield then {({Yield}, RetVal Unit, h)}
else
  let T = ty-of-htype (the (typeof-addr h a))
  in if P ⊢ T ≤ Class Thread then
    let t-a = addr2thread-id a
    in if M = start then
      {({NewThread t-a (the-Class T, run, a) h, ThreadStart t-a}, RetVal Unit, h),
       ({ThreadExists t-a True}, execute.RetEXC IllegalThreadState, h)}
    else if M = join then
      {({Join t-a, IsInterrupted t False, ThreadJoin t-a}, RetVal Unit, h),
       ({IsInterrupted t True, ClearInterrupt t, ObsInterrupted t}, execute.RetEXC InterruptedEx-
ception, h)}
    else if M = interrupt then
      {({ThreadExists t-a True, WakeUp t-a, Interrupt t-a, ObsInterrupt t-a}, RetVal Unit, h),
       ({ThreadExists t-a False}, RetVal Unit, h)}
    else if M = isInterrupted then
      {({IsInterrupted t-a False}, RetVal (Bool False), h),
       ({IsInterrupted t-a True, ObsInterrupted t-a}, RetVal (Bool True), h)}
    else {({}, undefined)}
  else {({}, undefined)}
by (auto simp add: execute.red-external-aggr-def
    split del: option.splits) auto

```

end

```

lemmas [code] =
  heap-execute.heap-copy-loc-code
  heap-execute.heap-copies-code
  heap-execute.heap-clone-code
  heap-execute.red-external-aggr-code

```

end

9.8 An optimized JVM

```

theory JVMExec-Execute2
imports
  ../BV/BVNoTypeError
  ExternalCall-Execute

```

begin

This JVM must lookup the method declaration of the top call frame at every step to find the next instruction. It is more efficient to refine it such that the instruction list and the exception table are cached in the call frame. Even further, this theory adds keeps track of *drop pc ins*, whose head is the next instruction to execute.

```

locale JVM-heap-execute = heap-execute +
  constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
  and thread-id2addr :: 'thread-id ⇒ 'addr
  and spurious-wakeups :: bool
  and empty-heap :: 'heap
  and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
  and typeof-addr :: 'heap ⇒ 'addr ⇒ htype option
  and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val set
  and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap set

```

```

sublocale JVM-heap-execute < execute: JVM-heap-base
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr
  λh a ad v. v ∈ heap-read h a ad λh a ad v h'. h' ∈ heap-write h a ad v
.

```

type-synonym

```

'addr frame' = ('addr instr list × 'addr instr list × ex-table) × 'addr val list × 'addr val list × cname
× mname × pc

```

type-synonym

```

('addr, 'heap) jvm-state' = 'addr option × 'heap × 'addr frame' list

```

type-synonym

```

'addr jvm-thread-state' = 'addr option × 'addr frame' list

```

type-synonym

```

('addr, 'thread-id, 'heap) jvm-thread-action' = ('addr, 'thread-id, 'addr jvm-thread-state', 'heap) Jinja-thread-action

```

type-synonym

```

('addr, 'thread-id, 'heap) jvm-ta-state' = ('addr, 'thread-id, 'heap) jvm-thread-action' × ('addr, 'heap)
jvm-state'

```

```

fun frame'-of-frame :: 'addr jvm-prog ⇒ 'addr frame ⇒ 'addr frame'

```

where

```

frame'-of-frame P (stk, loc, C, M, pc) =
  ((drop pc (instrs-of P C M), instrs-of P C M, ex-table-of P C M), stk, loc, C, M, pc)

```

```

fun jvm-state'-of-jvm-state :: 'addr jvm-prog ⇒ ('addr, 'heap) jvm-state ⇒ ('addr, 'heap) jvm-state'

```

```

where jvm-state'-of-jvm-state P (xcp, h, frs) = (xcp, h, map (frame'-of-frame P) frs)

```

```

fun jvm-thread-state'-of-jvm-thread-state :: 'addr jvm-prog ⇒ 'addr jvm-thread-state ⇒ 'addr jvm-thread-state'

```

where

```

jvm-thread-state'-of-jvm-thread-state P (xcp, frs) = (xcp, map (frame'-of-frame P) frs)

```

```

definition jvm-thread-action'-of-jvm-thread-action ::

```

```

'addr jvm-prog ⇒ ('addr, 'thread-id, 'heap) jvm-thread-action ⇒ ('addr, 'thread-id, 'heap) jvm-thread-action'

```

where

$jvm\text{-thread-action}'\text{-of-}jvm\text{-thread-action } P = \text{convert-extTA } (jvm\text{-thread-state}'\text{-of-}jvm\text{-thread-state } P)$

fun $jvm\text{-ta-state}'\text{-of-}jvm\text{-ta-state} ::$

$'addr\ jvm\text{-prog} \Rightarrow ('addr, 'thread\text{-id}, 'heap)\ jvm\text{-ta-state} \Rightarrow ('addr, 'thread\text{-id}, 'heap)\ jvm\text{-ta-state}'$

where

$jvm\text{-ta-state}'\text{-of-}jvm\text{-ta-state } P (ta, s) = (jvm\text{-thread-action}'\text{-of-}jvm\text{-thread-action } P\ ta, jvm\text{-state}'\text{-of-}jvm\text{-state } P\ s)$

abbreviation $(input)\ frame\text{-of-}frame' :: 'addr\ frame' \Rightarrow 'addr\ frame$

where $frame\text{-of-}frame' \equiv \text{snd}$

definition $jvm\text{-state-of-}jvm\text{-state}' :: ('addr, 'heap)\ jvm\text{-state}' \Rightarrow ('addr, 'heap)\ jvm\text{-state}$

where $[simp]:$

$jvm\text{-state-of-}jvm\text{-state}' = \text{map-prod id } (\text{map-prod id } (\text{map } frame\text{-of-}frame'))$

definition $jvm\text{-thread-state-of-}jvm\text{-thread-state}' :: 'addr\ jvm\text{-thread-state}' \Rightarrow 'addr\ jvm\text{-thread-state}$

where $[simp]:$

$jvm\text{-thread-state-of-}jvm\text{-thread-state}' = \text{map-prod id } (\text{map } frame\text{-of-}frame')$

definition $jvm\text{-thread-action-of-}jvm\text{-thread-action}' ::$

$('addr, 'thread\text{-id}, 'heap)\ jvm\text{-thread-action}' \Rightarrow ('addr, 'thread\text{-id}, 'heap)\ jvm\text{-thread-action}$

where $[simp]:$

$jvm\text{-thread-action-of-}jvm\text{-thread-action}' = \text{convert-extTA } jvm\text{-thread-state-of-}jvm\text{-thread-state}'$

definition $jvm\text{-ta-state-of-}jvm\text{-ta-state}' ::$

$('addr, 'thread\text{-id}, 'heap)\ jvm\text{-ta-state}' \Rightarrow ('addr, 'thread\text{-id}, 'heap)\ jvm\text{-ta-state}$

where $[simp]:$

$jvm\text{-ta-state-of-}jvm\text{-ta-state}' = \text{map-prod } jvm\text{-thread-action-of-}jvm\text{-thread-action}'\ jvm\text{-state-of-}jvm\text{-state}'$

fun $frame'\text{-ok} :: 'addr\ jvm\text{-prog} \Rightarrow 'addr\ frame' \Rightarrow \text{bool}$

where

$frame'\text{-ok } P ((ins', insxt), stk, loc, C, M, pc) \longleftrightarrow$

$ins' = \text{drop } pc\ (\text{instrs-of } P\ C\ M) \wedge insxt = \text{snd } (\text{snd } (\text{the } (\text{snd } (\text{snd } (\text{snd } (\text{method } P\ C\ M))))))$

lemma $frame'\text{-ok-}frame'\text{-of-}frame\ [iff]:$

$frame'\text{-ok } P (frame'\text{-of-}frame\ P\ f)$

by $(cases\ f)\ (simp)$

lemma $frames'\text{-ok-inverse } [simp]:$

$\forall x \in \text{set } frs. frame'\text{-ok } P\ x \implies \text{map } (frame'\text{-of-}frame\ P \circ frame\text{-of-}frame')\ frs = frs$

by $(rule\ map\text{-idI})\ auto$

fun $jvm\text{-state}'\text{-ok} :: 'addr\ jvm\text{-prog} \Rightarrow ('addr, 'heap)\ jvm\text{-state}' \Rightarrow \text{bool}$

where $jvm\text{-state}'\text{-ok } P (xcp, h, frs) = (\forall f \in \text{set } frs. frame'\text{-ok } P\ f)$

lemma $jvm\text{-state}'\text{-ok-}jvm\text{-state}'\text{-of-}jvm\text{-state}\ [iff]:$

$jvm\text{-state}'\text{-ok } P (jvm\text{-state}'\text{-of-}jvm\text{-state } P\ s)$

by $(cases\ s)\ simp$

fun $jvm\text{-thread-state}'\text{-ok} :: 'addr\ jvm\text{-prog} \Rightarrow 'addr\ jvm\text{-thread-state}' \Rightarrow \text{bool}$

where $jvm\text{-thread-state}'\text{-ok } P (xcp, frs) \longleftrightarrow (\forall f \in \text{set } frs. frame'\text{-ok } P\ f)$

lemma $jvm\text{-thread-state}'\text{-ok-}jvm\text{-thread-state}'\text{-of-}jvm\text{-thread-state}\ [iff]:$

jvm-thread-state'-ok P (*jvm-thread-state'-of-jvm-thread-state* P s)
by(cases s) *simp*

definition *jvm-thread-action'-ok* :: 'addr *jvm-prog* \Rightarrow ('addr, 'thread-id, 'heap) *jvm-thread-action'* \Rightarrow bool

where *jvm-thread-action'-ok* P $ta \longleftrightarrow (\forall nt \in \text{set } \{\{ta\}_t. \forall t x h. nt = \text{NewThread } t x h \longrightarrow \text{jvm-thread-state'-ok } P x)$

lemma *jvm-thread-action'-ok-jvm-thread-action'-of-jvm-thread-action* [*iff*]:
jvm-thread-action'-ok P (*jvm-thread-action'-of-jvm-thread-action* P ta)

by(cases ta)(*fastforce dest: sym simp add: jvm-thread-action'-ok-def jvm-thread-action'-of-jvm-thread-action-def*)

lemma *jvm-thread-action'-ok- ε* [*simp*]: *jvm-thread-action'-ok* P ε

by(*simp add: jvm-thread-action'-ok-def*)

fun *jvm-ta-state'-ok* :: 'addr *jvm-prog* \Rightarrow ('addr, 'thread-id, 'heap) *jvm-ta-state'* \Rightarrow bool

where *jvm-ta-state'-ok* P (ta, s) $\longleftrightarrow \text{jvm-thread-action'-ok } P ta \wedge \text{jvm-state'-ok } P s$

lemma *jvm-ta-state'-ok-jvm-ta-state'-of-jvm-ta-state* [*iff*]:

jvm-ta-state'-ok P (*jvm-ta-state'-of-jvm-ta-state* P tas)

by(cases tas)(*simp*)

lemma *frame-of-frame'-inverse* [*simp*]: *frame-of-frame'* \circ *frame'-of-frame* $P = \text{id}$

by(*clarsimp simp add: fun-eq-iff*)

lemma *convert-new-thread-action-frame-of-frame'-inverse* [*simp*]:

convert-new-thread-action (*map-prod id* (*map frame-of-frame'*)) \circ *convert-new-thread-action* (*jvm-thread-state'-of-P*) = *id*

by(*auto intro!: convert-new-thread-action-eqI simp add: fun-eq-iff List.map.id*)

primrec *extRet2JVM'* ::

'addr *instr list* \Rightarrow 'addr *instr list* \Rightarrow *ex-table*

\Rightarrow *nat* \Rightarrow 'heap \Rightarrow 'addr *val list* \Rightarrow 'addr *val list* \Rightarrow *cname* \Rightarrow *mname* \Rightarrow *pc* \Rightarrow 'addr *frame' list*

\Rightarrow 'addr *extCallRet* \Rightarrow ('addr, 'heap) *jvm-state'*

where

extRet2JVM' ins' ins xt n h stk loc C M pc frs (*RetVal* v) = (*None*, h , ((*tl ins'*, *ins*, *xt*), v # *drop* (*Suc n*) *stk*, *loc*, C , M , *pc* + 1) # *frs*)

| *extRet2JVM' ins' ins xt n h stk loc C M pc frs* (*RetExc* a) = ($[a]$, h , ((*ins'*, *ins*, *xt*), *stk*, *loc*, C , M , *pc*) # *frs*)

| *extRet2JVM' ins' ins xt n h stk loc C M pc frs* *RetStaySame* = (*None*, h , ((*ins'*, *ins*, *xt*), *stk*, *loc*, C , M , *pc*) # *frs*)

definition *extNTA2JVM'* :: 'addr *jvm-prog* \Rightarrow (*cname* \times *mname* \times 'addr) \Rightarrow 'addr *jvm-thread-state'*

where *extNTA2JVM'* $P \equiv (\lambda(C, M, a). \text{let } (D, Ts, T, meth) = \text{method } P C M; (mxs, mxl0, ins, xt) = \text{the meth}$

$\text{in } (None, [((ins, ins, xt), [], \text{Addr } a \# \text{replicate } mxl0 \text{ undefined-value}, D, M, 0)]))$

abbreviation *extTA2JVM'* ::

'addr *jvm-prog* \Rightarrow ('addr, 'thread-id, 'heap) *external-thread-action* \Rightarrow ('addr, 'thread-id, 'heap) *jvm-thread-action'*

where *extTA2JVM'* $P \equiv \text{convert-extTA } (extNTA2JVM' P)$

lemma *jvm-state'-ok-extRet2JVM'* [*simp*]:

assumes [*simp*]: *ins* = *instrs-of* $P C M xt = \text{ex-table-of } P C M \forall f \in \text{set frs. frame'-ok } P f$

shows *jvm-state'-ok* P (*extRet2JVM'* (*drop pc ins*) *ins xt n h stk loc C M pc frs va*)
by(*cases va*)(*simp-all add: drop-tl drop-Suc*)

lemma *jvm-state'-of-jvm-state-extRet2JVM* [*simp*]:

assumes [*simp*]: *ins = instrs-of P C M xt = ex-table-of P C M* $\forall f \in \text{set frs. frame'-ok } P f$
shows

jvm-state'-of-jvm-state P (extRet2JVM n h' stk loc C M pc (map frame-of-frame' frs) va) =
extRet2JVM' (drop pc (instrs-of P C M)) ins xt n h' stk loc C M pc frs va

by(*cases va*)(*simp-all add: drop-tl drop-Suc*)

lemma *extRet2JVM'-extRet2JVM* [*simp*]:

jvm-state-of-jvm-state' (extRet2JVM' ins' ins xt n h' stk loc C M pc frs va) =
extRet2JVM n h' stk loc C M pc (map frame-of-frame' frs) va

by(*cases va simp-all*)

lemma *jvm-ta-state'-ok-inverse*:

assumes *jvm-ta-state'-ok P tas*

shows *jvm-ta-state-of-jvm-ta-state' tas* $\in A \longleftrightarrow \text{tas} \in \text{jvm-ta-state'-of-jvm-ta-state } P ' A$

using *assms*

apply(*cases tas*)

apply(*fastforce simp add: o-def jvm-thread-action'-of-jvm-thread-action-def jvm-thread-action'-ok-def*
intro!: map-idI[symmetric] map-idI convert-new-thread-action-eqI dest: bspec intro!: rev-image-eqI elim!:
rev-iffD1[OF - arg-cong[where f= $\lambda x. x : A$]])

done

context *JVM-heap-execute begin*

primrec *exec-instr* ::

'addr instr list \Rightarrow *'addr instr list* \Rightarrow *ex-table*

\Rightarrow *'addr instr* \Rightarrow *'addr jvm-prog* \Rightarrow *'thread-id* \Rightarrow *'heap* \Rightarrow *'addr val list* \Rightarrow *'addr val list*

\Rightarrow *cname* \Rightarrow *mname* \Rightarrow *pc* \Rightarrow *'addr frame' list*

\Rightarrow ((*'addr, 'thread-id, 'heap*) *jvm-ta-state'*) *set*

where

exec-instr ins' ins xt (Load n) P t h stk loc C₀ M₀ pc frs =

$\{(\varepsilon, (\text{None}, h, ((\text{tl } \text{ins}', \text{ins}, \text{xt}), (\text{loc } ! n) \# \text{stk}, \text{loc}, C_0, M_0, \text{pc}+1) \# \text{frs}))\}$

| *exec-instr ins' ins xt (Store n) P t h stk loc C₀ M₀ pc frs =*

$\{(\varepsilon, (\text{None}, h, ((\text{tl } \text{ins}', \text{ins}, \text{xt}), \text{tl } \text{stk}, \text{loc}[n:=\text{hd } \text{stk}], C_0, M_0, \text{pc}+1) \# \text{frs}))\}$

| *exec-instr ins' ins xt (Push v) P t h stk loc C₀ M₀ pc frs =*

$\{(\varepsilon, (\text{None}, h, ((\text{tl } \text{ins}', \text{ins}, \text{xt}), v \# \text{stk}, \text{loc}, C_0, M_0, \text{pc}+1) \# \text{frs}))\}$

| *exec-instr ins' ins xt (New C) P t h stk loc C₀ M₀ pc frs =*

(*let HA = allocate h (Class-type C)*

in if HA = {} then $\{(\varepsilon, [\text{execute.addr-of-sys-xcpt OutOfMemory}], h, ((\text{ins}', \text{ins}, \text{xt}), \text{stk}, \text{loc}, C_0, M_0, \text{pc}) \# \text{frs})\}$

else do $\{ (h', a) \leftarrow HA;$

$\{(\{\text{NewHeapElem } a \text{ (Class-type C)\}, \text{None}, h', ((\text{tl } \text{ins}', \text{ins}, \text{xt}), \text{Addr } a \# \text{stk}, \text{loc}, C_0, M_0, \text{pc} + 1) \# \text{frs})\}\}$

| *exec-instr ins' ins xt (NewArray T) P t h stk loc C₀ M₀ pc frs =*

(*let si = the-Intg (hd stk);*

i = nat (sint si)

in if si < s 0

then $\{(\varepsilon, [\text{execute.addr-of-sys-xcpt NegativeArraySize}], h, ((\text{ins}', \text{ins}, \text{xt}), \text{stk}, \text{loc}, C_0, M_0, \text{pc}) \# \text{frs})\}$

```

else let HA = allocate h (Array-type T i) in
  if HA = {} then {(\varepsilon, [execute.addr-of-sys-xcpt OutOfMemory], h, ((ins', ins, xt), stk, loc, C0,
M0, pc) # frs)}
  else do { (h', a) ← HA;
    {(\NewHeapElem a (Array-type T i) \}, None, h', ((tl ins', ins, xt), Addr a # tl stk, loc,
C0, M0, pc + 1) # frs)}})
| exec-instr ins' ins xt ALoad P t h stk loc C0 M0 pc frs =
  (let va = hd (tl stk)
  in (if va = Null then {(\varepsilon, [execute.addr-of-sys-xcpt NullPointer], h, ((ins', ins, xt), stk, loc, C0,
M0, pc) # frs)}
  else
    let i = the-Intg (hd stk);
        a = the-Addr va;
        len = alen-of-htype (the (typeof-addr h a))
    in if i < s 0 ∨ int len ≤ sint i then
      {(\varepsilon, [execute.addr-of-sys-xcpt ArrayIndexOutOfBounds], h, ((ins', ins, xt), stk, loc, C0,
M0, pc) # frs)}
      else do {
        v ← heap-read h a (ACell (nat (sint i)));
        {(\ReadMem a (ACell (nat (sint i))) v\}, None, h, ((tl ins', ins, xt), v # tl (tl stk), loc,
C0, M0, pc + 1) # frs)}
      })))
| exec-instr ins' ins xt AStore P t h stk loc C0 M0 pc frs =
  (let ve = hd stk;
    vi = hd (tl stk);
    va = hd (tl (tl stk))
  in (if va = Null then {(\varepsilon, [execute.addr-of-sys-xcpt NullPointer], h, ((ins', ins, xt), stk, loc, C0,
M0, pc) # frs)}
  else (let i = the-Intg vi;
        idx = nat (sint i);
        a = the-Addr va;
        hT = the (typeof-addr h a);
        T = ty-of-htype hT;
        len = alen-of-htype hT;
        U = the (execute.typeof-h h ve)
    in (if i < s 0 ∨ int len ≤ sint i then
      {(\varepsilon, [execute.addr-of-sys-xcpt ArrayIndexOutOfBounds], h, ((ins', ins, xt), stk, loc,
C0, M0, pc) # frs)}
      else if P ⊢ U ≤ the-Array T then
        do {
          h' ← heap-write h a (ACell idx) ve;
          {(\WriteMem a (ACell idx) ve\}, None, h', ((tl ins', ins, xt), tl (tl (tl stk)), loc,
C0, M0, pc+1) # frs)}
        }
      else {(\varepsilon, [execute.addr-of-sys-xcpt ArrayStore], h, ((ins', ins, xt), stk, loc, C0, M0, pc)
# frs)}))))))
| exec-instr ins' ins xt ALength P t h stk loc C0 M0 pc frs =
  {(\varepsilon, (let va = hd stk
  in if va = Null
    then ([execute.addr-of-sys-xcpt NullPointer], h, ((ins', ins, xt), stk, loc, C0, M0, pc) # frs)
    else (None, h, ((tl ins', ins, xt), Intg (word-of-int (int (alen-of-htype (the (typeof-addr h
(the-Addr va)))))) # tl stk, loc, C0, M0, pc+1) # frs))}})
| exec-instr ins' ins xt (Getfield F C) P t h stk loc C0 M0 pc frs =
  (let v = hd stk

```

```

in if v = Null then {(\varepsilon, [execute.addr-of-sys-xcpt NullPointer], h, ((ins', ins, xt), stk, loc, C_0, M_0,
pc) # frs)}
  else let a = the-Addr v
    in do {
      v' \leftarrow heap-read h a (CField C F);
      {(\{ReadMem a (CField C F) v'\}, None, h, ((tl ins', ins, xt), v' # (tl stk), loc, C_0, M_0,
pc + 1) # frs)}
    }
| exec-instr ins' ins xt (Putfield F C) P t h stk loc C_0 M_0 pc frs =
  (let v = hd stk;
    r = hd (tl stk)
  in if r = Null then {(\varepsilon, [execute.addr-of-sys-xcpt NullPointer], h, ((ins', ins, xt), stk, loc, C_0, M_0,
pc) # frs)}
  else let a = the-Addr r
    in do {
      h' \leftarrow heap-write h a (CField C F) v;
      {(\{WriteMem a (CField C F) v'\}, None, h', ((tl ins', ins, xt), tl (tl stk), loc, C_0, M_0, pc
+ 1) # frs)}
    }
| exec-instr ins' ins xt (CAS F C) P t h stk loc C_0 M_0 pc frs =
  (let v'' = hd stk; v' = hd (tl stk); v = hd (tl (tl stk))
  in if v = Null then {(\varepsilon, [execute.addr-of-sys-xcpt NullPointer], h, ((ins', ins, xt), stk, loc, C_0, M_0,
pc) # frs)}
  else let a = the-Addr v
    in do {
      v''' \leftarrow heap-read h a (CField C F);
      if v''' = v' then do {
        h' \leftarrow heap-write h a (CField C F) v'';
        {(\{ReadMem a (CField C F) v', WriteMem a (CField C F) v'''\}, None, h', ((tl ins', ins,
xt), Bool True # tl (tl (tl stk)), loc, C_0, M_0, pc + 1) # frs)}
      } else {(\{ReadMem a (CField C F) v'''\}, None, h, ((tl ins', ins, xt), Bool False # tl (tl
(tl stk)), loc, C_0, M_0, pc + 1) # frs)}
    }
| exec-instr ins' ins xt (Checkcast T) P t h stk loc C_0 M_0 pc frs =
  {(\varepsilon, let U = the (typeof_h (hd stk))
  in if P \vdash U \leq T then (None, h, ((tl ins', ins, xt), stk, loc, C_0, M_0, pc + 1) # frs)
  else ([execute.addr-of-sys-xcpt ClassCast], h, ((ins', ins, xt), stk, loc, C_0, M_0, pc) # frs))}
| exec-instr ins' ins xt (Instanceof T) P t h stk loc C_0 M_0 pc frs =
  {(\varepsilon, None, h, ((tl ins', ins, xt), Bool (hd stk \neq Null \wedge P \vdash the (typeof_h (hd stk)) \leq T) # tl stk,
loc, C_0, M_0, pc + 1) # frs)}
| exec-instr ins' ins xt (Invoke M n) P t h stk loc C_0 M_0 pc frs =
  (let r = stk ! n
  in (if r = Null then {(\varepsilon, [execute.addr-of-sys-xcpt NullPointer], h, ((ins', ins, xt), stk, loc, C_0,
M_0, pc) # frs)}
  else (let ps = rev (take n stk);
    a = the-Addr r;
    T = the (typeof-addr h a);
    (D, Ts, T, meth) = method P (class-type-of T) M
  in case meth of
    Native \Rightarrow
      do {
        (ta, va, h') \leftarrow red-external-aggr P t a M ps h;
        {(\{extTA2JVM' P ta, extRet2JVM' ins' ins xt n h' stk loc C_0 M_0 pc frs va\})}
      }
  )

```

```

| [(mxs,mxl0,ins'',xt'')] ⇒
  let f' = ((ins'', ins'', xt''), [], [r]@ps@(replicate mxl0 undefined-value), D, M, 0)
  in {(ε, None, h, f' # ((ins', ins, xt), stk, loc, C0, M0, pc) # frs)}})
| exec-instr ins' ins xt Return P t h stk0 loc0 C0 M0 pc frs =
  {(ε, (if frs=[] then (None, h, []))
  else
    let v = hd stk0;
        ((ins', ins, xt), stk, loc, C, m, pc) = hd frs;
        n = length (fst (snd (method P C0 M0)))
        in (None, h, ((tl ins', ins, xt), v#(drop (n+1) stk), loc, C, m, pc+1)#tl frs))}}
| exec-instr ins' ins xt Pop P t h stk loc C0 M0 pc frs =
  {(ε, (None, h, ((tl ins', ins, xt), tl stk, loc, C0, M0, pc+1)#frs))}
| exec-instr ins' ins xt Dup P t h stk loc C0 M0 pc frs =
  {(ε, (None, h, ((tl ins', ins, xt), hd stk # stk, loc, C0, M0, pc+1)#frs))}
| exec-instr ins' ins xt Swap P t h stk loc C0 M0 pc frs =
  {(ε, (None, h, ((tl ins', ins, xt), hd (tl stk) # hd stk # tl (tl stk), loc, C0, M0, pc+1)#frs))}
| exec-instr ins' ins xt (BinOpInstr bop) P t h stk loc C0 M0 pc frs =
  {(ε,
    case the (execute.binop bop (hd (tl stk)) (hd stk)) of
      Inl v ⇒ (None, h, ((tl ins', ins, xt), v # tl (tl stk), loc, C0, M0, pc + 1) # frs)
      | Inr a ⇒ (Some a, h, ((ins', ins, xt), stk, loc, C0, M0, pc) # frs))}
| exec-instr ins' ins xt (IfFalse i) P t h stk loc C0 M0 pc frs =
  {(ε, (let pc' = if hd stk = Bool False then nat(int pc+i) else pc+1
        in (None, h, ((drop pc' ins, ins, xt), tl stk, loc, C0, M0, pc')#frs))}}
| exec-instr ins' ins xt (Goto i) P t h stk loc C0 M0 pc frs =
  {(let pc' = nat(int pc+i)
    in (ε, (None, h, ((drop pc' ins, ins, xt), stk, loc, C0, M0, pc')#frs))}
| exec-instr ins' ins xt ThrowExc P t h stk loc C0 M0 pc frs =
  {(ε, (let xp' = if hd stk = Null then [execute.addr-of-sys-xcpt NullPointer] else [the-Addr(hd stk)]
        in (xp', h, ((ins', ins, xt), stk, loc, C0, M0, pc)#frs))}}
| exec-instr ins' ins xt MEnter P t h stk loc C0 M0 pc frs =
  {(let v = hd stk
    in if v = Null
      then (ε, [execute.addr-of-sys-xcpt NullPointer], h, ((ins', ins, xt), stk, loc, C0, M0, pc) # frs)
      else ({Lock→the-Addr v, SyncLock (the-Addr v)}, None, h, ((tl ins', ins, xt), tl stk, loc, C0, M0,
pc + 1) # frs))}
| exec-instr ins' ins xt MExit P t h stk loc C0 M0 pc frs =
  (let v = hd stk
  in if v = Null
    then {(ε, [execute.addr-of-sys-xcpt NullPointer], h, ((ins', ins, xt), stk, loc, C0, M0, pc)#frs)}
    else {( {Unlock→the-Addr v, SyncUnlock (the-Addr v)}, None, h, ((tl ins', ins, xt), tl stk, loc, C0,
M0, pc + 1) # frs),
  ( {UnlockFail→the-Addr v}, [execute.addr-of-sys-xcpt IllegalMonitorState], h, ((ins', ins, xt),
stk, loc, C0, M0, pc) # frs)}})

```

fun exception-step :: 'addr jvm-prog ⇒ 'addr ⇒ 'heap ⇒ 'addr frame' ⇒ 'addr frame' list ⇒ ('addr, 'heap) jvm-state'

where

```

exception-step P a h ((ins', ins, xt), stk, loc, C, M, pc) frs =
  (case match-ex-table P (execute.cname-of h a) pc xt of
    None ⇒ ([a], h, frs)
    | Some (pc', d) ⇒ (None, h, ((drop pc' ins, ins, xt), Addr a # drop (size stk - d) stk, loc, C,
M, pc') # frs))

```

fun *exec* :: 'addr jvm-prog \Rightarrow 'thread-id \Rightarrow ('addr, 'heap) jvm-state' \Rightarrow ('addr, 'thread-id, 'heap) jvm-ta-state' set

where

exec *P t* (*xcp*, *h*, []) = {}
| *exec* *P t* (*None*, *h*, ((*ins'*, *ins*, *xt*), *stk*, *loc*, *C*, *M*, *pc*) # *frs*) =
exec-instr ins' ins xt (hd ins') *P t h stk loc C M pc frs*
| *exec* *P t* ([*a*], *h*, *fr* # *frs*) = {(ϵ , *exception-step P a h fr frs*)}

definition *exec-1* ::

'addr jvm-prog \Rightarrow 'thread-id \Rightarrow ('addr, 'heap) jvm-state'
 \Rightarrow (('addr, 'thread-id, 'heap) jvm-thread-action' \times ('addr, 'heap) jvm-state') Predicate.pred
where *exec-1 P t σ* = pred-of-set (*exec P t σ*)

lemma *check-exec-instr-ok*:

assumes *wf*: *wf-prog wf-md P*

and *execute.check-instr i P h stk loc C M pc (map frame-of-frame' frs)*

and *P \vdash C sees M:Ts \rightarrow T = [m] in D*

and *jvm-state'-ok P (None, h, ((ins', ins, xt), stk, loc, C, M, pc) # frs)*

and *tas \in exec-instr ins' ins xt i P t h stk loc C M pc frs*

shows *jvm-ta-state'-ok P tas*

proof –

note [*simp*] = *drop-Suc drop-tl split-beta jvm-thread-action'-ok-def has-method-def*

note [*split*] = *if-split-asm sum.split*

from *assms* **show** ?*thesis*

proof(*cases i*)

case *Return*

thus ?*thesis* **using** *assms* **by**(*cases frs*) *auto*

next

case *Invoke*

thus ?*thesis* **using** *assms*

apply(*cases m*)

apply(*auto simp add: extNTA2JVM'-def dest: sees-method-idemp execute.red-external-aggr-new-thread-sub-thread sub-Thread-sees-run[OF wf]*)

apply(*drule execute.red-external-aggr-new-thread-sub-thread, clarsimp, clarsimp, assumption, clarsimp*)

apply(*drule sub-Thread-sees-run[OF wf], clarsimp*)

apply(*fastforce dest: sees-method-idemp*)

apply(*drule execute.red-external-aggr-new-thread-sub-thread, clarsimp, clarsimp, assumption, clarsimp*)

apply(*drule sub-Thread-sees-run[OF wf], clarsimp*)

apply(*fastforce dest: sees-method-idemp*)

done

next

case *Goto* **thus** ?*thesis* **using** *assms*

by(*cases m*) *simp*

next

case *IfFalse* **thus** ?*thesis* **using** *assms*

by(*cases m*) *simp*

qed(*auto*)

qed

lemma *check-exec-instr-complete*:

assumes *wf*: *wf-prog wf-md P*

and *execute.check-instr i P h stk loc C M pc (map frame-of-frame' frs)*

```

and  $P \vdash C \text{ sees } M:Ts \rightarrow T = \lfloor m \rfloor \text{ in } D$ 
and  $jvm\text{-state}'\text{-ok } P (None, h, ((ins', ins, xt), stk, loc, C, M, pc) \# frs)$ 
and  $tas \in \text{execute.exec-instr } i P t h stk loc C M pc (\text{map frame-of-frame}' frs)$ 
shows  $jvm\text{-ta-state}'\text{-of-jvm-ta-state } P tas \in \text{exec-instr } ins' ins xt i P t h stk loc C M pc frs$ 
proof –
  note  $[simp] =$ 
     $\text{drop-Suc drop-tl split-beta jvm-thread-action}'\text{-ok-def jvm-thread-action}'\text{-of-jvm-thread-action-def}$ 
 $\text{has-method-def}$ 
     $\text{ta-upd-simps map-tl}$ 
  note  $[split] = \text{if-split-asm sum.split}$ 
from  $assms$  show  $?thesis$ 
proof ( $\text{cases } i$ )
  case  $\text{Return}$  thus  $?thesis$  using  $assms$  by ( $\text{cases } frs$ )  $\text{auto}$ 
next
  case  $\text{Goto}$  thus  $?thesis$  using  $assms$ 
  by ( $\text{cases } m$ )  $\text{simp}$ 
next
  case  $\text{IfFalse}$  thus  $?thesis$  using  $assms$ 
  by ( $\text{cases } m$ )  $\text{simp}$ 
next
  case  $\text{Invoke}$  thus  $?thesis$  using  $assms$ 
  apply ( $\text{cases } m$ )
  apply ( $\text{auto intro! : rev-beX convert-new-thread-action-eqI simp add: extNTA2JVM}'\text{-def extNTA2JVM-def}$ 
 $\text{dest: execute.red-external-aggr-new-thread-sub-thread sub-Thread-sees-run[OF wf] sees-method-idemp}$ )
  apply ( $\text{drule execute.red-external-aggr-new-thread-sub-thread, clarsimp, clarsimp, assumption, clarsimp}$ )
  apply ( $\text{drule sub-Thread-sees-run[OF wf], clarsimp}$ )
  apply ( $\text{fastforce dest: sees-method-idemp}$ )
  apply ( $\text{drule execute.red-external-aggr-new-thread-sub-thread, clarsimp, clarsimp, assumption, clarsimp}$ )
  apply ( $\text{drule sub-Thread-sees-run[OF wf], clarsimp}$ )
  apply ( $\text{fastforce dest: sees-method-idemp}$ )
  done
  qed ( $\text{auto 4 4}$ )
qed

```

lemma $\text{check-exec-instr-refine}$:

```

assumes  $wf: wf\text{-prog } wf\text{-md } P$ 
and  $\text{execute.check-instr } i P h stk loc C M pc (\text{map frame-of-frame}' frs)$ 
and  $P \vdash C \text{ sees } M:Ts \rightarrow T = \lfloor m \rfloor \text{ in } D$ 
and  $jvm\text{-state}'\text{-ok } P (None, h, ((ins', ins, xt), stk, loc, C, M, pc) \# frs)$ 
and  $tas \in \text{exec-instr } ins' ins xt i P t h stk loc C M pc frs$ 
shows  $tas \in jvm\text{-ta-state}'\text{-of-jvm-ta-state } P ' \text{execute.exec-instr } i P t h stk loc C M pc (\text{map}$ 
 $\text{frame-of-frame}' frs)$ 
proof –
  note  $[simp] =$ 
     $\text{drop-Suc drop-tl split-beta jvm-thread-action}'\text{-ok-def jvm-thread-action}'\text{-of-jvm-thread-action-def}$ 
 $\text{has-method-def}$ 
     $\text{ta-upd-simps map-tl o-def}$ 
  note  $[split] = \text{if-split-asm sum.split}$ 
from  $assms$  have  $jvm\text{-ta-state}'\text{-of-jvm-ta-state}' tas \in \text{execute.exec-instr } i P t h stk loc C M pc (\text{map}$ 
 $\text{frame-of-frame}' frs)$ 
proof ( $\text{cases } i$ )
  case  $\text{Invoke}$  thus  $?thesis$  using  $assms$ 

```

```

  by(fastforce simp add: extNTA2JVM'-def extNTA2JVM-def split-def extRet2JVM'-extRet2JVM[simplified])
next
  case Return thus ?thesis using assms by(auto simp add: neq-Nil-conv)
qed (auto cong del: image-cong-simp)
also from assms have ok': jvm-ta-state'-ok P tas by(rule check-exec-instr-ok)
hence jvm-ta-state-of-jvm-ta-state' tas ∈ execute.exec-instr i P t h stk loc C M pc (map frame-of-frame'
frs) ⟷
  tas ∈ jvm-ta-state'-of-jvm-ta-state P ' execute.exec-instr i P t h stk loc C M pc (map frame-of-frame'
frs)
  by(rule jvm-ta-state'-ok-inverse)
  finally show ?thesis .
qed

```

lemma *exception-step-ok:*

```

  assumes frame'-ok P fr ∀f∈set frs. frame'-ok P f
  shows jvm-state'-ok P (exception-step P a h fr frs)
  and exception-step P a h fr frs = jvm-state'-of-jvm-state P (execute.exception-step P a h (snd fr)
(map frame-of-frame' frs))
using assms
by(cases fr, case-tac the (snd (snd (snd (method P d e))))), clarsimp)+

```

lemma *exec-step-conv:*

```

  assumes wf-prog wf-md P
  and jvm-state'-ok P s
  and execute.check P (jvm-state-of-jvm-state' s)
  shows exec P t s = jvm-ta-state'-of-jvm-ta-state P ' execute.exec P t (jvm-state-of-jvm-state' s)
using assms
apply(cases s)
apply(rename-tac xcp h frs)
apply(case-tac frs)
  apply(simp)
apply(case-tac xcp)
prefer 2
  apply(simp add: jvm-thread-action'-of-jvm-thread-action-def exception-step-ok)
apply(clarsimp simp add: execute.check-def)
apply(rule equalityI)
  apply(clarsimp simp add: has-method-def)
  apply(erule (2) check-exec-instr-refine)
  apply fastforce
  apply(simp add: hd-drop-conv-nth)
  apply(clarsimp simp add: has-method-def)
  apply(drule (2) check-exec-instr-complete)
  apply fastforce
  apply(assumption)
  apply(simp add: hd-drop-conv-nth)
done

```

lemma *exec-step-ok:*

```

  assumes wf-prog wf-md P
  and jvm-state'-ok P s
  and execute.check P (jvm-state-of-jvm-state' s)
  and tas ∈ exec P t s
  shows jvm-ta-state'-ok P tas
using assms

```

```

apply(cases s)
apply(rename-tac xcp h frs)
apply(case-tac frs)
  apply simp
apply(rename-tac fr frs')
apply(case-tac xcp)
  apply(clarsimp simp add: execute.check-def has-method-def)
  apply(erule (2) check-exec-instr-ok)
  apply fastforce
  apply(simp add: hd-drop-conv-nth)
apply(case-tac fr)
apply(rename-tac cache stk loc C M pc)
apply(case-tac the (snd (snd (snd (method P C M))))))
apply auto
done

end

```

```

locale JVM-heap-execute-conf-read = JVM-heap-execute +
  execute: JVM-conf-read
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr
   $\lambda h a ad v. v \in \text{heap-read } h a ad \ \lambda h a ad v h'. h' \in \text{heap-write } h a ad v$ 
  +
  constrains addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
  and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
  and spurious-wakeups :: bool
  and empty-heap :: 'heap
  and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
  and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  htype option
  and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val set
  and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap set
  and hconf :: 'heap  $\Rightarrow$  bool
  and P :: 'addr jvm-prog
begin

```

```

lemma exec-correct-state:
  assumes wt: wf-jvm-prog $\Phi$  P
  and correct: execute.correct-state  $\Phi$  t (jvm-state-of-jvm-state' s)
  and ok: jvm-state'-ok P s
  shows exec P t s = jvm-ta-state'-of-jvm-ta-state P ' execute.exec P t (jvm-state-of-jvm-state' s)
  (is ?thesis1)
  and (ta, s')  $\in$  exec P t s  $\Longrightarrow$  execute.correct-state  $\Phi$  t (jvm-state-of-jvm-state' s') (is -  $\Longrightarrow$  ?thesis2)
  and tas  $\in$  exec P t s  $\Longrightarrow$  jvm-ta-state'-ok P tas
proof -
  from wt obtain wf-md where wf: wf-prog wf-md P by(blast dest: wt-jvm-progD)
  from execute.no-type-error[OF wt correct]
  have check: execute.check P (jvm-state-of-jvm-state' s)
    by(simp add: execute.exec-d-def split: if-split-asm)
  with wf ok show eq: ?thesis1 by(rule exec-step-conv)

  { fix tas

```



```

assume  $tas \in \text{exec } P \ t \ s$ 
with  $wf \ ok \ check$  show  $jvm\text{-}ta\text{-}state'\text{-}ok \ P \ tas$ 
  by( $rule \ \text{exec}\text{-}step\text{-}ok$ ) }
note  $this[of \ (ta, \ s')]$ 
moreover
assume  $(ta, \ s') \in \text{exec } P \ t \ s$ 
moreover
hence  $(ta, \ s') \in jvm\text{-}ta\text{-}state'\text{-}of\text{-}jvm\text{-}ta\text{-}state \ P \ ' \ \text{execute}.\text{exec } P \ t \ (jvm\text{-}state\text{-}of\text{-}jvm\text{-}state' \ s)$ 
  unfolding  $eq$  by  $simp$ 
ultimately have  $jvm\text{-}ta\text{-}state'\text{-}of\text{-}jvm\text{-}ta\text{-}state' \ (ta, \ s') \in \text{execute}.\text{exec } P \ t \ (jvm\text{-}state\text{-}of\text{-}jvm\text{-}state' \ s)$ 
  using  $jvm\text{-}ta\text{-}state'\text{-}ok\text{-}inverse[of \ P \ (ta, \ s')]$  by  $blast$ 
  hence  $\text{execute}.\text{exec}\text{-}1 \ P \ t \ (jvm\text{-}state\text{-}of\text{-}jvm\text{-}state' \ s) \ (jvm\text{-}thread\text{-}action\text{-}of\text{-}jvm\text{-}thread\text{-}action' \ ta)$ 
 $(jvm\text{-}state\text{-}of\text{-}jvm\text{-}state' \ s')$ 
  by( $simp \ add: \ \text{execute}.\text{exec}\text{-}1\text{-}iff$ )
  with  $wt \ correct$  show  $?thesis2$  by( $rule \ \text{execute}.\text{BV}\text{-}correct\text{-}1$ )
qed

```

end

```

lemmas [ $code$ ] =
   $JVM\text{-}heap\text{-}execute.\text{exec}\text{-}instr.\text{simps}$ 
   $JVM\text{-}heap\text{-}execute.\text{exception}\text{-}step.\text{simps}$ 
   $JVM\text{-}heap\text{-}execute.\text{exec}.\text{simps}$ 
   $JVM\text{-}heap\text{-}execute.\text{exec}\text{-}1\text{-}def$ 

```

end

theory $JVM\text{-}Execute2$

imports

```

   $SC\text{-}Schedulers$ 
   $JVMExec\text{-}Execute2$ 
   $../BV/BVProgressThreaded$ 

```

begin

abbreviation $sc\text{-}heap\text{-}read\text{-}cset :: heap \Rightarrow addr \Rightarrow addr\text{-}loc \Rightarrow addr \text{ val } set$
where $sc\text{-}heap\text{-}read\text{-}cset \ h \ ad \ al \equiv set\text{-}of\text{-}pred \ (sc\text{-}heap\text{-}read\text{-}i\text{-}i\text{-}i\text{-}o \ h \ ad \ al)$

abbreviation $sc\text{-}heap\text{-}write\text{-}cset :: heap \Rightarrow addr \Rightarrow addr\text{-}loc \Rightarrow addr \text{ val } \Rightarrow heap \text{ set}$
where $sc\text{-}heap\text{-}write\text{-}cset \ h \ ad \ al \ v \equiv set\text{-}of\text{-}pred \ (sc\text{-}heap\text{-}write\text{-}i\text{-}i\text{-}i\text{-}i\text{-}o \ h \ ad \ al \ v)$

interpretation sc :

```

   $JVM\text{-}heap\text{-}execute$ 
   $addr2thread\text{-}id$ 
   $thread\text{-}id2addr$ 
   $sc\text{-}spurious\text{-}wakeups$ 
   $sc\text{-}empty$ 
   $sc\text{-}allocate \ P$ 
   $sc\text{-}typeof\text{-}addr$ 
   $sc\text{-}heap\text{-}read\text{-}cset$ 
   $sc\text{-}heap\text{-}write\text{-}cset$ 

```

rewrites $\bigwedge h \ ad \ al \ v. \ v \in sc\text{-}heap\text{-}read\text{-}cset \ h \ ad \ al \equiv sc\text{-}heap\text{-}read \ h \ ad \ al \ v$
and $\bigwedge h \ ad \ al \ v \ h'. \ h' \in sc\text{-}heap\text{-}write\text{-}cset \ h \ ad \ al \ v \equiv sc\text{-}heap\text{-}write \ h \ ad \ al \ v \ h'$
for P

apply(*simp-all* *addr*: *eval-sc-heap-read-i-i-i-o* *eval-sc-heap-write-i-i-i-o*)
done

interpretation *sc*:

JVM-heap-execute-conf-read
addr2thread-id
thread-id2addr
sc-spurious-wakeups
sc-empty
sc-allocate *P*
sc-typeof-addr
sc-heap-read-cset
sc-heap-write-cset
sc-hconf *P*
P

rewrites $\bigwedge h \text{ ad al } v. v \in \text{sc-heap-read-cset } h \text{ ad al} \equiv \text{sc-heap-read } h \text{ ad al } v$
and $\bigwedge h \text{ ad al } v \text{ h}'. h' \in \text{sc-heap-write-cset } h \text{ ad al } v \equiv \text{sc-heap-write } h \text{ ad al } v \text{ h}'$
for *P*

proof –

show *unfolds*: $\bigwedge h \text{ ad al } v. v \in \text{sc-heap-read-cset } h \text{ ad al} \equiv \text{sc-heap-read } h \text{ ad al } v$
 $\bigwedge h \text{ ad al } v \text{ h}'. h' \in \text{sc-heap-write-cset } h \text{ ad al } v \equiv \text{sc-heap-write } h \text{ ad al } v \text{ h}'$
by(*simp-all* *addr*: *eval-sc-heap-read-i-i-i-o* *eval-sc-heap-write-i-i-i-o*)

show *JVM-heap-execute-conf-read*
addr2thread-id *thread-id2addr*
sc-empty (*sc-allocate* *P*)
sc-typeof-addr *sc-heap-read-cset* *sc-heap-write-cset*
(*sc-hconf* *P*) *P*
apply(*rule* *JVM-heap-execute-conf-read.intro*)
apply(*unfold* *unfolds*)
apply(*unfold-locales*)
done

qed

abbreviation *sc-JVM-start-state* :: *addr jvm-prog* \Rightarrow *cname* \Rightarrow *mname* \Rightarrow *addr val list* \Rightarrow (*addr,thread-id,addr*
jvm-thread-state,heap,addr) *state*

where *sc-JVM-start-state* *P* \equiv *sc.execute.JVM-start-state* *TYPE(addr jvm-method)* *P P*

abbreviation *sc-exec* :: *addr jvm-prog* \Rightarrow *thread-id* \Rightarrow (*addr, heap*) *jvm-state'* \Rightarrow (*addr, thread-id,*
heap) *jvm-ta-state' set*

where *sc-exec* *P* \equiv *sc.exec* *TYPE(addr jvm-method)* *P P*

abbreviation *sc-execute-mexec* :: *addr jvm-prog* \Rightarrow *thread-id* \Rightarrow (*addr jvm-thread-state* \times *heap*)
 \Rightarrow (*addr, thread-id, heap*) *jvm-thread-action* \Rightarrow (*addr jvm-thread-state* \times *heap*) \Rightarrow *bool*

where *sc-execute-mexec* *P* \equiv *sc.execute.mexec* *TYPE(addr jvm-method)* *P P*

fun *sc-mexec* ::

addr jvm-prog \Rightarrow *thread-id* \Rightarrow (*addr jvm-thread-state'* \times *heap*)
 \Rightarrow ((*addr, thread-id, heap*) *jvm-thread-action'* \times *addr jvm-thread-state'* \times *heap*) *Predicate.pred*

where

sc-mexec *P t* ((*xcp, frs*), *h*) =
sc.exec-1 (*TYPE(addr jvm-method)*) *P P t* (*xcp, h, frs*) \ggg (λ (*ta, xcp, h, frs*). *Predicate.single* (*ta,*
(*xcp, frs*), *h*))

abbreviation *sc-jvm-start-state-refine* ::

$addr\ jvm\ prog \Rightarrow cname \Rightarrow mname \Rightarrow addr\ val\ list \Rightarrow$
 $(addr, thread-id, heap, (thread-id, (addr\ jvm\ thread\ state') \times addr\ released\ locks)\ rbt, (thread-id, addr\ wait\ set\ status)\ rbt, thread-id\ rs)\ state\ refine$

where

$sc\ jvm\ start\ state\ refine \equiv$
 $sc\ start\ state\ refine\ (rm\ empty\ ())\ rm\ update\ (rm\ empty\ ())\ (rs\ empty\ ())\ (\lambda C\ M\ Ts\ T\ (m\ xs, m\ xl0,$
 $ins, xt)\ vs.\ (None, [((ins, ins, xt), [], Null\ \# vs\ @\ replicate\ m\ xl0\ undefined\ value, C, M, 0)]))$

fun $jvm\ mstate\ of\ jvm\ mstate' ::$

$(addr, thread-id, addr\ jvm\ thread\ state', heap, addr)\ state \Rightarrow (addr, thread-id, addr\ jvm\ thread\ state', heap, addr)$
 $state$

where

$jvm\ mstate\ of\ jvm\ mstate'\ (ls, (ts, m), ws) = (ls, (\lambda t.\ map\ option\ (map\ prod\ jvm\ thread\ state\ of\ jvm\ thread\ state'\ id)\ (ts\ t), m), ws)$

definition $sc\ jvm\ state\ invar :: addr\ jvm\ prog \Rightarrow ty_P \Rightarrow (addr, thread-id, addr\ jvm\ thread\ state', heap, addr)$
 $state\ set$

where

$sc\ jvm\ state\ invar\ P\ \Phi \equiv$
 $\{s.\ jvm\ mstate\ of\ jvm\ mstate'\ s \in sc.\ execute.\ correct\ jvm\ state\ P\ \Phi\} \cap$
 $\{s.\ ts\ ok\ (\lambda t\ (xcp, frs)\ h.\ jvm\ state'\ ok\ P\ (xcp, h, frs))\ (thr\ s)\ (shr\ s)\}$

fun $JVM\ final' :: 'addr\ jvm\ thread\ state' \Rightarrow bool$

where $JVM\ final'\ (xcp, frs) \longleftrightarrow frs = []$

lemma $shr\ jvm\ mstate\ of\ jvm\ mstate'\ [simp]: shr\ (jvm\ mstate\ of\ jvm\ mstate'\ s) = shr\ s$
by $(cases\ s)\ clarsimp$

lemma $jvm\ mstate\ of\ jvm\ mstate'\ sc\ start\ state\ [simp]:$

$jvm\ mstate\ of\ jvm\ mstate'\$
 $(sc\ start\ state\ (\lambda C\ M\ Ts\ T\ (m\ xs, m\ xl0, ins, xt)\ vs.\ (None, [((ins, ins, xt), [], Null\ \# vs\ @\ replicate\ m\ xl0\ undefined\ value, C, M, 0)]))\ P\ C\ M\ vs) = sc\ JVM\ start\ state\ P\ C\ M\ vs$
by $(simp\ add:\ sc.\ start\ state\ def\ split\ beta\ fun\ eq\ iff)$

lemma $sc\ jvm\ start\ state\ invar:$

assumes $wf\ jvm\ prog_{\Phi}\ P$
and $sc\ wf\ start\ state\ P\ C\ M\ vs$
shows $sc\ state\ \alpha\ (sc\ jvm\ start\ state\ refine\ P\ C\ M\ vs) \in sc\ jvm\ state\ invar\ P\ \Phi$

unfolding $sc\ jvm\ state\ invar\ def\ Int\ iff\ mem\ Collect\ eq$

apply $(rule\ conjI)$

apply $(simp\ add:\ sc.\ execute.\ correct\ jvm\ state\ initial\ [OF\ assms])$

apply $(rule\ ts\ okI)$

using $\langle sc\ wf\ start\ state\ P\ C\ M\ vs \rangle$

apply $(auto\ simp\ add:\ sc.\ start\ state\ def\ split\ beta\ sc\ wf\ start\ state\ iff\ split:\ if\ split\ asm\ dest:\ sees\ method\ idemp)$

done

lemma $invariant3p\ sc\ jvm\ state\ invar:$

assumes $wf\ jvm\ prog_{\Phi}\ P$
shows $invariant3p\ (multithreaded\ base.\ redT\ JVM\ final'\ (\lambda t\ xm\ ta\ x'm'.\ Predicate.\ eval\ (sc\ mexec\ P\ t\ xm)\ (ta, x'm'))\ convert\ RA)\ (sc\ jvm\ state\ invar\ P\ \Phi)$

proof $(rule\ invariant3pI)$

fix $s\ tl\ s'$

assume $red:\ multithreaded\ base.\ redT\ JVM\ final'\ (\lambda t\ xm\ ta\ x'm'.\ Predicate.\ eval\ (sc\ mexec\ P\ t\ xm)\ (ta, x'm'))\ convert\ RA\ s\ tl\ s'$

```

and invar:  $s \in \text{sc-jvm-state-invar } P \ \Phi$ 
obtain  $t \ ta$  where  $tl: tl = (t, ta)$  by(cases  $tl$ )
from red have  $red': \text{multithreaded-base.redT JVM-final } (sc\text{-execute-mexec } P) \text{ convert-RA } (jvm\text{-mstate-of-jvm-mstate}$ 
 $s) (t, jvm\text{-thread-action-of-jvm-thread-action}' ta) (jvm\text{-mstate-of-jvm-mstate}' s')$ 
proof(cases  $rule: \text{multithreaded-base.redT.cases}[\text{consumes } 1, \text{case-names normal acquire}]$ )
  case (acquire  $s \ t \ x \ ln \ n \ s'$ )
  thus ?thesis using  $tl$  by(cases  $s$ )(auto intro!:  $\text{multithreaded-base.redT.redT-acquire}$ )
next
  case (normal  $t \ x \ s \ ta \ x' \ m' \ s'$ )
  obtain  $xcp \ frs$  where  $x: x = (xcp, frs)$  by(cases  $x$ )
  with invar  $normal \ tl$ 
  have correct:  $sc.execute.correct\text{-state } P \ \Phi \ t (jvm\text{-state-of-jvm-state}' (xcp, shr \ s, frs))$ 
  and ok:  $jvm\text{-state}'\text{-ok } P (xcp, shr \ s, frs)$ 
  apply –
  apply(case-tac  $[\!]$   $s$ )
  apply(fastforce simp add:  $sc\text{-jvm-state-invar-def } sc.execute.correct\text{-jvm-state-def } dest: ts\text{-ok}D$ ) +
  done
  note  $eq = sc.execute.correct\text{-state}(1)[OF \text{ assms this}]$ 
  with  $normal \ x \ tl$ 
  have  $sc\text{-execute-mexec } P \ t (jvm\text{-thread-state-of-jvm-thread-state}' x, shr (jvm\text{-mstate-of-jvm-mstate}'$ 
 $s)) (jvm\text{-thread-action-of-jvm-thread-action}' ta) (jvm\text{-thread-state-of-jvm-thread-state}' x', m')$ 
  by(auto simp add:  $sc.execute\text{-1-def } eq \ jvm\text{-thread-action}'\text{-of-jvm-thread-action-def } sc.execute.execute\text{-1-iff}$ )
  with  $normal \ tl$  show ?thesis
  by(cases  $s$ )(fastforce intro!:  $\text{multithreaded-base.redT.redT-normal simp add: final-thread.actions-ok-iff}$ 
 $\text{fun-eq-iff map-redT-updTs elim: rev-iff}D1[OF - \text{thread-oks-ts-change}] \text{ cond-action-oks-final-change}$ )
  qed
  moreover from invar
  have  $sc.execute.correct\text{-state-ts } P \ \Phi (thr (jvm\text{-mstate-of-jvm-mstate}' s)) (shr (jvm\text{-mstate-of-jvm-mstate}'$ 
 $s))$ 
  and lock-thread-ok ( $locks (jvm\text{-mstate-of-jvm-mstate}' s) (thr (jvm\text{-mstate-of-jvm-mstate}' s))$ )
  by(simp-all add:  $sc\text{-jvm-state-invar-def } sc.execute.correct\text{-jvm-state-def}$ )
  ultimately have  $sc.execute.correct\text{-state-ts } P \ \Phi (thr (jvm\text{-mstate-of-jvm-mstate}' s')) (shr (jvm\text{-mstate-of-jvm-mstate}'$ 
 $s'))$ 
  and lock-thread-ok ( $locks (jvm\text{-mstate-of-jvm-mstate}' s') (thr (jvm\text{-mstate-of-jvm-mstate}' s'))$ )
  by(blast intro:  $\text{lifting-wf.redT-preserves}[OF \text{ sc.execute.lifting-wf-correct-state, } OF \text{ assms}] \text{ sc.execute.execute-mthr.red}$ )
  hence  $jvm\text{-mstate-of-jvm-mstate}' s' \in sc.execute.correct\text{-jvm-state } P \ \Phi$ 
  by(simp add:  $sc.execute.correct\text{-jvm-state-def}$ )
  moreover from red have  $ts\text{-ok } (\lambda t (xcp, frs) h. \forall f \in \text{set } frs. \text{frame}'\text{-ok } P \ f) (thr \ s') (shr \ s')$  unfolding
 $tl$ 
  proof(cases  $rule: \text{multithreaded-base.redT.cases}[\text{consumes } 1, \text{case-names normal acquire}]$ )
  case acquire thus ?thesis using invar
  by(fastforce simp add:  $sc\text{-jvm-state-invar-def } intro!: ts\text{-ok}I \ dest: ts\text{-ok}D \ \text{bspec split: if-split-asm}$ )
  next
  case (normal  $t \ x \ s \ ta \ x' \ m' \ s'$ )
  obtain  $xcp \ frs$  where  $x: x = (xcp, frs)$  by(cases  $x$ )
  with invar  $normal \ tl$ 
  have correct:  $sc.execute.correct\text{-state } P \ \Phi \ t (jvm\text{-state-of-jvm-state}' (xcp, shr \ s, frs))$ 
  and ok:  $jvm\text{-state}'\text{-ok } P (xcp, shr \ s, frs)$ 
  apply –
  apply(case-tac  $[\!]$   $s$ )
  apply(fastforce simp add:  $sc\text{-jvm-state-invar-def } sc.execute.correct\text{-jvm-state-def } dest: ts\text{-ok}D$ ) +
  done
  from  $normal \ x \ \text{invar}$  show ?thesis
  apply(auto simp add:  $sc.execute\text{-1-def } final\text{-thread.actions-ok-iff } jvm\text{-thread-action}'\text{-ok-def } sc\text{-jvm-state-invar-def}$ )

```

```

apply hypsubst-thin
apply(drule sc.exec-correct-state(3)[OF assms correct ok])
apply(rule ts-okI)
apply(clarsimp split: if-split-asm simp add: jvm-thread-action'-ok-def)
apply(drule (1) bspec)
apply simp
apply(case-tac thr s t)
apply(drule (2) redT-updTs-new-thread)
apply clarsimp
apply(drule (1) bspec)
apply simp
apply(drule (1) bspec)
apply simp
apply(erule thin-rl)
apply(frule (1) redT-updTs-Some)
apply(fastforce dest: ts-okD)
done
qed
ultimately show  $s' \in \text{sc-jvm-state-invar } P \ \Phi$  by(simp add: sc-jvm-state-invar-def)
qed

lemma sc-exec-deterministic:
  assumes wf-jvm-prog $\Phi$  P
  shows multithreaded-base.deterministic JVM-final' ( $\lambda t \ x m \ ta \ x' m'. \text{Predicate.eval } (sc-mexec \ P \ t \ x m)$ 
( $ta, x' m'$ )) convert-RA
  (sc-jvm-state-invar P  $\Phi$ )
proof –
  from assms sc-deterministic-heap-ops
  have det: multithreaded-base.deterministic JVM-final' (sc-execute-mexec P) convert-RA {s. sc.execute.correct-state-ts
P  $\Phi$  (thr s) (shr s)}
  by(rule sc.execute.mexec-deterministic)(simp add: sc-spurious-wakeups)
  show ?thesis
proof(rule multithreaded-base.deterministicI)
  fix s t x ta' x' m' ta'' x'' m''
  assume inv: s  $\in$  sc-jvm-state-invar P  $\Phi$ 
  and tst: thr s t =  $\lfloor(x, \text{no-wait-locks})\rfloor$ 
  and exec1: Predicate.eval (sc-mexec P t (x, shr s)) (ta', x', m')
  and exec2: Predicate.eval (sc-mexec P t (x, shr s)) (ta'', x'', m'')
  and aok1: final-thread.actions-ok JVM-final' s t ta'
  and aok2: final-thread.actions-ok JVM-final' s t ta''
  obtain xcp frs where  $x = (xcp, frs)$  by(cases x)
  from inv tst x have correct: sc.execute.correct-state P  $\Phi$  t (jvm-state-of-jvm-state' (xcp, shr s, frs))
  and ok: jvm-state'-ok P (xcp, shr s, frs)
  by(cases s, fastforce simp add: sc-jvm-state-invar-def sc.execute.correct-jvm-state-def dest: ts-okD)+
  note eq = sc.exec-correct-state(1)[OF assms this]

  from exec1 exec2 x
  have sc-execute-mexec P t (jvm-thread-state-of-jvm-thread-state' x, shr (jvm-mstate-of-jvm-mstate'
s)) (jvm-thread-action-of-jvm-thread-action' ta') (jvm-thread-state-of-jvm-thread-state' x', m')
  and sc-execute-mexec P t (jvm-thread-state-of-jvm-thread-state' x, shr (jvm-mstate-of-jvm-mstate'
s)) (jvm-thread-action-of-jvm-thread-action' ta'') (jvm-thread-state-of-jvm-thread-state' x'', m'')
  by(auto simp add: sc.exec-1-def eq jvm-thread-action'-of-jvm-thread-action-def sc.execute.exec-1-iff)
  moreover have thr (jvm-mstate-of-jvm-mstate' s) t =  $\lfloor(jvm-thread-state-of-jvm-thread-state' \ x,$ 
no-wait-locks)

```

```

    using tst by(cases s) clarsimp
  moreover have final-thread.actions-ok JVM-final (jvm-mstate-of-jvm-mstate' s) t (jvm-thread-action-of-jvm-thr
ta')
  and final-thread.actions-ok JVM-final (jvm-mstate-of-jvm-mstate' s) t (jvm-thread-action-of-jvm-thread-action'
ta'')
    using aok1 aok2
    by  $-(\text{case-tac } [!]\ s, \text{auto simp add: } \text{final-thread.actions-ok-iff elim: rev-iffD1} [OF\ \text{thread-oks-ts-change}]$ 
cond-action-oks-final-change)
  moreover have sc.execute.correct-state-ts P  $\Phi$  (thr (jvm-mstate-of-jvm-mstate' s)) (shr (jvm-mstate-of-jvm-mst
s))
    using inv
    by(cases s)(auto intro!: ts-okI simp add: sc-jvm-state-invar-def sc.execute.correct-jvm-state-def
dest: ts-okD)
  ultimately
  have jvm-thread-action-of-jvm-thread-action' ta' = jvm-thread-action-of-jvm-thread-action' ta''  $\wedge$ 
jvm-thread-state-of-jvm-thread-state' x' = jvm-thread-state-of-jvm-thread-state' x''  $\wedge$ 
m' = m''
    by  $-(\text{drule } (4)\ \text{multithreaded-base.deterministicD} [OF\ \text{det}],\ \text{simp-all})$ 
  moreover from exec1 exec2 x
  have (ta', (fst x', m', snd x'))  $\in$  sc-exec P t (xcp, shr s, frs)
  and (ta'', (fst x'', m'', snd x''))  $\in$  sc-exec P t (xcp, shr s, frs)
  by(auto simp add: sc-exec-1-def)
  hence jvm-ta-state'-ok P (ta', (fst x', m', snd x'))
  and jvm-ta-state'-ok P (ta'', (fst x'', m'', snd x''))
  by(blast intro: sc.exec-correct-state [OF assms correct ok])+
  hence ta' = jvm-thread-action'-of-jvm-thread-action P (jvm-thread-action-of-jvm-thread-action' ta')
  and ta'' = jvm-thread-action'-of-jvm-thread-action P (jvm-thread-action-of-jvm-thread-action'
ta'')
  and x' = jvm-thread-state'-of-jvm-thread-state P (jvm-thread-state-of-jvm-thread-state' x')
  and x'' = jvm-thread-state'-of-jvm-thread-state P (jvm-thread-state-of-jvm-thread-state' x'')
  apply  $-$ 
  apply(case-tac [!] ta')
  apply(case-tac [!] ta'')
  apply(case-tac [!] x')
  apply(case-tac [!] x'')
  apply(fastforce simp add: jvm-thread-action'-of-jvm-thread-action-def jvm-thread-action'-ok-def
intro!: map-idI [symmetric] convert-new-thread-action-eqI dest: bspec)+
  done
  ultimately
  show ta' = ta''  $\wedge$  x' = x''  $\wedge$  m' = m'' by simp
  qed(rule invariant3p-sc-jvm-state-invar [OF assms])
qed

```

9.8.1 Round-robin scheduler

interpretation *JVM-rr*:

sc-round-robin-base

JVM-final' sc-mexec P convert-RA Jinja-output

for *P*

.

definition *sc-rr-JVM-start-state* :: *nat* \Rightarrow *'m prog* \Rightarrow *thread-id fifo round-robin*

where *sc-rr-JVM-start-state n0 P = JVM-rr.round-robin-start n0 (sc-start-tid P)*

definition *exec-JVM-rr* ::

nat \Rightarrow *addr jvm-prog* \Rightarrow *cname* \Rightarrow *mname* \Rightarrow *addr val list* \Rightarrow
(thread-id \times (*addr, thread-id*) *obs-event list*,
(addr, thread-id) locks \times (*(thread-id, addr jvm-thread-state'* \times *addr released-locks)* *RBT.rbt* \times *heap*)
 \times
(thread-id, addr wait-set-status) *RBT.rbt* \times *thread-id rs*) *tllist*

where

exec-JVM-rr n0 P C M vs = *JVM-rr.exec P n0 (sc-rr-JVM-start-state n0 P) (sc-jvm-start-state-refine P C M vs)*

interpretation *JVM-rr*:

sc-round-robin
JVM-final' sc-mexec P convert-RA Jinja-output
for *P*
by(*unfold-locales*)

lemma *JVM-rr*:

assumes *wf-jvm-prog Φ P*
shows
sc-scheduler
JVM-final' (sc-mexec P) convert-RA
(JVM-rr.round-robin P n0) (pick-wakeup-via-sel ($\lambda s P. rm-sel s (\lambda(k,v). P k v)$)) JVM-rr.round-robin-invar
(sc-jvm-state-invar P Φ)

unfolding *sc-scheduler-def*

apply(*rule JVM-rr.round-robin-scheduler*)
apply(*rule sc-exec-deterministic[OF assms]*)
done

9.8.2 Random scheduler

interpretation *JVM-rnd*:

sc-random-scheduler-base
JVM-final' sc-mexec P convert-RA Jinja-output
for *P*
.

definition *sc-rnd-JVM-start-state* :: *Random.seed* \Rightarrow *random-scheduler*

where *sc-rnd-JVM-start-state seed* = *seed*

definition *exec-JVM-rnd* ::

Random.seed \Rightarrow *addr jvm-prog* \Rightarrow *cname* \Rightarrow *mname* \Rightarrow *addr val list* \Rightarrow
(thread-id \times (*addr, thread-id*) *obs-event list*,
(addr, thread-id) locks \times (*(thread-id, addr jvm-thread-state'* \times *addr released-locks)* *RBT.rbt* \times *heap*)
 \times
(thread-id, addr wait-set-status) *RBT.rbt* \times *thread-id rs*) *tllist*

where *exec-JVM-rnd seed P C M vs* = *JVM-rnd.exec P (sc-rnd-JVM-start-state seed) (sc-jvm-start-state-refine P C M vs)*

interpretation *JVM-rnd*:

sc-random-scheduler
JVM-final' sc-mexec P convert-RA Jinja-output
for *P*
by(*unfold-locales*)

```

lemma JVM-rnd:
  assumes wf-jvm-prog  $\Phi$  P
  shows
    sc-scheduler
      JVM-final' (sc-mexec P) convert-RA
      (JVM-rnd.random-scheduler P) (pick-wakeup-via-sel ( $\lambda s P. rm-sel s (\lambda(k,v). P k v)$ )) ( $\lambda - . True$ )
      (sc-jvm-state-invar P  $\Phi$ )
  unfolding sc-scheduler-def
  apply(rule JVM-rnd.random-scheduler-scheduler)
  apply(rule sc-exec-deterministic[OF assms])
  done

ML-val  $\langle @\{code\} exec-JVM-rr \rangle$ 

ML-val  $\langle @\{code\} exec-JVM-rnd \rangle$ 

end

```

9.9 Code generator setup

```

theory Code-Generation
imports
  J-Execute
  JVM-Execute2
  ../Compiler/Preprocessor
  ../BV/BCVExec
  ../Compiler/Compiler
  Coinductive.Lazy-TLList
  HOL-Library.Code-Cardinality
  HOL-Library.Code-Target-Int
  HOL-Library.Code-Target-Numeral
begin

  Avoid module dependency cycles.

code-identifier
  code-module More-Set  $\rightarrow$  (SML) Set
| code-module Set  $\rightarrow$  (SML) Set
| code-module Complete-Lattices  $\rightarrow$  (SML) Set
| code-module Complete-Partial-Order  $\rightarrow$  (SML) Set

  new code equation for insort-insert-key to avoid module dependency cycle with set.

lemma insort-insert-key-code [code]:
  insort-insert-key f x xs =
    (if List.member (map f xs) (f x) then xs else insort-key f x xs)
by(simp add: insort-insert-key-def List.member-def split del: if-split)

  equations on predicate operations for code inlining

lemma eq-i-o-conv-single: eq-i-o = Predicate.single
by(rule ext)(simp add: Predicate.single-bind eq.equation)

lemma eq-o-i-conv-single: eq-o-i = Predicate.single
by(rule ext)(simp add: Predicate.single-bind eq.equation)

```


lemma *sup-case-exp-case-exp-same*:

```

sup-class.sup
(case-exp cNew cNewArray cCast cInstanceOf cVal cBinOp cVar cLAss cAAcc cAAss cALen cFAcc
cFAss cCAS cCall cBlock cSync cInSync cSeq cCond cWhile cThrow cTry e)
(case-exp cNew' cNewArray' cCast' cInstanceOf' cVal' cBinOp' cVar' cLAss' cAAcc' cAAss'
cALen' cFAcc' cFAss' cCAS' cCall' cBlock' cSync' cInSync' cSeq' cCond' cWhile' cThrow' cTry' e)
=
(case e of
  new C ⇒ sup-class.sup (cNew C) (cNew' C)
| newArray T e ⇒ sup-class.sup (cNewArray T e) (cNewArray' T e)
| Cast T e ⇒ sup-class.sup (cCast T e) (cCast' T e)
| InstanceOf e T ⇒ sup-class.sup (cInstanceOf e T) (cInstanceOf' e T)
| Val v ⇒ sup-class.sup (cVal v) (cVal' v)
| BinOp e bop e' ⇒ sup-class.sup (cBinOp e bop e') (cBinOp' e bop e')
| Var V ⇒ sup-class.sup (cVar V) (cVar' V)
| LAss V e ⇒ sup-class.sup (cLAss V e) (cLAss' V e)
| AAcc a e ⇒ sup-class.sup (cAAcc a e) (cAAcc' a e)
| AAss a i e ⇒ sup-class.sup (cAAss a i e) (cAAss' a i e)
| ALen a ⇒ sup-class.sup (cALen a) (cALen' a)
| FAcc e F D ⇒ sup-class.sup (cFAcc e F D) (cFAcc' e F D)
| FAss e F D e' ⇒ sup-class.sup (cFAss e F D e') (cFAss' e F D e')
| CompareAndSwap e D F e' e'' ⇒ sup-class.sup (cCAS e D F e' e'') (cCAS' e D F e' e'')
| Call e M es ⇒ sup-class.sup (cCall e M es) (cCall' e M es)
| Block V T vo e ⇒ sup-class.sup (cBlock V T vo e) (cBlock' V T vo e)
| Synchronized v e e' ⇒ sup-class.sup (cSync v e e') (cSync' v e e')
| InSynchronized v a e ⇒ sup-class.sup (cInSync v a e) (cInSync' v a e)
| Seq e e' ⇒ sup-class.sup (cSeq e e') (cSeq' e e')
| Cond b e e' ⇒ sup-class.sup (cCond b e e') (cCond' b e e')
| While b e ⇒ sup-class.sup (cWhile b e) (cWhile' b e)
| throw e ⇒ sup-class.sup (cThrow e) (cThrow' e)
| TryCatch e C V e' ⇒ sup-class.sup (cTry e C V e') (cTry' e C V e'))
apply(cases e)
apply(simp-all)
done

```

lemma *sup-case-exp-case-exp-other*:

```

fixes p :: 'a :: semilattice-sup shows
sup-class.sup
(case-exp cNew cNewArray cCast cInstanceOf cVal cBinOp cVar cLAss cAAcc cAAss cALen cFAcc
cFAss cCAS cCall cBlock cSync cInSync cSeq cCond cWhile cThrow cTry e)
(sup-class.sup (case-exp cNew' cNewArray' cCast' cInstanceOf' cVal' cBinOp' cVar' cLAss' cAAcc'
cAAss' cALen' cFAcc' cFAss' cCAS' cCall' cBlock' cSync' cInSync' cSeq' cCond' cWhile' cThrow'
cTry' e) p) =
sup-class.sup (case e of
  new C ⇒ sup-class.sup (cNew C) (cNew' C)
| newArray T e ⇒ sup-class.sup (cNewArray T e) (cNewArray' T e)
| Cast T e ⇒ sup-class.sup (cCast T e) (cCast' T e)
| InstanceOf e T ⇒ sup-class.sup (cInstanceOf e T) (cInstanceOf' e T)
| Val v ⇒ sup-class.sup (cVal v) (cVal' v)
| BinOp e bop e' ⇒ sup-class.sup (cBinOp e bop e') (cBinOp' e bop e')
| Var V ⇒ sup-class.sup (cVar V) (cVar' V)
| LAss V e ⇒ sup-class.sup (cLAss V e) (cLAss' V e)
| AAcc a e ⇒ sup-class.sup (cAAcc a e) (cAAcc' a e)
| AAss a i e ⇒ sup-class.sup (cAAss a i e) (cAAss' a i e)

```

```

| ALen a ⇒ sup-class.sup (cALen a) (cALen' a)
| FAcc e F D ⇒ sup-class.sup (cFAcc e F D) (cFAcc' e F D)
| FAss e F D e' ⇒ sup-class.sup (cFAss e F D e') (cFAss' e F D e')
| CompareAndSwap e D F e' e'' ⇒ sup-class.sup (cCAS e D F e' e'') (cCAS' e D F e' e'')
| Call e M es ⇒ sup-class.sup (cCall e M es) (cCall' e M es)
| Block V T vo e ⇒ sup-class.sup (cBlock V T vo e) (cBlock' V T vo e)
| Synchronized v e e' ⇒ sup-class.sup (cSync v e e') (cSync' v e e')
| InSynchronized v a e ⇒ sup-class.sup (cInSync v a e) (cInSync' v a e)
| Seq e e' ⇒ sup-class.sup (cSeq e e') (cSeq' e e')
| Cond b e e' ⇒ sup-class.sup (cCond b e e') (cCond' b e e')
| While b e ⇒ sup-class.sup (cWhile b e) (cWhile' b e)
| throw e ⇒ sup-class.sup (cThrow e) (cThrow' e)
| TryCatch e C V e' ⇒ sup-class.sup (cTry e C V e') (cTry' e C V e') p
apply(cases e)
apply(simp-all add: inf-sup-aci sup.assoc)
done

```

lemma *sup-bot1*: *sup-class.sup bot a* = (*a :: 'a :: {semilattice-sup, order-bot}*)
by(*rule sup-absorb2*)*auto*

lemma *sup-bot2*: *sup-class.sup a bot* = (*a :: 'a :: {semilattice-sup, order-bot}*)
by(*rule sup-absorb1*) *auto*

lemma *sup-case-val-case-val-same*:

```

sup-class.sup (case-val cUnit cNull cBool cIntg cAddr v) (case-val cUnit' cNull' cBool' cIntg' cAddr'
v) =
  (case v of
    Unit ⇒ sup-class.sup cUnit cUnit'
  | Null ⇒ sup-class.sup cNull cNull'
  | Bool b ⇒ sup-class.sup (cBool b) (cBool' b)
  | Intg i ⇒ sup-class.sup (cIntg i) (cIntg' i)
  | Addr a ⇒ sup-class.sup (cAddr a) (cAddr' a))
apply(cases v)
apply simp-all
done

```

lemma *sup-case-bool-case-bool-same*:

```

sup-class.sup (case-bool t f b) (case-bool t' f' b) =
  (if b then sup-class.sup t t' else sup-class.sup f f')
by simp

```

lemmas *predicate-code-inline* [*code-unfold*] =

```

Predicate.single-bind Predicate.bind-single split
eq-i-o-conv-single eq-o-i-conv-single
sup-case-exp-case-exp-same sup-case-exp-case-exp-other unit.case
sup-bot1 sup-bot2 sup-case-val-case-val-same sup-case-bool-case-bool-same

```

lemma *op-case-ty-case-ty-same*:

```

f (case-ty cVoid cBoolean cInteger cNT cClass cArray e)
  (case-ty cVoid' cBoolean' cInteger' cNT' cClass' cArray' e) =
  (case e of
    Void ⇒ f cVoid cVoid'
  | Boolean ⇒ f cBoolean cBoolean'
  | Integer ⇒ f cInteger cInteger'

```

```

| NT ⇒ f cNT cNT'
| Class C ⇒ f (cClass C) (cClass' C)
| Array T ⇒ f (cArray T) (cArray' T))
by(simp split: ty.split)

```

declare *op-case-ty-case-ty-same*[**where** $f = \text{sup-class.sup}$, *code-unfold*]

lemma *op-case-bop-case-bop-same*:

```

f (case-bop cEq cNotEq cLessThan cLessOrEqual cGreaterThan cGreaterOrEqual cAdd cSubtract
cMult cDiv cMod cBinAnd cBinOr cBinXor cShiftLeft cShiftRightZeros cShiftRightSigned bop)
(case-bop cEq' cNotEq' cLessThan' cLessOrEqual' cGreaterThan' cGreaterOrEqual' cAdd' cSub-
tract' cMult' cDiv' cMod' cBinAnd' cBinOr' cBinXor' cShiftLeft' cShiftRightZeros' cShiftRightSigned'
bop)
= case-bop (f cEq cEq') (f cNotEq cNotEq') (f cLessThan cLessThan') (f cLessOrEqual cLessOrE-
qual') (f cGreaterThan cGreaterThan') (f cGreaterOrEqual cGreaterOrEqual') (f cAdd cAdd') (f cSub-
tract cSubtract') (f cMult cMult') (f cDiv cDiv') (f cMod cMod') (f cBinAnd cBinAnd') (f cBinOr cBi-
nOr') (f cBinXor cBinXor') (f cShiftLeft cShiftLeft') (f cShiftRightZeros cShiftRightZeros') (f cShiftRight-
Signed cShiftRightSigned') bop
by(simp split: bop.split)

```

lemma *sup-case-bop-case-bop-other* [*code-unfold*]:

```

fixes p :: 'a :: semilattice-sup shows
sup-class.sup (case-bop cEq cNotEq cLessThan cLessOrEqual cGreaterThan cGreaterOrEqual cAdd
cSubtract cMult cDiv cMod cBinAnd cBinOr cBinXor cShiftLeft cShiftRightZeros cShiftRightSigned
bop)
(sup-class.sup (case-bop cEq' cNotEq' cLessThan' cLessOrEqual' cGreaterThan' cGreaterOrE-
qual' cAdd' cSubtract' cMult' cDiv' cMod' cBinAnd' cBinOr' cBinXor' cShiftLeft' cShiftRightZeros'
cShiftRightSigned' bop) p)
= sup-class.sup (case-bop (sup-class.sup cEq cEq') (sup-class.sup cNotEq cNotEq') (sup-class.sup
cLessThan cLessThan') (sup-class.sup cLessOrEqual cLessOrEqual') (sup-class.sup cGreaterThan cGreaterThan')
(sup-class.sup cGreaterOrEqual cGreaterOrEqual') (sup-class.sup cAdd cAdd') (sup-class.sup cSub-
tract cSubtract') (sup-class.sup cMult cMult') (sup-class.sup cDiv cDiv') (sup-class.sup cMod cMod')
(sup-class.sup cBinAnd cBinAnd') (sup-class.sup cBinOr cBinOr') (sup-class.sup cBinXor cBinXor')
(sup-class.sup cShiftLeft cShiftLeft') (sup-class.sup cShiftRightZeros cShiftRightZeros') (sup-class.sup
cShiftRightSigned cShiftRightSigned') bop) p
apply(cases bop)
apply(simp-all add: inf-sup-aci sup.assoc)
done

```

declare *op-case-bop-case-bop-same*[**where** $f = \text{sup-class.sup}$, *code-unfold*]

end

theory *JVMExec-Execute*

imports

```

../JVM/JVMExec
ExternalCall-Execute

```

begin

9.9.1 Manual translation of the JVM to use sets instead of predicates

locale *JVM-heap-execute* = *heap-execute* +

```

constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id

```

and *thread-id2addr* :: 'thread-id ⇒ 'addr
and *spurious-wakeups* :: bool
and *empty-heap* :: 'heap
and *allocate* :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and *typeof-addr* :: 'heap ⇒ 'addr ⇒ htype option
and *heap-read* :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val set
and *heap-write* :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap set

sublocale *JVM-heap-execute* < *execute*: *JVM-heap-base*
addr2thread-id thread-id2addr
spurious-wakeups
empty-heap allocate typeof-addr
 $\lambda h a ad v. v \in \text{heap-read } h a ad \ \lambda h a ad v h'. h' \in \text{heap-write } h a ad v$

context *JVM-heap-execute* **begin**

definition *exec-instr* ::

'addr instr ⇒ 'addr jvm-prog ⇒ 'thread-id ⇒ 'heap ⇒ 'addr val list ⇒ 'addr val list
 ⇒ cname ⇒ mname ⇒ pc ⇒ 'addr frame list
 ⇒ (('addr, 'thread-id, 'heap) jvm-thread-action × ('addr, 'heap) jvm-state) set

where [*simp*]: *exec-instr* = *execute.exec-instr*

lemma *exec-instr-code* [*code*]:

exec-instr (*Load n*) *P t h stk loc C₀ M₀ pc frs* =
 { $(\varepsilon, (\text{None}, h, ((\text{loc} ! n) \# \text{stk}, \text{loc}, C_0, M_0, \text{pc} + 1) \# \text{frs}))$ }
exec-instr (*Store n*) *P t h stk loc C₀ M₀ pc frs* =
 { $(\varepsilon, (\text{None}, h, (\text{tl } \text{stk}, \text{loc}[n := \text{hd } \text{stk}], C_0, M_0, \text{pc} + 1) \# \text{frs}))$ }
exec-instr (*Push v*) *P t h stk loc C₀ M₀ pc frs* =
 { $(\varepsilon, (\text{None}, h, (v \# \text{stk}, \text{loc}, C_0, M_0, \text{pc} + 1) \# \text{frs}))$ }
exec-instr (*New C*) *P t h stk loc C₀ M₀ pc frs* =
 (let *HA* = *allocate h (Class-type C)* in
 if *HA* = {} then { $(\varepsilon, [\text{execute.addr-of-sys-xcpt OutOfMemory}], h, (\text{stk}, \text{loc}, C_0, M_0, \text{pc}) \# \text{frs})$ }
 else do { $(h', a) \leftarrow \text{HA}; \{([\text{NewHeapElem } a (\text{Class-type } C)], \text{None}, h', (\text{Addr } a \# \text{stk}, \text{loc}, C_0,$
 $M_0, \text{pc} + 1) \# \text{frs})\}$ }
exec-instr (*NewArray T*) *P t h stk loc C₀ M₀ pc frs* =
 (let *si* = *the-Intg (hd stk)*;
 i = *nat (sint si)*
 in if *si* < 0
 then { $(\varepsilon, [\text{execute.addr-of-sys-xcpt NegativeArraySize}], h, (\text{stk}, \text{loc}, C_0, M_0, \text{pc}) \# \text{frs})$ }
 else let *HA* = *allocate h (Array-type T i)* in
 if *HA* = {} then { $(\varepsilon, [\text{execute.addr-of-sys-xcpt OutOfMemory}], h, (\text{stk}, \text{loc}, C_0, M_0, \text{pc}) \#$
 *frs})}
 else do { $(h', a) \leftarrow \text{HA}; \{([\text{NewHeapElem } a (\text{Array-type } T i)], \text{None}, h', (\text{Addr } a \# \text{tl } \text{stk}, \text{loc},$
 $C_0, M_0, \text{pc} + 1) \# \text{frs})\}$ }
exec-instr *ALoad* *P t h stk loc C₀ M₀ pc frs* =
 (let *va* = *hd (tl stk)*
 in (if *va* = *Null* then { $(\varepsilon, [\text{execute.addr-of-sys-xcpt NullPointer}], h, (\text{stk}, \text{loc}, C_0, M_0, \text{pc}) \# \text{frs})$ }
 else
 let *i* = *the-Intg (hd stk)*;
 a = *the-Addr va*;
 len = *alen-of-htype (the (typeof-addr h a))*
 in if *i* < 0 ∨ *int len* ≤ *sint i* then
 { $(\varepsilon, [\text{execute.addr-of-sys-xcpt ArrayIndexOutOfBounds}], h, (\text{stk}, \text{loc}, C_0, M_0, \text{pc}) \# \text{frs})$ }*

```

else do {
  v ← heap-read h a (ACell (nat (sint i)));
  {(\ReadMem a (ACell (nat (sint i))) v\}, None, h, (v # tl (tl stk), loc, C0, M0, pc +
1) # frs)}
  })
exec-instr AStore P t h stk loc C0 M0 pc frs =
(let ve = hd stk;
 vi = hd (tl stk);
 va = hd (tl (tl stk))
 in (if va = Null then {(\ε, [execute.addr-of-sys-xcpt NullPointer], h, (stk, loc, C0, M0, pc) # frs)}
 else (let i = the-Intg vi;
 idx = nat (sint i);
 a = the-Addr va;
 hT = the (typeof-addr h a);
 T = ty-of-htype hT;
 len = alen-of-htype hT;
 U = the (execute.typeof-h h ve)
 in (if i < s 0 ∨ int len ≤ sint i then
 {(\ε, [execute.addr-of-sys-xcpt ArrayIndexOutOfBounds], h, (stk, loc, C0, M0, pc) #
frs)}
 else if P ⊢ U ≤ the-Array T then
 do {
 h' ← heap-write h a (ACell idx) ve;
 {(\WriteMem a (ACell idx) ve\}, None, h', (tl (tl (tl stk)), loc, C0, M0, pc+1) #
frs)}
 }
 else {(\ε, ([execute.addr-of-sys-xcpt ArrayStore], h, (stk, loc, C0, M0, pc) # frs)}))))
exec-instr ALength P t h stk loc C0 M0 pc frs =
{(\ε, (let va = hd stk
 in if va = Null
 then ([execute.addr-of-sys-xcpt NullPointer], h, (stk, loc, C0, M0, pc) # frs)
 else (None, h, (Intg (word-of-int (int (alen-of-htype (the (typeof-addr h (the-Addr va)))))) #
tl stk, loc, C0, M0, pc+1) # frs))}
exec-instr (Getfield F C) P t h stk loc C0 M0 pc frs =
(let v = hd stk
 in if v = Null then {(\ε, [execute.addr-of-sys-xcpt NullPointer], h, (stk, loc, C0, M0, pc) # frs)}
 else let a = the-Addr v
 in do {
 v' ← heap-read h a (CField C F);
 {(\ReadMem a (CField C F) v'\}, None, h, (v' # (tl stk), loc, C0, M0, pc + 1) # frs)}
 })
exec-instr (Putfield F C) P t h stk loc C0 M0 pc frs =
(let v = hd stk;
 r = hd (tl stk)
 in if r = Null then {(\ε, [execute.addr-of-sys-xcpt NullPointer], h, (stk, loc, C0, M0, pc) # frs)}
 else let a = the-Addr r
 in do {
 h' ← heap-write h a (CField C F) v;
 {(\WriteMem a (CField C F) v\}, None, h', (tl (tl stk), loc, C0, M0, pc + 1) # frs)}
 })
exec-instr (Checkcast T) P t h stk loc C0 M0 pc frs =
{(\ε, let U = the (typeofh (hd stk))
 in if P ⊢ U ≤ T then (None, h, (stk, loc, C0, M0, pc + 1) # frs)
 else ([execute.addr-of-sys-xcpt ClassCast], h, (stk, loc, C0, M0, pc) # frs))}

```

```

exec-instr (Instanceof T) P t h stk loc C0 M0 pc frs =
  { (ε, None, h, (Bool (hd stk ≠ Null ∧ P ⊢ the (typeofh (hd stk)) ≤ T) # tl stk, loc, C0, M0, pc + 1) # frs) }
exec-instr (Invoke M n) P t h stk loc C0 M0 pc frs =
  (let r = stk ! n
   in (if r = Null then { (ε, [execute.addr-of-sys-xcpt NullPointer], h, (stk, loc, C0, M0, pc) # frs) }
      else (let ps = rev (take n stk);
             a = the-Addr r;
             T = the (typeof-addr h a);
             (D, M', Ts, meth) = method P (class-type-of T) M
           in case meth of
             Native ⇒
               do {
                 (ta, va, h') ← red-external-aggr P t a M ps h;
                 { (extTA2JVM P ta, extRet2JVM n h' stk loc C0 M0 pc frs va) }
               }
             | [(mxs, mxl0, ins, xt)] ⇒
               let f' = ([, [r]] @ ps @ (replicate mxl0 undefined-value), D, M, 0)
                 in { (ε, None, h, f' # (stk, loc, C0, M0, pc) # frs) })))
exec-instr Return P t h stk0 loc0 C0 M0 pc frs =
  { (ε, (if frs = [] then (None, h, []))
    else
      let v = hd stk0;
          (stk, loc, C, m, pc) = hd frs;
          n = length (fst (snd (method P C0 M0)))
        in (None, h, (v # (drop (n+1) stk), loc, C, m, pc+1) # tl frs) ) }
exec-instr Pop P t h stk loc C0 M0 pc frs = { (ε, (None, h, (tl stk, loc, C0, M0, pc+1) # frs) ) }
exec-instr Dup P t h stk loc C0 M0 pc frs = { (ε, (None, h, (hd stk # stk, loc, C0, M0, pc+1) # frs) ) }
exec-instr Swap P t h stk loc C0 M0 pc frs = { (ε, (None, h, (hd (tl stk) # hd stk # tl (tl stk), loc, C0, M0, pc+1) # frs) ) }
exec-instr (BinOpInstr bop) P t h stk loc C0 M0 pc frs =
  { (ε,
    case the (execute.binop bop (hd (tl stk)) (hd stk)) of
      Inl v ⇒ (None, h, (v # tl (tl stk), loc, C0, M0, pc + 1) # frs)
    | Inr a ⇒ (Some a, h, (stk, loc, C0, M0, pc) # frs) }
exec-instr (IfFalse i) P t h stk loc C0 M0 pc frs =
  { (ε, (let pc' = if hd stk = Bool False then nat(int pc+i) else pc+1
        in (None, h, (tl stk, loc, C0, M0, pc') # frs) ) ) }
exec-instr (Goto i) P t h stk loc C0 M0 pc frs = { (ε, (None, h, (stk, loc, C0, M0, nat(int pc+i) # frs) ) }
exec-instr ThrowExc P t h stk loc C0 M0 pc frs =
  { (ε, (let xp' = if hd stk = Null then [execute.addr-of-sys-xcpt NullPointer] else [the-Addr(hd stk)]
        in (xp', h, (stk, loc, C0, M0, pc) # frs) ) ) }
exec-instr MEnter P t h stk loc C0 M0 pc frs =
  { (let v = hd stk
    in if v = Null
      then (ε, [execute.addr-of-sys-xcpt NullPointer], h, (stk, loc, C0, M0, pc) # frs)
      else ( { Lock → the-Addr v, SyncLock (the-Addr v) }, None, h, (tl stk, loc, C0, M0, pc + 1) # frs) ) }
exec-instr MExit P t h stk loc C0 M0 pc frs =
  (let v = hd stk
   in if v = Null
     then { (ε, [execute.addr-of-sys-xcpt NullPointer], h, (stk, loc, C0, M0, pc) # frs) }
     else ( { Unlock → the-Addr v, SyncUnlock (the-Addr v) }, None, h, (tl stk, loc, C0, M0, pc + 1) # frs),

```

($\{\text{UnlockFail} \rightarrow \text{the-Addr } v\}$, [$\text{execute.addr-of-sys-xcpt IllegalMonitorState}$], h , (stk , loc , C_0 , M_0 , pc) $\# \text{frs}$)})
by(*auto 4 4 intro: rev-bexI*)

definition *exec* :: 'addr jvm-prog \Rightarrow 'thread-id \Rightarrow ('addr, 'heap) jvm-state \Rightarrow ('addr, 'thread-id, 'heap) jvm-ta-state set
where *exec* = *execute.exec*

lemma *exec-code*:

exec P t (xcp , h , []) = {}
exec P t (None , h , (stk , loc , C , M , pc) $\# \text{frs}$) = *exec-instr* (*instrs-of* P C M ! pc) P t h stk loc C M pc frs
exec P t ([a], h , fr $\# \text{frs}$) = {(ε , *execute.exception-step* P a h fr frs)}
by(*simp-all add: exec-def*)

definition *exec-1* ::

'addr jvm-prog \Rightarrow 'thread-id \Rightarrow ('addr, 'heap) jvm-state
 \Rightarrow (('addr, 'thread-id, 'heap) jvm-thread-action \times ('addr, 'heap) jvm-state) *Predicate.pred*
where *exec-1* P t σ = *pred-of-set* (*exec* P t σ)

lemma *exec-1I*: *execute.exec-1* P t σ ta σ' \Longrightarrow *Predicate.eval* (*exec-1* P t σ) (ta , σ')
by(*erule execute.exec-1.cases*)(*simp add: exec-1-def exec-def*)

lemma *exec-1E*:

assumes *Predicate.eval* (*exec-1* P t σ) (ta , σ')
obtains *execute.exec-1* P t σ ta σ'
using *assms*
by(*auto simp add: exec-1-def exec-def intro: execute.exec-1.intros*)

lemma *exec-1-eq* [*simp*]:

Predicate.eval (*exec-1* P t σ) (ta , σ') \longleftrightarrow *execute.exec-1* P t σ ta σ'
by(*auto intro: exec-1I elim: exec-1E*)

lemma *exec-1-eq'*:

Predicate.eval (*exec-1* P t σ) = ($\lambda(\text{ta}, \sigma').$ *execute.exec-1* P t σ ta σ')
by(*rule ext*)(*simp split: prod.split*)

end

lemmas [*code*] =

JVM-heap-execute.exec-instr-code
JVM-heap-base.exception-step.simps
JVM-heap-execute.exec-code
JVM-heap-execute.exec-1-def

end

theory *JVM-Execute*

imports

SC-Schedulers
JVMExec-Execute
../BV/BVProgressThreaded

begin

abbreviation *sc-heap-read-cset* :: *heap* \Rightarrow *addr* \Rightarrow *addr-loc* \Rightarrow *addr val set*
where *sc-heap-read-cset* *h ad al* \equiv *set-of-pred* (*sc-heap-read-i-i-i-o* *h ad al*)

abbreviation *sc-heap-write-cset* :: *heap* \Rightarrow *addr* \Rightarrow *addr-loc* \Rightarrow *addr val* \Rightarrow *heap set*
where *sc-heap-write-cset* *h ad al v* \equiv *set-of-pred* (*sc-heap-write-i-i-i-o* *h ad al v*)

interpretation *sc*:

JVM-heap-execute
addr2thread-id
thread-id2addr
sc-spurious-wakeups
sc-empty
sc-allocate P
sc-typeof-addr
sc-heap-read-cset
sc-heap-write-cset

rewrites $\bigwedge h ad al v. v \in sc\text{-heap-read-cset } h ad al \equiv sc\text{-heap-read } h ad al v$
and $\bigwedge h ad al v h'. h' \in sc\text{-heap-write-cset } h ad al v \equiv sc\text{-heap-write } h ad al v h'$
for *P*

apply(*simp-all add: eval-sc-heap-read-i-i-i-o eval-sc-heap-write-i-i-i-o*)
done

interpretation *sc-execute*:

JVM-conf-read
addr2thread-id
thread-id2addr
sc-spurious-wakeups
sc-empty
sc-allocate P
sc-typeof-addr
sc-heap-read
sc-heap-write
sc-hconf P

for *P*

by(*unfold-locales*)

fun *sc-mexec* ::

addr jvm-prog \Rightarrow *thread-id* \Rightarrow (*addr jvm-thread-state* \times *heap*)
 \Rightarrow ((*addr, thread-id, heap*) *jvm-thread-action* \times *addr jvm-thread-state* \times *heap*) *Predicate.pred*

where

sc-mexec P t ((xcp, frs), h) =
sc.exec-1 (TYPE(addr jvm-method)) P P t (xcp, h, frs) \ggg ($\lambda(ta, xcp, h, frs). Predicate.single (ta,$
(xcp, frs), h))

abbreviation *sc-jvm-start-state-refine* ::

addr jvm-prog \Rightarrow *cname* \Rightarrow *mname* \Rightarrow *addr val list* \Rightarrow

(*addr, thread-id, heap, (thread-id, (addr jvm-thread-state) \times addr released-locks)* *rm, (thread-id, addr*
wait-set-status) rm, thread-id rs) *state-refine*

where

sc-jvm-start-state-refine \equiv

sc-start-state-refine (rm-empty ()) rm-update (rm-empty ()) (rs-empty ()) ($\lambda C M Ts T (mxs, mxl0,$
b) vs. (None, [([], Null # vs @ replicate mxl0 undefined-value, C, M, 0)]))

abbreviation *sc-jvm-state-invar* :: *addr jvm-prog* \Rightarrow *ty_P* \Rightarrow (*addr,thread-id,addr jvm-thread-state,heap,addr*)
state set

where *sc-jvm-state-invar* *P* $\Phi \equiv \{s. \text{sc-execute.correct-state-ts } P \Phi (\text{thr } s) (\text{shr } s)\}$

lemma *eval-sc-mexec*:

($\lambda t \ x m \ ta \ x'm'. \text{Predicate.eval } (\text{sc-mexec } P \ t \ x m) \ (ta, \ x'm') =$
($\lambda t \ ((xcp, \ frs), \ h) \ ta \ ((xcp', \ frs'), \ h'). \ \text{sc.execute.exec-1} \ (\text{TYPE}(\text{addr } \text{jvm-method})) \ P \ P \ t \ (xcp, \ h,$
frs) *ta* (*xcp'*, *h'*, *frs'*))

by(rule *ext*)+(fastforce *intro!*: *SUP1-I simp add: sc.exec-1-eq'*)

lemma *sc-jvm-start-state-invar*:

assumes *wf-jvm-prog* Φ *P*

and *sc-wf-start-state* *P C M vs*

shows *sc-state- α* (*sc-jvm-start-state-refine* *P C M vs*) \in *sc-jvm-state-invar* *P* Φ

using *sc-execute.correct-jvm-state-initial*[*OF assms*]

by(*simp add: sc-execute.correct-jvm-state-def*)

9.9.2 Round-robin scheduler

interpretation *JVM-rr*:

sc-round-robin-base

JVM-final sc-mexec P convert-RA Jinja-output

for *P*

.

definition *sc-rr-JVM-start-state* :: *nat* \Rightarrow '*m prog* \Rightarrow *thread-id* *fifo* *round-robin*

where *sc-rr-JVM-start-state* *n0 P* = *JVM-rr.round-robin-start* *n0* (*sc-start-tid* *P*)

definition *exec-JVM-rr* ::

nat \Rightarrow *addr jvm-prog* \Rightarrow *cname* \Rightarrow *mname* \Rightarrow *addr val list* \Rightarrow

(*thread-id* \times (*addr, thread-id*) *obs-event list*,

(*addr, thread-id*) *locks* \times ((*thread-id, addr jvm-thread-state* \times *addr released-locks*) *rm* \times *heap*) \times

(*thread-id, addr wait-set-status*) *rm* \times *thread-id rs*) *tllist*

where

exec-JVM-rr *n0 P C M vs* = *JVM-rr.exec* *P n0* (*sc-rr-JVM-start-state* *n0 P*) (*sc-jvm-start-state-refine*
P C M vs)

interpretation *JVM-rr*:

sc-round-robin

JVM-final sc-mexec P convert-RA Jinja-output

for *P*

by(*unfold-locales*)

lemma *JVM-rr*:

assumes *wf-jvm-prog* Φ *P*

shows

sc-scheduler

JVM-final (*sc-mexec* *P*) *convert-RA*

(*JVM-rr.round-robin* *P n0*) (*pick-wakeup-via-sel* ($\lambda s \ P. \ \text{rm-sel } s \ (\lambda(k,v). \ P \ k \ v)$)) *JVM-rr.round-robin-invar*

(*sc-jvm-state-invar* *P* Φ)

unfolding *sc-scheduler-def*

apply(rule *JVM-rr.round-robin-scheduler*)

apply(*unfold eval-sc-mexec*)

apply(rule *sc-execute.mexec-deterministic*[*OF assms sc-deterministic-heap-ops*])

```

apply(simp add: sc-spurious-wakeups)
done

```

9.9.3 Random scheduler

```

interpretation JVM-rnd:
  sc-random-scheduler-base
  JVM-final sc-mexec P convert-RA Jinja-output
for P
.

```

```

definition sc-rnd-JVM-start-state :: Random.seed  $\Rightarrow$  random-scheduler
where sc-rnd-JVM-start-state seed = seed

```

```

definition exec-JVM-rnd ::
  Random.seed  $\Rightarrow$  addr jvm-prog  $\Rightarrow$  cname  $\Rightarrow$  mname  $\Rightarrow$  addr val list  $\Rightarrow$ 
  (thread-id  $\times$  (addr, thread-id) obs-event list,
   (addr, thread-id) locks  $\times$  ((thread-id, addr jvm-thread-state  $\times$  addr released-locks) rm  $\times$  heap)  $\times$ 
   (thread-id, addr wait-set-status) rm  $\times$  thread-id rs) tllist
where exec-JVM-rnd seed P C M vs = JVM-rnd.exec P (sc-rnd-JVM-start-state seed) (sc-jvm-start-state-refine
P C M vs)

```

```

interpretation JVM-rnd:
  sc-random-scheduler
  JVM-final sc-mexec P convert-RA Jinja-output
for P
by(unfold-locales)

```

```

lemma JVM-rnd:
  assumes wf-jvm-prog $\Phi$  P
  shows
    sc-scheduler
    JVM-final (sc-mexec P) convert-RA
    (JVM-rnd.random-scheduler P) (pick-wakeup-via-sel ( $\lambda s P$ . rm-sel s ( $\lambda(k,v)$ . P k v))) ( $\lambda$ - . True)
    (sc-jvm-state-invar P  $\Phi$ )
unfolding sc-scheduler-def
apply(rule JVM-rnd.random-scheduler-scheduler)
apply(unfold eval-sc-mexec)
apply(rule sc-execute.mexec-deterministic[OF assms sc-deterministic-heap-ops])
apply(simp add: sc-spurious-wakeups)
done

```

```

ML-val <@{code exec-JVM-rr}>

```

```

ML-val <@{code exec-JVM-rnd}>

```

```

end

```

9.10 String representation of types

```

theory ToString imports
  ../J/Expr
  ../JVM/JVMInstructions

```

```

../Basic/JT-ICF
begin

class toString =
  fixes toString :: 'a ⇒ String.literal

instantiation bool :: toString begin
definition [code]: toString b = (case b of True ⇒ STR "True" | False ⇒ STR "False")
instance proof qed
end

instantiation char :: toString begin
definition [code]: toString (c :: char) = String.implode [c]
instance proof qed
end

instantiation String.literal :: toString begin
definition [code]: toString (s :: String.literal) = s
instance proof qed
end

fun list-toString :: String.literal ⇒ 'a :: toString list ⇒ String.literal list
where
  list-toString sep [] = []
| list-toString sep [x] = [toString x]
| list-toString sep (x#xs) = toString x # sep # list-toString sep xs

instantiation list :: (toString) toString begin
definition [code]:
  toString (xs :: 'a list) = sum-list (STR "[" # list-toString (STR ",") xs @ [STR "]" ])
instance proof qed
end

definition digit-toString :: int ⇒ String.literal
where
  digit-toString k = (if k = 0 then STR "0"
    else if k = 1 then STR "1"
    else if k = 2 then STR "2"
    else if k = 3 then STR "3"
    else if k = 4 then STR "4"
    else if k = 5 then STR "5"
    else if k = 6 then STR "6"
    else if k = 7 then STR "7"
    else if k = 8 then STR "8"
    else if k = 9 then STR "9"
    else undefined)

function int-toString :: int ⇒ String.literal list
where
  int-toString n =
    (if n < 0 then STR "-" # int-toString (- n)
     else if n < 10 then [digit-toString n]
     else int-toString (n div 10) @ [digit-toString (n mod 10)])
by pat-completeness simp

```

termination by *size-change*

instantiation *int* :: *toString* **begin**

definition [*code*]: *toString* *i* = *sum-list* (*int-toString* *i*)

instance proof qed

end

instantiation *nat* :: *toString* **begin**

definition [*code*]: *toString* *n* = *toString* (*int* *n*)

instance proof qed

end

instantiation *option* :: (*toString*) *toString* **begin**

primrec *toString-option* :: '*a* *option* ⇒ *String.literal* **where**

toString *None* = *STR* "*None*"

| *toString* (*Some* *a*) = *sum-list* [*STR* "*Some* ('', *toString* *a*, *STR* ')"']

instance proof qed

end

instantiation *finfun* :: ({*toString*, *card-UNIV*, *equal*, *linorder*}, *toString*) *toString* **begin**

definition [*code*]:

toString (*f* :: '*a* ⇒*f* '*b*) =

sum-list

(*STR* "("

toString (*finfun-default* *f*)

concat (*map* (λx . [*STR* ",", *toString* *x*, *STR* "|->", *toString* (*f* \$ *x*)] (*finfun-to-list* *f*))

@ [*STR* ")"])

instance proof qed

end

instantiation *word* :: (*len*) *toString* **begin**

definition [*code*]: *toString* (*w* :: '*a* *word*) = *toString* (*sint* *w*)

instance proof qed

end

instantiation *fun* :: (*type*, *type*) *toString* **begin**

definition [*code*]: *toString* (*f* :: '*a* ⇒ '*b*) = *STR* "*fn*"

instance proof qed

end

instantiation *val* :: (*toString*) *toString* **begin**

fun *toString-val* :: ('*a* :: *toString*) *val* ⇒ *String.literal*

where

toString *Unit* = *STR* "*Unit*"

| *toString* *Null* = *STR* "*Null*"

| *toString* (*Bool* *b*) = *sum-list* [*STR* "*Bool* ", *toString* *b*]

| *toString* (*Intg* *i*) = *sum-list* [*STR* "*Intg* ", *toString* *i*]

| *toString* (*Addr* *a*) = *sum-list* [*STR* "*Addr* ", *toString* *a*]

instance proof qed

end

instantiation *ty* :: *toString* **begin**

primrec *toString-ty* :: *ty* ⇒ *String.literal*

where

```

toString Void = STR "Void"
| toString Boolean = STR "Boolean"
| toString Integer = STR "Integer"
| toString NT = STR "NT"
| toString (Class C) = sum-list [STR "Class ", toString C]
| toString (T[]) = sum-list [toString T, STR "[]"]
instance proof qed
end

```

```

instantiation bop :: toString begin
primrec toString-bop :: bop ⇒ String.literal where
  toString Eq = STR "=="
| toString NotEq = STR "!="
| toString LessThan = STR "<"
| toString LessOrEqual = STR "<="
| toString GreaterThan = STR ">"
| toString GreaterOrEqual = STR ">="
| toString Add = STR "+"
| toString Subtract = STR "-"
| toString Mult = STR "*"
| toString Div = STR "/"
| toString Mod = STR "%"
| toString BinAnd = STR "&"
| toString BinOr = STR "|"
| toString BinXor = STR "^"
| toString ShiftLeft = STR "<<"
| toString ShiftRightZeros = STR ">>"
| toString ShiftRightSigned = STR ">>>"
instance proof qed
end

```

```

instantiation addr-loc :: toString begin
primrec toString-addr-loc :: addr-loc ⇒ String.literal where
  toString (CField C F) = sum-list [STR "CField ", F, STR "{", C, STR "}"]
| toString (ACell n) = sum-list [STR "ACell ", toString n]
instance proof qed
end

```

```

instantiation htype :: toString begin
fun toString-htype :: htype ⇒ String.literal where
  toString (Class-type C) = C
| toString (Array-type T n) = sum-list [toString T, STR "[", toString n, STR "]"]
instance proof qed
end

```

```

instantiation obs-event :: (toString, toString) toString begin
primrec toString-obs-event :: ('a :: toString, 'b :: toString) obs-event ⇒ String.literal
where
  toString (ExternalCall ad M vs v) =
    sum-list [STR "ExternalCall ", M, STR "(", toString vs, STR ") = ", toString v]
| toString (ReadMem ad al v) =
  sum-list [STR "ReadMem ", toString ad, STR "@", toString al, STR "=", toString v]
| toString (WriteMem ad al v) =
  sum-list [STR "WriteMem ", toString ad, STR "@", toString al, STR "=", toString v]

```

```

| toString (NewHeapElem ad hT) = sum-list [STR "Allocate ", toString ad, STR ":", toString hT]
| toString (ThreadStart t) = sum-list [STR "ThreadStart ", toString t]
| toString (ThreadJoin t) = sum-list [STR "ThreadJoin ", toString t]
| toString (SyncLock ad) = sum-list [STR "SyncLock ", toString ad]
| toString (SyncUnlock ad) = sum-list [STR "SyncUnlock ", toString ad]
| toString (ObsInterrupt t) = sum-list [STR "Interrupt ", toString t]
| toString (ObsInterrupted t) = sum-list [STR "Interrupted ", toString t]

```

instance proof qed
end

instantiation prod :: (toString, toString) toString **begin**

definition toString = ($\lambda(a, b).$ sum-list [STR "(" , toString a, STR ", ", toString b, STR ")"])

instance proof qed
end

instantiation fmod-ext :: (toString) toString **begin**

definition toString fd = sum-list [STR "{|volatile=", toString (volatile fd), STR ", ", toString (fmod.more fd), STR "}"]

instance proof qed
end

instantiation unit :: toString **begin**

definition toString (u :: unit) = STR "()"

instance proof qed
end

instantiation exp :: (toString, toString, toString) toString **begin**

fun toString-exp :: ('a :: toString, 'b :: toString, 'c :: toString) exp \Rightarrow String.literal

where

```

  toString (new C) = sum-list [STR "new ", C]
| toString (newArray T e) = sum-list [STR "new ", toString T, STR "[", toString e, STR "]"]
| toString (Cast T e) = sum-list [STR "(" , toString T, STR ") (" , toString e, STR ")"]
| toString (InstanceOf e T) = sum-list [STR "(" , toString e, STR ") instanceof ", toString T]
| toString (Val v) = sum-list [STR "Val (" , toString v, STR ")"]
| toString (e1 «bop» e2) = sum-list [STR "(" , toString e1, STR ") ", toString bop, STR " (" , toString e2, STR ")"]
| toString (Var V) = sum-list [STR "Var ", toString V]
| toString (V := e) = sum-list [toString V, STR " := (" , toString e, STR ")"]
| toString (AAcc a i) = sum-list [STR "(" , toString a, STR ") [" , toString i, STR "]"]
| toString (AAss a i e) = sum-list [STR "(" , toString a, STR ") [" , toString i, STR "] := (" , toString e, STR ")"]
| toString (ALen a) = sum-list [STR "(" , toString a, STR ").length"]
| toString (FAcc e F D) = sum-list [STR "(" , toString e, STR ").", F, STR "{", D, STR "}"]
| toString (FAss e F D e') = sum-list [STR "(" , toString e, STR ").", F, STR "{", D, STR "} := (" , toString e', STR ")"]
| toString (Call e M es) = sum-list ([STR "(" , toString e, STR ").", M, STR "("] @ map toString es @ [STR ")"])
| toString (Block V T vo e) = sum-list ([STR "{", toString V, STR ":", toString T] @ (case vo of
  None  $\Rightarrow$  [] | Some v  $\Rightarrow$  [STR "=", toString v]) @ [STR "; ", toString e, STR "]"])
| toString (Synchronized V e e') = sum-list [STR "synchronized-", toString V, STR "-(" , toString e,
  STR ") {" , toString e', STR "}"]
| toString (InSynchronized V ad e) = sum-list [STR "insynchronized-", toString V, STR "-(" , toString ad,
  STR ") {" , toString e, STR "}"]
| toString (e;;e') = sum-list [toString e, STR "; ", toString e']

```

```

| toString (if (e) e' else e'') = sum-list [STR "if (" , toString e, STR ") { " , toString e' , STR " } else
{ " , toString e'' , STR " }"]
| toString (while (e) e') = sum-list [STR "while (" , toString e, STR ") { " , toString e' , STR " }"]
| toString (throw e) = sum-list [STR "throw (" , toString e, STR ")"]
| toString (try e catch (C V) e') = sum-list [STR "try { " , toString e, STR " } catch (" , C , STR " "
" , toString V, STR ") { " , toString e' , STR " }"]

```

instance proof qed

end

instantiation instr :: (toString) toString begin

primrec toString-instr :: 'a instr ⇒ String.literal **where**

```

  toString (Load i) = sum-list [STR "Load (" , toString i, STR ")"]
| toString (Store i) = sum-list [STR "Store (" , toString i, STR ")"]
| toString (Push v) = sum-list [STR "Push (" , toString v, STR ")"]
| toString (New C) = sum-list [STR "New " , toString C]
| toString (NewArray T) = sum-list [STR "NewArray " , toString T]
| toString ALoad = STR "ALoad"
| toString AStore = STR "AStore"
| toString ALength = STR "ALength"
| toString (Getfield F D) = sum-list [STR "Getfield " , toString F, STR " " , toString D]
| toString (Putfield F D) = sum-list [STR "Putfield " , toString F, STR " " , toString D]
| toString (Checkcast T) = sum-list [STR "Checkcast " , toString T]
| toString (Instanceof T) = sum-list [STR "Instanceof " , toString T]
| toString (Invoke M n) = sum-list [STR "Invoke " , toString M, STR " " , toString n]
| toString Return = STR "Return"
| toString Pop = STR "Pop"
| toString Dup = STR "Dup"
| toString Swap = STR "Swap"
| toString (BinOpInstr bop) = sum-list [STR "BinOpInstr " , toString bop]
| toString (Goto i) = sum-list [STR "Goto " , toString i]
| toString (IfFalse i) = sum-list [STR "IfFalse " , toString i]
| toString ThrowExc = STR "ThrowExc"
| toString MEnter = STR "monitorenter"
| toString MExit = STR "monitorexit"

```

instance proof qed

end

instantiation trie :: (toString, toString) toString begin

definition [code]: toString (t :: ('a, 'b) trie) = toString (tm-to-list t)

instance proof qed

end

instantiation rbt :: ({toString, linorder}, toString) toString begin

definition [code]:

```

  toString (t :: ('a, 'b) rbt) =
    sum-list (list-toString (STR "⊔") (rm-to-list t))

```

instance proof qed

end

instantiation assoc-list :: (toString, toString) toString begin

definition [code]: toString = toString ∘ Assoc-List.impl-of

instance proof qed

end

```

code-printing
  class-instance String.literal :: toString  $\rightarrow$  (Haskell) -
end

```

9.11 Setup for converter Java2Jinja

```

theory Java2Jinja
imports
  Code-Generation
  ToString
begin

```

```

code-identifier
  code-module Java2Jinja  $\rightarrow$  (SML) Code-Generation

```

```

definition j-Program :: addr J-mb cdecl list  $\Rightarrow$  addr J-prog
where j-Program = Program

```

```

export-code wf-J-prog' j-Program in SML file  $\langle$ JWellForm.ML $\rangle$ 

```

Functions for extracting calls to the native print method

```

definition purge where
   $\bigwedge$ run.
  purge run =
  lmap ( $\lambda$ obs. case obs of ExternalCall - - (Cons (Intg i) -) v  $\Rightarrow$  i)
  (lfilter
    ( $\lambda$ obs. case obs of ExternalCall - M (Cons (Intg i) Nil) -  $\Rightarrow$  M = print | -  $\Rightarrow$  False)
    (lconcat (lmap (llist-of  $\circ$  snd) (llist-of-tllist run))))

```

Various other functions

```

instantiation heapobj :: toString begin
primrec toString-heapobj :: heapobj  $\Rightarrow$  String.literal where
  toString (Obj C fs) = sum-list [STR "(Obj ", toString C, STR ", ", toString fs, STR ")"]
| toString (Arr T si fs el) =
  sum-list [STR "([", toString si, STR "]", toString T, STR ", ", toString fs, STR ", ", toString
(map snd (rm-to-list el)), STR ")"]
instance proof qed
end

```

```

definition case-llist' where case-llist' = case-llist
definition case-tllist' where case-tllist' = case-tllist
definition terminal' where terminal' = terminal
definition llist-of-tllist' where llist-of-tllist' = llist-of-tllist
definition thr' where thr' = thr
definition shr' where shr' = shr

```

```

definition heap-toString :: heap  $\Rightarrow$  String.literal
where heap-toString = toString

```

```

definition thread-toString :: (thread-id, (addr expr  $\times$  addr locals)  $\times$  (addr  $\Rightarrow$  f nat)) rbt  $\Rightarrow$  String.literal
where thread-toString = toString

```

```

definition thread-toString' :: (thread-id, addr jvm-thread-state'  $\times$  (addr  $\Rightarrow$  f nat)) rbt  $\Rightarrow$  String.literal

```


where *thread-toString'* = *toString*

definition *trace-toString* :: *thread-id* × (*addr*, *thread-id*) *obs-event list* ⇒ *String.literal*
 where *trace-toString* = *toString*

code-identifier

code-module *Cardinality* → (*SML*) *Set*
 | **code-module** *Code-Cardinality* → (*SML*) *Set*
 | **code-module** *Conditionally-Complete-Lattices* → (*SML*) *Set*
 | **code-module** *List* → (*SML*) *Set*
 | **code-module** *Predicate* → (*SML*) *Set*
 | **code-module** *Parity* → (*SML*) *Bit-Operations*
 | **type-class** *semiring-parity* → (*SML*) *Bit-Operations.semiring-parity*
 | **class-instance** *int* :: *semiring-parity* → (*SML*) *Bit-Operations.semiring-parity-int*
 | **class-instance** *int* :: *ring-parity* → (*SML*) *Bit-Operations.semiring-parity-int*
 | **constant** *member-i-i* → (*SML*) *Set.member-i-i*

export-code

wf-J-prog' *exec-J-rr* *exec-J-rnd*
j-Program
purge case-llist' *case-tllist'* *terminal'* *llist-of-tllist'*
thr' *shr'* *heap-toString* *thread-toString* *trace-toString*
in *SML*
file ⟨*J-Execute.ML*⟩

definition *j2jvm* :: *addr J-prog* ⇒ *addr jvm-prog* **where** *j2jvm* = *J2JVM*

export-code

wf-jvm-prog' *exec-JVM-rr* *exec-JVM-rnd* *j2jvm*
j-Program
purge case-llist' *case-tllist'* *terminal'* *llist-of-tllist'*
thr' *shr'* *heap-toString* *thread-toString'* *trace-toString*
in *SML*
file ⟨*JVM-Execute2.ML*⟩

end

theory *Execute-Main*

imports

SC-Schedulers
PCompilerRefine
Code-Generation
JVM-Execute
Java2Jinja

begin

end

Chapter 10

Examples

10.1 Apprentice challenge

```
theory ApprenticeChallenge
imports
  ../Execute/Code-Generation
begin
```

This theory implements the apprentice challenge by Porter and Moore [5].

```
definition ThreadC :: addr J-mb cdecl
where
  ThreadC =
    (Thread, Object, [],
     [(run, [], Void, [([], unit)]),
      (start, [], Void, Native),
      (join, [], Void, Native),
      (interrupt, [], Void, Native),
      (isInterrupted, [], Boolean, Native)])
```

```
definition Container :: cname
where Container = STR "Container"
```

```
definition ContainerC :: addr J-mb cdecl
where ContainerC = (Container, Object, [(STR "counter", Integer, (volatile=False))], [])
```

```
definition String :: cname
where String = STR "String"
```

```
definition StringC :: addr J-mb cdecl
where
  StringC = (String, Object, [], [])
```

```
definition Job :: cname
where Job = STR "Job"
```

```
definition JobC :: addr J-mb cdecl
where
  JobC =
    (Job, Thread, [(STR "objref", Class Container, (volatile=False))],
     [(STR "incr", [], Class Job, [([],
```

```

    sync(Var (STR "objref"))
      ((Var (STR "objref"))•STR "counter"{STR ""} := ((Var (STR "objref"))•STR "counter"{STR
""} «Add» Val (Intg 1))));
    Var this)],
    (STR "setref", [Class Container], Void, [(STR "o"],
    LAss (STR "objref") (Var (STR "o")))],
    (run, [], Void, [(],
    while (true) (Var this•STR "incr"([[]]))
  ])

```

definition *Apprentice* :: *cname*
where *Apprentice* = STR "Apprentice"

definition *ApprenticeC* :: *addr J-mb cdecl*
where

```

ApprenticeC =
(Apprentice, Object, [],
[(STR "main", [Class String[]], Void, [(STR "args"],
{STR "container":Class Container=None;
(STR "container" := new Container);
(while (true)
{STR "job":Class Job=None;
(STR "job" := new Job);
(Var (STR "job")•STR "setref"([Var (STR "container")]));
(Var (STR "job")•Type.start([[]]))
}
)
}]]))

```

definition *ApprenticeChallenge*

where

ApprenticeChallenge = Program (SystemClasses @ [StringC, ThreadC, ContainerC, JobC, Apprent-
iceC])

definition *ApprenticeChallenge-annotated*

where *ApprenticeChallenge-annotated* = *annotate-prog-code* *ApprenticeChallenge*

lemma *wf-J-prog* *ApprenticeChallenge-annotated*

by *eval*

lemmas [*code-unfold*] =

Container-def *Job-def* *String-def* *Apprentice-def*

definition *main* :: *String.literal* **where** *main* = STR "main"

ML-val ‹

```

val - = tracing started;
val program = @{code ApprenticeChallenge-annotated};
val - = tracing prg;
val compiled = @{code J2JVM} program;
val - = tracing compiled;

```

```

@{code exec-J-rr}
@{code 1 :: nat}

```

```

program
  @{\code Apprentice}
  @{\code main}
  [ @{\code Null}];

val - = tracing J-rr;
@{\code exec-JVM-rr}
  @{\code 1 :: nat}
  compiled
  @{\code Apprentice}
  @{\code main}
  [ @{\code Null}];
val - = tracing JVM-rr;
,

end

```

10.2 Buffer example

theory *BufferExample* **imports**

../Execute/Code-Generation

begin

definition *ThreadC* :: *addr J-mb cdecl*

where

```

ThreadC =
  (Thread, Object, [],
   [(run, [], Void, [([], unit)]),
    (start, [], Void, Native),
    (join, [], Void, Native),
    (interrupt, [], Void, Native),
    (isInterrupted, [], Boolean, Native)])

```

definition *IntegerC* :: *addr J-mb cdecl*

where *IntegerC* = (*STR "Integer"*, *Object*, [(*STR "value"*, *Integer*, (|volatile=False|))], [])

definition *Buffer* :: *cname*

where *Buffer* = *STR "Buffer"*

definition *BufferC* :: *addr J-mb cdecl*

where

```

BufferC =
  (Buffer, Object,
   [(STR "buffer", Class Object[], (|volatile=False|)),
    (STR "front", Integer, (|volatile=False|)),
    (STR "back", Integer, (|volatile=False|)),
    (STR "size", Integer, (|volatile=False|))],
   [(STR "constructor", [Integer], Void, [(STR "size",
    (STR "buffer" := newA (Class Object)[Var (STR "size")])];
    (STR "front" := Val (Intg 0));;
    (STR "back" := Val (Intg (- 1))));;
    (Var this.(STR "size"){STR ""} := Val (Intg 0))]),
   (STR "empty", [], Boolean, [([], sync(Var this) (Var (STR "size") «Eq» Val (Intg 0)))]),

```

```

(STR "full", [], Boolean, [([],
  sync(Var this) (Var (STR "size") «Eq» ((Var (STR "buffer"))•length))),
(STR "get", [], Class Object, [([],
  sync(Var this) (
    (while (Var this•(STR "empty"))([]))
      (try (Var this•wait([])) catch(InterruptedException (STR "e") unit));;
    (STR "size" := (Var (STR "size") «Subtract» Val (Intg 1))));;
    {(STR "result"):Class Object=None;
      ((STR "result") := ((Var (STR "buffer"))[Var (STR "front")]));;
      (STR "front" := (Var (STR "front") «Add» Val (Intg 1))));;
      (if ((Var (STR "front") «Eq» ((Var (STR "buffer"))•length))
        (STR "front" := Val (Intg 0))
        else unit));;
      (Var this•notifyAll([]));;
      Var (STR "result")
    }
  )]),
(STR "put", [Class Object], Void, [([STR "o"],
  sync(Var this) (
    (while (Var this•STR "full"([]))
      (try (Var this•wait([])) catch(InterruptedException STR "e") unit));;
    (STR "back" := (Var (STR "back") «Add» Val (Intg 1))));;
    (if (Var (STR "back") «Eq» ((Var (STR "buffer"))•length))
      (STR "back" := Val (Intg 0))
      else unit));;
    (AAss (Var (STR "buffer")) (Var (STR "back")) (Var (STR "o")));;
    (STR "size" := ((Var (STR "size") «Add» Val (Intg 1))));;
    (Var this•notifyAll([]))
  )])
])

```

definition *Producer* :: *cname*
where *Producer* = STR "Producer"

definition *ProducerC* :: *int* ⇒ *addr J-mb cdecl*
where

```

ProducerC n =
  (Producer, Thread, [(STR "buffer", Class Buffer, (|volatile=False|)],
  [(run, [], Void, [([],
    {STR "i":Integer=[Intg 0];
      while (Var (STR "i") «NotEq» Val (Intg (word-of-int n))) (
        (Var (STR "buffer"))•STR "put"({STR "temp":Class (STR "Integer")=None; (STR "temp"
:= new (STR "Integer"); ((FAss (Var (STR "temp")) (STR "value") (STR "")) (Var (STR "i")));;
Var (STR "temp"))} ]));;
        STR "i" := (Var (STR "i") «Add» (Val (Intg 1))))
      }])])

```

definition *Consumer* :: *cname*
where *Consumer* = STR "Consumer"

definition *ConsumerC* :: *int* ⇒ *addr J-mb cdecl*
where

```

ConsumerC n =
  (Consumer, Thread, [(STR "buffer", Class Buffer, (|volatile=False|)],

```

```

[(run, [], Void, [([],
  {STR "i":Integer=[Intg 0];
  while (Var (STR "i") «NotEq» Val (Intg (word-of-int n))) (
    {STR "o":Class Object=None;
    Seq (STR "o" := ((Var (STR "buffer"))·STR "get"([]))
      (STR "i" := (Var (STR "i") «Add» Val (Intg 1))))}
  )])])])

```

definition *String* :: *cname*
where *String* = STR "String"

definition *StringC* :: *addr J-mb cdecl*
where
StringC = (*String*, *Object*, [], [])

definition *Test* :: *cname*
where *Test* = STR "Test"

definition *TestC* :: *addr J-mb cdecl*
where
TestC =
(*Test*, *Object*, [],
[(STR "main", [Class String[]], Void, [([STR "args"],
 {STR "b":Class Buffer=None; (STR "b" := new Buffer);
 (Var (STR "b")·STR "constructor"([Val (Intg 10)]));
 {STR "p":Class Producer=None; STR "p" := new Producer;;
 {STR "c":Class Consumer=None;
 (STR "c" := new Consumer);
 (Var (STR "c")·STR "buffer"{STR ""} := Var (STR "b"));
 (Var (STR "p")·STR "buffer"{STR ""} := Var (STR "b"));
 (Var (STR "c")·Type.start([]));(Var (STR "p")·Type.start([]))
 }
 }
])]))])])])

definition *BufferExample*
where
BufferExample *n* = Program (SystemClasses @ [ThreadC, StringC, IntegerC, BufferC, ProducerC
n, ConsumerC *n*, TestC])

definition *BufferExample-annotated*
where
BufferExample-annotated *n* = annotate-prog-code (*BufferExample* *n*)

lemmas [code-unfold] =
IntegerC-def Buffer-def Producer-def Consumer-def Test-def
String-def

lemma *wf-J-prog* (*BufferExample-annotated* 10)
by *eval*

definition *main* **where** *main* = STR "main"
definition *five* :: *int* **where** *five* = 5

ML-val ‹

```
val program = @{code BufferExample-annotated} @{code five};
val compiled = @{code J2JVM} program;
```

```
val run =
  @{code exec-J-rr}
  @{code 1 :: nat}
  program
  @{code Test}
  @{code main}
  [ @{code Null}];
val - = @{code terminal} run;
```

```
val jvm-run =
  @{code exec-JVM-rr}
  @{code 1 :: nat}
  compiled
  @{code Test}
  @{code main}
  [ @{code Null}];
val - = @{code terminal} run;
```

›

end

theory *Examples-Main*

imports

ApprenticeChallenge

BufferExample

begin

end

theory *JinjaThreads*

imports

Basic/Basic-Main

Common/Common-Main

J/J-Main

JVM/JVM-Main

BV/BV-Main

Compiler/Compiler-Main

MM/MM-Main

Execute/Execute-Main

Examples/Examples-Main

begin

end

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