

# Jinja with Threads

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**Abstract.** We extend the Jinja source code semantics by Klein and Nipkow with Java-style arrays and threads. Concurrency is captured in a generic framework semantics for adding concurrency through interleaving to a sequential semantics, which features dynamic thread creation, inter-thread communication via shared memory, lock synchronisation and joins. Also, threads can suspend themselves and be notified by others. We instantiate the framework with the adapted versions of both Jinja source and byte code and show type safety for the multithreaded case. Equally, the compiler from source to byte code is extended, for which we prove weak bisimilarity between the source code small step semantics and the defensive Jinja virtual machine. On top of this, we formalise the JMM and show the DRF guarantee and consistency.

For description of the different parts, see [1, 2, 4, 3].

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# Chapter 1

## The generic multithreaded semantics

### 1.1 State of the multithreaded semantics

```
theory FWState
imports
  ../Basic/Auxiliary
begin

datatype lock-action =
  Lock
  | Unlock
  | UnlockFail
  | ReleaseAcquire

datatype ('t, 'x, 'm) new-thread-action =
  NewThread 't 'x 'm
  | ThreadExists 't bool

datatype 't conditional-action =
  Join 't
  | Yield

datatype ('t, 'w) wait-set-action =
  Suspend 'w
  | Notify 'w
  | NotifyAll 'w
  | WakeUp 't
  | Notified
  | WokenUp

datatype 't interrupt-action
  = IsInterrupted 't bool
  | Interrupt 't
  | ClearInterrupt 't

type-synonym 'l lock-actions = 'l  $\Rightarrow$  f lock-action list
```

**translations**

$(type) \ 'l \ lock\text{-}actions \leq (type) \ 'l \Rightarrow f \ lock\text{-}action \ list$

**type-synonym**

$(l, t, x, m, w, o) \ thread\text{-}action =$   
 $\ 'l \ lock\text{-}actions \times (t, x, m) \ new\text{-}thread\text{-}action \ list \times$   
 $\ 't \ conditional\text{-}action \ list \times (t, w) \ wait\text{-}set\text{-}action \ list \times$   
 $\ 't \ interrupt\text{-}action \ list \times 'o \ list$

**print-translation**  $\langle$ 

*let*  
*fun*  $tr'$   
 $[Const (@\{type\text{-}syntax \ finfun\}, -) \$ l \$$   
 $(Const (@\{type\text{-}syntax \ list\}, -) \$ Const (@\{type\text{-}syntax \ lock\text{-}action\}, -)),$   
 $Const (@\{type\text{-}syntax \ prod\}, -) \$$   
 $(Const (@\{type\text{-}syntax \ list\}, -) \$ (Const (@\{type\text{-}syntax \ new\text{-}thread\text{-}action\}, -) \$ t1 \$ x \$ m))$   
 $\$$   
 $(Const (@\{type\text{-}syntax \ prod\}, -) \$$   
 $(Const (@\{type\text{-}syntax \ list\}, -) \$ (Const (@\{type\text{-}syntax \ conditional\text{-}action\}, -) \$ t2)) \$$   
 $(Const (@\{type\text{-}syntax \ prod\}, -) \$$   
 $(Const (@\{type\text{-}syntax \ list\}, -) \$ (Const (@\{type\text{-}syntax \ wait\text{-}set\text{-}action\}, -) \$ t3 \$ w)) \$$   
 $(Const (@\{type\text{-}syntax \ prod\}, -) \$$   
 $(Const (@\{type\text{-}syntax \ list\}, -) \$ (Const (@\{type\text{-}syntax \ interrupt\text{-}action\}, -) \$ t4)) \$$   
 $(Const (@\{type\text{-}syntax \ list\}, -) \$ o1)))] =$   
 $if \ t1 = t2 \ andalso \ t2 = t3 \ andalso \ t3 = t4 \ then \ Syntax.const \ @\{type\text{-}syntax \ thread\text{-}action\} \$ l \$$   
 $t1 \$ x \$ m \$ w \$ o1$   
 $else \ raise \ Match;$   
 $in \ [(@\{type\text{-}syntax \ prod\}, K \ tr^{\wedge})]$   
 $end$   
 $\rangle$   
**typ**  $(l, t, x, m, w, o) \ thread\text{-}action$

**definition**  $locks\text{-}a :: (l, t, x, m, w, o) \ thread\text{-}action \Rightarrow 'l \ lock\text{-}actions \ (\mathbb{N} \rightarrow [0] \ 1000) \ \mathbf{where}$   
 $locks\text{-}a \equiv fst$

**definition**  $thr\text{-}a :: (l, t, x, m, w, o) \ thread\text{-}action \Rightarrow (t, x, m) \ new\text{-}thread\text{-}action \ list \ (\mathbb{N} \rightarrow [0] \ 1000) \ \mathbf{where}$   
 $thr\text{-}a \equiv fst \ o \ snd$

**definition**  $cond\text{-}a :: (l, t, x, m, w, o) \ thread\text{-}action \Rightarrow 't \ conditional\text{-}action \ list \ (\mathbb{N} \rightarrow [0] \ 1000) \ \mathbf{where}$   
 $cond\text{-}a = fst \ o \ snd \ o \ snd$

**definition**  $wset\text{-}a :: (l, t, x, m, w, o) \ thread\text{-}action \Rightarrow (t, w) \ wait\text{-}set\text{-}action \ list \ (\mathbb{N} \rightarrow [0] \ 1000) \ \mathbf{where}$   
 $wset\text{-}a = fst \ o \ snd \ o \ snd \ o \ snd$

**definition**  $interrupt\text{-}a :: (l, t, x, m, w, o) \ thread\text{-}action \Rightarrow 't \ interrupt\text{-}action \ list \ (\mathbb{N} \rightarrow [0] \ 1000) \ \mathbf{where}$   
 $interrupt\text{-}a = fst \ o \ snd \ o \ snd \ o \ snd \ o \ snd$

**definition**  $obs\text{-}a :: (l, t, x, m, w, o) \ thread\text{-}action \Rightarrow 'o \ list \ (\mathbb{N} \rightarrow [0] \ 1000) \ \mathbf{where}$   
 $obs\text{-}a \equiv snd \ o \ snd \ o \ snd \ o \ snd \ o \ snd$

**lemma**  $locks\text{-}a\text{-}conv \ [simp]: locks\text{-}a \ (ls, ntsjswss) = ls$

**by**(*simp add:locks-a-def*)

**lemma** *thr-a-conv* [*simp*]: *thr-a* (*ls*, *nts*, *js*, *wss*) = *nts*

**by**(*simp add: thr-a-def*)

**lemma** *cond-a-conv* [*simp*]: *cond-a* (*ls*, *nts*, *js*, *wss*) = *js*

**by**(*simp add: cond-a-def*)

**lemma** *wset-a-conv* [*simp*]: *wset-a* (*ls*, *nts*, *js*, *wss*, *is*, *obs*) = *wss*

**by**(*simp add: wset-a-def*)

**lemma** *interrupt-a-conv* [*simp*]: *interrupt-a* (*ls*, *nts*, *js*, *ws*, *is*, *obs*) = *is*

**by**(*simp add: interrupt-a-def*)

**lemma** *obs-a-conv* [*simp*]: *obs-a* (*ls*, *nts*, *js*, *wss*, *is*, *obs*) = *obs*

**by**(*simp add: obs-a-def*)

**fun** *ta-update-locks* :: ('l,'t,'x,'m,'w,'o) *thread-action*  $\Rightarrow$  *lock-action*  $\Rightarrow$  'l  $\Rightarrow$  ('l,'t,'x,'m,'w,'o) *thread-action*  
**where**

*ta-update-locks* (*ls*, *nts*, *js*, *wss*, *obs*) *lta* *l* = (*ls*(*l* \$:= *ls* \$ *l* @ [*lta*]), *nts*, *js*, *wss*, *obs*)

**fun** *ta-update-NewThread* :: ('l,'t,'x,'m,'w,'o) *thread-action*  $\Rightarrow$  ('t,'x,'m) *new-thread-action*  $\Rightarrow$  ('l,'t,'x,'m,'w,'o) *thread-action* **where**

*ta-update-NewThread* (*ls*, *nts*, *js*, *wss*, *is*, *obs*) *nt* = (*ls*, *nts* @ [*nt*], *js*, *wss*, *is*, *obs*)

**fun** *ta-update-Conditional* :: ('l,'t,'x,'m,'w,'o) *thread-action*  $\Rightarrow$  't *conditional-action*  $\Rightarrow$  ('l,'t,'x,'m,'w,'o) *thread-action*

**where**

*ta-update-Conditional* (*ls*, *nts*, *js*, *wss*, *is*, *obs*) *j* = (*ls*, *nts*, *js* @ [*j*], *wss*, *is*, *obs*)

**fun** *ta-update-wait-set* :: ('l,'t,'x,'m,'w,'o) *thread-action*  $\Rightarrow$  ('t, 'w) *wait-set-action*  $\Rightarrow$  ('l,'t,'x,'m,'w,'o) *thread-action* **where**

*ta-update-wait-set* (*ls*, *nts*, *js*, *wss*, *is*, *obs*) *ws* = (*ls*, *nts*, *js*, *wss* @ [*ws*], *is*, *obs*)

**fun** *ta-update-interrupt* :: ('l,'t,'x,'m,'w,'o) *thread-action*  $\Rightarrow$  't *interrupt-action*  $\Rightarrow$  ('l,'t,'x,'m,'w,'o) *thread-action*

**where**

*ta-update-interrupt* (*ls*, *nts*, *js*, *wss*, *is*, *obs*) *i* = (*ls*, *nts*, *js*, *wss*, *is* @ [*i*], *obs*)

**fun** *ta-update-obs* :: ('l,'t,'x,'m,'w,'o) *thread-action*  $\Rightarrow$  'o  $\Rightarrow$  ('l,'t,'x,'m,'w,'o) *thread-action*

**where**

*ta-update-obs* (*ls*, *nts*, *js*, *wss*, *is*, *obs*) *ob* = (*ls*, *nts*, *js*, *wss*, *is*, *obs* @ [*ob*])

**abbreviation** *empty-ta* :: ('l,'t,'x,'m,'w,'o) *thread-action* **where**

*empty-ta*  $\equiv$  (*K* \$ [], [], [], [], [], [])

**notation** (*input*) *empty-ta* ( $\varepsilon$ )

Pretty syntax for specifying thread actions: Write  $\{ \text{Lock} \rightarrow l, \text{Unlock} \rightarrow l, \text{Suspend } w, \text{Interrupt } t \}$  instead of  $((K\$ []) (l \$:= [\text{Lock}, \text{Unlock}]), [], [\text{Suspend } w], [\text{Interrupt } t], [])$ .

*thread-action'* is a type that contains of all basic thread actions. Automatically coerce basic thread actions into that type and then dispatch to the right update function by pattern matching. For coercion, adhoc overloading replaces the generic injection *inject-thread-action* by the specific ones, i.e. constructors. To avoid ambiguities with observable actions, the

observable actions must be of sort *obs-action*, which the basic thread action types are not.

**class** *obs-action*

**datatype** (*'l, 't, 'x, 'm, 'w, 'o*) *thread-action'*  
 = *LockAction lock-action* × *'l*  
 | *NewThreadAction ('t, 'x, 'm) new-thread-action*  
 | *ConditionalAction 't conditional-action*  
 | *WaitSetAction ('t, 'w) wait-set-action*  
 | *InterruptAction 't interrupt-action*  
 | *ObsAction 'o*

**setup** <

*Sign.add-const-constraint* (@{*const-name ObsAction*}, *SOME* @{*typ 'o :: obs-action* ⇒ (*'l, 't, 'x, 'm, 'w, 'o*)  
*thread-action'*})

>

**fun** *thread-action'-to-thread-action* ::

(*'l, 't, 'x, 'm, 'w, 'o :: obs-action*) *thread-action'* ⇒ (*'l, 't, 'x, 'm, 'w, 'o*) *thread-action* ⇒ (*'l, 't, 'x, 'm, 'w, 'o*)  
*thread-action*

**where**

*thread-action'-to-thread-action* (*LockAction (la, l)*) *ta* = *ta-update-locks ta la l*  
 | *thread-action'-to-thread-action* (*NewThreadAction nt*) *ta* = *ta-update-NewThread ta nt*  
 | *thread-action'-to-thread-action* (*ConditionalAction ca*) *ta* = *ta-update-Conditional ta ca*  
 | *thread-action'-to-thread-action* (*WaitSetAction wa*) *ta* = *ta-update-wait-set ta wa*  
 | *thread-action'-to-thread-action* (*InterruptAction ia*) *ta* = *ta-update-interrupt ta ia*  
 | *thread-action'-to-thread-action* (*ObsAction ob*) *ta* = *ta-update-obs ta ob*

**consts** *inject-thread-action* :: *'a* ⇒ (*'l, 't, 'x, 'm, 'w, 'o*) *thread-action'*

**nonterminal** *ta-let* and *ta-lets*

**syntax**

*-ta-snoc* :: *ta-lets* ⇒ *ta-let* ⇒ *ta-lets* (-, / -)  
*-ta-block* :: *ta-lets* ⇒ *'a* (⌊-⌋ [0] 1000)  
*-ta-empty* :: *ta-lets* ()  
*-ta-single* :: *ta-let* ⇒ *ta-lets* (-)  
*-ta-inject* :: *logic* ⇒ *ta-let* ((-))  
*-ta-lock* :: *logic* ⇒ *logic* ⇒ *ta-let* (->-)

**translations**

*-ta-block -ta-empty* == *CONST empty-ta*  
*-ta-block (-ta-single bta)* == *-ta-block (-ta-snoc -ta-empty bta)*  
*-ta-inject bta* == *CONST inject-thread-action bta*  
*-ta-lock la l* == *CONST inject-thread-action (CONST Pair la l)*  
*-ta-block (-ta-snoc btas bta)* == *CONST thread-action'-to-thread-action bta (-ta-block btas)*

**adhoc-overloading**

*inject-thread-action NewThreadAction ConditionalAction WaitSetAction InterruptAction ObsAction*  
*LockAction*

**lemma** *ta-upd-proj-simps* [simp]:

**shows** *ta-obs-proj-simps*:

$\llbracket ta\text{-update-obs } ta \text{ obs} \rrbracket_l = \llbracket ta \rrbracket_l \llbracket ta\text{-update-obs } ta \text{ obs} \rrbracket_t = \llbracket ta \rrbracket_t \llbracket ta\text{-update-obs } ta \text{ obs} \rrbracket_w = \llbracket ta \rrbracket_w$   
 $\llbracket ta\text{-update-obs } ta \text{ obs} \rrbracket_c = \llbracket ta \rrbracket_c \llbracket ta\text{-update-obs } ta \text{ obs} \rrbracket_i = \llbracket ta \rrbracket_i \llbracket ta\text{-update-obs } ta \text{ obs} \rrbracket_o = \llbracket ta \rrbracket_o @$

[obs]

**and** *ta-lock-proj-simps*:

$$\begin{aligned} \llbracket ta\text{-update-locks } ta \ x \ l \rrbracket_l &= (\text{let } ls = \llbracket ta \rrbracket_l \text{ in } ls(l \ \$ := ls \ \$ \ l \ @ \ [x])) \\ \llbracket ta\text{-update-locks } ta \ x \ l \rrbracket_t &= \llbracket ta \rrbracket_t \llbracket ta\text{-update-locks } ta \ x \ l \rrbracket_w = \llbracket ta \rrbracket_w \llbracket ta\text{-update-locks } ta \ x \ l \rrbracket_c = \llbracket ta \rrbracket_c \end{aligned}$$

$$\llbracket ta\text{-update-locks } ta \ x \ l \rrbracket_i = \llbracket ta \rrbracket_i \llbracket ta\text{-update-locks } ta \ x \ l \rrbracket_o = \llbracket ta \rrbracket_o$$

**and** *ta-thread-proj-simps*:

$$\begin{aligned} \llbracket ta\text{-update-NewThread } ta \ t \rrbracket_l &= \llbracket ta \rrbracket_l \llbracket ta\text{-update-NewThread } ta \ t \rrbracket_t = \llbracket ta \rrbracket_t \ @ \ [t] \llbracket ta\text{-update-NewThread } \\ ta \ t \rrbracket_w &= \llbracket ta \rrbracket_w \end{aligned}$$

$$\begin{aligned} \llbracket ta\text{-update-NewThread } ta \ t \rrbracket_c &= \llbracket ta \rrbracket_c \llbracket ta\text{-update-NewThread } ta \ t \rrbracket_i = \llbracket ta \rrbracket_i \llbracket ta\text{-update-NewThread } \\ ta \ t \rrbracket_o &= \llbracket ta \rrbracket_o \end{aligned}$$

**and** *ta-wset-proj-simps*:

$$\begin{aligned} \llbracket ta\text{-update-wait-set } ta \ w \rrbracket_l &= \llbracket ta \rrbracket_l \llbracket ta\text{-update-wait-set } ta \ w \rrbracket_t = \llbracket ta \rrbracket_t \llbracket ta\text{-update-wait-set } ta \ w \rrbracket_w = \\ \llbracket ta \rrbracket_w \ @ \ [w] \end{aligned}$$

$$\begin{aligned} \llbracket ta\text{-update-wait-set } ta \ w \rrbracket_c &= \llbracket ta \rrbracket_c \llbracket ta\text{-update-wait-set } ta \ w \rrbracket_i = \llbracket ta \rrbracket_i \llbracket ta\text{-update-wait-set } ta \ w \rrbracket_o = \\ \llbracket ta \rrbracket_o \end{aligned}$$

**and** *ta-cond-proj-simps*:

$$\begin{aligned} \llbracket ta\text{-update-Conditional } ta \ c \rrbracket_l &= \llbracket ta \rrbracket_l \llbracket ta\text{-update-Conditional } ta \ c \rrbracket_t = \llbracket ta \rrbracket_t \llbracket ta\text{-update-Conditional } \\ ta \ c \rrbracket_w &= \llbracket ta \rrbracket_w \end{aligned}$$

$$\begin{aligned} \llbracket ta\text{-update-Conditional } ta \ c \rrbracket_c &= \llbracket ta \rrbracket_c \ @ \ [c] \llbracket ta\text{-update-Conditional } ta \ c \rrbracket_i = \llbracket ta \rrbracket_i \llbracket ta\text{-update-Conditional } \\ ta \ c \rrbracket_o &= \llbracket ta \rrbracket_o \end{aligned}$$

**and** *ta-interrupt-proj-simps*:

$$\begin{aligned} \llbracket ta\text{-update-interrupt } ta \ i \rrbracket_l &= \llbracket ta \rrbracket_l \llbracket ta\text{-update-interrupt } ta \ i \rrbracket_t = \llbracket ta \rrbracket_t \llbracket ta\text{-update-interrupt } ta \ i \rrbracket_c \\ &= \llbracket ta \rrbracket_c \end{aligned}$$

$$\begin{aligned} \llbracket ta\text{-update-interrupt } ta \ i \rrbracket_w &= \llbracket ta \rrbracket_w \llbracket ta\text{-update-interrupt } ta \ i \rrbracket_i = \llbracket ta \rrbracket_i \ @ \ [i] \llbracket ta\text{-update-interrupt } \\ ta \ i \rrbracket_o &= \llbracket ta \rrbracket_o \end{aligned}$$

**by**(cases *ta*, *simp*)+

**lemma** *thread-action'-to-thread-action-proj-simps* [simp]:

**shows** *thread-action'-to-thread-action-proj-locks-simps*:

$$\begin{aligned} \llbracket \text{thread-action}'\text{-to-thread-action } (\text{LockAction } (la, l)) \ ta \rrbracket_l &= \llbracket ta\text{-update-locks } ta \ la \ l \rrbracket_l \\ \llbracket \text{thread-action}'\text{-to-thread-action } (\text{NewThreadAction } nt) \ ta \rrbracket_l &= \llbracket ta\text{-update-NewThread } ta \ nt \rrbracket_l \\ \llbracket \text{thread-action}'\text{-to-thread-action } (\text{ConditionalAction } ca) \ ta \rrbracket_l &= \llbracket ta\text{-update-Conditional } ta \ ca \rrbracket_l \\ \llbracket \text{thread-action}'\text{-to-thread-action } (\text{WaitSetAction } wa) \ ta \rrbracket_l &= \llbracket ta\text{-update-wait-set } ta \ wa \rrbracket_l \\ \llbracket \text{thread-action}'\text{-to-thread-action } (\text{InterruptAction } ia) \ ta \rrbracket_l &= \llbracket ta\text{-update-interrupt } ta \ ia \rrbracket_l \\ \llbracket \text{thread-action}'\text{-to-thread-action } (\text{ObsAction } ob) \ ta \rrbracket_l &= \llbracket ta\text{-update-obs } ta \ ob \rrbracket_l \end{aligned}$$

**and** *thread-action'-to-thread-action-proj-nt-simps*:

$$\begin{aligned} \llbracket \text{thread-action}'\text{-to-thread-action } (\text{LockAction } (la, l)) \ ta \rrbracket_t &= \llbracket ta\text{-update-locks } ta \ la \ l \rrbracket_t \\ \llbracket \text{thread-action}'\text{-to-thread-action } (\text{NewThreadAction } nt) \ ta \rrbracket_t &= \llbracket ta\text{-update-NewThread } ta \ nt \rrbracket_t \\ \llbracket \text{thread-action}'\text{-to-thread-action } (\text{ConditionalAction } ca) \ ta \rrbracket_t &= \llbracket ta\text{-update-Conditional } ta \ ca \rrbracket_t \\ \llbracket \text{thread-action}'\text{-to-thread-action } (\text{WaitSetAction } wa) \ ta \rrbracket_t &= \llbracket ta\text{-update-wait-set } ta \ wa \rrbracket_t \\ \llbracket \text{thread-action}'\text{-to-thread-action } (\text{InterruptAction } ia) \ ta \rrbracket_t &= \llbracket ta\text{-update-interrupt } ta \ ia \rrbracket_t \\ \llbracket \text{thread-action}'\text{-to-thread-action } (\text{ObsAction } ob) \ ta \rrbracket_t &= \llbracket ta\text{-update-obs } ta \ ob \rrbracket_t \end{aligned}$$

**and** *thread-action'-to-thread-action-proj-cond-simps*:

$$\begin{aligned} \llbracket \text{thread-action}'\text{-to-thread-action } (\text{LockAction } (la, l)) \ ta \rrbracket_c &= \llbracket ta\text{-update-locks } ta \ la \ l \rrbracket_c \\ \llbracket \text{thread-action}'\text{-to-thread-action } (\text{NewThreadAction } nt) \ ta \rrbracket_c &= \llbracket ta\text{-update-NewThread } ta \ nt \rrbracket_c \\ \llbracket \text{thread-action}'\text{-to-thread-action } (\text{ConditionalAction } ca) \ ta \rrbracket_c &= \llbracket ta\text{-update-Conditional } ta \ ca \rrbracket_c \\ \llbracket \text{thread-action}'\text{-to-thread-action } (\text{WaitSetAction } wa) \ ta \rrbracket_c &= \llbracket ta\text{-update-wait-set } ta \ wa \rrbracket_c \\ \llbracket \text{thread-action}'\text{-to-thread-action } (\text{InterruptAction } ia) \ ta \rrbracket_c &= \llbracket ta\text{-update-interrupt } ta \ ia \rrbracket_c \\ \llbracket \text{thread-action}'\text{-to-thread-action } (\text{ObsAction } ob) \ ta \rrbracket_c &= \llbracket ta\text{-update-obs } ta \ ob \rrbracket_c \end{aligned}$$

**and** *thread-action'-to-thread-action-proj-wset-simps*:

$$\begin{aligned} \llbracket \text{thread-action}'\text{-to-thread-action } (\text{LockAction } (la, l)) \ ta \rrbracket_w &= \llbracket ta\text{-update-locks } ta \ la \ l \rrbracket_w \\ \llbracket \text{thread-action}'\text{-to-thread-action } (\text{NewThreadAction } nt) \ ta \rrbracket_w &= \llbracket ta\text{-update-NewThread } ta \ nt \rrbracket_w \\ \llbracket \text{thread-action}'\text{-to-thread-action } (\text{ConditionalAction } ca) \ ta \rrbracket_w &= \llbracket ta\text{-update-Conditional } ta \ ca \rrbracket_w \end{aligned}$$

```

 $\{ \text{thread-action}'\text{-to-thread-action } (\text{WaitSetAction } wa) \text{ ta} \}_w = \{ \text{ta-update-wait-set } ta \text{ wa} \}_w$ 
 $\{ \text{thread-action}'\text{-to-thread-action } (\text{InterruptAction } ia) \text{ ta} \}_w = \{ \text{ta-update-interrupt } ta \text{ ia} \}_w$ 
 $\{ \text{thread-action}'\text{-to-thread-action } (\text{ObsAction } ob) \text{ ta} \}_w = \{ \text{ta-update-obs } ta \text{ ob} \}_w$ 
and thread-action'-to-thread-action-proj-interrupt-simps:
 $\{ \text{thread-action}'\text{-to-thread-action } (\text{LockAction } (la, l)) \text{ ta} \}_i = \{ \text{ta-update-locks } ta \text{ la } l \}_i$ 
 $\{ \text{thread-action}'\text{-to-thread-action } (\text{NewThreadAction } nt) \text{ ta} \}_i = \{ \text{ta-update-NewThread } ta \text{ nt} \}_i$ 
 $\{ \text{thread-action}'\text{-to-thread-action } (\text{ConditionalAction } ca) \text{ ta} \}_i = \{ \text{ta-update-Conditional } ta \text{ ca} \}_i$ 
 $\{ \text{thread-action}'\text{-to-thread-action } (\text{WaitSetAction } wa) \text{ ta} \}_i = \{ \text{ta-update-wait-set } ta \text{ wa} \}_i$ 
 $\{ \text{thread-action}'\text{-to-thread-action } (\text{InterruptAction } ia) \text{ ta} \}_i = \{ \text{ta-update-interrupt } ta \text{ ia} \}_i$ 
 $\{ \text{thread-action}'\text{-to-thread-action } (\text{ObsAction } ob) \text{ ta} \}_i = \{ \text{ta-update-obs } ta \text{ ob} \}_i$ 
and thread-action'-to-thread-action-proj-obs-simps:
 $\{ \text{thread-action}'\text{-to-thread-action } (\text{LockAction } (la, l)) \text{ ta} \}_o = \{ \text{ta-update-locks } ta \text{ la } l \}_o$ 
 $\{ \text{thread-action}'\text{-to-thread-action } (\text{NewThreadAction } nt) \text{ ta} \}_o = \{ \text{ta-update-NewThread } ta \text{ nt} \}_o$ 
 $\{ \text{thread-action}'\text{-to-thread-action } (\text{ConditionalAction } ca) \text{ ta} \}_o = \{ \text{ta-update-Conditional } ta \text{ ca} \}_o$ 
 $\{ \text{thread-action}'\text{-to-thread-action } (\text{WaitSetAction } wa) \text{ ta} \}_o = \{ \text{ta-update-wait-set } ta \text{ wa} \}_o$ 
 $\{ \text{thread-action}'\text{-to-thread-action } (\text{InterruptAction } ia) \text{ ta} \}_o = \{ \text{ta-update-interrupt } ta \text{ ia} \}_o$ 
 $\{ \text{thread-action}'\text{-to-thread-action } (\text{ObsAction } ob) \text{ ta} \}_o = \{ \text{ta-update-obs } ta \text{ ob} \}_o$ 
by (simp-all)

lemmas ta-upd-simps =
  ta-update-locks.simps ta-update-NewThread.simps ta-update-Conditional.simps
  ta-update-wait-set.simps ta-update-interrupt.simps ta-update-obs.simps
  thread-action'-to-thread-action.simps

declare ta-upd-simps [simp del]

hide-const (open)
  LockAction NewThreadAction ConditionalAction WaitSetAction InterruptAction ObsAction
  thread-action'-to-thread-action
hide-type (open) thread-action'

datatype wake-up-status =
  WSNotified
| WSWokenUp

datatype 'w wait-set-status =
  InWS 'w
| PostWS wake-up-status

type-synonym 't lock = ('t × nat) option
type-synonym ('l, 't) locks = 'l ⇒f 't lock
type-synonym 'l released-locks = 'l ⇒f nat
type-synonym ('l, 't, 'x) thread-info = 't → ('x × 'l released-locks)
type-synonym ('w, 't) wait-sets = 't → 'w wait-set-status
type-synonym 't interrupts = 't set
type-synonym ('l, 't, 'x, 'm, 'w) state = ('l, 't) locks × (('l, 't, 'x) thread-info × 'm) × ('w, 't) wait-sets
  × 't interrupts

translations
  (type) ('l, 't) locks ≤ (type) 'l ⇒f ('t × nat) option
  (type) ('l, 't, 'x) thread-info ≤ (type) 't → ('x × ('l ⇒f nat))

print-translation <

```



```

let
  fun tr'
    [Const (@{type-syntax finfun}, -) $ l1 $
      (Const (@{type-syntax option}, -) $
        (Const (@{type-syntax prod}, -) $ t1 $ Const (@{type-syntax nat}, -))),
      Const (@{type-syntax prod}, -) $
        (Const (@{type-syntax prod}, -) $
          (Const (@{type-syntax fun}, -) $ t2 $
            (Const (@{type-syntax option}, -) $
              (Const (@{type-syntax prod}, -) $ x $
                (Const (@{type-syntax finfun}, -) $ l2 $ Const (@{type-syntax nat}, -)))))) $
          m) $
        (Const (@{type-syntax prod}, -) $
          (Const (@{type-syntax fun}, -) $ t3 $
            (Const (@{type-syntax option}, -) $ (Const (@{type-syntax wait-set-status}, -) $ w))) $
            (Const (@{type-syntax fun}, -) $ t4 $ (Const (@{type-syntax bool}, -)))] =
      if t1 = t2 andalso t1 = t3 andalso t1 = t4 andalso l1 = l2
      then Syntax.const @ {type-syntax state} $ l1 $ t1 $ x $ m $ w
      else raise Match;
  in [(@{type-syntax prod}, K tr')]
end
>
typ ('l, 't, 'x, 'm, 'w) state

```

**abbreviation** *no-wait-locks* :: 'l  $\Rightarrow$  f nat  
**where** *no-wait-locks*  $\equiv$  (K\$ 0)

**lemma** *neg-no-wait-locks-conv*:  
 $\bigwedge ln. ln \neq \text{no-wait-locks} \longleftrightarrow (\exists l. ln \$ l > 0)$   
**by**(*auto simp add: expand-funfun-eq fun-eq-iff*)

**lemma** *neg-no-wait-locksE*:  
**fixes** *ln* **assumes** *ln*  $\neq$  *no-wait-locks* **obtains** *l* **where** *ln* \$ *l* > 0  
**using** *assms*  
**by**(*auto simp add: neg-no-wait-locks-conv*)

Use type variables for components instead of ('l, 't, 'x, 'm, 'w) *state* in types for state projections to allow to reuse them for refined state implementations for code generation.

**definition** *locks* :: ('locks  $\times$  ('thread-info  $\times$  'm)  $\times$  'wsets  $\times$  'interrupts)  $\Rightarrow$  'locks **where**  
*locks lstsmws*  $\equiv$  fst lstsmws

**definition** *thr* :: ('locks  $\times$  ('thread-info  $\times$  'm)  $\times$  'wsets  $\times$  'interrupts)  $\Rightarrow$  'thread-info **where**  
*thr lstsmws*  $\equiv$  fst (fst (snd lstsmws))

**definition** *shr* :: ('locks  $\times$  ('thread-info  $\times$  'm)  $\times$  'wsets  $\times$  'interrupts)  $\Rightarrow$  'm **where**  
*shr lstsmws*  $\equiv$  snd (fst (snd lstsmws))

**definition** *wset* :: ('locks  $\times$  ('thread-info  $\times$  'm)  $\times$  'wsets  $\times$  'interrupts)  $\Rightarrow$  'wsets **where**  
*wset lstsmws*  $\equiv$  fst (snd (snd lstsmws))

**definition** *interrupts* :: ('locks  $\times$  ('thread-info  $\times$  'm)  $\times$  'wsets  $\times$  'interrupts)  $\Rightarrow$  'interrupts **where**  
*interrupts lstsmws*  $\equiv$  snd (snd (snd lstsmws))

**lemma** *locks-conv* [simp]: *locks* (*ls*, *tsmws*) = *ls*  
**by**(*simp add: locks-def*)

**lemma** *thr-conv* [simp]: *thr* (*ls*, (*ts*, *m*), *ws*) = *ts*  
**by**(*simp add: thr-def*)

**lemma** *shr-conv* [simp]: *shr* (*ls*, (*ts*, *m*), *ws*, *is*) = *m*  
**by**(*simp add: shr-def*)

**lemma** *wset-conv* [simp]: *wset* (*ls*, (*ts*, *m*), *ws*, *is*) = *ws*  
**by**(*simp add: wset-def*)

**lemma** *interrupts-conv* [simp]: *interrupts* (*ls*, (*ts*, *m*), *ws*, *is*) = *is*  
**by**(*simp add: interrupts-def*)

**primrec** *convert-new-thread-action* :: (*'x*  $\Rightarrow$  *'x'*)  $\Rightarrow$  (*'t*, *'x*, *'m*) *new-thread-action*  $\Rightarrow$  (*'t*, *'x'*, *'m*) *new-thread-action*  
**where**

*convert-new-thread-action* *f* (*NewThread* *t* *x* *m*) = *NewThread* *t* (*f* *x*) *m*  
| *convert-new-thread-action* *f* (*ThreadExists* *t* *b*) = *ThreadExists* *t* *b*

**lemma** *convert-new-thread-action-inv* [simp]:  
*NewThread* *t* *x* *h* = *convert-new-thread-action* *f* *nta*  $\longleftrightarrow$  ( $\exists$  *x'*. *nta* = *NewThread* *t* *x'* *h*  $\wedge$  *x* = *f* *x'*)  
*ThreadExists* *t* *b* = *convert-new-thread-action* *f* *nta*  $\longleftrightarrow$  *nta* = *ThreadExists* *t* *b*  
*convert-new-thread-action* *f* *nta* = *NewThread* *t* *x* *h*  $\longleftrightarrow$  ( $\exists$  *x'*. *nta* = *NewThread* *t* *x'* *h*  $\wedge$  *x* = *f* *x'*)  
*convert-new-thread-action* *f* *nta* = *ThreadExists* *t* *b*  $\longleftrightarrow$  *nta* = *ThreadExists* *t* *b*  
**by**(*cases nta, auto*)**+**

**lemma** *convert-new-thread-action-eqI*:  
 $\llbracket \bigwedge t\ x\ m. \text{nta} = \text{NewThread } t\ x\ m \implies \text{nta}' = \text{NewThread } t\ (f\ x)\ m;$   
 $\bigwedge t\ b. \text{nta} = \text{ThreadExists } t\ b \implies \text{nta}' = \text{ThreadExists } t\ b \rrbracket$   
 $\implies \text{convert-new-thread-action } f\ \text{nta} = \text{nta}'$

**apply**(*cases nta*)  
**apply** *fastforce***+**  
**done**

**lemma** *convert-new-thread-action-compose* [simp]:  
*convert-new-thread-action* *f* (*convert-new-thread-action* *g* *ta*) = *convert-new-thread-action* (*f*  $\circ$  *g*) *ta*  
**apply**(*cases ta*)  
**apply**(*simp-all add: convert-new-thread-action-def*)  
**done**

**lemma** *inj-convert-new-thread-action* [simp]:  
*inj* (*convert-new-thread-action* *f*) = *inj* *f*  
**apply**(*rule iffI*)  
**apply**(*rule injI*)  
**apply**(*drule-tac x=NewThread undefined x undefined in injD*)  
**apply** *auto*[2]  
**apply**(*rule injI*)  
**apply**(*case-tac x*)  
**apply**(*auto dest: injD*)  
**done**

**lemma** *convert-new-thread-action-id*:  
*convert-new-thread-action* *id* = (*id* :: (*'t*, *'x*, *'m*) *new-thread-action*  $\Rightarrow$  (*'t*, *'x*, *'m*) *new-thread-action*)

```

(is ?thesis1)
  convert-new-thread-action ( $\lambda x. x$ ) = (id :: ('t, 'x, 'm) new-thread-action  $\Rightarrow$  ('t, 'x, 'm) new-thread-action)
(is ?thesis2)
proof -
  show ?thesis1 by (rule ext)(case-tac x, simp-all)
  thus ?thesis2 by (simp add: id-def)
qed

```

**definition** *convert-extTA* :: ('x  $\Rightarrow$  'x')  $\Rightarrow$  ('l, 't, 'x, 'm, 'w, 'o) thread-action  $\Rightarrow$  ('l, 't, 'x, 'm, 'w, 'o) thread-action  
**where** *convert-extTA* f ta = ( $\llbracket ta \rrbracket_l$ , map (convert-new-thread-action f)  $\llbracket ta \rrbracket_t$ , snd (snd ta))

**lemma** *convert-extTA-simps* [simp]:

```

convert-extTA f  $\varepsilon$  =  $\varepsilon$ 
 $\llbracket \text{convert-extTA } f \text{ ta} \rrbracket_l = \llbracket ta \rrbracket_l$ 
 $\llbracket \text{convert-extTA } f \text{ ta} \rrbracket_t = \text{map } (\text{convert-new-thread-action } f) \llbracket ta \rrbracket_t$ 
 $\llbracket \text{convert-extTA } f \text{ ta} \rrbracket_c = \llbracket ta \rrbracket_c$ 
 $\llbracket \text{convert-extTA } f \text{ ta} \rrbracket_w = \llbracket ta \rrbracket_w$ 
 $\llbracket \text{convert-extTA } f \text{ ta} \rrbracket_i = \llbracket ta \rrbracket_i$ 
convert-extTA f (las, tas, was, cas, is, obs) = (las, map (convert-new-thread-action f) tas, was, cas,
is, obs)
apply (simp-all add: convert-extTA-def)
apply (cases ta, simp)+
done

```

**lemma** *convert-extTA-eq-conv*:

```

convert-extTA f ta = ta'  $\longleftrightarrow$ 
 $\llbracket ta \rrbracket_l = \llbracket ta' \rrbracket_l \wedge \llbracket ta \rrbracket_c = \llbracket ta' \rrbracket_c \wedge \llbracket ta \rrbracket_w = \llbracket ta' \rrbracket_w \wedge \llbracket ta \rrbracket_o = \llbracket ta' \rrbracket_o \wedge \llbracket ta \rrbracket_i = \llbracket ta' \rrbracket_i \wedge \text{length}$ 
 $\llbracket ta \rrbracket_t = \text{length } \llbracket ta' \rrbracket_t \wedge$ 
 $(\forall n < \text{length } \llbracket ta \rrbracket_t. \text{convert-new-thread-action } f (\llbracket ta \rrbracket_t ! n) = \llbracket ta' \rrbracket_t ! n)$ 
apply (cases ta, cases ta')
apply (auto simp add: convert-extTA-def map-eq-all-nth-conv)
done

```

**lemma** *convert-extTA-compose* [simp]:

```

convert-extTA f (convert-extTA g ta) = convert-extTA (f o g) ta
by (simp add: convert-extTA-def)

```

**lemma** *obs-a-convert-extTA* [simp]: *obs-a* (convert-extTA f ta) = *obs-a* ta  
**by** (cases ta) simp

Actions for thread start/finish

```

datatype 'o action =
  NormalAction 'o
| InitialThreadAction
| ThreadFinishAction

```

**instance** *action* :: (type) *obs-action*  
**proof** qed

**definition** *convert-obs-initial* :: ('l, 't, 'x, 'm, 'w, 'o) thread-action  $\Rightarrow$  ('l, 't, 'x, 'm, 'w, 'o) action thread-action  
**where**

```

convert-obs-initial ta = ( $\llbracket ta \rrbracket_l$ ,  $\llbracket ta \rrbracket_t$ ,  $\llbracket ta \rrbracket_c$ ,  $\llbracket ta \rrbracket_w$ ,  $\llbracket ta \rrbracket_i$ , map NormalAction  $\llbracket ta \rrbracket_o$ )

```

**lemma** *inj-NormalAction* [simp]: *inj* NormalAction

**by**(*rule injI*) *auto*

**lemma** *convert-obs-initial-inject* [*simp*]:

*convert-obs-initial ta = convert-obs-initial ta'  $\longleftrightarrow$  ta = ta'*

**by**(*cases ta*)(*cases ta'*, *auto simp add: convert-obs-initial-def*)

**lemma** *convert-obs-initial-empty-TA* [*simp*]:

*convert-obs-initial  $\varepsilon$  =  $\varepsilon$*

**by**(*simp add: convert-obs-initial-def*)

**lemma** *convert-obs-initial-eq-empty-TA* [*simp*]:

*convert-obs-initial ta =  $\varepsilon \longleftrightarrow ta = \varepsilon$*

*$\varepsilon = \text{convert-obs-initial } ta \longleftrightarrow ta = \varepsilon$*

**by**(*case-tac [!] ta*)(*auto simp add: convert-obs-initial-def*)

**lemma** *convert-obs-initial-simps* [*simp*]:

$\llbracket \text{convert-obs-initial } ta \rrbracket_o = \text{map NormalAction } \llbracket ta \rrbracket_o$

$\llbracket \text{convert-obs-initial } ta \rrbracket_l = \llbracket ta \rrbracket_l$

$\llbracket \text{convert-obs-initial } ta \rrbracket_t = \llbracket ta \rrbracket_t$

$\llbracket \text{convert-obs-initial } ta \rrbracket_c = \llbracket ta \rrbracket_c$

$\llbracket \text{convert-obs-initial } ta \rrbracket_w = \llbracket ta \rrbracket_w$

$\llbracket \text{convert-obs-initial } ta \rrbracket_i = \llbracket ta \rrbracket_i$

**by**(*simp-all add: convert-obs-initial-def*)

**type-synonym**

(*l, 't, 'x, 'm, 'w, 'o*) *semantics* =

*'t  $\Rightarrow$  'x  $\times$  'm  $\Rightarrow$  (*l, 't, 'x, 'm, 'w, 'o*) thread-action  $\Rightarrow$  'x  $\times$  'm  $\Rightarrow$  bool*

**print-translation**  $\langle$

*let*

*fun tr'*

[*t4*,

*Const* (@{*type-syntax fun*}, -) \$

(*Const* (@{*type-syntax prod*}, -) \$ *x1* \$ *m1*) \$

(*Const* (@{*type-syntax fun*}, -) \$

(*Const* (@{*type-syntax prod*}, -) \$

(*Const* (@{*type-syntax finfun*}, -) \$ *l* \$

(*Const* (@{*type-syntax list*}, -) \$ *Const* (@{*type-syntax lock-action*}, -))) \$

(*Const* (@{*type-syntax prod*}, -) \$

(*Const* (@{*type-syntax list*}, -) \$ ( *Const* (@{*type-syntax new-thread-action*}, -) \$ *t1* \$ *x2*

\$ *m2*)) \$

(*Const* (@{*type-syntax prod*}, -) \$

(*Const* (@{*type-syntax list*}, -) \$ ( *Const* (@{*type-syntax conditional-action*}, -) \$ *t2*)) \$

(*Const* (@{*type-syntax prod*}, -) \$

(*Const* (@{*type-syntax list*}, -) \$ ( *Const* (@{*type-syntax wait-set-action*}, -) \$ *t3* \$ *w*))

\$

(*Const* (@{*type-syntax prod*}, -) \$

(*Const* (@{*type-syntax list*}, -) \$ ( *Const* (@{*type-syntax interrupt-action*}, -) \$ *t5*))

\$

(*Const* (@{*type-syntax list*}, -) \$ *o1*)))))) \$

(*Const* (@{*type-syntax fun*}, -) \$ ( *Const* (@{*type-syntax prod*}, -) \$ *x3* \$ *m3*) \$

*Const* (@{*type-syntax bool*}, -))) =

*if x1 = x2 andalso x1 = x3 andalso m1 = m2 andalso m1 = m3*

```

    andalso t1 = t2 andalso t2 = t3 andalso t3 = t4 andalso t4 = t5
  then Syntax.const @{type-syntax semantics} $ l $ t1 $ x1 $ m1 $ w $ o1
  else raise Match;
in [(@{type-syntax fun}, K tr')]
end
>
typ ('l,'t,'x,'m,'w,'o) semantics

end

```

## 1.2 All about a managing a single lock

```

theory FWLock
imports
  FWState
begin

fun has-locks :: 't lock  $\Rightarrow$  't  $\Rightarrow$  nat where
  has-locks None t = 0
| has-locks [(t', n)] t = (if t = t' then Suc n else 0)

lemma has-locks-iff:
  has-locks l t = n  $\longleftrightarrow$ 
  (l = None  $\wedge$  n = 0)  $\vee$ 
  ( $\exists n'. l = [(t', n')] \wedge$  Suc n' = n)  $\vee$  ( $\exists t' n'. l = [(t', n')] \wedge$  t'  $\neq$  t  $\wedge$  n = 0)
by(cases l, auto)

lemma has-locksE:
   $\llbracket$  has-locks l t = n;
   $\llbracket$  l = None; n = 0  $\rrbracket \Longrightarrow$  P;
   $\bigwedge n'. \llbracket$  l = [(t', n)]; Suc n' = n  $\rrbracket \Longrightarrow$  P;
   $\bigwedge t' n'. \llbracket$  l = [(t', n)]; t'  $\neq$  t; n = 0  $\rrbracket \Longrightarrow$  P  $\rrbracket \Longrightarrow$  P
by(auto simp add: has-locks-iff split: if-split-asm prod.split-asm)

inductive may-lock :: 't lock  $\Rightarrow$  't  $\Rightarrow$  bool where
  may-lock None t
| may-lock [(t, n)] t

lemma may-lock-iff [code]:
  may-lock l t = (case l of None  $\Rightarrow$  True | [(t', n)]  $\Rightarrow$  t = t')
by(auto intro: may-lock.intros elim: may-lock.cases)

lemma may-lockI:
  l = None  $\vee$  ( $\exists n. l = [(t, n)]$ )  $\Longrightarrow$  may-lock l t
by(cases l, auto intro: may-lock.intros)

lemma may-lockE [consumes 1, case-names None Locked]:
   $\llbracket$  may-lock l t; l = None  $\Longrightarrow$  P;  $\bigwedge n. l = [(t, n)] \Longrightarrow$  P  $\rrbracket \Longrightarrow$  P
by(auto elim: may-lock.cases)

lemma may-lock-may-lock-t-eq:

```

$\llbracket \text{may-lock } l \ t; \text{may-lock } l \ t' \rrbracket \implies (l = \text{None}) \vee (t = t')$   
**by**(*auto elim!:* *may-lockE*)

**abbreviation** *has-lock* ::  $'t \text{ lock} \Rightarrow 't \Rightarrow \text{bool}$   
**where** *has-lock*  $l \ t \equiv 0 < \text{has-locks } l \ t$

**lemma** *has-locks-Suc-has-lock*:  
 $\text{has-locks } l \ t = \text{Suc } n \implies \text{has-lock } l \ t$   
**by**(*auto*)

**lemmas** *has-lock-has-locks-Suc* = *gr0-implies-Suc*[**where**  $n = \text{has-locks } l \ t$ ] **for**  $l \ t$

**lemma** *has-lock-has-locks-conv*:  
 $\text{has-lock } l \ t \longleftrightarrow (\exists n. \text{has-locks } l \ t = (\text{Suc } n))$   
**by**(*auto intro:* *has-locks-Suc-has-lock has-lock-has-locks-Suc*)

**lemma** *has-lock-may-lock*:  
 $\text{has-lock } l \ t \implies \text{may-lock } l \ t$   
**by**(*cases l, auto intro:* *may-lockI*)

**lemma** *has-lock-may-lock-t-eq*:  
 $\llbracket \text{has-lock } l \ t; \text{may-lock } l \ t' \rrbracket \implies t = t'$   
**by**(*auto elim!:* *may-lockE split: if-split-asm*)

**lemma** *has-locks-has-locks-t-eq*:  
 $\llbracket \text{has-locks } l \ t = \text{Suc } n; \text{has-locks } l \ t' = \text{Suc } n' \rrbracket \implies t = t'$   
**by**(*auto elim:* *has-locksE*)

**lemma** *has-lock-has-lock-t-eq*:  
 $\llbracket \text{has-lock } l \ t; \text{has-lock } l \ t' \rrbracket \implies t = t'$   
**unfolding** *has-lock-has-locks-conv*  
**by**(*auto intro:* *has-locks-has-locks-t-eq*)

**lemma** *not-may-lock-conv*:  
 $\neg \text{may-lock } l \ t \longleftrightarrow (\exists t'. t' \neq t \wedge \text{has-lock } l \ t')$   
**by**(*cases l, auto intro:* *may-lock.intros elim:* *may-lockE*)

**fun** *lock-lock* ::  $'t \text{ lock} \Rightarrow 't \Rightarrow 't \text{ lock}$  **where**  
 $\text{lock-lock } \text{None } t = \lfloor (t, 0) \rfloor$   
 $\text{lock-lock } \lfloor (t', n) \rfloor \ t = \lfloor (t', \text{Suc } n) \rfloor$

**fun** *unlock-lock* ::  $'t \text{ lock} \Rightarrow 't \text{ lock}$  **where**  
 $\text{unlock-lock } \text{None} = \text{None}$   
 $\text{unlock-lock } \lfloor (t, n) \rfloor = (\text{case } n \text{ of } 0 \Rightarrow \text{None} \mid \text{Suc } n' \Rightarrow \lfloor (t, n') \rfloor)$

**fun** *release-all* ::  $'t \text{ lock} \Rightarrow 't \Rightarrow 't \text{ lock}$  **where**  
 $\text{release-all } \text{None } t = \text{None}$   
 $\text{release-all } \lfloor (t', n) \rfloor \ t = (\text{if } t = t' \text{ then } \text{None} \text{ else } \lfloor (t', n) \rfloor)$

**fun** *acquire-locks* ::  $'t \text{ lock} \Rightarrow 't \Rightarrow \text{nat} \Rightarrow 't \text{ lock}$  **where**

$acquire\_locks\ L\ t\ 0 = L$   
 $| acquire\_locks\ L\ t\ (Suc\ m) = acquire\_locks\ (lock\_lock\ L\ t)\ t\ m$

**lemma** *acquire-locks-conv*:

$acquire\_locks\ L\ t\ n = (case\ L\ of\ None \Rightarrow (case\ n\ of\ 0 \Rightarrow None\ |\ Suc\ m \Rightarrow \lfloor (t, m) \rfloor) | \lfloor (t', m) \rfloor \Rightarrow \lfloor (t', n + m) \rfloor)$   
**by**(*induct n arbitrary: L*)(*auto*)

**lemma** *lock-lock-ls-Some*:

$\exists t'\ n. lock\_lock\ l\ t = \lfloor (t', n) \rfloor$   
**by**(*cases l, auto*)

**lemma** *unlock-lock-SomeD*:

$unlock\_lock\ l = \lfloor (t', n) \rfloor \Longrightarrow l = \lfloor (t', Suc\ n) \rfloor$   
**by**(*cases l, auto split: nat.split-asm*)

**lemma** *has-locks-Suc-lock-lock-has-locks-Suc-Suc*:

$has\_locks\ l\ t = Suc\ n \Longrightarrow has\_locks\ (lock\_lock\ l\ t)\ t = Suc\ (Suc\ n)$   
**by**(*auto elim!: has-locksE*)

**lemma** *has-locks-lock-lock-conv [simp]*:

$may\_lock\ l\ t \Longrightarrow has\_locks\ (lock\_lock\ l\ t)\ t = Suc\ (has\_locks\ l\ t)$   
**by**(*auto elim: may-lockE*)

**lemma** *has-locks-release-all-conv [simp]*:

$has\_locks\ (release\_all\ l\ t)\ t = 0$   
**by**(*cases l, auto split: if-split-asm*)

**lemma** *may-lock-lock-lock-conv [simp]*:  $may\_lock\ (lock\_lock\ l\ t)\ t = may\_lock\ l\ t$

**by**(*cases l, auto elim!: may-lock.cases intro: may-lock.intros*)

**lemma** *has-locks-acquire-locks-conv [simp]*:

$may\_lock\ l\ t \Longrightarrow has\_locks\ (acquire\_locks\ l\ t\ n)\ t = has\_locks\ l\ t + n$   
**by**(*induct n arbitrary: l, auto*)

**lemma** *may-lock-unlock-lock-conv [simp]*:

$has\_lock\ l\ t \Longrightarrow may\_lock\ (unlock\_lock\ l)\ t = may\_lock\ l\ t$   
**by**(*cases l*)(*auto split: if-split-asm nat.splits elim!: may-lock.cases intro: may-lock.intros*)

**lemma** *may-lock-release-all-conv [simp]*:

$may\_lock\ (release\_all\ l\ t)\ t = may\_lock\ l\ t$   
**by**(*cases l, auto split: if-split-asm intro!: may-lockI elim: may-lockE*)

**lemma** *may-lock-t-may-lock-unlock-lock-t*:

$may\_lock\ l\ t \Longrightarrow may\_lock\ (unlock\_lock\ l)\ t$   
**by**(*auto intro: may-lock.intros elim!: may-lockE split: nat.split*)

**lemma** *may-lock-has-locks-lock-lock-0*:

$\llbracket may\_lock\ l\ t'; t \neq t' \rrbracket \Longrightarrow has\_locks\ (lock\_lock\ l\ t')\ t = 0$   
**by**(*auto elim!: may-lock.cases*)

**lemma** *has-locks-unlock-lock-conv [simp]*:

$has\_lock\ l\ t \Longrightarrow has\_locks\ (unlock\_lock\ l)\ t = has\_locks\ l\ t - 1$

**by**(cases l)(auto split: nat.split)

**lemma** has-lock-lock-lock-unlock-lock-id [simp]:  
 $has\_lock\ l\ t \implies lock\_lock\ (unlock\_lock\ l)\ t = l$   
**by**(cases l)(auto split: if-split-asm nat.split)

**lemma** may-lock-unlock-lock-lock-lock-id [simp]:  
 $may\_lock\ l\ t \implies unlock\_lock\ (lock\_lock\ l\ t) = l$   
**by**(cases l) auto

**lemma** may-lock-has-locks-0:  
 $\llbracket may\_lock\ l\ t; t \neq t' \rrbracket \implies has\_locks\ l\ t' = 0$   
**by**(auto elim!: may-lockE)

**fun** upd-lock :: 't lock  $\Rightarrow$  't  $\Rightarrow$  lock-action  $\Rightarrow$  't lock  
**where**  
 upd-lock l t Lock = lock-lock l t  
 | upd-lock l t Unlock = unlock-lock l  
 | upd-lock l t UnlockFail = l  
 | upd-lock l t ReleaseAcquire = release-all l t

**fun** upd-locks :: 't lock  $\Rightarrow$  't  $\Rightarrow$  lock-action list  $\Rightarrow$  't lock  
**where**  
 upd-locks l t [] = l  
 | upd-locks l t (L # Ls) = upd-locks (upd-lock l t L) t Ls

**lemma** upd-locks-append [simp]:  
 $upd\_locks\ l\ t\ (Ls\ @\ Ls') = upd\_locks\ (upd\_locks\ l\ t\ Ls)\ t\ Ls'$   
**by**(induct Ls arbitrary: l, auto)

**lemma** upd-lock-Some-thread-idD:  
**assumes** ul: upd-lock l t L =  $\lfloor (t', n) \rfloor$   
**and** tt':  $t \neq t'$   
**shows**  $\exists n. l = \lfloor (t', n) \rfloor$   
**proof**(cases L)  
**case** Lock  
**with** ul tt' **show** ?thesis  
**by**(cases l, auto)  
**next**  
**case** Unlock  
**with** ul tt' **show** ?thesis  
**by**(auto dest: unlock-lock-SomeD)  
**next**  
**case** UnlockFail  
**with** ul **show** ?thesis **by**(simp)  
**next**  
**case** ReleaseAcquire  
**with** ul **show** ?thesis  
**by**(cases l, auto split: if-split-asm)  
**qed**



**lemma** *has-lock-upd-lock-implies-has-lock*:

$\llbracket \text{has-lock } (\text{upd-lock } l \ t \ L) \ t'; t \neq t' \rrbracket \implies \text{has-lock } l \ t'$   
**by**(cases  $l \ L$  rule: option.exhaust[case-product lock-action.exhaust])(auto split: if-split-asm nat.split-asm)

**lemma** *has-lock-upd-locks-implies-has-lock*:

$\llbracket \text{has-lock } (\text{upd-locks } l \ t \ Ls) \ t'; t \neq t' \rrbracket \implies \text{has-lock } l \ t'$   
**by**(induct  $l \ t \ Ls$  rule: upd-locks.induct)(auto intro: has-lock-upd-lock-implies-has-lock)

**fun** *lock-action-ok* :: ' $t$  lock  $\Rightarrow$  ' $t \Rightarrow$  lock-action  $\Rightarrow$  bool **where**

lock-action-ok  $l \ t \ \text{Lock} = \text{may-lock } l \ t$   
| lock-action-ok  $l \ t \ \text{Unlock} = \text{has-lock } l \ t$   
| lock-action-ok  $l \ t \ \text{UnlockFail} = (\neg \text{has-lock } l \ t)$   
| lock-action-ok  $l \ t \ \text{ReleaseAcquire} = \text{True}$

**fun** *lock-actions-ok* :: ' $t$  lock  $\Rightarrow$  ' $t \Rightarrow$  lock-action list  $\Rightarrow$  bool **where**

lock-actions-ok  $l \ t \ [] = \text{True}$   
| lock-actions-ok  $l \ t \ (L \ \# \ Ls) = (\text{lock-action-ok } l \ t \ L \wedge \text{lock-actions-ok } (\text{upd-lock } l \ t \ L) \ t \ Ls)$

**lemma** *lock-actions-ok-append* [simp]:

lock-actions-ok  $l \ t \ (Ls \ @ \ Ls') \longleftrightarrow \text{lock-actions-ok } l \ t \ Ls \wedge \text{lock-actions-ok } (\text{upd-locks } l \ t \ Ls) \ t \ Ls'$   
**by**(induct  $Ls$  arbitrary:  $l$ ) auto

**lemma** *not-lock-action-okE* [consumes 1, case-names Lock Unlock UnlockFail]:

$\llbracket \neg \text{lock-action-ok } l \ t \ L; \llbracket L = \text{Lock}; \neg \text{may-lock } l \ t \rrbracket \implies Q; \llbracket L = \text{Unlock}; \neg \text{has-lock } l \ t \rrbracket \implies Q; \llbracket L = \text{UnlockFail}; \text{has-lock } l \ t \rrbracket \implies Q \rrbracket \implies Q$

**by**(cases  $L$ ) auto

**lemma** *may-lock-upd-lock-conv* [simp]:

lock-action-ok  $l \ t \ L \implies \text{may-lock } (\text{upd-lock } l \ t \ L) \ t = \text{may-lock } l \ t$   
**by**(cases  $L$ ) auto

**lemma** *may-lock-upd-locks-conv* [simp]:

lock-actions-ok  $l \ t \ Ls \implies \text{may-lock } (\text{upd-locks } l \ t \ Ls) \ t = \text{may-lock } l \ t$   
**by**(induct  $l \ t \ Ls$  rule: upd-locks.induct) simp-all

**lemma** *lock-actions-ok-Lock-may-lock*:

$\llbracket \text{lock-actions-ok } l \ t \ Ls; \text{Lock} \in \text{set } Ls \rrbracket \implies \text{may-lock } l \ t$   
**by**(induct  $l \ t \ Ls$  rule: lock-actions-ok.induct) auto

**lemma** *has-locks-lock-lock-conv'* [simp]:

$\llbracket \text{may-lock } l \ t'; t \neq t' \rrbracket \implies \text{has-locks } (\text{lock-lock } l \ t') \ t = \text{has-locks } l \ t$   
**by**(cases  $l$ )(auto elim: may-lock.cases)

**lemma** *has-locks-unlock-lock-conv'* [simp]:

$\llbracket \text{has-lock } l \ t'; t \neq t' \rrbracket \implies \text{has-locks } (\text{unlock-lock } l) \ t = \text{has-locks } l \ t$   
**by**(cases  $l$ )(auto split: if-split-asm nat.split)

**lemma** *has-locks-release-all-conv'* [simp]:

$t \neq t' \implies \text{has-locks } (\text{release-all } l \ t') \ t = \text{has-locks } l \ t$

**by**(cases l) auto

**lemma** has-locks-acquire-locks-conv' [simp]:

$\llbracket \text{may-lock } l \ t; t \neq t' \rrbracket \implies \text{has-locks } (\text{acquire-locks } l \ t \ n) \ t' = \text{has-locks } l \ t'$

**by**(induct l t n rule: acquire-locks.induct) simp-all

**lemma** lock-action-ok-has-locks-upd-lock-eq-has-locks [simp]:

$\llbracket \text{lock-action-ok } l \ t' \ L; t \neq t' \rrbracket \implies \text{has-locks } (\text{upd-lock } l \ t' \ L) \ t = \text{has-locks } l \ t$

**by**(cases L) auto

**lemma** lock-actions-ok-has-locks-upd-locks-eq-has-locks [simp]:

$\llbracket \text{lock-actions-ok } l \ t' \ Ls; t \neq t' \rrbracket \implies \text{has-locks } (\text{upd-locks } l \ t' \ Ls) \ t = \text{has-locks } l \ t$

**by**(induct l t' Ls rule: upd-locks.induct) simp-all

**lemma** has-lock-acquire-locks-implies-has-lock:

$\llbracket \text{has-lock } (\text{acquire-locks } l \ t \ n) \ t'; t \neq t' \rrbracket \implies \text{has-lock } l \ t'$

**unfolding** acquire-locks-conv

**by**(cases n)(auto split: if-split-asm)

**lemma** has-lock-has-lock-acquire-locks:

$\text{has-lock } l \ T \implies \text{has-lock } (\text{acquire-locks } l \ t \ n) \ T$

**unfolding** acquire-locks-conv

**by**(auto)

**fun** lock-actions-ok' :: 't lock  $\Rightarrow$  't  $\Rightarrow$  lock-action list  $\Rightarrow$  bool **where**

lock-actions-ok' l t [] = True

| lock-actions-ok' l t (L#Ls) = ((L = Lock  $\wedge$   $\neg$  may-lock l t)  $\vee$   
lock-action-ok l t L  $\wedge$  lock-actions-ok' (upd-lock l t L) t Ls)

**lemma** lock-actions-ok'-iff:

lock-actions-ok' l t las  $\longleftrightarrow$

lock-actions-ok l t las  $\vee$  ( $\exists$  xs ys. las = xs @ Lock # ys  $\wedge$  lock-actions-ok l t xs  $\wedge$   $\neg$  may-lock (upd-locks l t xs) t)

**proof**(induct l t las rule: lock-actions-ok.induct)

**case** (2 L t LA LAS)

**show** ?case

**proof**(cases LA = Lock  $\wedge$   $\neg$  may-lock L t)

**case** True

**hence** ( $\exists$  ys. Lock # LAS = [] @ Lock # ys)  $\wedge$  lock-actions-ok L t []  $\wedge$   $\neg$  may-lock (upd-locks L t []) t

**by**(simp)

**with** True **show** ?thesis **by**(simp (no-asm))(blast)

**next**

**case** False

**with** 2 **show** ?thesis

**by**(fastforce simp add: Cons-eq-append-conv elim: allE[**where** x=LA # xs **for** xs])

**qed**

**qed** simp

**lemma** lock-actions-ok'E[consumes 1, case-names ok Lock]:

$\llbracket \text{lock-actions-ok}' \ l \ t \ \text{las};$

lock-actions-ok l t las  $\implies P;$

$\bigwedge xs \ \text{ys}. \llbracket \text{las} = xs @ \text{Lock} \# \text{ys}; \text{lock-actions-ok } l \ t \ xs; \neg \text{may-lock } (\text{upd-locks } l \ t \ xs) \ t \rrbracket \implies P \rrbracket$

$\Rightarrow P$   
**by**(*auto simp add: lock-actions-ok'-iff*)  
**end**

### 1.3 Semantics of the thread actions for locking

**theory** *FWLocking*  
**imports**  
     *FWLock*  
**begin**

**definition** *redT-updLs* ::  $(l, t) \text{ locks} \Rightarrow t \Rightarrow l \text{ lock-actions} \Rightarrow (l, t) \text{ locks}$  **where**  
      $\text{redT-updLs } ls \ t \ las \equiv (\lambda(l, la). \text{upd-locks } l \ t \ la) \circ \$ (\$ls, las\$)$

**lemma** *redT-updLs-iff* [*simp*]:  $\text{redT-updLs } ls \ t \ las \ \$ \ l = \text{upd-locks } (ls \ \$ \ l) \ t \ (las \ \$ \ l)$   
**by**(*simp add: redT-updLs-def*)

**lemma** *upd-locks-empty-conv* [*simp*]:  $(\lambda(l, las). \text{upd-locks } l \ t \ las) \circ \$ (\$ls, K\$ []) = ls$   
**by**(*auto intro: finfun-ext*)

**lemma** *redT-updLs-Some-thread-idD*:  
 $\llbracket \text{has-lock } (\text{redT-updLs } ls \ t \ las \ \$ \ l) \ t'; t \neq t' \rrbracket \Longrightarrow \text{has-lock } (ls \ \$ \ l) \ t'$   
**by**(*auto simp add: redT-updLs-def intro: has-lock-upd-locks-implies-has-lock*)

**definition** *acquire-all* ::  $(l, t) \text{ locks} \Rightarrow t \Rightarrow (l \Rightarrow f \text{ nat}) \Rightarrow (l, t) \text{ locks}$   
**where**  $\bigwedge ln. \text{acquire-all } ls \ t \ ln \equiv (\lambda(l, la). \text{acquire-locks } l \ t \ la) \circ \$ (\$ls, ln\$)$

**lemma** *acquire-all-iff* [*simp*]:  
 $\bigwedge ln. \text{acquire-all } ls \ t \ ln \ \$ \ l = \text{acquire-locks } (ls \ \$ \ l) \ t \ (ln \ \$ \ l)$   
**by**(*simp add: acquire-all-def*)

**definition** *lock-ok-las* ::  $(l, t) \text{ locks} \Rightarrow t \Rightarrow l \text{ lock-actions} \Rightarrow \text{bool}$  **where**  
      $\text{lock-ok-las } ls \ t \ las \equiv \forall l. \text{lock-actions-ok } (ls \ \$ \ l) \ t \ (las \ \$ \ l)$

**lemma** *lock-ok-lasI* [*intro*]:  
 $(\bigwedge l. \text{lock-actions-ok } (ls \ \$ \ l) \ t \ (las \ \$ \ l)) \Longrightarrow \text{lock-ok-las } ls \ t \ las$   
**by**(*simp add: lock-ok-las-def*)

**lemma** *lock-ok-lasE*:  
 $\llbracket \text{lock-ok-las } ls \ t \ las; (\bigwedge l. \text{lock-actions-ok } (ls \ \$ \ l) \ t \ (las \ \$ \ l)) \Longrightarrow Q \rrbracket \Longrightarrow Q$   
**by**(*simp add: lock-ok-las-def*)

**lemma** *lock-ok-lasD*:  
 $\text{lock-ok-las } ls \ t \ las \Longrightarrow \text{lock-actions-ok } (ls \ \$ \ l) \ t \ (las \ \$ \ l)$   
**by**(*simp add: lock-ok-las-def*)

**lemma** *lock-ok-las-code* [*code*]:  
 $\text{lock-ok-las } ls \ t \ las = \text{finfun-All } ((\lambda(l, la). \text{lock-actions-ok } l \ t \ la) \circ \$ (\$ls, las\$))$   
**by**(*simp add: lock-ok-las-def finfun-All-All o-def*)

**lemma** *lock-ok-las-may-lock*:

$\llbracket \text{lock-ok-las } ls \ t \ \text{las}; \text{Lock} \in \text{set } (las \ \$ \ l) \rrbracket \implies \text{may-lock } (ls \ \$ \ l) \ t$   
**by**(erule lock-ok-lasE)(rule lock-actions-ok-Lock-may-lock)

**lemma** redT-updLs-may-lock [simp]:

$\text{lock-ok-las } ls \ t \ \text{las} \implies \text{may-lock } (\text{redT-updLs } ls \ t \ \text{las} \ \$ \ l) \ t = \text{may-lock } (ls \ \$ \ l) \ t$   
**by**(auto dest!: lock-ok-lasD[**where**  $l=l$ ])

**lemma** redT-updLs-has-locks [simp]:

$\llbracket \text{lock-ok-las } ls \ t' \ \text{las}; t \neq t' \rrbracket \implies \text{has-locks } (\text{redT-updLs } ls \ t' \ \text{las} \ \$ \ l) \ t = \text{has-locks } (ls \ \$ \ l) \ t$   
**by**(auto dest!: lock-ok-lasD[**where**  $l=l$ ])

**definition** may-acquire-all ::  $(l, t) \text{ locks} \Rightarrow t \Rightarrow (l \Rightarrow f \text{ nat}) \Rightarrow \text{bool}$

**where**  $\bigwedge l. \text{may-acquire-all } ls \ t \ ln \equiv \forall l. ln \ \$ \ l > 0 \longrightarrow \text{may-lock } (ls \ \$ \ l) \ t$

**lemma** may-acquire-allI [intro]:

$\bigwedge l. (\bigwedge l. ln \ \$ \ l > 0 \implies \text{may-lock } (ls \ \$ \ l) \ t) \implies \text{may-acquire-all } ls \ t \ ln$   
**by**(simp add: may-acquire-all-def)

**lemma** may-acquire-allE:

$\bigwedge l. \llbracket \text{may-acquire-all } ls \ t \ ln; \forall l. ln \ \$ \ l > 0 \longrightarrow \text{may-lock } (ls \ \$ \ l) \ t \implies P \rrbracket \implies P$   
**by**(auto simp add: may-acquire-all-def)

**lemma** may-acquire-allD [dest]:

$\bigwedge l. \llbracket \text{may-acquire-all } ls \ t \ ln; ln \ \$ \ l > 0 \rrbracket \implies \text{may-lock } (ls \ \$ \ l) \ t$   
**by**(auto simp add: may-acquire-all-def)

**lemma** may-acquire-all-has-locks-acquire-locks [simp]:

**fixes**  $ln$   
**shows**  $\llbracket \text{may-acquire-all } ls \ t \ ln; t \neq t' \rrbracket \implies \text{has-locks } (\text{acquire-locks } (ls \ \$ \ l) \ t \ (ln \ \$ \ l)) \ t' = \text{has-locks } (ls \ \$ \ l) \ t'$   
**by**(cases  $ln \ \$ \ l > 0$ )(auto dest: may-acquire-allD)

**lemma** may-acquire-all-code [code]:

$\bigwedge l. \text{may-acquire-all } ls \ t \ ln \longleftrightarrow \text{finfun-All } ((\lambda(\text{lock}, n). n > 0 \longrightarrow \text{may-lock } \text{lock } t) \circ \$ (\$ls, ln\$))$   
**by**(auto simp add: may-acquire-all-def finfun-All-All o-def)

**definition** collect-locks ::  $l \text{ lock-actions} \Rightarrow l \text{ set}$  **where**

$\text{collect-locks } las = \{l. \text{Lock} \in \text{set } (las \ \$ \ l)\}$

**lemma** collect-locksI:

$\text{Lock} \in \text{set } (las \ \$ \ l) \implies l \in \text{collect-locks } las$   
**by**(simp add: collect-locks-def)

**lemma** collect-locksE:

$\llbracket l \in \text{collect-locks } las; \text{Lock} \in \text{set } (las \ \$ \ l) \rrbracket \implies P \implies P$   
**by**(simp add: collect-locks-def)

**lemma** collect-locksD:

$l \in \text{collect-locks } las \implies \text{Lock} \in \text{set } (las \ \$ \ l)$   
**by**(simp add: collect-locks-def)

**fun** must-acquire-lock ::  $\text{lock-action list} \Rightarrow \text{bool}$  **where**

```

  must-acquire-lock [] = False
| must-acquire-lock (Lock # las) = True
| must-acquire-lock (Unlock # las) = False
| must-acquire-lock (- # las) = must-acquire-lock las

```

**lemma** *must-acquire-lock-append*:

*must-acquire-lock (xs @ ys)  $\longleftrightarrow$  (if Lock  $\in$  set xs  $\vee$  Unlock  $\in$  set xs then must-acquire-lock xs else must-acquire-lock ys)*

**proof**(*induct xs*)

case Nil **thus** ?case **by** simp

**next**

case (Cons L Ls)

**thus** ?case **by** (cases L, simp-all)

**qed**

**lemma** *must-acquire-lock-contains-lock*:

*must-acquire-lock las  $\implies$  Lock  $\in$  set las*

**proof**(*induct las*)

case (Cons l las) **thus** ?case **by**(cases l) auto

**qed** simp

**lemma** *must-acquire-lock-conv*:

*must-acquire-lock las = (case (filter ( $\lambda L. L = \text{Lock} \vee L = \text{Unlock}$ ) las) of []  $\Rightarrow$  False | L # Ls  $\Rightarrow$  L = Lock)*

**proof**(*induct las*)

case Nil **thus** ?case **by** simp

**next**

case (Cons LA LAS) **thus** ?case

**by**(cases LA, auto split: list.split-asm)

**qed**

**definition** *collect-locks'* :: '*l* lock-actions  $\Rightarrow$  '*l* set **where**

*collect-locks' las  $\equiv$  {l. must-acquire-lock (las \$ l)}*

**lemma** *collect-locks'I*:

*must-acquire-lock (las \$ l)  $\implies$  l  $\in$  collect-locks' las*

**by**(simp add: collect-locks'-def)

**lemma** *collect-locks'E*:

*[l  $\in$  collect-locks' las; must-acquire-lock (las \$ l)  $\implies$  P]  $\implies$  P*

**by**(simp add: collect-locks'-def)

**lemma** *collect-locks'-subset-collect-locks*:

*collect-locks' las  $\subseteq$  collect-locks las*

**by**(auto simp add: collect-locks'-def collect-locks-def intro: must-acquire-lock-contains-lock)

**definition** *lock-ok-las'* :: ('l,'t) locks  $\Rightarrow$  't  $\Rightarrow$  '*l* lock-actions  $\Rightarrow$  bool **where**

*lock-ok-las' ls t las  $\equiv$   $\forall l. \text{lock-actions-ok}' (ls \$ l) t (las \$ l)$*

**lemma** *lock-ok-las'I*: ( $\bigwedge l. \text{lock-actions-ok}' (ls \$ l) t (las \$ l)$ )  $\implies$  *lock-ok-las' ls t las*

**by**(simp add: lock-ok-las'-def)

**lemma** *lock-ok-las'D*: *lock-ok-las' ls t las  $\implies$  lock-actions-ok' (ls \$ l) t (las \$ l)*

**by**(*simp add: lock-ok-las'-def*)

**lemma** *not-lock-ok-las'-conv*:

$\neg \text{lock-ok-las}' \text{ ls } t \text{ las} \longleftrightarrow (\exists l. \neg \text{lock-actions-ok}' (l \text{ \$ } l) \text{ t } (las \text{ \$ } l))$

**by**(*simp add: lock-ok-las'-def*)

**lemma** *lock-ok-las'-code*:

$\text{lock-ok-las}' \text{ ls } t \text{ las} = \text{finfun-All } ((\lambda(l, la). \text{lock-actions-ok}' l \text{ t } la) \circ \$ (\$ls, las\$))$

**by**(*simp add: lock-ok-las'-def finfun-All-All o-def*)

**lemma** *lock-ok-las'-collect-locks'-may-lock*:

**assumes** *lot'*:  $\text{lock-ok-las}' \text{ ls } t \text{ las}$

**and** *mayl*:  $\forall l \in \text{collect-locks}' \text{ las}. \text{may-lock } (l \text{ \$ } l) \text{ t}$

**and** *l*:  $l \in \text{collect-locks } las$

**shows**  $\text{may-lock } (l \text{ \$ } l) \text{ t}$

**proof**(*cases l ∈ collect-locks' las*)

**case** *True* **thus** *?thesis* **using** *mayl* **by** *auto*

**next**

**case** *False*

**hence** *nmal*:  $\neg \text{must-acquire-lock } (las \text{ \$ } l)$

**by**(*auto intro: collect-locks'I*)

**from** *l* **have** *locklasl*:  $\text{Lock} \in \text{set } (las \text{ \$ } l)$

**by**(*rule collect-locksD*)

**then obtain** *ys zs*

**where** *las*:  $las \text{ \$ } l = ys @ \text{Lock} \# zs$

**and** *notin*:  $\text{Lock} \notin \text{set } ys$

**by**(*auto dest: split-list-first*)

**from** *lot'* **have** *lock-actions-ok'*  $(l \text{ \$ } l) \text{ t } (las \text{ \$ } l)$

**by**(*auto simp add: lock-ok-las'-def*)

**thus** *?thesis*

**proof**(*induct rule: lock-actions-ok'E*)

**case** *ok*

**with** *locklasl* **show** *?thesis*

**by**  $\neg(\text{rule lock-actions-ok-Lock-may-lock})$

**next**

**case**  $(\text{Lock } YS \text{ } ZS)$

**note** *LAS* =  $\langle las \text{ \$ } l = YS @ \text{Lock} \# ZS \rangle$

**note** *lao* =  $\langle \text{lock-actions-ok } (l \text{ \$ } l) \text{ t } YS \rangle$

**note** *nml* =  $\langle \neg \text{may-lock } (\text{upd-locks } (l \text{ \$ } l) \text{ t } YS) \text{ t} \rangle$

**from** *LAS las nmal notin* **have** *Unlock*  $\in \text{set } YS$

**by**  $\neg(\text{erule contrapos-np, auto simp add: must-acquire-lock-append append-eq-append-conv2 append-eq-Cons-conv})$

**then obtain** *ys' zs'*

**where** *YS*:  $YS = ys' @ \text{Unlock} \# zs'$

**and** *unlock*:  $\text{Unlock} \notin \text{set } ys'$

**by**(*auto dest: split-list-first*)

**from** *YS las LAS lao* **have** *lao'*:  $\text{lock-actions-ok } (l \text{ \$ } l) \text{ t } (ys' @ [\text{Unlock}])$  **by**(*auto*)

**hence** *has-lock*  $(\text{upd-locks } (l \text{ \$ } l) \text{ t } ys') \text{ t}$  **by** *simp*

**hence** *may-lock*  $(\text{upd-locks } (l \text{ \$ } l) \text{ t } ys') \text{ t}$

**by**(*rule has-lock-may-lock*)

**moreover from** *lao'* **have** *lock-actions-ok*  $(l \text{ \$ } l) \text{ t } ys'$  **by** *simp*

**ultimately show** *?thesis* **by** *simp*

**qed**

qed

**lemma** *lock-actions-ok'-must-acquire-lock-lock-actions-ok:*

$\llbracket \text{lock-actions-ok}' \ l \ t \ Ls; \text{must-acquire-lock } Ls \longrightarrow \text{may-lock } l \ t \rrbracket \implies \text{lock-actions-ok } l \ t \ Ls$

**proof**(*induct*  $l \ t \ Ls$  *rule: lock-actions-ok.induct*)

case 1 **thus** ?case **by** *simp*

**next**

case (2  $l \ t \ L \ LS$ ) **thus** ?case

**proof**(*cases*  $L = \text{Lock} \vee L = \text{Unlock}$ )

case *True*

**with** 2 **show** ?thesis **by**(*auto simp add: lock-actions-ok'-iff Cons-eq-append-conv intro: has-lock-may-lock*)

qed(*cases L, auto*)

qed

**lemma** *lock-ok-las'-collect-locks-lock-ok-las:*

**assumes** *lol'*: *lock-ok-las'*  $ls \ t \ las$

**and** *clml*:  $\bigwedge l. l \in \text{collect-locks } las \implies \text{may-lock } (ls \ \$ \ l) \ t$

**shows** *lock-ok-las*  $ls \ t \ las$

**proof**(*rule lock-ok-lasI*)

**fix**  $l$

**from** *lol'* **have** *lock-actions-ok'*  $(ls \ \$ \ l) \ t \ (las \ \$ \ l)$  **by**(*rule lock-ok-las'D*)

**thus** *lock-actions-ok*  $(ls \ \$ \ l) \ t \ (las \ \$ \ l)$

**proof**(*rule lock-actions-ok'-must-acquire-lock-lock-actions-ok[OF - impI]*)

**assume** *mal*: *must-acquire-lock*  $(las \ \$ \ l)$

**thus** *may-lock*  $(ls \ \$ \ l) \ t$

**by**(*auto intro!: clml collect-locksI elim: must-acquire-lock-contains-lock*)

qed

qed

**lemma** *lock-ok-las'-into-lock-on-las:*

$\llbracket \text{lock-ok-las}' \ ls \ t \ las; \bigwedge l. l \in \text{collect-locks}' \ las \implies \text{may-lock } (ls \ \$ \ l) \ t \rrbracket \implies \text{lock-ok-las } ls \ t \ las$   
**by** (*metis lock-ok-las'-collect-locks'-may-lock lock-ok-las'-collect-locks-lock-ok-las*)

end

## 1.4 Semantics of the thread actions for thread creation

**theory** *FWThread*

**imports**

*FWState*

**begin**

Abstractions for thread ids

**context**

**notes**  $\llbracket \text{inductive-internals} \rrbracket$

**begin**

**inductive** *free-thread-id* ::  $(l, t, x) \text{ thread-info} \Rightarrow t \Rightarrow \text{bool}$

**for**  $ts :: (l, t, x) \text{ thread-info}$  **and**  $t :: t$

**where**  $ts \ t = \text{None} \implies \text{free-thread-id } ts \ t$

**declare** *free-thread-id.cases*  $\llbracket \text{elim} \rrbracket$

**end**

**lemma** *free-thread-id-iff*: *free-thread-id ts t = (ts t = None)*  
**by**(*auto elim: free-thread-id.cases intro: free-thread-id.intros*)

Update functions for the multithreaded state

**fun** *redT-updT* :: (*'l, 't, 'x*) *thread-info*  $\Rightarrow$  (*'t, 'x, 'm*) *new-thread-action*  $\Rightarrow$  (*'l, 't, 'x*) *thread-info*  
**where**  
   *redT-updT ts (NewThread t' x m) = ts(t'  $\mapsto$  (x, no-wait-locks))*  
 | *redT-updT ts - = ts*

**fun** *redT-updTs* :: (*'l, 't, 'x*) *thread-info*  $\Rightarrow$  (*'t, 'x, 'm*) *new-thread-action list*  $\Rightarrow$  (*'l, 't, 'x*) *thread-info*  
**where**  
   *redT-updTs ts [] = ts*  
 | *redT-updTs ts (ta#tas) = redT-updTs (redT-updT ts ta) tas*

**lemma** *redT-updTs-append* [*simp*]:  
   *redT-updTs ts (tas @ tas') = redT-updTs (redT-updTs ts tas) tas'*  
**by**(*induct ts tas rule: redT-updTs.induct*) *auto*

**lemma** *redT-updT-None*:  
   *redT-updT ts ta t = None  $\implies$  ts t = None*  
**by**(*cases ta*)(*auto split: if-splits*)

**lemma** *redT-updTs-None*: *redT-updTs ts tas t = None  $\implies$  ts t = None*  
**by**(*induct ts tas rule: redT-updTs.induct*)(*auto intro: redT-updT-None*)

**lemma** *redT-updT-Some1*:  
   *ts t = [xw]  $\implies \exists xw. redT-updT ts ta t = [xw]$*   
**by**(*cases ta*) *auto*

**lemma** *redT-updTs-Some1*:  
   *ts t = [xw]  $\implies \exists xw. redT-updTs ts tas t = [xw]$*   
**unfolding** *not-None-eq[symmetric]*  
**by**(*induct ts tas arbitrary: xw rule: redT-updTs.induct*)(*simp-all del: split-paired-Ex, blast dest: redT-updT-Some1*)

**lemma** *redT-updT-finite-dom-inv*:  
   *finite (dom (redT-updT ts ta)) = finite (dom ts)*  
**by**(*cases ta*) *auto*

**lemma** *redT-updTs-finite-dom-inv*:  
   *finite (dom (redT-updTs ts tas)) = finite (dom ts)*  
**by**(*induct ts tas rule: redT-updTs.induct*)(*simp-all add: redT-updT-finite-dom-inv*)

Preconditions for thread creation actions

These primed versions are for checking preconditions only. They allow the thread actions to have a type for thread-local information that is different than the thread info state itself.

**fun** *redT-updT'* :: (*'l, 't, 'x*) *thread-info*  $\Rightarrow$  (*'t, 'x', 'm*) *new-thread-action*  $\Rightarrow$  (*'l, 't, 'x*) *thread-info*  
**where**  
   *redT-updT' ts (NewThread t' x m) = ts(t'  $\mapsto$  (undefined, no-wait-locks))*  
 | *redT-updT' ts - = ts*

**fun** *redT-updTs'* :: (*'l, 't, 'x*) *thread-info*  $\Rightarrow$  (*'t, 'x', 'm*) *new-thread-action list*  $\Rightarrow$  (*'l, 't, 'x*) *thread-info*  
**where**



$redT\text{-}updTs' \text{ } ts \ [] = ts$   
 $| redT\text{-}updTs' \text{ } ts \ (ta\#tas) = redT\text{-}updTs' \ (redT\text{-}updT' \text{ } ts \ ta) \ tas$

**lemma**  $redT\text{-}updT'\text{-}None$ :

$redT\text{-}updT' \text{ } ts \ ta \ t = None \implies ts \ t = None$

**by**(cases ta)(auto split: if-splits)

**primrec**  $thread\text{-}ok :: ('l, 't, 'x) \text{ thread-info} \Rightarrow ('t, 'x', 'm) \text{ new-thread-action} \Rightarrow bool$

**where**

$thread\text{-}ok \text{ } ts \ (NewThread \text{ } t \ x \ m) = free\text{-}thread\text{-}id \text{ } ts \ t$

$| thread\text{-}ok \text{ } ts \ (ThreadExists \text{ } t \ b) = (b \neq free\text{-}thread\text{-}id \text{ } ts \ t)$

**fun**  $thread\text{-}oks :: ('l, 't, 'x) \text{ thread-info} \Rightarrow ('t, 'x', 'm) \text{ new-thread-action list} \Rightarrow bool$

**where**

$thread\text{-}oks \text{ } ts \ [] = True$

$| thread\text{-}oks \text{ } ts \ (ta\#tas) = (thread\text{-}ok \text{ } ts \ ta \wedge thread\text{-}oks \ (redT\text{-}updT' \text{ } ts \ ta) \ tas)$

**lemma**  $thread\text{-}ok\text{-}ts\text{-}change$ :

$(\bigwedge t. ts \ t = None \longleftrightarrow ts' \ t = None) \implies thread\text{-}ok \text{ } ts \ ta \longleftrightarrow thread\text{-}ok \text{ } ts' \ ta$

**by**(cases ta)(auto simp add: free-thread-id-iff)

**lemma**  $thread\text{-}oks\text{-}ts\text{-}change$ :

$(\bigwedge t. ts \ t = None \longleftrightarrow ts' \ t = None) \implies thread\text{-}oks \text{ } ts \ tas \longleftrightarrow thread\text{-}oks \text{ } ts' \ tas$

**proof**(induct tas arbitrary: ts ts')

**case** Nil **thus** ?case **by** simp

**next**

**case** (Cons ta tas ts ts')

**note**  $IH = \langle \bigwedge ts \ ts'. (\bigwedge t. (ts \ t = None) = (ts' \ t = None)) \implies thread\text{-}oks \text{ } ts \ tas = thread\text{-}oks \text{ } ts' \ tas \rangle$

**note**  $eq = \langle \bigwedge t. (ts \ t = None) = (ts' \ t = None) \rangle$

**from** eq **have**  $thread\text{-}ok \text{ } ts \ ta \longleftrightarrow thread\text{-}ok \text{ } ts' \ ta$  **by**(rule thread-ok-ts-change)

**moreover from** eq **have**  $\bigwedge t. (redT\text{-}updT' \text{ } ts \ ta \ t = None) = (redT\text{-}updT' \text{ } ts' \ ta \ t = None)$

**by**(cases ta)(auto)

**hence**  $thread\text{-}oks \ (redT\text{-}updT' \text{ } ts \ ta) \ tas = thread\text{-}oks \ (redT\text{-}updT' \text{ } ts' \ ta) \ tas$  **by**(rule IH)

**ultimately show** ?case **by** simp

**qed**

**lemma**  $redT\text{-}updT'\text{-}eq\text{-}None\text{-}conv$ :

$(\bigwedge t. ts \ t = None \longleftrightarrow ts' \ t = None) \implies redT\text{-}updT' \text{ } ts \ ta \ t = None \longleftrightarrow redT\text{-}updT \text{ } ts' \ ta \ t = None$

**by**(cases ta) simp-all

**lemma**  $redT\text{-}updTs'\text{-}eq\text{-}None\text{-}conv$ :

$(\bigwedge t. ts \ t = None \longleftrightarrow ts' \ t = None) \implies redT\text{-}updTs' \text{ } ts \ tas \ t = None \longleftrightarrow redT\text{-}updTs \text{ } ts' \ tas \ t = None$

**apply**(induct tas arbitrary: ts ts')

**apply** simp-all

**apply**(blast intro: redT-updT'-eq-None-conv del: iffI)

**done**

**lemma**  $thread\text{-}oks\text{-}redT\text{-}updT\text{-}conv$  [simp]:

$thread\text{-}oks \ (redT\text{-}updT' \text{ } ts \ ta) \ tas = thread\text{-}oks \ (redT\text{-}updT \text{ } ts \ ta) \ tas$

**by**(rule thread-oks-ts-change)(rule redT-updT'-eq-None-conv refl)+

**lemma**  $thread\text{-}oks\text{-}append$  [simp]:

$thread\text{-}oks \text{ } ts \ (tas \ @ \ tas') = (thread\text{-}oks \text{ } ts \ tas \wedge thread\text{-}oks \ (redT\text{-}updTs' \text{ } ts \ tas) \ tas')$

**by**(*induct tas arbitrary: ts, auto*)

**lemma** *thread-oks-redT-updTs-conv [simp]:*

*thread-oks (redT-updTs' ts ta) tas = thread-oks (redT-updTs ts ta) tas*

**by**(*rule thread-oks-ts-change*)(*rule redT-updTs'-eq-None-conv refl*)+

**lemma** *redT-updT-Some:*

$\llbracket ts\ t = \lfloor xw \rfloor; \text{thread-ok } ts\ ta \rrbracket \implies \text{redT-updT } ts\ ta\ t = \lfloor xw \rfloor$

**by**(*cases ta*) *auto*

**lemma** *redT-updTs-Some:*

$\llbracket ts\ t = \lfloor xw \rfloor; \text{thread-oks } ts\ tas \rrbracket \implies \text{redT-updTs } ts\ tas\ t = \lfloor xw \rfloor$

**by**(*induct ts tas rule: redT-updTs.induct*)(*auto intro: redT-updT-Some*)

**lemma** *redT-updT'-Some:*

$\llbracket ts\ t = \lfloor xw \rfloor; \text{thread-ok } ts\ ta \rrbracket \implies \text{redT-updT}'\ ts\ ta\ t = \lfloor xw \rfloor$

**by**(*cases ta*) *auto*

**lemma** *redT-updTs'-Some:*

$\llbracket ts\ t = \lfloor xw \rfloor; \text{thread-oks } ts\ tas \rrbracket \implies \text{redT-updTs}'\ ts\ tas\ t = \lfloor xw \rfloor$

**by**(*induct ts tas rule: redT-updTs'.induct*)(*auto intro: redT-updT'-Some*)

**lemma** *thread-ok-new-thread:*

*thread-ok ts (NewThread t m' x)  $\implies$  ts t = None*

**by**(*auto*)

**lemma** *thread-oks-new-thread:*

$\llbracket \text{thread-oks } ts\ tas; \text{NewThread } t\ x\ m \in \text{set } tas \rrbracket \implies ts\ t = \text{None}$

**by**(*induct ts tas rule: thread-oks.induct*)(*auto intro: redT-updT'-None*)

**lemma** *redT-updT-new-thread-ts:*

*thread-ok ts (NewThread t x m)  $\implies$  redT-updT ts (NewThread t x m) t =  $\lfloor (x, \text{no-wait-locks}) \rfloor$*

**by**(*simp*)

**lemma** *redT-updTs-new-thread-ts:*

$\llbracket \text{thread-oks } ts\ tas; \text{NewThread } t\ x\ m \in \text{set } tas \rrbracket \implies \text{redT-updTs } ts\ tas\ t = \lfloor (x, \text{no-wait-locks}) \rfloor$

**by**(*induct ts tas rule: redT-updTs.induct*)(*auto intro: redT-updT-Some*)

**lemma** *redT-updT-new-thread:*

$\llbracket \text{redT-updT } ts\ ta\ t = \lfloor (x, w) \rfloor; \text{thread-ok } ts\ ta; ts\ t = \text{None} \rrbracket \implies \exists m. ta = \text{NewThread } t\ x\ m \wedge w = \text{no-wait-locks}$

**by**(*cases ta*)(*auto split: if-split-asm*)

**lemma** *redT-updTs-new-thread:*

$\llbracket \text{redT-updTs } ts\ tas\ t = \lfloor (x, w) \rfloor; \text{thread-oks } ts\ tas; ts\ t = \text{None} \rrbracket$

$\implies \exists m. \text{NewThread } t\ x\ m \in \text{set } tas \wedge w = \text{no-wait-locks}$

**proof**(*induct tas arbitrary: ts*)

**case Nil thus ?case** **by** *simp*

**next**

**case** (*Cons TA TAS TS*)

**note**  $IH = \langle \bigwedge ts. \llbracket \text{redT-updT}s \text{ TS } TAS \text{ } t = \lfloor (x, w) \rfloor; \text{thread-oks } ts \text{ } TAS; ts \text{ } t = \text{None} \rrbracket \implies \exists m. \text{NewThread } t \text{ } x \text{ } m \in \text{set } TAS \wedge w = \text{no-wait-locks} \rangle$   
**note**  $es't = \langle \text{redT-updT}s \text{ TS } (TA \# TAS) \text{ } t = \lfloor (x, w) \rfloor \rangle$   
**note**  $cct = \langle \text{thread-oks } TS \text{ } (TA \# TAS) \rangle$   
**hence**  $cctta: \text{thread-ok } TS \text{ } TA$  **and**  $ccts: \text{thread-oks } (\text{redT-updT } TS \text{ } TA) \text{ } TAS$  **by** *auto*  
**note**  $est = \langle TS \text{ } t = \text{None} \rangle$   
**{ fix**  $X \text{ } W$   
**assume**  $rest: \text{redT-updT } TS \text{ } TA \text{ } t = \lfloor (X, W) \rfloor$   
**then obtain**  $m$  **where**  $TA = \text{NewThread } t \text{ } X \text{ } m \wedge W = \text{no-wait-locks}$  **using**  $cctta \text{ } est$   
**by**  $(\text{auto } dest!: \text{redT-updT-new-thread})$   
**then obtain**  $TA = \text{NewThread } t \text{ } X \text{ } m \text{ } W = \text{no-wait-locks} \dots$   
**moreover from**  $rest \text{ } ccts$   
**have**  $\text{redT-updT}s \text{ TS } (TA \# TAS) \text{ } t = \lfloor (X, W) \rfloor$   
**by**  $(\text{auto } intro: \text{redT-updT}s\text{-Some})$   
**with**  $es't$  **have**  $X = x \text{ } W = w$  **by** *auto*  
**ultimately have**  $?case$  **by** *auto* **}**  
**moreover**  
**{ assume**  $rest: \text{redT-updT } TS \text{ } TA \text{ } t = \text{None}$   
**hence**  $\bigwedge m. TA \neq \text{NewThread } t \text{ } x \text{ } m$  **using**  $est \text{ } cct$   
**by**  $(\text{clarsimp})$   
**with**  $rest \text{ } ccts \text{ } es't$  **have**  $?case$  **by**  $(\text{auto } dest: IH)$  **}**  
**ultimately show**  $?case$  **by**  $(\text{cases } \text{redT-updT } TS \text{ } TA \text{ } t, \text{auto})$   
**qed**

**lemma**  $\text{redT-updT-upd}$ :

$\llbracket ts \text{ } t = \lfloor xw \rfloor; \text{thread-ok } ts \text{ } ta \rrbracket \implies (\text{redT-updT } ts \text{ } ta)(t \mapsto xw') = \text{redT-updT } (ts(t \mapsto xw')) \text{ } ta$   
**by**  $(\text{cases } ta)(\text{fastforce } intro: \text{fun-upd-twist})+$

**lemma**  $\text{redT-updT}s\text{-upd}$ :

$\llbracket ts \text{ } t = \lfloor xw \rfloor; \text{thread-oks } ts \text{ } tas \rrbracket \implies (\text{redT-updT}s \text{ } ts \text{ } tas)(t \mapsto xw') = \text{redT-updT}s \text{ } (ts(t \mapsto xw')) \text{ } tas$   
**by**  $(\text{induct } ts \text{ } tas \text{ rule: } \text{redT-updT}s.\text{induct})(\text{auto } \text{simp del: fun-upd-apply simp add: redT-updT-upd dest: redT-updT-Some})$

**lemma**  $\text{thread-ok-upd}$ :

$ts \text{ } t = \lfloor xln \rfloor \implies \text{thread-ok } (ts(t \mapsto xln')) \text{ } ta = \text{thread-ok } ts \text{ } ta$   
**by**  $(\text{rule } \text{thread-ok-ts-change}) \text{ simp}$

**lemma**  $\text{thread-oks-upd}$ :

$ts \text{ } t = \lfloor xln \rfloor \implies \text{thread-oks } (ts(t \mapsto xln')) \text{ } tas = \text{thread-oks } ts \text{ } tas$   
**by**  $(\text{rule } \text{thread-oks-ts-change}) \text{ simp}$

**lemma**  $\text{thread-ok-convert-new-thread-action [simp]}$ :

$\text{thread-ok } ts \text{ } (\text{convert-new-thread-action } f \text{ } ta) = \text{thread-ok } ts \text{ } ta$   
**by**  $(\text{cases } ta) \text{ auto}$

**lemma**  $\text{redT-updT}'\text{-convert-new-thread-action-eq-None}$ :

$\text{redT-updT}' \text{ } ts \text{ } (\text{convert-new-thread-action } f \text{ } ta) \text{ } t = \text{None} \longleftrightarrow \text{redT-updT}' \text{ } ts \text{ } ta \text{ } t = \text{None}$   
**by**  $(\text{cases } ta) \text{ auto}$

**lemma**  $\text{thread-oks-convert-new-thread-action [simp]}$ :

$\text{thread-oks } ts \text{ } (\text{map } (\text{convert-new-thread-action } f) \text{ } tas) = \text{thread-oks } ts \text{ } tas$   
**by**  $(\text{induct } ts \text{ } tas \text{ rule: } \text{thread-oks.induct})(\text{simp-all add: thread-oks-ts-change}[OF \text{redT-updT}'\text{-convert-new-thread-action-eq-None}])$

**lemma**  $\text{map-redT-updT}$ :

```

map-option (map-prod f id) (redT-updT ts ta t) =
redT-updT (λt. map-option (map-prod f id) (ts t)) (convert-new-thread-action f ta) t
by(cases ta) auto

```

**lemma** *map-redT-updTs*:

```

map-option (map-prod f id) (redT-updTs ts tas t) =
redT-updTs (λt. map-option (map-prod f id) (ts t)) (map (convert-new-thread-action f) tas) t
by(induct tas arbitrary: ts)(auto simp add: map-redT-updT)

```

**end**

## 1.5 Semantics of the thread actions for wait, notify and interrupt

**theory** *FWWait*

**imports**

*FWState*

**begin**

Update functions for the wait sets in the multithreaded state

**inductive** *redT-updW* :: 't ⇒ ('w, 't) wait-sets ⇒ ('t, 'w) wait-set-action ⇒ ('w, 't) wait-sets ⇒ bool  
**for** *t* :: 't **and** *ws* :: ('w, 't) wait-sets

**where**

```

ws t' = [InWS w] ⇒ redT-updW t ws (Notify w) (ws(t' ↦ PostWS WSNotified))
| (∧ t'. ws t' ≠ [InWS w]) ⇒ redT-updW t ws (Notify w) ws
| redT-updW t ws (NotifyAll w) (λt. if ws t = [InWS w] then [PostWS WSNotified] else ws t)
| redT-updW t ws (Suspend w) (ws(t ↦ InWS w))
| ws t' = [InWS w] ⇒ redT-updW t ws (WakeUp t') (ws(t' ↦ PostWS WSInterrupted))
| (∧ w. ws t' ≠ [InWS w]) ⇒ redT-updW t ws (WakeUp t') ws
| redT-updW t ws Notified (ws(t := None))
| redT-updW t ws WokenUp (ws(t := None))

```

**definition** *redT-updWs* :: 't ⇒ ('w, 't) wait-sets ⇒ ('t, 'w) wait-set-action list ⇒ ('w, 't) wait-sets ⇒ bool

**where** *redT-updWs* *t* = *rtrancl3p* (*redT-updW* *t*)

**inductive-simps** *redT-updW-simps* [*simp*]:

```

redT-updW t ws (Notify w) ws'
redT-updW t ws (NotifyAll w) ws'
redT-updW t ws (Suspend w) ws'
redT-updW t ws (WakeUp t') ws'
redT-updW t ws WokenUp ws'
redT-updW t ws Notified ws'

```

**lemma** *redT-updW-total*: ∃ *ws'*. *redT-updW* *t* *ws* *wa* *ws'*

**by**(cases *wa*)(auto simp add: *redT-updW.simps*)

**lemma** *redT-updWs-total*: ∃ *ws'*. *redT-updWs* *t* *ws* *was* *ws'*

**proof**(induct *was* rule: *rev-induct*)

**case** *Nil* **thus** ?*case* **by**(auto simp add: *redT-updWs-def*)

**next**

**case** (*snoc* *wa* *was*)

**then obtain** *ws'* **where** *redT-updWs* *t* *ws* *was* *ws'* ..

also from  $\text{redT-updW-total}[of\ t\ ws'\ wa]$   
 obtain  $ws''$  where  $\text{redT-updW}\ t\ ws'\ wa\ ws'' ..$   
 ultimately show  $?case\ \text{unfolding}\ \text{redT-updWs-def}\ \text{by}(\text{auto intro: rtrancl3p-step})$   
 qed

**lemma**  $\text{redT-updWs-trans}$ :  $\llbracket \text{redT-updWs}\ t\ ws\ was\ ws'; \text{redT-updWs}\ t\ ws'\ was'\ ws'' \rrbracket \implies \text{redT-updWs}\ t\ ws\ (was\ @\ was')\ ws''$   
**unfolding**  $\text{redT-updWs-def}\ \text{by}(\text{rule}\ \text{rtrancl3p-trans})$

**lemma**  $\text{redT-updW-None-implies-None}$ :  
 $\llbracket \text{redT-updW}\ t'\ ws\ wa\ ws'; ws\ t = \text{None}; t \neq t' \rrbracket \implies ws'\ t = \text{None}$   
**by**( $\text{auto simp add: redT-updW.simps}$ )

**lemma**  $\text{redT-updWs-None-implies-None}$ :  
 assumes  $\text{redT-updWs}\ t'\ ws\ was\ ws'$   
 and  $t \neq t'$  and  $ws\ t = \text{None}$   
 shows  $ws'\ t = \text{None}$   
**using**  $\langle \text{redT-updWs}\ t'\ ws\ was\ ws' \rangle \langle ws\ t = \text{None} \rangle$  **unfolding**  $\text{redT-updWs-def}$   
**by**  $\text{induct}(\text{auto intro: redT-updW-None-implies-None}[OF\ -\ -\ \langle t \neq t' \rangle])$

**lemma**  $\text{redT-updW-PostWS-imp-PostWS}$ :  
 $\llbracket \text{redT-updW}\ t\ ws\ wa\ ws'; ws\ t'' = \lfloor \text{PostWS}\ w \rfloor; t'' \neq t \rrbracket \implies ws'\ t'' = \lfloor \text{PostWS}\ w \rfloor$   
**by**( $\text{auto simp add: redT-updW.simps}$ )

**lemma**  $\text{redT-updWs-PostWS-imp-PostWS}$ :  
 $\llbracket \text{redT-updWs}\ t\ ws\ was\ ws'; t'' \neq t; ws\ t'' = \lfloor \text{PostWS}\ w \rfloor \rrbracket \implies ws'\ t'' = \lfloor \text{PostWS}\ w \rfloor$   
**unfolding**  $\text{redT-updWs-def}$   
**by**( $\text{induct rule: rtrancl3p.induct})(\text{auto dest: redT-updW-PostWS-imp-PostWS})$

**lemma**  $\text{redT-updW-Some-otherD}$ :  
 $\llbracket \text{redT-updW}\ t'\ ws\ wa\ ws'; ws'\ t = \lfloor w \rfloor; t \neq t' \rrbracket$   
 $\implies (\text{case } w \text{ of } \text{InWS } w' \Rightarrow ws\ t = \lfloor \text{InWS } w' \rfloor \mid - \Rightarrow ws\ t = \lfloor w \rfloor \vee (\exists w'. ws\ t = \lfloor \text{InWS } w' \rfloor))$   
**by**( $\text{auto simp add: redT-updW.simps split: if-split-asm wait-set-status.split}$ )

**lemma**  $\text{redT-updWs-Some-otherD}$ :  
 $\llbracket \text{redT-updWs}\ t'\ ws\ was\ ws'; ws'\ t = \lfloor w \rfloor; t \neq t' \rrbracket$   
 $\implies (\text{case } w \text{ of } \text{InWS } w' \Rightarrow ws\ t = \lfloor \text{InWS } w' \rfloor \mid - \Rightarrow ws\ t = \lfloor w \rfloor \vee (\exists w'. ws\ t = \lfloor \text{InWS } w' \rfloor))$   
**unfolding**  $\text{redT-updWs-def}$   
**apply**( $\text{induct arbitrary: } w \text{ rule: rtrancl3p.induct}$ )  
**apply**( $\text{fastforce split: wait-set-status.splits dest: redT-updW-Some-otherD}$ )  
**done**

**lemma**  $\text{redT-updW-None-SomeD}$ :  
 $\llbracket \text{redT-updW}\ t\ ws\ wa\ ws'; ws'\ t' = \lfloor w \rfloor; ws\ t' = \text{None} \rrbracket \implies t = t' \wedge (\exists w'. w = \text{InWS } w' \wedge wa = \text{Suspend } w')$   
**by**( $\text{auto simp add: redT-updW.simps split: if-split-asm}$ )

**lemma**  $\text{redT-updWs-None-SomeD}$ :  
 $\llbracket \text{redT-updWs}\ t\ ws\ was\ ws'; ws'\ t' = \lfloor w \rfloor; ws\ t' = \text{None} \rrbracket \implies t = t' \wedge (\exists w'. \text{Suspend } w' \in \text{set } was)$   
**unfolding**  $\text{redT-updWs-def}$   
**proof**( $\text{induct arbitrary: } w \text{ rule: rtrancl3p.induct}$ )  
 case ( $\text{rtrancl3p-refl } ws$ ) **thus**  $?case\ \text{by}\ \text{simp}$   
**next**  
 case ( $\text{rtrancl3p-step } ws\ was\ ws'\ wa\ ws''$ )

```

show ?case
proof(cases ws' t')
  case None
    from redT-updW-None-SomeD[OF ⟨redT-updW t ws' wa ws'⟩, OF ⟨ws'' t' = [w]⟩ this]
    show ?thesis by auto
  next
    case (Some w')
    with ⟨ws t' = None⟩ rtrancl3p-step.hyps(2) show ?thesis by auto
qed
qed

```

**lemma** redT-updW-neq-Some-SomeD:

$\llbracket \text{redT-updW } t' \text{ ws wa ws'; ws' } t = \lfloor \text{InWS } w \rfloor; \text{ws } t \neq \lfloor \text{InWS } w \rfloor \rrbracket \implies t = t' \wedge \text{wa} = \text{Suspend } w$   
**by**(auto simp add: redT-updW.simps split: if-split-asm)

**lemma** redT-updWs-neq-Some-SomeD:

$\llbracket \text{redT-updWs } t \text{ ws was ws'; ws' } t' = \lfloor \text{InWS } w \rfloor; \text{ws } t' \neq \lfloor \text{InWS } w \rfloor \rrbracket \implies t = t' \wedge \text{Suspend } w \in \text{set was}$

**unfolding** redT-updWs-def

**proof**(induct rule: rtrancl3p.induct)

**case** rtrancl3p-refl **thus** ?case **by** simp

**next**

**case** (rtrancl3p-step ws was ws' wa ws')

**show** ?case

**proof**(cases ws' t' =  $\lfloor \text{InWS } w \rfloor$ )

**case** True

**with** ⟨ws t'  $\neq \lfloor \text{InWS } w \rfloor$ ⟩ ⟨ $\llbracket \text{ws' } t' = \lfloor \text{InWS } w \rfloor; \text{ws } t' \neq \lfloor \text{InWS } w \rfloor \rrbracket \implies t = t' \wedge \text{Suspend } w \in \text{set was}$ ⟩

**show** ?thesis **by** simp

**next**

**case** False

**with** ⟨redT-updW t ws' wa ws'⟩ ⟨ws'' t' =  $\lfloor \text{InWS } w \rfloor$ ⟩

**have** t' = t  $\wedge$  wa = Suspend w **by**(rule redT-updW-neq-Some-SomeD)

**thus** ?thesis **by** auto

**qed**

**qed**

**lemma** redT-updW-not-Suspend-Some:

$\llbracket \text{redT-updW } t \text{ ws wa ws'; ws' } t = \lfloor w' \rfloor; \text{ws } t = \lfloor w \rfloor; \bigwedge w. \text{wa} \neq \text{Suspend } w \rrbracket$   
 $\implies w' = w \vee (\exists w'' w'''. w = \text{InWS } w'' \wedge w' = \text{PostWS } w''')$

**by**(auto simp add: redT-updW.simps split: if-split-asm)

**lemma** redT-updWs-not-Suspend-Some:

$\llbracket \text{redT-updWs } t \text{ ws was ws'; ws' } t = \lfloor w' \rfloor; \text{ws } t = \lfloor w \rfloor; \bigwedge w. \text{Suspend } w \notin \text{set was} \rrbracket$   
 $\implies w' = w \vee (\exists w'' w'''. w = \text{InWS } w'' \wedge w' = \text{PostWS } w''')$

**unfolding** redT-updWs-def

**proof**(induct arbitrary: w rule: rtrancl3p-converse-induct)

**case** refl **thus** ?case **by** simp

**next**

**case** (step ws wa ws' was ws')

**note** ⟨ws'' t =  $\lfloor w' \rfloor$ ⟩

**moreover**

**have** ws' t  $\neq \text{None}$

**proof**

```

assume  $ws' t = None$ 
with  $\langle rtrancl3p (redT-updW t) ws' was ws'' \rangle \langle ws'' t = \lfloor w' \rfloor \rangle$ 
obtain  $w'$  where  $Suspend w' \in set was$  unfolding  $redT-updWs-def[symmetric]$ 
  by( $auto dest: redT-updWs-None-SomeD$ )
with  $\langle Suspend w' \notin set (wa \# was) \rangle$  show  $False$  by  $simp$ 
qed
then obtain  $w''$  where  $ws' t = \lfloor w'' \rfloor$  by  $auto$ 
moreover {
  fix  $w$ 
  from  $\langle Suspend w \notin set (wa \# was) \rangle$  have  $Suspend w \notin set was$  by  $simp$  }
ultimately have  $w' = w'' \vee (\exists w''' w'''. w'' = InWS w''' \wedge w' = PostWS w''')$  by( $rule step.hyps$ )
moreover { fix  $w$ 
  from  $\langle Suspend w \notin set (wa \# was) \rangle$  have  $wa \neq Suspend w$  by  $auto$  }
note  $redT-updW-not-Suspend-Some[OF \langle redT-updW t ws wa ws' \rangle, OF \langle ws' t = \lfloor w'' \rfloor \rangle \langle ws t = \lfloor w \rfloor \rangle$ 
 $this]$ 
ultimately show  $?case$  by  $auto$ 
qed

lemma  $redT-updWs-WokenUp-SuspendD$ :
   $\llbracket redT-updWs t ws was ws'; Notified \in set was \vee WokenUp \in set was; ws' t = \lfloor w \rfloor \rrbracket \implies \exists w. Suspend$ 
 $w \in set was$ 
unfolding  $redT-updWs-def$ 
by( $induct rule: rtrancl3p-converse-induct$ )( $auto dest: redT-updWs-None-SomeD[unfolded redT-updWs-def]$ )

lemma  $redT-updW-Woken-Up-same-no-Notified-Interrupted$ :
   $\llbracket redT-updW t ws wa ws'; ws' t = \lfloor PostWS w \rfloor; ws t = \lfloor PostWS w \rfloor; \wedge w. wa \neq Suspend w \rrbracket$ 
 $\implies wa \neq Notified \wedge wa \neq WokenUp$ 
by( $fastforce$ )

lemma  $redT-updWs-Woken-Up-same-no-Notified-Interrupted$ :
   $\llbracket redT-updWs t ws was ws'; ws' t = \lfloor PostWS w \rfloor; ws t = \lfloor PostWS w \rfloor; \wedge w. Suspend w \notin set was \rrbracket$ 
 $\implies Notified \notin set was \wedge WokenUp \notin set was$ 
unfolding  $redT-updWs-def$ 
proof( $induct rule: rtrancl3p-converse-induct$ )
  case refl thus  $?case$  by  $simp$ 
next
  case ( $step ws wa ws' was ws''$ )
  note  $Suspend = \langle \wedge w. Suspend w \notin set (wa \# was) \rangle$ 
  note  $\langle ws'' t = \lfloor PostWS w \rfloor \rangle$ 
  moreover have  $ws' t = \lfloor PostWS w \rfloor$ 
  proof( $cases ws' t$ )
    case None
    with  $\langle rtrancl3p (redT-updW t) ws' was ws'' \rangle \langle ws'' t = \lfloor PostWS w \rfloor \rangle$ 
    obtain  $w$  where  $Suspend w \in set was$  unfolding  $redT-updWs-def[symmetric]$ 
    by( $auto dest: redT-updWs-None-SomeD$ )
    with  $Suspend[of w]$  have  $False$  by  $simp$ 
    thus  $?thesis ..$ 
  next
  case ( $Some w'$ )
  thus  $?thesis$  using  $\langle ws t = \lfloor PostWS w \rfloor \rangle Suspend \langle redT-updW t ws wa ws' \rangle$ 
    by( $auto simp add: redT-updW.simps split: if-split-asm$ )
  qed
moreover
  { fix  $w$  from  $Suspend[of w]$  have  $Suspend w \notin set was$  by  $simp$  }

```

```

ultimately have  $\text{Notified} \notin \text{set was} \wedge \text{WokenUp} \notin \text{set was}$  by(rule step.hyps)
moreover
{ fix  $w$  from  $\text{Suspend}[of\ w]$  have  $wa \neq \text{Suspend } w$  by auto }
with  $\langle \text{redT-updW } t\ ws\ wa\ ws' \rangle \langle ws' \ t = \lfloor \text{PostWS } w \rfloor \rangle \langle ws\ t = \lfloor \text{PostWS } w \rfloor \rangle$ 
have  $wa \neq \text{Notified} \wedge wa \neq \text{WokenUp}$  by(rule redT-updW-Woken-Up-same-no-Notified-Interrupted)
ultimately show ?case by auto
qed

```

Preconditions for wait set actions

**definition**  $\text{wset-actions-ok} :: ('w, 't) \text{ wait-sets} \Rightarrow 't \Rightarrow ('t, 'w) \text{ wait-set-action list} \Rightarrow \text{bool}$

**where**

```

 $\text{wset-actions-ok } ws\ t\ was \longleftrightarrow$ 
  (if  $\text{Notified} \in \text{set was}$  then  $ws\ t = \lfloor \text{PostWS } \text{WSNotified} \rfloor$ 
   else if  $\text{WokenUp} \in \text{set was}$  then  $ws\ t = \lfloor \text{PostWS } \text{WSWokenUp} \rfloor$ 
   else  $ws\ t = \text{None}$ )

```

**lemma**  $\text{wset-actions-ok-Nil}$  [*simp*]:

```

 $\text{wset-actions-ok } ws\ t\ [] \longleftrightarrow ws\ t = \text{None}$ 

```

**by**(*simp add: wset-actions-ok-def*)

**definition**  $\text{waiting} :: 'w \text{ wait-set-status option} \Rightarrow \text{bool}$

**where**  $\text{waiting } w \longleftrightarrow (\exists w'. w = \lfloor \text{InWS } w' \rfloor)$

**lemma**  $\text{not-waiting-iff}$ :

```

 $\neg \text{waiting } w \longleftrightarrow w = \text{None} \vee (\exists w'. w = \lfloor \text{PostWS } w' \rfloor)$ 

```

**apply**(*cases w*)

**apply**(*case-tac [2] a*)

**apply**(*auto simp add: waiting-def*)

**done**

**lemma**  $\text{waiting-code}$  [*code*]:

```

 $\text{waiting } \text{None} = \text{False}$ 
 $\bigwedge w. \text{waiting } \lfloor \text{PostWS } w \rfloor = \text{False}$ 
 $\bigwedge w. \text{waiting } \lfloor \text{InWS } w \rfloor = \text{True}$ 

```

**by**(*simp-all add: waiting-def*)

**end**

## 1.6 Semantics of the thread actions for purely conditional purpose such as Join

**theory**  $\text{FWCondAction}$

**imports**

$\text{FWState}$

**begin**

**locale**  $\text{final-thread} =$

**fixes**  $\text{final} :: 'x \Rightarrow \text{bool}$

**begin**

**primrec**  $\text{cond-action-ok} :: ('l, 't, 'x, 'm, 'w) \text{ state} \Rightarrow 't \Rightarrow 't \text{ conditional-action} \Rightarrow \text{bool}$  **where**

```

 $\bigwedge ln. \text{cond-action-ok } s\ t\ (\text{Join } T) =$ 
  (case  $\text{thr } s\ T$  of  $\text{None} \Rightarrow \text{True} \mid \lfloor (x, ln) \rfloor \Rightarrow t \neq T \wedge \text{final } x \wedge ln = \text{no-wait-locks} \wedge \text{wset } s\ T =$ 

```



*None*)  
 | *cond-action-ok s t Yield = True*

**primrec** *cond-action-oks* :: ('l,'t,'x,'m,'w) state  $\Rightarrow$  't  $\Rightarrow$  't conditional-action list  $\Rightarrow$  bool **where**  
*cond-action-oks s t [] = True*  
 | *cond-action-oks s t (ct#cts) = (cond-action-ok s t ct  $\wedge$  cond-action-oks s t cts)*

**lemma** *cond-action-oks-append [simp]*:  
*cond-action-oks s t (cts @ cts')  $\longleftrightarrow$  cond-action-oks s t cts  $\wedge$  cond-action-oks s t cts'*  
**by**(*induct cts, auto*)

**lemma** *cond-action-oks-conv-set*:  
*cond-action-oks s t cts  $\longleftrightarrow$  ( $\forall ct \in \text{set } cts. \text{cond-action-ok } s t ct$ )*  
**by**(*induct cts*) *simp-all*

**lemma** *cond-action-ok-Join*:  
 $\bigwedge ln. \llbracket \text{cond-action-ok } s t (\text{Join } T); \text{thr } s T = \lfloor (x, ln) \rfloor \rrbracket \Longrightarrow \text{final } x \wedge ln = \text{no-wait-locks} \wedge \text{wset } s T = \text{None}$   
**by**(*auto*)

**lemma** *cond-action-oks-Join*:  
 $\bigwedge ln. \llbracket \text{cond-action-oks } s t \text{ cas}; \text{Join } T \in \text{set } \text{cas}; \text{thr } s T = \lfloor (x, ln) \rfloor \rrbracket$   
 $\Longrightarrow \text{final } x \wedge ln = \text{no-wait-locks} \wedge \text{wset } s T = \text{None} \wedge t \neq T$   
**by**(*induct cas*)(*auto*)

**lemma** *cond-action-oks-upd*:  
**assumes** *tst*: *thr s t =  $\lfloor xln \rfloor$*   
**shows** *cond-action-oks (locks s, ((thr s)(t  $\mapsto$  xln'), shr s), wset s, interrupts s) t cas = cond-action-oks s t cas*  
**proof**(*induct cas*)  
**case** *Nil* **thus** ?*case* **by** *simp*  
**next**  
**case** (*Cons ca cas*)  
**from** *tst* **have** *eq*: *cond-action-ok (locks s, ((thr s)(t  $\mapsto$  xln'), shr s), wset s, interrupts s) t ca = cond-action-ok s t ca*  
**by**(*cases ca*) *auto*  
**with** *Cons* **show** ?*case* **by**(*auto simp del: fun-upd-apply*)  
**qed**

**lemma** *cond-action-ok-shr-change*:  
*cond-action-ok (ls, (ts, m), ws, is) t ct  $\Longrightarrow$  cond-action-ok (ls, (ts, m'), ws, is) t ct*  
**by**(*cases ct*) *auto*

**lemma** *cond-action-oks-shr-change*:  
*cond-action-oks (ls, (ts, m), ws, is) t cts  $\Longrightarrow$  cond-action-oks (ls, (ts, m'), ws, is) t cts*  
**by**(*auto simp add: cond-action-oks-conv-set intro: cond-action-ok-shr-change*)

**primrec** *cond-action-ok'* :: ('l,'t,'x,'m,'w) state  $\Rightarrow$  't  $\Rightarrow$  't conditional-action  $\Rightarrow$  bool  
**where**  
*cond-action-ok' - - (Join t) = True*  
 | *cond-action-ok' - - Yield = True*

**primrec** *cond-action-oks'* :: ('l,'t,'x,'m,'w) state  $\Rightarrow$  't  $\Rightarrow$  't conditional-action list  $\Rightarrow$  bool **where**  
*cond-action-oks' s t [] = True*

|  $\text{cond-action-oks}' s t (ct \# cts) = (\text{cond-action-ok}' s t ct \wedge \text{cond-action-oks}' s t cts)$

**lemma** *cond-action-oks'-append* [simp]:

$\text{cond-action-oks}' s t (cts @ cts') \longleftrightarrow \text{cond-action-oks}' s t cts \wedge \text{cond-action-oks}' s t cts'$   
**by**(*induct cts, auto*)

**lemma** *cond-action-oks'-subset-Join*:

$\text{set } cts \subseteq \text{insert Yield (range Join)} \implies \text{cond-action-oks}' s t cts$

**apply**(*induct cts*)

**apply**(*auto*)

**done**

**end**

**definition** *collect-cond-actions* ::  $'t$  conditional-action list  $\Rightarrow 't$  set **where**

$\text{collect-cond-actions } cts = \{t. \text{Join } t \in \text{set } cts\}$

**declare** *collect-cond-actions-def* [simp]

**lemma** *cond-action-ok-final-change*:

$\llbracket \text{final-thread.cond-action-ok final1 s1 t ca};$   
 $\bigwedge t. \text{thr } s1 t = \text{None} \longleftrightarrow \text{thr } s2 t = \text{None};$   
 $\bigwedge t x1. \llbracket \text{thr } s1 t = \lfloor (x1, \text{no-wait-locks}) \rfloor; \text{final1 } x1; \text{wset } s1 t = \text{None} \rrbracket$   
 $\implies \exists x2. \text{thr } s2 t = \lfloor (x2, \text{no-wait-locks}) \rfloor \wedge \text{final2 } x2 \wedge \text{ln2} = \text{no-wait-locks} \wedge \text{wset } s2 t = \text{None} \rrbracket$   
 $\implies \text{final-thread.cond-action-ok final2 s2 t ca}$

**apply**(*cases ca*)

**apply**(*fastforce simp add: final-thread.cond-action-ok.simps*) +

**done**

**lemma** *cond-action-oks-final-change*:

**assumes** *major*:  $\text{final-thread.cond-action-oks final1 s1 t cas}$

**and** *minor*:  $\bigwedge t. \text{thr } s1 t = \text{None} \longleftrightarrow \text{thr } s2 t = \text{None}$

$\bigwedge t x1. \llbracket \text{thr } s1 t = \lfloor (x1, \text{no-wait-locks}) \rfloor; \text{final1 } x1; \text{wset } s1 t = \text{None} \rrbracket$

$\implies \exists x2. \text{thr } s2 t = \lfloor (x2, \text{no-wait-locks}) \rfloor \wedge \text{final2 } x2 \wedge \text{ln2} = \text{no-wait-locks} \wedge \text{wset } s2 t = \text{None}$

**shows**  $\text{final-thread.cond-action-oks final2 s2 t cas}$

**using** *major*

**by**(*induct cas*)(*auto simp add: final-thread.cond-action-oks.simps intro: cond-action-ok-final-change[OF - minor]*)

**end**

## 1.7 Wellformedness conditions for the multithreaded state

**theory** *FWWellform*

**imports**

*FWLocking*

*FWThread*

*FWWait*

*FWCondaAction*

**begin**

Well-formedness property: Locks are held by real threads

**definition**

$\text{lock-thread-ok} :: ('l, 't) \text{locks} \Rightarrow ('l, 't, 'x) \text{thread-info} \Rightarrow \text{bool}$

**where**  $[code\ del]:$

$lock-thread-ok\ ls\ ts \equiv \forall l\ t. has-lock\ (ls\ \$\ l)\ t \longrightarrow (\exists xw. ts\ t = \lfloor xw \rfloor)$

**lemma**  $lock-thread-ok-code\ [code]:$

$lock-thread-ok\ ls\ ts = finfun-All\ ((\lambda l. case\ l\ of\ None \Rightarrow True \mid \lfloor (t, n) \rfloor \Rightarrow (ts\ t \neq None)) \circ \$\ ls)$

**by**( $simp\ add: lock-thread-ok-def\ finfun-All-All\ has-lock-has-locks-conv\ has-locks-iff\ o-def$ )

**lemma**  $lock-thread-okI:$

$(\bigwedge l\ t. has-lock\ (ls\ \$\ l)\ t \Longrightarrow \exists xw. ts\ t = \lfloor xw \rfloor) \Longrightarrow lock-thread-ok\ ls\ ts$

**by**( $auto\ simp\ add: lock-thread-ok-def$ )

**lemma**  $lock-thread-okD:$

$\llbracket lock-thread-ok\ ls\ ts; has-lock\ (ls\ \$\ l)\ t \rrbracket \Longrightarrow \exists xw. ts\ t = \lfloor xw \rfloor$

**by**( $fastforce\ simp\ add: lock-thread-ok-def$ )

**lemma**  $lock-thread-okD':$

$\llbracket lock-thread-ok\ ls\ ts; has-locks\ (ls\ \$\ l)\ t = Suc\ n \rrbracket \Longrightarrow \exists xw. ts\ t = \lfloor xw \rfloor$

**by**( $auto\ elim: lock-thread-okD[\textbf{where}\ l=l]\ simp\ del: split-paired-Ex$ )

**lemma**  $lock-thread-okE:$

$\llbracket lock-thread-ok\ ls\ ts; \forall l\ t. has-lock\ (ls\ \$\ l)\ t \longrightarrow (\exists xw. ts\ t = \lfloor xw \rfloor) \Longrightarrow P \rrbracket \Longrightarrow P$

**by**( $auto\ simp\ add: lock-thread-ok-def\ simp\ del: split-paired-Ex$ )

**lemma**  $lock-thread-ok-upd:$

$lock-thread-ok\ ls\ ts \Longrightarrow lock-thread-ok\ ls\ (ts(t \mapsto xw))$

**by**( $auto\ intro!: lock-thread-okI\ dest: lock-thread-okD$ )

**lemma**  $lock-thread-ok-has-lockE:$

**assumes**  $lock-thread-ok\ ls\ ts$

**and**  $has-lock\ (ls\ \$\ l)\ t$

**obtains**  $x\ ln'$  **where**  $ts\ t = \lfloor (x, ln') \rfloor$

**using**  $assms$

**by**( $auto\ dest!: lock-thread-okD$ )

**lemma**  $redT-updLs-preserves-lock-thread-ok:$

**assumes**  $lto: lock-thread-ok\ ls\ ts$

**and**  $tst: ts\ t = \lfloor xw \rfloor$

**shows**  $lock-thread-ok\ (redT-updLs\ ls\ t\ las)\ ts$

**proof**( $rule\ lock-thread-okI$ )

**fix**  $L\ T$

**assume**  $ru: has-lock\ (redT-updLs\ ls\ t\ las\ \$\ L)\ T$

**show**  $\exists xw. ts\ T = \lfloor xw \rfloor$

**proof**( $cases\ t = T$ )

**case**  $True$

**thus**  $?thesis$  **using**  $tst\ lto$

**by**( $auto\ elim: lock-thread-okE$ )

**next**

**case**  $False$

**with**  $ru$  **have**  $has-lock\ (ls\ \$\ L)\ T$

**by**( $rule\ redT-updLs-Some-thread-idD$ )

**thus**  $?thesis$  **using**  $lto$

**by**( $auto\ elim!: lock-thread-okE\ simp\ del: split-paired-Ex$ )

**qed**

**qed**

**lemma** *redT-updTs-preserves-lock-thread-ok*:  
**assumes** *lto*: *lock-thread-ok* *ls* *ts*  
**shows** *lock-thread-ok* *ls* (*redT-updTs* *ts* *nts*)  
**proof**(*rule lock-thread-okI*)  
**fix** *l t*  
**assume** *has-lock* (*ls* \$ *l*) *t*  
**with** *lto* **have**  $\exists xw. ts\ t = \lfloor xw \rfloor$   
**by**(*auto elim!*: *lock-thread-okE* *simp del: split-paired-Ex*)  
**thus**  $\exists xw. redT-updTs\ ts\ nts\ t = \lfloor xw \rfloor$   
**by**(*auto intro: redT-updTs-Some1 simp del: split-paired-Ex*)  
**qed**

**lemma** *lock-thread-ok-has-lock*:  
**assumes** *lock-thread-ok* *ls* *ts*  
**and** *has-lock* (*ls* \$ *l*) *t*  
**obtains** *xw* **where** *ts* *t* =  $\lfloor xw \rfloor$   
**using** *assms*  
**by**(*auto dest!*: *lock-thread-okD*)

**lemma** *lock-thread-ok-None-has-locks-0*:  
 $\llbracket lock-thread-ok\ ls\ ts; ts\ t = None \rrbracket \implies has-locks\ (ls\ \$\ l)\ t = 0$   
**by**(*rule ccontr*)(*auto dest: lock-thread-okD*)

**lemma** *redT-upds-preserves-lock-thread-ok*:  
 $\llbracket lock-thread-ok\ ls\ ts; ts\ t = \lfloor xw \rfloor; thread-oks\ ts\ tas \rrbracket$   
 $\implies lock-thread-ok\ (redT-updLs\ ls\ t\ las)\ ((redT-updTs\ ts\ tas)(t \mapsto xw'))$   
**apply**(*rule lock-thread-okI*)  
**apply**(*clarsimp simp del: split-paired-Ex*)  
**apply**(*drule has-lock-upd-locks-implies-has-lock, simp*)  
**apply**(*drule lock-thread-okD, assumption*)  
**apply**(*erule exE*)  
**by**(*rule redT-updTs-Some1*)

**lemma** *acquire-all-preserves-lock-thread-ok*:  
**fixes** *ln*  
**shows**  $\llbracket lock-thread-ok\ ls\ ts; ts\ t = \lfloor (x, ln) \rfloor \rrbracket \implies lock-thread-ok\ (acquire-all\ ls\ t\ ln)\ (ts(t \mapsto xw))$   
**by**(*rule lock-thread-okI*)(*auto dest!: has-lock-acquire-locks-implies-has-lock dest: lock-thread-okD*)

Well-formedness condition: Wait sets contain only real threads

**definition** *wset-thread-ok* :: (*'w*, *'t*) *wait-sets*  $\Rightarrow$  (*'l*, *'t*, *'x*) *thread-info*  $\Rightarrow$  *bool*  
**where** *wset-thread-ok* *ws* *ts*  $\equiv \forall t. ts\ t = None \longrightarrow ws\ t = None$

**lemma** *wset-thread-okI*:  
 $(\bigwedge t. ts\ t = None \implies ws\ t = None) \implies wset-thread-ok\ ws\ ts$   
**by**(*simp add: wset-thread-ok-def*)

**lemma** *wset-thread-okD*:  
 $\llbracket wset-thread-ok\ ws\ ts; ts\ t = None \rrbracket \implies ws\ t = None$   
**by**(*simp add: wset-thread-ok-def*)

**lemma** *wset-thread-ok-conv-dom*:  
 $wset-thread-ok\ ws\ ts \longleftrightarrow dom\ ws \subseteq dom\ ts$   
**by**(*auto simp add: wset-thread-ok-def*)

**lemma** *wset-thread-ok-upd*:

$wset\text{-}thread\text{-}ok\ ls\ ts \implies wset\text{-}thread\text{-}ok\ ls\ (ts(t \mapsto xw))$

**by**(*auto intro!*: *wset-thread-okI dest: wset-thread-okD split: if-split-asm*)

**lemma** *wset-thread-ok-upd-None*:

$wset\text{-}thread\text{-}ok\ ws\ ts \implies wset\text{-}thread\text{-}ok\ (ws(t := None))\ (ts(t := None))$

**by**(*auto intro!*: *wset-thread-okI dest: wset-thread-okD split: if-split-asm*)

**lemma** *wset-thread-ok-upd-Some*:

$wset\text{-}thread\text{-}ok\ ws\ ts \implies wset\text{-}thread\text{-}ok\ (ws(t := wo))\ (ts(t \mapsto xln))$

**by**(*auto intro!*: *wset-thread-okI dest: wset-thread-okD split: if-split-asm*)

**lemma** *wset-thread-ok-upd-ws*:

$\llbracket wset\text{-}thread\text{-}ok\ ws\ ts; ts\ t = \lfloor xln \rfloor \rrbracket \implies wset\text{-}thread\text{-}ok\ (ws(t := w))\ ts$

**by**(*auto intro!*: *wset-thread-okI dest: wset-thread-okD*)

**lemma** *wset-thread-ok-NotifyAllI*:

$wset\text{-}thread\text{-}ok\ ws\ ts \implies wset\text{-}thread\text{-}ok\ (\lambda t. \text{if } ws\ t = \lfloor w\ t \rfloor \text{ then } \lfloor w' t \rfloor \text{ else } ws\ t)\ ts$

**by**(*simp add: wset-thread-ok-def*)

**lemma** *redT-updTs-preserves-wset-thread-ok*:

**assumes** *wto: wset-thread-ok ws ts*

**shows**  $wset\text{-}thread\text{-}ok\ ws\ (redT\text{-}updTs\ ts\ nts)$

**proof**(*rule wset-thread-okI*)

**fix** *t*

**assume**  $redT\text{-}updTs\ ts\ nts\ t = None$

**hence**  $ts\ t = None$  **by**(*rule redT-updTs-None*)

**with** *wto* **show**  $ws\ t = None$  **by**(*rule wset-thread-okD*)

**qed**

**lemma** *redT-updW-preserve-wset-thread-ok*:

$\llbracket wset\text{-}thread\text{-}ok\ ws\ ts; redT\text{-}updW\ t\ ws\ wa\ ws'; ts\ t = \lfloor xln \rfloor \rrbracket \implies wset\text{-}thread\text{-}ok\ ws'\ ts$

**by**(*fastforce simp add: redT-updW.simps intro: wset-thread-okI wset-thread-ok-NotifyAllI wset-thread-ok-upd-ws dest: wset-thread-okD*)

**lemma** *redT-updWs-preserve-wset-thread-ok*:

$\llbracket wset\text{-}thread\text{-}ok\ ws\ ts; redT\text{-}updWs\ t\ ws\ was\ ws'; ts\ t = \lfloor xln \rfloor \rrbracket \implies wset\text{-}thread\text{-}ok\ ws'\ ts$

**unfolding** *redT-updWs-def* **apply**(*rotate-tac 1*)

**by**(*induct rule: rtranc13p-converse-induct*)(*auto intro: redT-updW-preserve-wset-thread-ok*)

Well-formedness condition: Wait sets contain only non-final threads

**context** *final-thread* **begin**

**definition** *wset-final-ok* ::  $('w, 't)\ wait\text{-}sets \Rightarrow ('l, 't, 'x)\ thread\text{-}info \Rightarrow bool$

**where**  $wset\text{-}final\text{-}ok\ ws\ ts \iff (\forall t \in dom\ ws. \exists x\ ln. ts\ t = \lfloor (x, ln) \rfloor \wedge \neg final\ x)$

**lemma** *wset-final-okI*:

$(\bigwedge t\ w. ws\ t = \lfloor w \rfloor \implies \exists x\ ln. ts\ t = \lfloor (x, ln) \rfloor \wedge \neg final\ x) \implies wset\text{-}final\text{-}ok\ ws\ ts$

**unfolding** *wset-final-ok-def* **by**(*blast*)

**lemma** *wset-final-okD*:

$\llbracket wset\text{-}final\text{-}ok\ ws\ ts; ws\ t = \lfloor w \rfloor \rrbracket \implies \exists x\ ln. ts\ t = \lfloor (x, ln) \rfloor \wedge \neg final\ x$

**unfolding** *wset-final-ok-def* **by**(*blast*)

**lemma** *wset-final-okE*:  
**assumes** *wset-final-ok ws ts ws t = [w]*  
**and**  $\bigwedge x \ln. ts\ t = [(x, \ln)] \implies \neg \text{final } x \implies \text{thesis}$   
**shows** *thesis*  
**using** *assms* **by**(*blast dest: wset-final-okD*)

**lemma** *wset-final-ok-imp-wset-thread-ok*:  
*wset-final-ok ws ts  $\implies$  wset-thread-ok ws ts*  
**apply**(*rule wset-thread-okI*)  
**apply**(*rule ccontr*)  
**apply**(*auto elim: wset-final-okE*)  
**done**

**end**

**end**

## 1.8 Semantics of the thread action ReleaseAcquire for the thread state

**theory** *FWLockingThread*  
**imports**  
*FWLocking*  
**begin**

**fun** *upd-threadR* :: *nat  $\Rightarrow$  't lock  $\Rightarrow$  't  $\Rightarrow$  lock-action  $\Rightarrow$  nat*  
**where**  
*upd-threadR n l t ReleaseAcquire = n + has-locks l t*  
*| upd-threadR n l t - = n*

**primrec** *upd-threadRs* :: *nat  $\Rightarrow$  't lock  $\Rightarrow$  't  $\Rightarrow$  lock-action list  $\Rightarrow$  nat*  
**where**  
*upd-threadRs n l t [] = n*  
*| upd-threadRs n l t (la # las) = upd-threadRs (upd-threadR n l t la) (upd-lock l t la) t las*

**lemma** *upd-threadRs-append [simp]*:  
*upd-threadRs n l t (las @ las') = upd-threadRs (upd-threadRs n l t las) (upd-locks l t las) t las'*  
**by**(*induct las arbitrary: n l, auto*)

**definition** *redT-updLns* :: *('l, 't) locks  $\Rightarrow$  't  $\Rightarrow$  ('l  $\Rightarrow$  nat)  $\Rightarrow$  'l lock-actions  $\Rightarrow$  ('l  $\Rightarrow$  nat)*  
**where**  $\bigwedge \ln. \text{redT-updLns } ls\ t\ \ln\ las = (\lambda(l, n, la). \text{upd-threadRs } n\ l\ t\ la) \circ \$ (\$ls, (\$ln, las\$)\$)$

**lemma** *redT-updLns-iff [simp]*:  
 $\bigwedge \ln. \text{redT-updLns } ls\ t\ \ln\ las\ \$\ l = \text{upd-threadRs } (\ln\ \$\ l) (ls\ \$\ l) t (las\ \$\ l)$   
**by**(*simp add: redT-updLns-def*)

**lemma** *upd-threadRs-comp-empty [simp]*:  $(\lambda(l, n, las). \text{upd-threadRs } n\ l\ t\ las) \circ \$ (\$ls, (\$lns, K\$ [])\$) = lns$   
**by**(*auto intro!: finfun-ext*)

**lemma** *redT-updLs-empty [simp]*: *redT-updLs ls t (K\$ []) = ls*  
**by**(*simp add: redT-updLs-def*)

end

## 1.9 Semantics of the thread actions for interruption

**theory** *FWInterrupt*

**imports**

*FWState*

**begin**

**primrec** *redT-updI* :: '*t* interrupts  $\Rightarrow$  '*t* interrupt-action  $\Rightarrow$  '*t* interrupts

**where**

*redT-updI is* (*Interrupt t*) = *insert t is*  
| *redT-updI is* (*ClearInterrupt t*) = *is* - {*t*}  
| *redT-updI is* (*IsInterrupted t b*) = *is*

**fun** *redT-updIs* :: '*t* interrupts  $\Rightarrow$  '*t* interrupt-action list  $\Rightarrow$  '*t* interrupts

**where**

*redT-updIs is* [] = *is*  
| *redT-updIs is* (*ia* # *ias*) = *redT-updIs* (*redT-updI is ia*) *ias*

**primrec** *interrupt-action-ok* :: '*t* interrupts  $\Rightarrow$  '*t* interrupt-action  $\Rightarrow$  bool

**where**

*interrupt-action-ok is* (*Interrupt t*) = *True*  
| *interrupt-action-ok is* (*ClearInterrupt t*) = *True*  
| *interrupt-action-ok is* (*IsInterrupted t b*) = (*b* = (*t*  $\in$  *is*))

**fun** *interrupt-actions-ok* :: '*t* interrupts  $\Rightarrow$  '*t* interrupt-action list  $\Rightarrow$  bool

**where**

*interrupt-actions-ok is* [] = *True*  
| *interrupt-actions-ok is* (*ia* # *ias*)  $\longleftrightarrow$  *interrupt-action-ok is ia*  $\wedge$  *interrupt-actions-ok* (*redT-updI is ia*) *ias*

**primrec** *interrupt-action-ok'* :: '*t* interrupts  $\Rightarrow$  '*t* interrupt-action  $\Rightarrow$  bool

**where**

*interrupt-action-ok' is* (*Interrupt t*) = *True*  
| *interrupt-action-ok' is* (*ClearInterrupt t*) = *True*  
| *interrupt-action-ok' is* (*IsInterrupted t b*) = (*b*  $\vee$  *t*  $\notin$  *is*)

**fun** *interrupt-actions-ok'* :: '*t* interrupts  $\Rightarrow$  '*t* interrupt-action list  $\Rightarrow$  bool

**where**

*interrupt-actions-ok' is* [] = *True*  
| *interrupt-actions-ok' is* (*ia* # *ias*)  $\longleftrightarrow$  *interrupt-action-ok' is ia*  $\wedge$  *interrupt-actions-ok'* (*redT-updI is ia*) *ias*

**fun** *collect-interrupt* :: '*t* interrupt-action  $\Rightarrow$  '*t* set  $\Rightarrow$  '*t* set

**where**

*collect-interrupt* (*IsInterrupted t True*) *Ts* = *insert t Ts*  
| *collect-interrupt* (*Interrupt t*) *Ts* = *Ts* - {*t*}  
| *collect-interrupt* - *Ts* = *Ts*

**definition** *collect-interrupts* :: '*t* interrupt-action list  $\Rightarrow$  '*t* set

**where** *collect-interrupts ias* = *foldr collect-interrupt ias* {}

**lemma** *collect-interrupts-interrupted*:

$\llbracket \text{interrupt-actions-ok is ias; } t' \in \text{collect-interrupts ias} \rrbracket \implies t' \in \text{is}$   
**unfolding** *collect-interrupts-def*  
**proof**(*induct ias arbitrary: is*)  
**case** *Nil* **thus** ?*case* **by** *simp*  
**next**  
**case** (*Cons ia ias*) **thus** ?*case*  
**by**(*cases (ia, foldr collect-interrupt ias {})* *rule: collect-interrupt.cases*) *auto*  
**qed**

**lemma** *interrupt-actions-ok-append* [*simp*]:

$\text{interrupt-actions-ok is (ias @ ias')} \longleftrightarrow \text{interrupt-actions-ok is ias} \wedge \text{interrupt-actions-ok (redT-updIs is ias) ias'}$   
**by**(*induct ias arbitrary: is*) *auto*

**lemma** *collect-interrupt-subset*:  $Ts \subseteq Ts' \implies \text{collect-interrupt ia } Ts \subseteq \text{collect-interrupt ia } Ts'$   
**by**(*cases (ia, Ts)* *rule: collect-interrupt.cases*) *auto*

**lemma** *foldr-collect-interrupt-subset*:

$Ts \subseteq Ts' \implies \text{foldr collect-interrupt ias } Ts \subseteq \text{foldr collect-interrupt ias } Ts'$   
**by**(*induct ias*)(*simp-all add: collect-interrupt-subset*)

**lemma** *interrupt-actions-ok-all-nthI*:

**assumes**  $\bigwedge n. n < \text{length ias} \implies \text{interrupt-action-ok (redT-updIs is (take n ias)) (ias ! n)}$   
**shows** *interrupt-actions-ok is ias*  
**using** *assms*  
**proof**(*induct ias arbitrary: is*)  
**case** *Nil* **thus** ?*case* **by** *simp*  
**next**  
**case** (*Cons ia ias*)  
**from** *Cons.premis[of 0]* **have** *interrupt-action-ok is ia* **by** *simp*  
**moreover**  
 { **fix** *n*  
**assume**  $n < \text{length ias}$   
**hence** *interrupt-action-ok (redT-updIs (redT-updI is ia) (take n ias)) (ias ! n)*  
**using** *Cons.premis[of Suc n]* **by** *simp* }  
**hence** *interrupt-actions-ok (redT-updI is ia) ias* **by**(*rule Cons.hyps*)  
**ultimately show** ?*case* **by** *simp*  
**qed**

**lemma** *interrupt-actions-ok-nthD*:

**assumes** *interrupt-actions-ok is ias*  
**and**  $n < \text{length ias}$   
**shows** *interrupt-action-ok (redT-updIs is (take n ias)) (ias ! n)*  
**using** *assms*  
**by**(*induct n arbitrary: is ias*)(*case-tac [!]* *ias, auto*)

**lemma** *interrupt-actions-ok'-all-nthI*:

**assumes**  $\bigwedge n. n < \text{length ias} \implies \text{interrupt-action-ok}' (\text{redT-updIs is (take n ias)}) (ias ! n)$   
**shows** *interrupt-actions-ok' is ias*  
**using** *assms*  
**proof**(*induct ias arbitrary: is*)  
**case** *Nil* **thus** ?*case* **by** *simp*



**next**  
**case** (*Cons ia ias*)  
**from** *Cons.prem*s[*of 0*] **have** *interrupt-action-ok'* *is ia* **by** *simp*  
**moreover**  
**{ fix** *n*  
**assume** *n < length ias*  
**hence** *interrupt-action-ok'* (*redT-updIs* (*redT-updI is ia*) (*take n ias*)) (*ias ! n*)  
**using** *Cons.prem*s[*of Suc n*] **by** *simp* }  
**hence** *interrupt-actions-ok'* (*redT-updI is ia*) *ias* **by**(*rule Cons.hyps*)  
**ultimately show** ?*case* **by** *simp*  
**qed**

**lemma** *interrupt-actions-ok'-nthD*:  
**assumes** *interrupt-actions-ok'* *is ias*  
**and** *n < length ias*  
**shows** *interrupt-action-ok'* (*redT-updIs is* (*take n ias*)) (*ias ! n*)  
**using** *assms*  
**by**(*induct n arbitrary: is ias*)(*case-tac* [!] *ias, auto*)

**lemma** *interrupt-action-ok-imp-interrupt-action-ok'* [*simp*]:  
*interrupt-action-ok is ia  $\implies$  interrupt-action-ok' is ia*  
**by**(*cases ia*) *simp-all*

**lemma** *interrupt-actions-ok-imp-interrupt-actions-ok'* [*simp*]:  
*interrupt-actions-ok is ias  $\implies$  interrupt-actions-ok' is ias*  
**by**(*induct ias arbitrary: is*)(*simp-all*)

**lemma** *collect-interruptsE*:  
**assumes** *t'  $\in$  collect-interrupts ias'*  
**obtains** *n'* **where** *n' < length ias' ias' ! n' = IsInterrupted t' True*  
**and** *Interrupt t'  $\notin$  set (take n' ias')*  
**proof**(*atomize-elim*)  
**from** *assms* **show**  $\exists n' < \text{length } ias'. ias' ! n' = \text{IsInterrupted } t' \text{ True} \wedge \text{Interrupt } t' \notin \text{set } (\text{take } n' ias')$   
**unfolding** *collect-interrupts-def*  
**proof**(*induct ias' arbitrary: t'*)  
**case Nil** **thus** ?*case* **by** *simp*  
**next**  
**case** (*Cons ia ias*) **thus** ?*case*  
**by**(*cases (ia, foldr collect-interrupt ias {})*) *rule: collect-interrupt.cases*) *fastforce+*  
**qed**  
**qed**

**lemma** *collect-interrupts-prefix*:  
*collect-interrupts ias  $\subseteq$  collect-interrupts (ias @ ias')*  
**by** (*metis Un-empty collect-interrupts-def foldr-append foldr-collect-interrupt-subset inf-sup-ord(1) inf-sup-ord(2) subset-Un-eq*)

**lemma** *redT-updI-insert-Interrupt*:  
 $\llbracket t \in \text{redT-updI is ia}; t \notin \text{is} \rrbracket \implies ia = \text{Interrupt } t$   
**by**(*cases ia*) *simp-all*

**lemma** *redT-updIs-insert-Interrupt*:  
 $\llbracket t \in \text{redT-updIs is ias}; t \notin \text{is} \rrbracket \implies \text{Interrupt } t \in \text{set ias}$

```

proof(induct ias arbitrary: is)
  case Nil thus ?case by simp
next
  case (Cons ia ias) thus ?case
    by(cases t ∈ redT-updI is ia)(auto dest: redT-updI-insert-Interrupt)
qed

```

**lemma** *interrupt-actions-ok-takeI:*

*interrupt-actions-ok is ias*  $\implies$  *interrupt-actions-ok is (take n ias)*  
**by**(*subst (asm) append-take-drop-id[symmetric, where n=n]*)(*simp del: append-take-drop-id*)

**lemma** *interrupt-actions-ok'-collect-interrupts-imp-interrupt-actions-ok:*

```

assumes int: interrupt-actions-ok' is ias
and ci: collect-interrupts ias ⊆ is
and int': interrupt-actions-ok is' ias
shows interrupt-actions-ok is ias
proof(rule interrupt-actions-ok-all-nthI)
  fix n
  assume n: n < length ias
  show interrupt-action-ok (redT-updIs is (take n ias)) (ias ! n)
  proof(cases ∃ t. ias ! n = IsInterrupted t True)
    case False
      with interrupt-actions-ok'-nthD[OF int n] show ?thesis by(cases ias ! n simp-all)
    next
      case True
      then obtain t where ia: ias ! n = IsInterrupted t True ..
      from int' n have interrupt-action-ok (redT-updIs is' (take n ias)) (ias ! n) by(rule interrupt-actions-ok-nthD)
      with ia have t ∈ redT-updIs is' (take n ias) by simp
      moreover have ias = take (Suc n) ias @ drop (Suc n) ias by simp
      with ci have collect-interrupts (take (Suc n) ias) ⊆ is
        by (metis collect-interrupts-prefix subset-trans)
      ultimately have t ∈ redT-updIs is (take n ias) using n ia int int'
      proof(induct n arbitrary: is is' ias)
        case 0 thus ?case by(clarsimp simp add: neg-Nil-conv collect-interrupts-def)
      next
        case (Suc n)
        from  $\langle \text{Suc } n < \text{length } ias \rangle$  obtain ia ias'
          where ias [simp]: ias = ia # ias' by(cases ias auto)
        from  $\langle \text{interrupt-actions-ok is' ias} \rangle$ 
        have ia-ok: interrupt-action-ok is' ia by simp

        from  $\langle t \in \text{redT-updIs is' (take (Suc } n) \text{ ias)} \rangle$ 
        have t ∈ redT-updIs (redT-updI is' ia) (take n ias') by simp
        moreover from  $\langle \text{collect-interrupts (take (Suc (Suc } n)) ias) \subseteq is \rangle$  ia-ok
        have collect-interrupts (take (Suc n) ias') ⊆ redT-updI is ia
        proof(cases (ia, is) rule: collect-interrupt.cases)
          case (3-2 t' Ts)
            hence [simp]: ia = ClearInterrupt t' Ts = is by simp-all
            have t' ∉ collect-interrupts (take (Suc n) ias')
            proof
              assume t' ∈ collect-interrupts (take (Suc n) ias')
              then obtain n' where n' < length (take (Suc n) ias') take (Suc n) ias' ! n' = IsInterrupted

```

*t' True*

```

    Interrupt  $t' \notin \text{set } (\text{take } n' (\text{take } (\text{Suc } n) \text{ias}'))$  by (rule collect-interruptsE)
  hence  $n' \leq n$   $\text{ias}' ! n' = \text{IsInterrupted } t' \text{ True}$  Interrupt  $t' \notin \text{set } (\text{take } n' \text{ias}')$ 
    using  $\langle \text{Suc } n < \text{length } \text{ias} \rangle$  by (simp-all add: min-def split: if-split-asm)
  hence  $\text{Suc } n' < \text{length } \text{ias}$  using  $\langle \text{Suc } n < \text{length } \text{ias} \rangle$  by (simp add: min-def)
  with  $\langle \text{interrupt-actions-ok } \text{is}' \text{ias} \rangle$ 
  have interrupt-action-ok (redT-updIs  $\text{is}' (\text{take } (\text{Suc } n') \text{ias})$ ) ( $\text{ias} ! \text{Suc } n'$ )
    by (rule interrupt-actions-ok-nthD)
  with  $\langle \text{Suc } n < \text{length } \text{ias} \rangle$   $\langle \text{ias}' ! n' = \text{IsInterrupted } t' \text{ True} \rangle$ 
  have  $t' \in \text{redT-updIs } (\text{is}' - \{t'\}) (\text{take } n' \text{ias}')$  by simp
  hence Interrupt  $t' \in \text{set } (\text{take } n' \text{ias}')$ 
    by (rule redT-updIs-insert-Interrupt) simp
  with  $\langle \text{Interrupt } t' \notin \text{set } (\text{take } n' \text{ias}') \rangle$  show False by contradiction
qed
thus ?thesis using  $\langle \text{collect-interrupts } (\text{take } (\text{Suc } (\text{Suc } n)) \text{ias}) \subseteq \text{is} \rangle$ 
  by (auto simp add: collect-interrupts-def)
qed(auto simp add: collect-interrupts-def)
moreover from  $\langle \text{Suc } n < \text{length } \text{ias} \rangle$  have  $n < \text{length } \text{ias}'$  by simp
moreover from  $\langle \text{ias} ! \text{Suc } n = \text{IsInterrupted } t \text{ True} \rangle$  have  $\text{ias}' ! n = \text{IsInterrupted } t \text{ True}$  by
simp
moreover from  $\langle \text{interrupt-actions-ok}' \text{is } \text{ias} \rangle$  have interrupt-actions-ok' (redT-updI is ia)  $\text{ias}'$ 
  unfolding  $\text{ias}$  by simp
moreover from  $\langle \text{interrupt-actions-ok } \text{is}' \text{ias} \rangle$  have interrupt-actions-ok (redT-updI  $\text{is}' \text{ia}$ )  $\text{ias}'$ 
by simp
ultimately have  $t \in \text{redT-updIs } (\text{redT-updI is ia}) (\text{take } n \text{ias}')$  by (rule Suc)
thus ?case by simp
qed
thus ?thesis unfolding  $\text{ia}$  by simp
qed
qed
end

```

## 1.10 The multithreaded semantics

**theory** FWSemantics

**imports**

FWWellform

FWLockingThread

FWCondAction

FWInterrupt

**begin**

**inductive** redT-upd ::  $(\text{'l}, \text{'t}, \text{'x}, \text{'m}, \text{'w}) \text{ state} \Rightarrow \text{'t} \Rightarrow (\text{'l}, \text{'t}, \text{'x}, \text{'m}, \text{'w}, \text{'o}) \text{ thread-action} \Rightarrow \text{'x} \Rightarrow \text{'m} \Rightarrow$   
 $(\text{'l}, \text{'t}, \text{'x}, \text{'m}, \text{'w}) \text{ state} \Rightarrow \text{bool}$

**for**  $s \text{ t ta } x' m'$

**where**

$\text{redT-updWs } t (\text{wset } s) \llbracket \text{ta} \rrbracket_w \text{ws}'$   
 $\implies \text{redT-upd } s \text{ t ta } x' m' (\text{redT-updLs } (\text{locks } s) \text{ t } \llbracket \text{ta} \rrbracket_l, ((\text{redT-updTs } (\text{thr } s) \llbracket \text{ta} \rrbracket_t)(t \mapsto (x',$   
 $\text{redT-updLns } (\text{locks } s) \text{ t } (\text{snd } (\text{the } (\text{thr } s \text{ t}))) \llbracket \text{ta} \rrbracket_i), m'), \text{ws}', \text{redT-updIs } (\text{interrupts } s) \llbracket \text{ta} \rrbracket_i)$

**inductive-simps** redT-upd-simps [simp]:

$\text{redT-upd } s \text{ t ta } x' m' s'$

**definition**  $\text{redT-acq} :: ('l, 't, 'x, 'm, 'w) \text{ state} \Rightarrow 't \Rightarrow ('l \Rightarrow f \text{ nat}) \Rightarrow ('l, 't, 'x, 'm, 'w) \text{ state}$

**where**

$\bigwedge \text{ln. redT-acq } s \ t \ \text{ln} = (\text{acquire-all } (\text{locks } s) \ t \ \text{ln}, ((\text{thr } s)(t \mapsto (\text{fst } (\text{the } (\text{thr } s \ t))), \text{no-wait-locks})), \text{shr } s), \text{wset } s, \text{interrupts } s)$

**context** *final-thread* **begin**

**inductive**  $\text{actions-ok} :: ('l, 't, 'x, 'm, 'w) \text{ state} \Rightarrow 't \Rightarrow ('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \Rightarrow \text{bool}$

**for**  $s :: ('l, 't, 'x, 'm, 'w) \text{ state}$  **and**  $t :: 't$  **and**  $ta :: ('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action}$

**where**

$\llbracket \text{lock-ok-las } (\text{locks } s) \ t \ \llbracket ta \rrbracket_l; \text{thread-oks } (\text{thr } s) \ \llbracket ta \rrbracket_t; \text{cond-action-oks } s \ t \ \llbracket ta \rrbracket_c;$

$\text{wset-actions-ok } (\text{wset } s) \ t \ \llbracket ta \rrbracket_w; \text{interrupt-actions-ok } (\text{interrupts } s) \ \llbracket ta \rrbracket_i \rrbracket$

$\implies \text{actions-ok } s \ t \ ta$

**declare**  $\text{actions-ok.intros}$  [intro!]

**declare**  $\text{actions-ok.cases}$  [elim!]

**lemma**  $\text{actions-ok-iff}$  [simp]:

$\text{actions-ok } s \ t \ ta \longleftrightarrow$

$\text{lock-ok-las } (\text{locks } s) \ t \ \llbracket ta \rrbracket_l \wedge \text{thread-oks } (\text{thr } s) \ \llbracket ta \rrbracket_t \wedge \text{cond-action-oks } s \ t \ \llbracket ta \rrbracket_c \wedge$

$\text{wset-actions-ok } (\text{wset } s) \ t \ \llbracket ta \rrbracket_w \wedge \text{interrupt-actions-ok } (\text{interrupts } s) \ \llbracket ta \rrbracket_i$

**by**(*auto*)

**lemma**  $\text{actions-ok-thread-oksD}$ :

$\text{actions-ok } s \ t \ ta \implies \text{thread-oks } (\text{thr } s) \ \llbracket ta \rrbracket_t$

**by**(*erule actions-ok.cases*)

**inductive**  $\text{actions-ok}' :: ('l, 't, 'x, 'm, 'w) \text{ state} \Rightarrow 't \Rightarrow ('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \Rightarrow \text{bool}$  **where**

$\llbracket \text{lock-ok-las}' (\text{locks } s) \ t \ \llbracket ta \rrbracket_l; \text{thread-oks } (\text{thr } s) \ \llbracket ta \rrbracket_t; \text{cond-action-oks}' s \ t \ \llbracket ta \rrbracket_c;$

$\text{wset-actions-ok } (\text{wset } s) \ t \ \llbracket ta \rrbracket_w; \text{interrupt-actions-ok}' (\text{interrupts } s) \ \llbracket ta \rrbracket_i \rrbracket$

$\implies \text{actions-ok}' s \ t \ ta$

**declare**  $\text{actions-ok'.intros}$  [intro!]

**declare**  $\text{actions-ok'.cases}$  [elim!]

**lemma**  $\text{actions-ok'-iff}$ :

$\text{actions-ok}' s \ t \ ta \longleftrightarrow$

$\text{lock-ok-las}' (\text{locks } s) \ t \ \llbracket ta \rrbracket_l \wedge \text{thread-oks } (\text{thr } s) \ \llbracket ta \rrbracket_t \wedge \text{cond-action-oks}' s \ t \ \llbracket ta \rrbracket_c \wedge$

$\text{wset-actions-ok } (\text{wset } s) \ t \ \llbracket ta \rrbracket_w \wedge \text{interrupt-actions-ok}' (\text{interrupts } s) \ \llbracket ta \rrbracket_i$

**by** *auto*

**lemma**  $\text{actions-ok'-ta-upd-obs}$ :

$\text{actions-ok}' s \ t \ (\text{ta-update-obs } ta \ \text{obs}) \longleftrightarrow \text{actions-ok}' s \ t \ ta$

**by**(*auto simp add: actions-ok'-iff lock-ok-las'-def ta-upd-simps wset-actions-ok-def*)

**lemma**  $\text{actions-ok'-empty}$ :  $\text{actions-ok}' s \ t \ \varepsilon \longleftrightarrow \text{wset } s \ t = \text{None}$

**by**(*simp add: actions-ok'-iff lock-ok-las'-def*)

**lemma**  $\text{actions-ok'-convert-extTA}$ :

$\text{actions-ok}' s \ t \ (\text{convert-extTA } f \ ta) = \text{actions-ok}' s \ t \ ta$

**by**(*simp add: actions-ok'-iff*)

**inductive**  $\text{actions-subset} :: ('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \Rightarrow ('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \Rightarrow \text{bool}$

**where**

$\llbracket \text{collect-locks}' \{ta'\}_l \subseteq \text{collect-locks } \{ta\}_l;$   
 $\text{collect-cond-actions } \{ta'\}_c \subseteq \text{collect-cond-actions } \{ta\}_c;$   
 $\text{collect-interrupts } \{ta'\}_i \subseteq \text{collect-interrupts } \{ta\}_i \rrbracket$   
 $\implies \text{actions-subset } ta' \ ta$

**declare** *actions-subset.intros* [intro!]

**declare** *actions-subset.cases* [elim!]

**lemma** *actions-subset-iff*:

$\text{actions-subset } ta' \ ta \iff$   
 $\text{collect-locks}' \{ta'\}_l \subseteq \text{collect-locks } \{ta\}_l \wedge$   
 $\text{collect-cond-actions } \{ta'\}_c \subseteq \text{collect-cond-actions } \{ta\}_c \wedge$   
 $\text{collect-interrupts } \{ta'\}_i \subseteq \text{collect-interrupts } \{ta\}_i$

**by** *auto*

**lemma** *actions-subset-refl* [intro]:

$\text{actions-subset } ta \ ta$

**by**(*auto intro: actions-subset.intros collect-locks'-subset-collect-locks del: subsetI*)

**definition** *final-thread* ::  $(l, t, x, m, w)$  *state*  $\Rightarrow$   $t \Rightarrow \text{bool}$  **where**

$\bigwedge \text{ln. final-thread } s \ t \equiv (\text{case thr } s \ t \text{ of None} \Rightarrow \text{False} \mid \lfloor (x, \text{ln}) \rfloor \Rightarrow \text{final } x \wedge \text{ln} = \text{no-wait-locks} \wedge$   
 $\text{wset } s \ t = \text{None})$

**definition** *final-threads* ::  $(l, t, x, m, w)$  *state*  $\Rightarrow$   $t$  *set*

**where** *final-threads*  $s \equiv \{t. \text{final-thread } s \ t\}$

**lemma** [iff]:  $t \in \text{final-threads } s = \text{final-thread } s \ t$

**by** (*simp add: final-threads-def*)

**lemma** [pred-set-conv]:  $\text{final-thread } s = (\lambda t. t \in \text{final-threads } s)$

**by** *simp*

**definition** *mfinal* ::  $(l, t, x, m, w)$  *state*  $\Rightarrow \text{bool}$

**where** *mfinal*  $s \iff (\forall t \ x \ \text{ln. thr } s \ t = \lfloor (x, \text{ln}) \rfloor \longrightarrow \text{final } x \wedge \text{ln} = \text{no-wait-locks} \wedge \text{wset } s \ t = \text{None})$

**lemma** *final-threadI*:

$\llbracket \text{thr } s \ t = \lfloor (x, \text{no-wait-locks}) \rfloor; \text{final } x; \text{wset } s \ t = \text{None} \rrbracket \implies \text{final-thread } s \ t$

**by**(*simp add: final-thread-def*)

**lemma** *final-threadE*:

**assumes** *final-thread*  $s \ t$

**obtains**  $x$  **where**  $\text{thr } s \ t = \lfloor (x, \text{no-wait-locks}) \rfloor$  *final*  $x$   $\text{wset } s \ t = \text{None}$

**using** *assms* **by**(*auto simp add: final-thread-def*)

**lemma** *mfinalI*:

$(\bigwedge t \ x \ \text{ln. thr } s \ t = \lfloor (x, \text{ln}) \rfloor \implies \text{final } x \wedge \text{ln} = \text{no-wait-locks} \wedge \text{wset } s \ t = \text{None}) \implies \text{mfinal } s$

**unfolding** *mfinal-def* **by** *blast*

**lemma** *mfinalD*:

**fixes**  $\text{ln}$

**assumes** *mfinal*  $s$   $\text{thr } s \ t = \lfloor (x, \text{ln}) \rfloor$

**shows**  $\text{final } x \wedge \text{ln} = \text{no-wait-locks} \wedge \text{wset } s \ t = \text{None}$

**using** *assms* **unfolding** *mfinal-def* **by** *blast+*

**lemma** *mfinalE*:

**fixes** *ln*

**assumes** *mfinal s thr s t = [(x, ln)]*

**obtains** *final x ln = no-wait-locks wset s t = None*

**using** *mfinalD[OF assms]* **by**(*rule that*)

**lemma** *mfinal-def2*: *mfinal s  $\longleftrightarrow$  dom (thr s)  $\subseteq$  final-threads s*

**by**(*fastforce elim: mfinalE final-threadE intro: mfinalI final-threadI*)

**end**

**locale** *multithreaded-base = final-thread +*

**constrains** *final :: 'x  $\Rightarrow$  bool*

**fixes** *r :: ('l, 't, 'x, 'm, 'w, 'o) semantics (-  $\vdash$  -  $\dashrightarrow$  - [50, 0, 0, 50] 80)*

**and** *convert-RA :: 'l released-locks  $\Rightarrow$  'o list*

**begin**

**abbreviation**

*r-syntax :: 't  $\Rightarrow$  'x  $\Rightarrow$  'm  $\Rightarrow$  ('l, 't, 'x, 'm, 'w, 'o) thread-action  $\Rightarrow$  'x  $\Rightarrow$  'm  $\Rightarrow$  bool*

*(-  $\vdash$  <- , -)  $\dashrightarrow$  <- , -) [50, 0, 0, 0, 0, 0] 80)*

**where**

*t  $\vdash$  <x, m> -ta $\rightarrow$  <x', m'>  $\equiv$  t  $\vdash$  (x, m) -ta $\rightarrow$  (x', m')*

**inductive**

*redT :: ('l, 't, 'x, 'm, 'w) state  $\Rightarrow$  't  $\times$  ('l, 't, 'x, 'm, 'w, 'o) thread-action  $\Rightarrow$  ('l, 't, 'x, 'm, 'w) state  $\Rightarrow$  bool*

**and**

*redT-syntax1 :: ('l, 't, 'x, 'm, 'w) state  $\Rightarrow$  't  $\Rightarrow$  ('l, 't, 'x, 'm, 'w, 'o) thread-action  $\Rightarrow$  ('l, 't, 'x, 'm, 'w) state*

*$\Rightarrow$  bool (-  $\dashrightarrow$  - [50, 0, 0, 50] 80)*

**where**

*s -t>ta $\rightarrow$  s'  $\equiv$  redT s (t, ta) s'*

| *redT-normal*:

$\llbracket$  t  $\vdash$  <x, shr s> -ta $\rightarrow$  <x', m'>;

*thr s t = [(x, no-wait-locks)];*

*actions-ok s t ta;*

*redT-upd s t ta x' m' s'  $\rrbracket$*

$\implies$  s -t>ta $\rightarrow$  s'

| *redT-acquire*:

$\bigwedge$ ln.  $\llbracket$  thr s t = [(x, ln)];  $\neg$  waiting (wset s t);

*may-acquire-all (locks s) t ln; ln \$ n > 0;*

*s' = (acquire-all (locks s) t ln, ((thr s)(t  $\mapsto$  (x, no-wait-locks)), shr s), wset s, interrupts s)  $\rrbracket$*

$\implies$  s -t>(K\$  $\llbracket$ ),  $\llbracket$ ,  $\llbracket$ ,  $\llbracket$ ,  $\llbracket$ , convert-RA ln) $\rightarrow$  s'

**abbreviation**

*redT-syntax2 :: ('l, 't) locks  $\Rightarrow$  ('l, 't, 'x) thread-info  $\times$  'm  $\Rightarrow$  ('w, 't) wait-sets  $\Rightarrow$  't interrupts*

*$\Rightarrow$  't  $\Rightarrow$  ('l, 't, 'x, 'm, 'w, 'o) thread-action*

*$\Rightarrow$  ('l, 't) locks  $\Rightarrow$  ('l, 't, 'x) thread-info  $\times$  'm  $\Rightarrow$  ('w, 't) wait-sets  $\Rightarrow$  't interrupts  $\Rightarrow$  bool*

*(<- , - , - , -)  $\dashrightarrow$  <- , - , - , -) [0, 0, 0, 0, 0, 0, 0, 0] 80)*

**where**

*<ls, tsm, ws, is> -t>ta $\rightarrow$  <ls', tsm', ws', is'>  $\equiv$  (ls, tsm, ws, is) -t>ta $\rightarrow$  (ls', tsm', ws', is')*

**lemma** *redT-elim*s [consumes 1, case-names normal acquire]:

**assumes**  $red: s \multimap ta \rightarrow s'$   
**and**  $normal: \bigwedge x \ x' \ m' \ ws'.$   
 $\llbracket t \vdash \langle x, shr \ s \rangle \multimap ta \rightarrow \langle x', m' \rangle;$   
 $\quad thr \ s \ t = \lfloor (x, no\text{-}wait\text{-}locks) \rfloor;$   
 $\quad lock\text{-}ok\text{-}las \ (locks \ s) \ t \ \llbracket ta \rrbracket_l;$   
 $\quad thread\text{-}oks \ (thr \ s) \ \llbracket ta \rrbracket_t;$   
 $\quad cond\text{-}action\text{-}oks \ s \ t \ \llbracket ta \rrbracket_c;$   
 $\quad wset\text{-}actions\text{-}ok \ (wset \ s) \ t \ \llbracket ta \rrbracket_w;$   
 $\quad interrupt\text{-}actions\text{-}ok \ (interrupts \ s) \ \llbracket ta \rrbracket_i;$   
 $\quad redT\text{-}updWs \ t \ (wset \ s) \ \llbracket ta \rrbracket_w \ ws';$   
 $\quad s' = (redT\text{-}updLs \ (locks \ s) \ t \ \llbracket ta \rrbracket_l, ((redT\text{-}updTs \ (thr \ s) \ \llbracket ta \rrbracket_t)(t \mapsto (x', redT\text{-}updLns \ (locks \ s) \ t$   
 $no\text{-}wait\text{-}locks \ \llbracket ta \rrbracket_l)), m'), ws', redT\text{-}updIs \ (interrupts \ s) \ \llbracket ta \rrbracket_i) \rrbracket$   
 $\implies thesis$   
**and**  $acquire: \bigwedge x \ ln \ n.$   
 $\llbracket thr \ s \ t = \lfloor (x, ln) \rfloor;$   
 $\quad ta = (K\$ \ [], [], [], [], [], convert\text{-}RA \ ln);$   
 $\quad \neg waiting \ (wset \ s \ t);$   
 $\quad may\text{-}acquire\text{-}all \ (locks \ s) \ t \ ln; \ 0 < ln \ \$ \ n;$   
 $\quad s' = (acquire\text{-}all \ (locks \ s) \ t \ ln, ((thr \ s)(t \mapsto (x, no\text{-}wait\text{-}locks)), shr \ s), wset \ s, interrupts \ s) \rrbracket$   
 $\implies thesis$   
**shows**  $thesis$   
**using**  $red$   
**proof**  $cases$   
**case**  $redT\text{-}normal$   
**thus**  $?thesis$  **using**  $normal$  **by**  $(cases \ s')(auto)$   
**next**  
**case**  $redT\text{-}acquire$   
**thus**  $?thesis$  **by**  $-(rule \ acquire, fastforce+)$   
**qed**

### definition

$RedT :: (l, 't, 'x, 'm, 'w) \ state \Rightarrow ('t \times ('l, 't, 'x, 'm, 'w, 'o) \ thread\text{-}action) \ list \Rightarrow ('l, 't, 'x, 'm, 'w) \ state \Rightarrow$   
 $bool$

$(- \multimap \rightarrow * - [50, 0, 50] \ 80)$

**where**

$RedT \equiv rtrancl3p \ redT$

### lemma $RedTI$ :

$rtrancl3p \ redT \ s \ ttas \ s' \implies RedT \ s \ ttas \ s'$

**by**  $(simp \ add: RedT\text{-}def)$

### lemma $RedTE$ :

$\llbracket RedT \ s \ ttas \ s'; rtrancl3p \ redT \ s \ ttas \ s' \implies P \rrbracket \implies P$

**by**  $(auto \ simp \ add: RedT\text{-}def)$

### lemma $RedTD$ :

$RedT \ s \ ttas \ s' \implies rtrancl3p \ redT \ s \ ttas \ s'$

**by**  $(simp \ add: RedT\text{-}def)$

### lemma $RedT\text{-}induct$ $[consumes \ 1, case\text{-}names \ refl \ step]$ :

$\llbracket s \multimap ttas \rightarrow * \ s';$

$\bigwedge s. \ P \ s \rrbracket \ s;$

$\bigwedge s \ ttas \ s' \ t \ ta \ s''. \llbracket s \multimap ttas \rightarrow * \ s'; P \ s \ ttas \ s'; s' \multimap ta \rightarrow s'' \rrbracket \implies P \ s \ (ttas \ @ \ [(t, ta)]) \ s''$

$\implies P \ s \ ttas \ s'$

**unfolding** *RedT-def*  
**by**(*erule rtrancl3p.induct*) *auto*

**lemma** *RedT-induct'* [*consumes 1, case-names refl step*]:  

$$\llbracket s \multimap ttas \rightarrow^* s';$$

$$P\ s \llbracket s;$$

$$\bigwedge ttas\ s'\ t\ ta\ s''. \llbracket s \multimap ttas \rightarrow^* s'; P\ s\ ttas\ s'; s' \multimap ta \rightarrow s'' \rrbracket \implies P\ s\ (ttas\ @\ [(t,\ ta)])\ s'' \rrbracket$$

$$\implies P\ s\ ttas\ s'$$
**unfolding** *RedT-def*  
**apply**(*erule rtrancl3p.induct'*, *blast*)  
**apply**(*case-tac b, blast*)  
**done**

**lemma** *RedT-lift-preserveD*:  
**assumes** *Red*:  $s \multimap ttas \rightarrow^* s'$   
**and** *P*:  $P\ s$   
**and** *preserve*:  $\bigwedge s\ t\ tas\ s'. \llbracket s \multimap tas \rightarrow s'; P\ s \rrbracket \implies P\ s'$   
**shows**  $P\ s'$   
**using** *Red P*  
**by**(*induct rule: RedT-induct*)(*auto intro: preserve*)

**lemma** *RedT-refl* [*intro, simp*]:  
 $s \multimap \llbracket \rrbracket \rightarrow^* s$   
**by**(*rule RedTI*)(*rule rtrancl3p-refl*)

**lemma** *redT-has-locks-inv*:  

$$\llbracket \langle ls, (ts, m), ws, is \rangle \multimap ta \rightarrow \langle ls', (ts', m'), ws', is' \rangle; t \neq t' \rrbracket \implies$$

$$has\_locks\ (ls\ \$\ l)\ t' = has\_locks\ (ls'\ \$\ l)\ t'$$
**by**(*auto elim!: redT.cases intro: redT-updLs-has-locks[THEN sym, simplified] may-acquire-all-has-locks-acquire-lock*)

**lemma** *redT-has-lock-inv*:  

$$\llbracket \langle ls, (ts, m), ws, is \rangle \multimap ta \rightarrow \langle ls', (ts', m'), ws', is' \rangle; t \neq t' \rrbracket$$

$$\implies has\_lock\ (ls'\ \$\ l)\ t' = has\_lock\ (ls\ \$\ l)\ t'$$
**by**(*auto simp add: redT-has-locks-inv*)

**lemma** *redT-ts-Some-inv*:  

$$\llbracket \langle ls, (ts, m), ws, is \rangle \multimap ta \rightarrow \langle ls', (ts', m'), ws', is' \rangle; t \neq t'; ts\ t' = \lfloor x \rfloor \rrbracket \implies ts'\ t' = \lfloor x \rfloor$$
**by**(*fastforce elim!: redT.cases simp: redT-updTs-upd[THEN sym] intro: redT-updTs-Some*)

**lemma** *redT-thread-not-disappear*:  

$$\llbracket s \multimap ta \rightarrow s'; thr\ s'\ t' = None \rrbracket \implies thr\ s\ t' = None$$
**apply**(*cases t  $\neq$  t'*)  
**apply**(*auto elim!: redT-elim simp add: redT-updTs-upd[THEN sym] intro: redT-updTs-None*)  
**done**

**lemma** *RedT-thread-not-disappear*:  

$$\llbracket s \multimap ttas \rightarrow^* s'; thr\ s'\ t' = None \rrbracket \implies thr\ s\ t' = None$$
**apply**(*erule contrapos-pp[where Q=thr s' t' = None]*)  
**apply**(*drule (1) RedT-lift-preserveD*)  
**apply**(*erule-tac Q=thr sa t' = None in contrapos-nn*)  
**apply**(*erule redT-thread-not-disappear*)  
**apply**(*auto*)  
**done**



**lemma** *redT-preserves-wset-thread-ok*:

$\llbracket s \multimap ta \rightarrow s'; \text{wset-thread-ok } (\text{wset } s) \text{ (thr } s) \rrbracket \implies \text{wset-thread-ok } (\text{wset } s') \text{ (thr } s')$   
**by**(*fastforce elim!*: *redT.cases intro: wset-thread-ok-upd redT-updTs-preserves-wset-thread-ok redT-updWs-preserve-wset-thread*)

**lemma** *RedT-preserves-wset-thread-ok*:

$\llbracket s \multimap ttas \rightarrow s'; \text{wset-thread-ok } (\text{wset } s) \text{ (thr } s) \rrbracket \implies \text{wset-thread-ok } (\text{wset } s') \text{ (thr } s')$   
**by**(*erule (1) RedT-lift-preserveD*)(*erule redT-preserves-wset-thread-ok*)

**lemma** *redT-new-thread-ts-Some*:

$\llbracket s \multimap ta \rightarrow s'; \text{NewThread } t' x m'' \in \text{set } \llbracket ta \rrbracket_t; \text{wset-thread-ok } (\text{wset } s) \text{ (thr } s) \rrbracket$   
 $\implies \text{thr } s' t' = \lfloor (x, \text{no-wait-locks}) \rfloor$   
**by**(*erule redT-elim*s)(*auto dest: thread-oks-new-thread elim: redT-updTs-new-thread-ts*)

**lemma** *RedT-new-thread-ts-not-None*:

$\llbracket s \multimap ttas \rightarrow s'; \text{NewThread } t x m'' \in \text{set } (\text{concat } (\text{map } (\text{thr-a } \circ \text{snd}) \text{ ttas})); \text{wset-thread-ok } (\text{wset } s) \text{ (thr } s) \rrbracket$   
 $\implies \text{thr } s' t \neq \text{None}$

**proof**(*induct rule: RedT-induct*)

**case refl thus ?case by simp**

**next**

**case** (*step S TTAS S' T TA S''*)

**note** *Red* =  $\langle S \multimap TTAS \rightarrow s' \rangle$

**note** *IH* =  $\langle \llbracket \text{NewThread } t x m'' \in \text{set } (\text{concat } (\text{map } (\text{thr-a } \circ \text{snd}) \text{ TTAS})); \text{wset-thread-ok } (\text{wset } S) \text{ (thr } S) \rrbracket \implies \text{thr } S' t \neq \text{None} \rangle$

**note** *red* =  $\langle S' - T \multimap TA \rightarrow S'' \rangle$

**note** *ins* =  $\langle \text{NewThread } t x m'' \in \text{set } (\text{concat } (\text{map } (\text{thr-a } \circ \text{snd}) (\text{TTAS } @ \llbracket (T, TA) \rrbracket))) \rangle$

**note** *wto* =  $\langle \text{wset-thread-ok } (\text{wset } S) \text{ (thr } S) \rangle$

**from** *Red* *wto* **have** *wto'*:  $\text{wset-thread-ok } (\text{wset } S') \text{ (thr } S') \text{ by (auto dest: RedT-preserves-wset-thread-ok)}$

**show** ?*case*

**proof**(*cases NewThread t x m'' ∈ set ⟦TA⟧<sub>t</sub>*)

**case True thus ?thesis using red wto'**

**by**(*auto dest!*: *redT-new-thread-ts-Some*)

**next**

**case False**

**hence**  $\text{NewThread } t x m'' \in \text{set } (\text{concat } (\text{map } (\text{thr-a } \circ \text{snd}) \text{ TTAS})) \text{ using ins by (auto)}$

**hence**  $\text{thr } S' t \neq \text{None} \text{ using wto by (rule IH)}$

**with red show ?thesis**

**by**  $-(\text{erule contrapos-nn, auto dest: redT-thread-not-disappear})$

**qed**

**qed**

**lemma** *redT-preserves-lock-thread-ok*:

$\llbracket s \multimap ta \rightarrow s'; \text{lock-thread-ok } (\text{locks } s) \text{ (thr } s) \rrbracket \implies \text{lock-thread-ok } (\text{locks } s') \text{ (thr } s')$   
**by**(*auto elim!*: *redT-elim*s *intro: redT-upds-preserves-lock-thread-ok acquire-all-preserves-lock-thread-ok*)

**lemma** *RedT-preserves-lock-thread-ok*:

$\llbracket s \multimap ttas \rightarrow s'; \text{lock-thread-ok } (\text{locks } s) \text{ (thr } s) \rrbracket \implies \text{lock-thread-ok } (\text{locks } s') \text{ (thr } s')$   
**by**(*erule (1) RedT-lift-preserveD*)(*erule redT-preserves-lock-thread-ok*)

**lemma** *redT-ex-new-thread*:

**assumes**  $s \multimap ta \rightarrow s' \text{ wset-thread-ok } (\text{wset } s) \text{ (thr } s) \text{ thr } s' t = \lfloor (x, w) \rfloor \text{ thr } s t = \text{None}$

**shows**  $\exists m. \text{NewThread } t x m \in \text{set } \llbracket ta \rrbracket_t \wedge w = \text{no-wait-locks}$

**using** *assms*

by cases (fastforce split: if-split-asm dest: wset-thread-okD redT-updTs-new-thread)+

**lemma** redT-ex-new-thread':

assumes  $s \dashv t \triangleright ta \rightarrow s' \text{ thr } s' t = \lfloor (x, w) \rfloor \text{ thr } s t = \text{None}$

shows  $\exists m x. \text{NewThread } t x m \in \text{set } \{\{ta\}_t\}$

using assms

by(cases)(fastforce split: if-split-asm dest!: redT-updTs-new-thread)+

**definition** deterministic ::  $(l, t, x, m, w) \text{ state set} \Rightarrow \text{bool}$

where

deterministic  $I \iff$

$(\forall s t x ta' x' m' ta'' x'' m'.$

$s \in I$

$\rightarrow \text{thr } s t = \lfloor (x, \text{no-wait-locks}) \rfloor$

$\rightarrow t \vdash \langle x, \text{shr } s \rangle - ta' \rightarrow \langle x', m' \rangle$

$\rightarrow t \vdash \langle x, \text{shr } s \rangle - ta'' \rightarrow \langle x'', m'' \rangle$

$\rightarrow \text{actions-ok } s t ta' \rightarrow \text{actions-ok } s t ta''$

$\rightarrow ta' = ta'' \wedge x' = x'' \wedge m' = m'') \wedge \text{invariant3p redT } I$

**lemma** deterministicI:

$\llbracket \bigwedge s t x ta' x' m' ta'' x'' m'.$

$\llbracket s \in I; \text{thr } s t = \lfloor (x, \text{no-wait-locks}) \rfloor;$

$t \vdash \langle x, \text{shr } s \rangle - ta' \rightarrow \langle x', m' \rangle; t \vdash \langle x, \text{shr } s \rangle - ta'' \rightarrow \langle x'', m'' \rangle;$

$\text{actions-ok } s t ta'; \text{actions-ok } s t ta'' \rrbracket$

$\implies ta' = ta'' \wedge x' = x'' \wedge m' = m'';$

$\text{invariant3p redT } I \rrbracket$

$\implies \text{deterministic } I$

**unfolding** deterministic-def by blast

**lemma** deterministicD:

$\llbracket \text{deterministic } I;$

$t \vdash \langle x, \text{shr } s \rangle - ta' \rightarrow \langle x', m' \rangle; t \vdash \langle x, \text{shr } s \rangle - ta'' \rightarrow \langle x'', m'' \rangle;$

$\text{thr } s t = \lfloor (x, \text{no-wait-locks}) \rfloor; \text{actions-ok } s t ta'; \text{actions-ok } s t ta''; s \in I \rrbracket$

$\implies ta' = ta'' \wedge x' = x'' \wedge m' = m''$

**unfolding** deterministic-def by blast

**lemma** deterministic-invariant3p:

deterministic  $I \implies \text{invariant3p redT } I$

**unfolding** deterministic-def by blast

**lemma** deterministic-THE:

$\llbracket \text{deterministic } I; \text{thr } s t = \lfloor (x, \text{no-wait-locks}) \rfloor; t \vdash \langle x, \text{shr } s \rangle - ta \rightarrow \langle x', m' \rangle; \text{actions-ok } s t ta; s \in I \rrbracket$

$\implies (\text{THE } (ta, x', m'). t \vdash \langle x, \text{shr } s \rangle - ta \rightarrow \langle x', m' \rangle \wedge \text{actions-ok } s t ta) = (ta, x', m')$

by(rule the-equality)(blast dest: deterministicD)+

end

**locale** multithreaded = multithreaded-base +

constrains final ::  $'x \Rightarrow \text{bool}$

and  $r :: (l, t, x, m, w, o) \text{ semantics}$

and convert-RA ::  $l \text{ released-locks} \Rightarrow o \text{ list}$

assumes new-thread-memory:  $\llbracket t \vdash s - ta \rightarrow s'; \text{NewThread } t' x m \in \text{set } \{\{ta\}_t\} \rrbracket \implies m = \text{snd } s'$

and final-no-red:  $\llbracket t \vdash (x, m) - ta \rightarrow (x', m'); \text{final } x \rrbracket \implies \text{False}$

**begin**

**lemma** *redT-new-thread-common*:

$\llbracket s - t \triangleright ta \rightarrow s'; \text{NewThread } t' \ x \ m'' \in \text{set } \{ta\}_t; \{ta\}_w = [] \rrbracket \implies m'' = \text{shr } s'$   
**by**(*auto elim!; redT-elim; rtrancl3p-cases dest: new-thread-memory*)

**lemma** *redT-new-thread*:

**assumes**  $s - t \triangleright ta \rightarrow s' \text{ thr } s' \ t = \lfloor (x, w) \rfloor \text{ thr } s \ t = \text{None } \{ta\}_w = []$   
**shows**  $\text{NewThread } t \ x \ (\text{shr } s') \in \text{set } \{ta\}_t \wedge w = \text{no-wait-locks}$   
**using** *assms*  
**apply**(*cases rule: redT-elim*)  
**apply**(*auto split: if-split-asm del: conjI elim!: rtrancl3p-cases*)  
**apply**(*drule (2) redT-updTs-new-thread*)  
**apply**(*auto dest: new-thread-memory*)  
**done**

**lemma** *final-no-redT*:

$\llbracket s - t \triangleright ta \rightarrow s'; \text{thr } s \ t = \lfloor (x, \text{no-wait-locks}) \rfloor \rrbracket \implies \neg \text{final } x$   
**by**(*auto elim!; redT-elim; dest: final-no-red*)

**lemma** *mfinal-no-redT*:

**assumes** *redT*:  $s - t \triangleright ta \rightarrow s'$  **and** *mfinal*: *mfinal* *s*  
**shows** *False*  
**using** *redT mfinalD[OF mfinal, of t]*  
**by cases** (*metis final-no-red, metis neg-no-wait-locks-conv*)

**end**

**end**

## 1.11 Auxiliary definitions for the progress theorem for the multithreaded semantics

**theory** *FWProgressAux*

**imports**

*FWSemantics*

**begin**

**abbreviation** *collect-waits* ::  $(l, t, x, m, w, o) \text{ thread-action} \Rightarrow (l + t + x) \text{ set}$

**where** *collect-waits* *ta*  $\equiv \text{collect-locks } \{ta\}_l <+> \text{collect-cond-actions } \{ta\}_c <+> \text{collect-interrupts } \{ta\}_i$

**lemma** *collect-waits-unfold*:

*collect-waits* *ta*  $= \{l. \text{Lock} \in \text{set } (\{ta\}_l \ \$ \ l)\} <+> \{t. \text{Join } t \in \text{set } \{ta\}_c\} <+> \text{collect-interrupts } \{ta\}_i$   
**by**(*simp add: collect-locks-def*)

**context** *multithreaded-base* **begin**

**definition** *must-sync* ::  $t \Rightarrow x \Rightarrow m \Rightarrow \text{bool}$   $(- \vdash \langle -, / - \rangle / \wr [50, 0, 0] \ 81)$  **where**

$t \vdash \langle x, m \rangle \wr \longleftrightarrow (\exists ta \ x' \ m' \ s. t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle \wedge \text{shr } s = m \wedge \text{actions-ok } s \ t \ ta)$

**lemma** *must-sync-def2*:

$t \vdash \langle x, m \rangle \wr \longleftrightarrow (\exists ta\ x' m' s. t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle \wedge \text{actions-ok } s\ t\ ta)$   
**by**(*fastforce simp add: must-sync-def intro: cond-action-oks-shr-change*)

**lemma** *must-syncI*:

$\exists ta\ x' m' s. t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle \wedge \text{actions-ok } s\ t\ ta \implies t \vdash \langle x, m \rangle \wr$   
**by**(*fastforce simp add: must-sync-def2*)

**lemma** *must-syncE*:

$\llbracket t \vdash \langle x, m \rangle \wr; \bigwedge ta\ x' m' s. \llbracket t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle; \text{actions-ok } s\ t\ ta; m = \text{shr } s \rrbracket \implies \text{thesis} \rrbracket$   
 $\implies \text{thesis}$

**by**(*fastforce simp only: must-sync-def*)

**definition** *can-sync* ::  $'t \Rightarrow 'x \Rightarrow 'm \Rightarrow ('l + 't + 't)\ \text{set} \Rightarrow \text{bool}$   $(- \vdash \langle -, / - \rangle / - / \wr [50, 0, 0, 0]\ 81)$   
**where**

$t \vdash \langle x, m \rangle\ LT \wr \equiv \exists ta\ x' m'. t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle \wedge (LT = \text{collect-waits } ta)$

**lemma** *can-syncI*:

$\llbracket t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle;$   
 $LT = \text{collect-waits } ta \rrbracket$   
 $\implies t \vdash \langle x, m \rangle\ LT \wr$

**by**(*cases ta*)(*fastforce simp add: can-sync-def*)

**lemma** *can-syncE*:

**assumes**  $t \vdash \langle x, m \rangle\ LT \wr$   
**obtains**  $ta\ x' m'$   
**where**  $t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle$   
**and**  $LT = \text{collect-waits } ta$   
**using** *assms*

**by**(*clarsimp simp add: can-sync-def*)

**inductive-set** *active-threads* ::  $('l, 't, 'x, 'm, 'w)\ \text{state} \Rightarrow 't\ \text{set}$

**for**  $s :: ('l, 't, 'x, 'm, 'w)\ \text{state}$

**where**

*normal*:

$\bigwedge ln. \llbracket \text{thr } s\ t = \text{Some } (x, ln);$   
 $ln = \text{no-wait-locks};$   
 $t \vdash (x, \text{shr } s) -ta \rightarrow x'm';$   
 $\text{actions-ok } s\ t\ ta \rrbracket$   
 $\implies t \in \text{active-threads } s$

| *acquire*:

$\bigwedge ln. \llbracket \text{thr } s\ t = \text{Some } (x, ln);$   
 $ln \neq \text{no-wait-locks};$   
 $\neg \text{waiting } (\text{wset } s\ t);$   
 $\text{may-acquire-all } (\text{locks } s)\ t\ ln \rrbracket$   
 $\implies t \in \text{active-threads } s$

**lemma** *active-threads-iff*:

*active-threads*  $s =$

$\{t. \exists x\ ln. \text{thr } s\ t = \text{Some } (x, ln) \wedge$   
 $(\text{if } ln = \text{no-wait-locks}$   
 $\text{then } \exists ta\ x' m'. t \vdash (x, \text{shr } s) -ta \rightarrow \langle x', m' \rangle \wedge \text{actions-ok } s\ t\ ta$   
 $\text{else } \neg \text{waiting } (\text{wset } s\ t) \wedge \text{may-acquire-all } (\text{locks } s)\ t\ ln)\}$

**apply**(*auto elim!:* *active-threads.cases intro: active-threads.intros*)

**apply** *blast*

done

**lemma** *active-thread-ex-red*:

**assumes**  $t \in \text{active-threads } s$

**shows**  $\exists ta \ s'. \ s \rightarrow ta \rightarrow s'$

**using** *assms*

**proof** *cases*

**case** (*normal*  $x \ ta \ x'm' \ ln$ )

**with** *redT-updWs-total*[*of*  $t \ wset \ s \ \{ta\}_w$ ]

**show** *?thesis*

**by**(*cases*  $x'm'$ )(*fastforce* *intro!*: *redT-normal simp del: split-paired-Ex*)

**next**

**case** *acquire* **thus** *?thesis*

**by**(*fastforce* *intro*: *redT-acquire simp del: split-paired-Ex simp add: neq-no-wait-locks-conv*)

**qed**

**end**

Well-formedness conditions for final

**context** *final-thread* **begin**

**inductive** *not-final-thread* ::  $('l, 't, 'x, 'm, 'w) \text{ state} \Rightarrow 't \Rightarrow \text{bool}$

**for**  $s :: ('l, 't, 'x, 'm, 'w) \text{ state}$  **and**  $t :: 't$  **where**

*not-final-thread-final*:  $\bigwedge ln. \llbracket \text{thr } s \ t = \lfloor (x, ln) \rfloor; \neg \text{final } x \rrbracket \Longrightarrow \text{not-final-thread } s \ t$

| *not-final-thread-wait-locks*:  $\bigwedge ln. \llbracket \text{thr } s \ t = \lfloor (x, ln) \rfloor; ln \neq \text{no-wait-locks} \rrbracket \Longrightarrow \text{not-final-thread } s \ t$

| *not-final-thread-wait-set*:  $\bigwedge ln. \llbracket \text{thr } s \ t = \lfloor (x, ln) \rfloor; wset \ s \ t = \lfloor w \rfloor \rrbracket \Longrightarrow \text{not-final-thread } s \ t$

**declare** *not-final-thread.cases* [*elim*]

**lemmas** *not-final-thread-cases* = *not-final-thread.cases* [*consumes 1, case-names final wait-locks wait-set*]

**lemma** *not-final-thread-cases2* [*consumes 2, case-names final wait-locks wait-set*]:

$\bigwedge ln. \llbracket \text{not-final-thread } s \ t; \text{thr } s \ t = \lfloor (x, ln) \rfloor;$

$\neg \text{final } x \Longrightarrow \text{thesis}; ln \neq \text{no-wait-locks} \Longrightarrow \text{thesis}; \bigwedge w. wset \ s \ t = \lfloor w \rfloor \Longrightarrow \text{thesis} \rrbracket$

$\Longrightarrow \text{thesis}$

**by**(*auto*)

**lemma** *not-final-thread-iff*:

$\text{not-final-thread } s \ t \longleftrightarrow (\exists x \ ln. \text{thr } s \ t = \lfloor (x, ln) \rfloor \wedge (\neg \text{final } x \vee ln \neq \text{no-wait-locks} \vee (\exists w. wset \ s \ t = \lfloor w \rfloor)))$

**by**(*auto* *intro*: *not-final-thread.intros*)

**lemma** *not-final-thread-conv*:

$\text{not-final-thread } s \ t \longleftrightarrow \text{thr } s \ t \neq \text{None} \wedge \neg \text{final-thread } s \ t$

**by**(*auto* *simp* *add*: *final-thread-def* *intro*: *not-final-thread.intros*)

**lemma** *not-final-thread-existsE*:

**assumes** *not-final-thread*  $s \ t$

**and**  $\bigwedge x \ ln. \text{thr } s \ t = \lfloor (x, ln) \rfloor \Longrightarrow \text{thesis}$

**shows** *thesis*

**using** *assms* **by** *blast*

**lemma** *not-final-thread-final-thread-conv*:

$thr\ s\ t \neq None \implies \neg final\_thread\ s\ t \longleftrightarrow not\_final\_thread\ s\ t$   
**by**(simp add: not-final-thread-iff final-thread-def)

**lemma** *may-join-cond-action-oks*:

**assumes**  $\bigwedge t'. Join\ t' \in set\ cas \implies \neg not\_final\_thread\ s\ t' \wedge t \neq t'$

**shows** *cond-action-oks*  $s\ t\ cas$

**using** *assms*

**proof** (*induct cas*)

**case** *Nil* **thus** ?case **by** *clarsimp*

**next**

**case** (*Cons ca cas*)

**note**  $IH = \langle \bigwedge t'. Join\ t' \in set\ cas \implies \neg not\_final\_thread\ s\ t' \wedge t \neq t' \rangle$   
 $\implies cond\_action\_oks\ s\ t\ cas$

**note**  $ass = \langle \bigwedge t'. Join\ t' \in set\ (ca \# cas) \implies \neg not\_final\_thread\ s\ t' \wedge t \neq t' \rangle$

**hence**  $\bigwedge t'. Join\ t' \in set\ cas \implies \neg not\_final\_thread\ s\ t' \wedge t \neq t'$  **by** *simp*

**hence** *cond-action-oks*  $s\ t\ cas$  **by**(rule *IH*)

**moreover** **have** *cond-action-ok*  $s\ t\ ca$

**proof**(*cases ca*)

**case** (*Join t'*)

**with** *ass* **have**  $\neg not\_final\_thread\ s\ t' \wedge t \neq t'$  **by** *auto*

**thus** ?thesis **using** *Join* **by**(*auto simp add: not-final-thread-iff*)

**next**

**case** *Yield* **thus** ?thesis **by** *simp*

**qed**

**ultimately** **show** ?case **by** *simp*

**qed**

**end**

**context** *multithreaded* **begin**

**lemma** *red-not-final-thread*:

$s -t>ta \rightarrow s' \implies not\_final\_thread\ s\ t$

**by**(*fastforce elim: redT.cases intro: not-final-thread.intros dest: final-no-red*)

**lemma** *redT-preserves-final-thread*:

$\llbracket s -t>ta \rightarrow s'; final\_thread\ s\ t \rrbracket \implies final\_thread\ s'\ t$

**apply**(*erule redT.cases*)

**apply**(*clarsimp simp add: final-thread-def*)

**apply**(*auto simp add: final-thread-def dest: redT-updTs-None redT-updTs-Some final-no-red intro: redT-updWs-None*)

**done**

**end**

**context** *multithreaded-base* **begin**

**definition** *wset-Suspend-ok* ::  $(l, t, x, m, w)\ state\ set \Rightarrow (l, t, x, m, w)\ state\ set$

**where**

*wset-Suspend-ok*  $I =$

$\{s. s \in I \wedge$

$(\forall t \in dom\ (wset\ s). \exists s0 \in I. \exists s1 \in I. \exists ttas\ x\ x0\ ta\ w'\ ln'\ ln''. s0 -t>ta \rightarrow s1 \wedge s1 -\triangleright ttas \rightarrow* s \wedge$   
 $thr\ s0\ t = \llbracket (x0, no\_wait\_locks) \rrbracket \wedge t \vdash \langle x0, shr\ s0 \rangle -ta \rightarrow \langle x, shr\ s1 \rangle \wedge Suspend\ w' \in set$

$\llbracket ta \rrbracket_w \wedge$

$actions\_ok\ s0\ t\ ta \wedge thr\ s1\ t = \llbracket (x, ln') \rrbracket \wedge thr\ s\ t = \llbracket (x, ln'') \rrbracket \}$

**lemma** *wset-Suspend-okI*:

$\llbracket s \in I;$   
 $\bigwedge t w. \text{wset } s \ t = \lfloor w \rfloor \implies \exists s0 \in I. \exists s1 \in I. \exists ttas \ x \ x0 \ ta \ w' \ ln' \ ln''. s0 \dashv\triangleright ta \rightarrow s1 \wedge s1 \dashv\triangleright ttas \rightarrow * s \wedge$   
 $\text{thr } s0 \ t = \lfloor (x0, \text{no-wait-locks}) \rfloor \wedge t \vdash \langle x0, \text{shr } s0 \rangle \dashv\triangleright ta \rightarrow \langle x, \text{shr } s1 \rangle \wedge \text{Suspend } w' \in \text{set}$   
 $\{ \{ ta \} \}_w \wedge$   
 $\text{actions-ok } s0 \ t \ ta \wedge \text{thr } s1 \ t = \lfloor (x, \ln') \rfloor \wedge \text{thr } s \ t = \lfloor (x, \ln'') \rfloor \rrbracket$   
 $\implies s \in \text{wset-Suspend-ok } I$

**unfolding** *wset-Suspend-ok-def* **by** *blast*

**lemma** *wset-Suspend-okD1*:

$s \in \text{wset-Suspend-ok } I \implies s \in I$

**unfolding** *wset-Suspend-ok-def* **by** *blast*

**lemma** *wset-Suspend-okD2*:

$\llbracket s \in \text{wset-Suspend-ok } I; \text{wset } s \ t = \lfloor w \rfloor \rrbracket$   
 $\implies \exists s0 \in I. \exists s1 \in I. \exists ttas \ x \ x0 \ ta \ w' \ ln' \ ln''. s0 \dashv\triangleright ta \rightarrow s1 \wedge s1 \dashv\triangleright ttas \rightarrow * s \wedge$   
 $\text{thr } s0 \ t = \lfloor (x0, \text{no-wait-locks}) \rfloor \wedge t \vdash \langle x0, \text{shr } s0 \rangle \dashv\triangleright ta \rightarrow \langle x, \text{shr } s1 \rangle \wedge \text{Suspend } w' \in \text{set}$   
 $\{ \{ ta \} \}_w \wedge$   
 $\text{actions-ok } s0 \ t \ ta \wedge \text{thr } s1 \ t = \lfloor (x, \ln') \rfloor \wedge \text{thr } s \ t = \lfloor (x, \ln'') \rfloor \rrbracket$

**unfolding** *wset-Suspend-ok-def* **by** *blast*

**lemma** *wset-Suspend-ok-imp-wset-thread-ok*:

$s \in \text{wset-Suspend-ok } I \implies \text{wset-thread-ok } (\text{wset } s) (\text{thr } s)$

**apply**(*rule wset-thread-okI*)

**apply**(*rule ccontr*)

**apply**(*auto dest: wset-Suspend-okD2*)

**done**

**lemma** *invariant3p-wset-Suspend-ok*:

**assumes** *I: invariant3p redT I*

**shows** *invariant3p redT (wset-Suspend-ok I)*

**proof**(*rule invariant3pI*)

**fix** *s tl s'*

**assume** *wso: s ∈ wset-Suspend-ok I*

**and** *redT s tl s'*

**moreover obtain** *t' ta* **where** *tl: tl = (t', ta)* **by**(*cases tl*)

**ultimately have** *red: s -t'▷ta→ s'* **by** *simp*

**moreover from** *wso* **have** *s ∈ I* **by**(*rule wset-Suspend-okD1*)

**ultimately have** *s' ∈ I* **by**(*rule invariant3pD[OF I]*)

**thus** *s' ∈ wset-Suspend-ok I*

**proof**(*rule wset-Suspend-okI*)

**fix** *t w*

**assume** *ws't: wset s' t = ⌊w'⌋*

**show**  $\exists s0 \in I. \exists s1 \in I. \exists ttas \ x \ x0 \ ta \ w' \ ln' \ ln''. s0 \dashv\triangleright ta \rightarrow s1 \wedge s1 \dashv\triangleright ttas \rightarrow * s' \wedge$

$\text{thr } s0 \ t = \lfloor (x0, \text{no-wait-locks}) \rfloor \wedge t \vdash \langle x0, \text{shr } s0 \rangle \dashv\triangleright ta \rightarrow \langle x, \text{shr } s1 \rangle \wedge$

$\text{Suspend } w' \in \text{set } \{ \{ ta \} \}_w \wedge \text{actions-ok } s0 \ t \ ta \wedge$

$\text{thr } s1 \ t = \lfloor (x, \ln') \rfloor \wedge \text{thr } s' \ t = \lfloor (x, \ln'') \rfloor$

**proof**(*cases t = t'*)

**case** *False*

**with** *red ws't* **obtain** *w'* **where** *wst: wset s t = ⌊w'⌋*

**by** *cases(auto 4 4 dest: redT-updWs-Some-otherD split: wait-set-status.split-asm)*

**from** *wset-Suspend-okD2[OF wso this]* **obtain** *s0 s1 ttas x x0 ta' w' ln' ln''*

where reuse:  $s0 \in I \ s1 \in I \ s0 \dashv\triangleright ta' \rightarrow s1 \ thr \ s0 \ t = \lfloor (x0, no\text{-}wait\text{-}locks) \rfloor$   
 $t \vdash \langle x0, shr \ s0 \rangle \dashv\triangleright ta' \rightarrow \langle x, shr \ s1 \rangle \ Suspend \ w' \in set \ \{ \{ ta' \} \}_w \ actions\text{-}ok \ s0 \ t \ ta' \ thr \ s1 \ t = \lfloor (x,$   
 $ln') \rfloor$   
 and step:  $s1 \dashv\triangleright ttas \rightarrow^* s$  and  $tst: thr \ s \ t = \lfloor (x, ln'') \rfloor$  by blast  
 from step red have  $s1 \dashv\triangleright ttas @ [(t', ta)] \rightarrow^* s'$  unfolding RedT-def by (rule rtrancl3p-step)  
 moreover from red tst False have  $thr \ s' \ t = \lfloor (x, ln'') \rfloor$   
 by (cases) (auto intro: redT-updTs-Some)  
 ultimately show ?thesis using reuse by blast  
 next  
 case True  
 from red show ?thesis  
 proof (cases)  
 case (redT-normal  $x \ x' \ m$ )  
 note  $red' = \langle t' \vdash \langle x, shr \ s \rangle \dashv\triangleright ta \rightarrow \langle x', m \rangle \rangle$   
 and  $tst' = \langle thr \ s \ t' = \lfloor (x, no\text{-}wait\text{-}locks) \rfloor \rangle$   
 and  $aok = \langle actions\text{-}ok \ s \ t' \ ta \rangle$   
 and  $s' = \langle redT\text{-}upd \ s \ t' \ ta \ x' \ m \ s' \rangle$   
 from  $s'$  have  $ws': redT\text{-}updWs \ t' (wset \ s) \ \{ \{ ta \} \}_w (wset \ s')$   
 and  $m: m = shr \ s'$   
 and  $ts't: thr \ s' \ t' = \lfloor (x', redT\text{-}updIns \ (locks \ s) \ t' (snd \ (the \ (thr \ s \ t')))) \ \{ \{ ta \} \}_l \rfloor$  by auto  
 from aok have  $nwait: \neg waiting \ (wset \ s \ t')$   
 by (auto simp add: wset-actions-ok-def waiting-def split: if-split-asm)  
 have  $\exists w'. Suspend \ w' \in set \ \{ \{ ta \} \}_w$   
 proof (cases  $wset \ s \ t$ )  
 case None  
 from redT-updWs-None-SomeD[OF  $ws'$ , OF  $ws't \ None$ ]  
 show ?thesis ..  
 next  
 case (Some  $w'$ )  
 with True aok have  $Notified \in set \ \{ \{ ta \} \}_w \vee WokenUp \in set \ \{ \{ ta \} \}_w$   
 by (auto simp add: wset-actions-ok-def split: if-split-asm)  
 with  $ws'$  show ?thesis using  $ws't$  unfolding True  
 by (rule redT-updWs-WokenUp-SuspendD)  
 qed  
 with  $tst' \ ts't \ aok \ \langle s \in I \rangle \ \langle s' \in I \rangle$  red red' show ?thesis  
 unfolding True m by blast  
 next  
 case (redT-acquire  $x \ n \ ln$ )  
 with  $ws't \ True$  have  $wset \ s \ t = \lfloor w \rfloor$  by auto  
 from  $wset\text{-}Suspend\text{-}okD2$ [OF  $wso \ this$ ]  $\langle thr \ s \ t' = \lfloor (x, ln) \rfloor \rangle \ True$   
 obtain  $s0 \ s1 \ ttas \ x0 \ ta' \ w' \ ln' \ ln''$   
 where reuse:  $s0 \in I \ s1 \in I \ s0 \dashv\triangleright ta' \rightarrow s1 \ thr \ s0 \ t = \lfloor (x0, no\text{-}wait\text{-}locks) \rfloor$   
 $t \vdash \langle x0, shr \ s0 \rangle \dashv\triangleright ta' \rightarrow \langle x, shr \ s1 \rangle \ Suspend \ w' \in set \ \{ \{ ta' \} \}_w \ actions\text{-}ok \ s0 \ t \ ta' \ thr \ s1 \ t =$   
 $\lfloor (x, ln') \rfloor$   
 and step:  $s1 \dashv\triangleright ttas \rightarrow^* s$  by fastforce  
 from step red have  $s1 \dashv\triangleright ttas @ [(t', ta)] \rightarrow^* s'$  unfolding RedT-def by (rule rtrancl3p-step)  
 moreover from redT-acquire True have  $thr \ s' \ t = \lfloor (x, no\text{-}wait\text{-}locks) \rfloor$  by simp  
 ultimately show ?thesis using reuse by blast  
 qed  
 qed  
 qed  
 qed  
 end



end

## 1.12 Deadlock formalisation

theory *FWDeadlock*

imports

*FWProgressAux*

begin

context *final-thread* begin

**definition** *all-final-except* :: (*'l, 't, 'x, 'm, 'w*) *state*  $\Rightarrow$  *'t set*  $\Rightarrow$  *bool* **where**  
*all-final-except s Ts*  $\equiv \forall t. \text{not-final-thread } s \ t \longrightarrow t \in Ts$

**lemma** *all-final-except-mono* [*mono*]:

$(\bigwedge x. x \in A \longrightarrow x \in B) \Longrightarrow \text{all-final-except } ts \ A \longrightarrow \text{all-final-except } ts \ B$   
**by**(*auto simp add: all-final-except-def*)

**lemma** *all-final-except-mono'*:

$\llbracket \text{all-final-except } ts \ A; \bigwedge x. x \in A \Longrightarrow x \in B \rrbracket \Longrightarrow \text{all-final-except } ts \ B$   
**by**(*blast intro: all-final-except-mono[rule-format]*)

**lemma** *all-final-exceptI*:

$(\bigwedge t. \text{not-final-thread } s \ t \Longrightarrow t \in Ts) \Longrightarrow \text{all-final-except } s \ Ts$   
**by**(*auto simp add: all-final-except-def*)

**lemma** *all-final-exceptD*:

$\llbracket \text{all-final-except } s \ Ts; \text{not-final-thread } s \ t \rrbracket \Longrightarrow t \in Ts$   
**by**(*auto simp add: all-final-except-def*)

**inductive** *must-wait* :: (*'l, 't, 'x, 'm, 'w*) *state*  $\Rightarrow$  *'t*  $\Rightarrow$  (*'l + 't + 't*)  $\Rightarrow$  *'t set*  $\Rightarrow$  *bool*

**for** *s* :: (*'l, 't, 'x, 'm, 'w*) *state* **and** *t* :: *'t* **where**

— Lock *l*

$\llbracket \text{has-lock } (\text{locks } s \ \$ \ l) \ t'; t' \neq t; t' \in Ts \rrbracket \Longrightarrow \text{must-wait } s \ t \ (\text{Inl } l) \ Ts$

| — Join *t'*

$\llbracket \text{not-final-thread } s \ t'; t' \in Ts \rrbracket \Longrightarrow \text{must-wait } s \ t \ (\text{Inr } (\text{Inl } t')) \ Ts$

| — IsInterrupted *t'* True

$\llbracket \text{all-final-except } s \ Ts; t' \notin \text{interrupts } s \rrbracket \Longrightarrow \text{must-wait } s \ t \ (\text{Inr } (\text{Inr } t')) \ Ts$

**declare** *must-wait.cases* [*elim*]

**declare** *must-wait.intros* [*intro*]

**lemma** *must-wait-elim*s [*consumes 1, case-names lock join interrupt, cases pred*]:

**assumes** *must-wait s t lt Ts*

**obtains** *l t'* **where** *lt = Inl l has-lock (locks s \$ l) t' t'  $\neq$  t t'  $\in$  Ts*

| *t'* **where** *lt = Inr (Inl t') not-final-thread s t' t'  $\in$  Ts*

| *t'* **where** *lt = Inr (Inr t') all-final-except s Ts t'  $\notin$  interrupts s*

**using** *assms*

**by**(*auto*)

**inductive-cases** *must-wait-elim*s2 [*elim!*]:

$must\text{-}wait\ s\ t\ (Inl\ l)\ Ts$   
 $must\text{-}wait\ s\ t\ (Inr\ (Inl\ t'))\ Ts$   
 $must\text{-}wait\ s\ t\ (Inr\ (Inr\ t'))\ Ts$

**lemma** *must-wait-iff*:

$must\text{-}wait\ s\ lt\ Ts \longleftrightarrow$   
 $(case\ lt\ of\ Inl\ l \Rightarrow \exists t' \in Ts. t \neq t' \wedge has\text{-}lock\ (locks\ s\ \$\ l)\ t'$   
 $\quad | Inr\ (Inl\ t') \Rightarrow not\text{-}final\text{-}thread\ s\ t' \wedge t' \in Ts$   
 $\quad | Inr\ (Inr\ t') \Rightarrow all\text{-}final\text{-}except\ s\ Ts \wedge t' \notin interrupts\ s)$   
**by**(*auto simp add: must-wait.simps split: sum.splits*)

**end**

Deadlock as a system-wide property

**context** *multithreaded-base* **begin**

**definition**

$deadlock :: ('l, 't, 'x, 'm, 'w)\ state \Rightarrow bool$

**where**

$deadlock\ s$   
 $\equiv (\forall t\ x. thr\ s\ t = \lfloor (x, no\text{-}wait\text{-}locks) \rfloor \wedge \neg final\ x \wedge wset\ s\ t = None$   
 $\longrightarrow t \vdash \langle x, shr\ s \rangle \wr (\forall LT. t \vdash \langle x, shr\ s \rangle LT \wr \longrightarrow (\exists lt \in LT. must\text{-}wait\ s\ t\ lt\ (dom\ (thr\ s))))$   
 $\wedge (\forall t\ x\ ln. thr\ s\ t = \lfloor (x, ln) \rfloor \wedge (\exists l. ln\ \$\ l > 0) \wedge \neg waiting\ (wset\ s\ t)$   
 $\longrightarrow (\exists l\ t'. ln\ \$\ l > 0 \wedge t \neq t' \wedge thr\ s\ t' \neq None \wedge has\text{-}lock\ (locks\ s\ \$\ l)\ t'))$   
 $\wedge (\forall t\ x\ w. thr\ s\ t = \lfloor (x, no\text{-}wait\text{-}locks) \rfloor \longrightarrow wset\ s\ t \neq \lfloor PostWS\ w \rfloor)$

**lemma** *deadlockI*:

$\llbracket \bigwedge t\ x. \llbracket thr\ s\ t = \lfloor (x, no\text{-}wait\text{-}locks) \rfloor; \neg final\ x; wset\ s\ t = None \rrbracket$   
 $\implies t \vdash \langle x, shr\ s \rangle \wr (\forall LT. t \vdash \langle x, shr\ s \rangle LT \wr \longrightarrow (\exists lt \in LT. must\text{-}wait\ s\ t\ lt\ (dom\ (thr\ s))));$   
 $\bigwedge t\ x\ ln\ l. \llbracket thr\ s\ t = \lfloor (x, ln) \rfloor; ln\ \$\ l > 0; \neg waiting\ (wset\ s\ t) \rrbracket$   
 $\implies \exists l\ t'. ln\ \$\ l > 0 \wedge t \neq t' \wedge thr\ s\ t' \neq None \wedge has\text{-}lock\ (locks\ s\ \$\ l)\ t';$   
 $\bigwedge t\ x\ w. thr\ s\ t = \lfloor (x, no\text{-}wait\text{-}locks) \rfloor \implies wset\ s\ t \neq \lfloor PostWS\ w \rfloor \rrbracket$   
 $\implies deadlock\ s$

**by**(*auto simp add: deadlock-def*)

**lemma** *deadlockE*:

**assumes** *deadlock s*

**obtains**  $\forall t\ x. thr\ s\ t = \lfloor (x, no\text{-}wait\text{-}locks) \rfloor \wedge \neg final\ x \wedge wset\ s\ t = None$   
 $\longrightarrow t \vdash \langle x, shr\ s \rangle \wr (\forall LT. t \vdash \langle x, shr\ s \rangle LT \wr \longrightarrow (\exists lt \in LT. must\text{-}wait\ s\ t\ lt\ (dom\ (thr\ s))))$   
**and**  $\forall t\ x\ ln. thr\ s\ t = \lfloor (x, ln) \rfloor \wedge (\exists l. ln\ \$\ l > 0) \wedge \neg waiting\ (wset\ s\ t)$   
 $\longrightarrow (\exists l\ t'. ln\ \$\ l > 0 \wedge t \neq t' \wedge thr\ s\ t' \neq None \wedge has\text{-}lock\ (locks\ s\ \$\ l)\ t')$   
**and**  $\forall t\ x\ w. thr\ s\ t = \lfloor (x, no\text{-}wait\text{-}locks) \rfloor \longrightarrow wset\ s\ t \neq \lfloor PostWS\ w \rfloor$

**using** *assms* **unfolding** *deadlock-def* **by**(*blast*)

**lemma** *deadlockD1*:

**assumes** *deadlock s*

**and**  $thr\ s\ t = \lfloor (x, no\text{-}wait\text{-}locks) \rfloor$

**and**  $\neg final\ x$

**and**  $wset\ s\ t = None$

**obtains**  $t \vdash \langle x, shr\ s \rangle \wr$

**and**  $\forall LT. t \vdash \langle x, shr\ s \rangle LT \wr \longrightarrow (\exists lt \in LT. must\text{-}wait\ s\ t\ lt\ (dom\ (thr\ s)))$

**using** *assms* **unfolding** *deadlock-def* **by**(*blast*)

**lemma** *deadlockD2*:

fixes  $ln$   
 assumes  $deadlock\ s$   
 and  $thr\ s\ t = \lfloor (x, ln) \rfloor$   
 and  $ln\ \$\ l > 0$   
 and  $\neg waiting\ (wset\ s\ t)$   
 obtains  $l'\ t'$  where  $ln\ \$\ l' > 0\ t \neq t'\ thr\ s\ t' \neq None\ has-lock\ (locks\ s\ \$\ l')\ t'$   
 using *assms* **unfolding** *deadlock-def* **by** *blast*

**lemma** *deadlockD3*:  
 assumes  $deadlock\ s$   
 and  $thr\ s\ t = \lfloor (x, no-wait-locks) \rfloor$   
 shows  $\forall w. wset\ s\ t \neq \lfloor PostWS\ w \rfloor$   
 using *assms* **unfolding** *deadlock-def* **by** *blast*

**lemma** *deadlock-def2*:  
 $deadlock\ s \iff$   
 $(\forall t\ x. thr\ s\ t = \lfloor (x, no-wait-locks) \rfloor \wedge \neg final\ x \wedge wset\ s\ t = None$   
 $\longrightarrow t \vdash \langle x, shr\ s \rangle \wr \wedge (\forall LT. t \vdash \langle x, shr\ s \rangle\ LT \wr \longrightarrow (\exists lt \in LT. must-wait\ s\ t\ lt\ (dom\ (thr\ s))))$   
 $\wedge (\forall t\ x\ ln. thr\ s\ t = \lfloor (x, ln) \rfloor \wedge ln \neq no-wait-locks \wedge \neg waiting\ (wset\ s\ t)$   
 $\longrightarrow (\exists l. ln\ \$\ l > 0 \wedge must-wait\ s\ t\ (Inl\ l)\ (dom\ (thr\ s))))$   
 $\wedge (\forall t\ x\ w. thr\ s\ t = \lfloor (x, no-wait-locks) \rfloor \longrightarrow wset\ s\ t \neq \lfloor PostWS\ WSNotified \rfloor \wedge wset\ s\ t \neq \lfloor PostWS\ WSWokenUp \rfloor)$   
**unfolding** *neg-no-wait-locks-conv*  
**apply**(*rule iffI*)  
**apply**(*intro strip conjI*)  
**apply**(*blast dest: deadlockD1*)  
**apply**(*blast dest: deadlockD1*)  
**apply**(*blast elim: deadlockD2*)  
**apply**(*blast dest: deadlockD3*)  
**apply**(*blast dest: deadlockD3*)  
**apply**(*elim conjE exE*)  
**apply**(*rule deadlockI*)  
**apply** *blast*  
**apply**(*rotate-tac 1*)  
**apply**(*erule allE, rotate-tac -1*)  
**apply**(*erule allE, rotate-tac -1*)  
**apply**(*erule allE, rotate-tac -1*)  
**apply**(*erule impE, blast*)  
**apply**(*elim exE conjE*)  
**apply**(*erule must-wait.cases*)  
**apply**(*clarify*)  
**apply**(*rotate-tac 3*)  
**apply**(*rule exI conjI |erule not-sym |assumption*) +  
**apply** *blast*  
**apply** *blast*  
**apply** *blast*  
**apply** *blast*  
**apply**(*case-tac w*)  
**apply** *blast*  
**apply** *blast*  
**done**

**lemma** *all-waiting-implies-deadlock*:  
 assumes  $lock-thread-ok\ (locks\ s)\ (thr\ s)$

**and normal:**  $\bigwedge t x. \llbracket \text{thr } s \ t = \lfloor (x, \text{no-wait-locks}) \rfloor; \neg \text{final } x; \text{wset } s \ t = \text{None} \rrbracket$   
 $\implies t \vdash \langle x, \text{shr } s \rangle \wr \wedge (\forall LT. t \vdash \langle x, \text{shr } s \rangle \ LT \wr \longrightarrow (\exists lt \in LT. \text{must-wait } s \ t \ lt \ (\text{dom } (\text{thr } s))))$

**and acquire:**  $\bigwedge t x \ln l. \llbracket \text{thr } s \ t = \lfloor (x, \ln) \rfloor; \neg \text{waiting } (\text{wset } s \ t); \ln \ \$ \ l > 0 \rrbracket$   
 $\implies \exists l'. \ln \ \$ \ l' > 0 \wedge \neg \text{may-lock } (\text{locks } s \ \$ \ l') \ t$

**and wakeup:**  $\bigwedge t x w. \text{thr } s \ t = \lfloor (x, \text{no-wait-locks}) \rfloor \implies \text{wset } s \ t \neq \lfloor \text{PostWS } w \rfloor$   
**shows** *deadlock s*

**proof**(*rule deadlockI*)  
**fix**  $T \ X$   
**assume**  $\text{thr } s \ T = \lfloor (X, \text{no-wait-locks}) \rfloor \neg \text{final } X \ \text{wset } s \ T = \text{None}$   
**thus**  $T \vdash \langle X, \text{shr } s \rangle \wr \wedge (\forall LT. T \vdash \langle X, \text{shr } s \rangle \ LT \wr \longrightarrow (\exists lt \in LT. \text{must-wait } s \ T \ lt \ (\text{dom } (\text{thr } s))))$   
**by**(*rule normal*)

**next**  
**fix**  $T \ X \ LN \ l'$   
**assume**  $\text{thr } s \ T = \lfloor (X, LN) \rfloor$   
**and**  $0 < LN \ \$ \ l'$   
**and**  $\text{wset}: \neg \text{waiting } (\text{wset } s \ T)$   
**from**  $\text{acquire}[\text{OF } \langle \text{thr } s \ T = \lfloor (X, LN) \rfloor \rangle \ \text{wset}, \text{OF } \langle 0 < LN \ \$ \ l' \rangle]$   
**obtain**  $l'$  **where**  $0 < LN \ \$ \ l' \neg \text{may-lock } (\text{locks } s \ \$ \ l') \ T$  **by** *blast*  
**then obtain**  $t'$  **where**  $T \neq t' \text{ has-lock } (\text{locks } s \ \$ \ l') \ t'$   
**unfolding** *not-may-lock-conv* **by** *fastforce*  
**moreover with**  $\langle \text{lock-thread-ok } (\text{locks } s) \ (\text{thr } s) \rangle$   
**have**  $\text{thr } s \ t' \neq \text{None}$  **by**(*auto dest: lock-thread-okD*)  
**ultimately show**  $\exists l \ t'. 0 < LN \ \$ \ l \wedge T \neq t' \wedge \text{thr } s \ t' \neq \text{None} \wedge \text{has-lock } (\text{locks } s \ \$ \ l) \ t'$   
**using**  $\langle 0 < LN \ \$ \ l' \rangle$  **by**(*auto*)

**qed**(*rule wakeup*)

**lemma** *mfinal-deadlock:*  
 $mfinal \ s \implies \text{deadlock } s$   
**unfolding** *mfinal-def2*  
**by**(*rule deadlockI*)(*auto simp add: final-thread-def*)

Now deadlock for single threads

**lemma** *must-wait-mono:*  
 $(\bigwedge x. x \in A \longrightarrow x \in B) \implies \text{must-wait } s \ t \ lt \ A \longrightarrow \text{must-wait } s \ t \ lt \ B$   
**by**(*auto simp add: must-wait-iff split: sum.split elim: all-final-except-mono'*)

**lemma** *must-wait-mono':*  
 $\llbracket \text{must-wait } s \ t \ lt \ A; A \subseteq B \rrbracket \implies \text{must-wait } s \ t \ lt \ B$   
**using** *must-wait-mono[of A B s t lt]*  
**by** *blast*

**end**

**lemma** *UN-mono:*  $\llbracket x \in A \longrightarrow x \in A'; x \in B \longrightarrow x \in B' \rrbracket \implies x \in A \cup B \longrightarrow x \in A' \cup B'$   
**by** *blast*

**lemma** *Collect-mono-conv [mono]:*  $x \in \{x. P \ x\} \longleftrightarrow P \ x$   
**by** *blast*

**context** *multithreaded-base* **begin**

**coinductive-set** *deadlocked* ::  $(l, t, x, m, w) \ \text{state} \Rightarrow t \ \text{set}$   
**for**  $s :: (l, t, x, m, w) \ \text{state}$  **where**

*deadlockedLock:*

$\llbracket \text{thr } s \ t = \lfloor (x, \text{no-wait-locks}) \rfloor; t \vdash \langle x, \text{shr } s \rangle \wr; \text{wset } s \ t = \text{None};$   
 $\bigwedge LT. t \vdash \langle x, \text{shr } s \rangle \ LT \wr \implies \exists lt \in LT. \text{must-wait } s \ t \ lt \ (\text{deadlocked } s \cup \text{final-threads } s) \rrbracket$   
 $\implies t \in \text{deadlocked } s$

| *deadlockedWait:*

$\llbracket \text{ln}. \llbracket \text{thr } s \ t = \lfloor (x, \text{ln}) \rfloor; \text{all-final-except } s \ (\text{deadlocked } s); \text{waiting } (\text{wset } s \ t) \rrbracket \implies t \in \text{deadlocked } s$

| *deadlockedAcquire:*

$\llbracket \text{ln}. \llbracket \text{thr } s \ t = \lfloor (x, \text{ln}) \rfloor; \neg \text{waiting } (\text{wset } s \ t); \text{ln } \$ \ l > 0; \text{has-lock } (\text{locks } s \ \$ \ l) \ t'; t' \neq t;$   
 $t' \in \text{deadlocked } s \vee \text{final-thread } s \ t' \rrbracket$   
 $\implies t \in \text{deadlocked } s$

**monos** *must-wait-mono UN-mono*

**lemma** *deadlockedAcquire-must-wait:*

$\llbracket \text{ln}. \llbracket \text{thr } s \ t = \lfloor (x, \text{ln}) \rfloor; \neg \text{waiting } (\text{wset } s \ t); \text{ln } \$ \ l > 0; \text{must-wait } s \ t \ (\text{Inl } l) \ (\text{deadlocked } s \cup \text{final-threads } s) \rrbracket$   
 $\implies t \in \text{deadlocked } s$

**apply**(*erule must-wait-elim*s)

**apply**(*erule* (2) *deadlockedAcquire*)

**apply** *auto*

**done**

**lemma** *deadlocked-elim* [*consumes 1, case-names lock wait acquire*]:

**assumes**  $t \in \text{deadlocked } s$

**and** *lock*:  $\llbracket \text{thr } s \ t = \lfloor (x, \text{no-wait-locks}) \rfloor; t \vdash \langle x, \text{shr } s \rangle \wr; \text{wset } s \ t = \text{None};$

$\bigwedge LT. t \vdash \langle x, \text{shr } s \rangle \ LT \wr \implies \exists lt \in LT. \text{must-wait } s \ t \ lt \ (\text{deadlocked } s \cup \text{final-threads } s) \rrbracket$   
 $\implies \text{thesis}$

**and** *wait*:  $\llbracket \text{ln}. \llbracket \text{thr } s \ t = \lfloor (x, \text{ln}) \rfloor; \text{all-final-except } s \ (\text{deadlocked } s); \text{waiting } (\text{wset } s \ t) \rrbracket$   
 $\implies \text{thesis}$

**and** *acquire*:  $\llbracket \text{ln } l \ t'. \llbracket \text{thr } s \ t = \lfloor (x, \text{ln}) \rfloor; \neg \text{waiting } (\text{wset } s \ t); 0 < \text{ln } \$ \ l; \text{has-lock } (\text{locks } s \ \$ \ l) \ t'; t \neq t';$

$t' \in \text{deadlocked } s \vee \text{final-thread } s \ t' \rrbracket \implies \text{thesis}$

**shows** *thesis*

**using** *assms* **by** *cases blast+*

**lemma** *deadlocked-coinduct*

[*consumes 1, case-names deadlocked, case-conclusion deadlocked Lock Wait Acquire, coinduct set: deadlocked*]:

**assumes** *major*:  $t \in X$

**and** *step*:

$\bigwedge t. t \in X \implies$

$(\exists x. \text{thr } s \ t = \lfloor (x, \text{no-wait-locks}) \rfloor \wedge t \vdash \langle x, \text{shr } s \rangle \wr \wedge \text{wset } s \ t = \text{None} \wedge$

$(\forall LT. t \vdash \langle x, \text{shr } s \rangle \ LT \wr \longrightarrow (\exists lt \in LT. \text{must-wait } s \ t \ lt \ (X \cup \text{deadlocked } s \cup \text{final-threads } s))))$

$\vee$

$(\exists x \text{ ln}. \text{thr } s \ t = \lfloor (x, \text{ln}) \rfloor \wedge \text{all-final-except } s \ (X \cup \text{deadlocked } s) \wedge \text{waiting } (\text{wset } s \ t)) \vee$

$(\exists x \ l \ t' \text{ ln}. \text{thr } s \ t = \lfloor (x, \text{ln}) \rfloor \wedge \neg \text{waiting } (\text{wset } s \ t) \wedge 0 < \text{ln } \$ \ l \wedge \text{has-lock } (\text{locks } s \ \$ \ l) \ t' \wedge$

$t' \neq t \wedge ((t' \in X \vee t' \in \text{deadlocked } s) \vee \text{final-thread } s \ t'))$

**shows**  $t \in \text{deadlocked } s$

**using** *major*

**proof**(*coinduct*)

**case** (*deadlocked t*)

**have**  $X \cup \text{deadlocked } s \cup \text{final-threads } s = \{x. x \in X \vee x \in \text{deadlocked } s \vee x \in \text{final-threads } s\}$

**by** *auto*

**moreover have**  $X \cup \text{deadlocked } s = \{x. x \in X \vee x \in \text{deadlocked } s\}$  **by** *blast*  
**ultimately show** *?case using step[OF deadlocked]* **by** *(elim disjE) simp-all*  
**qed**

**definition** *deadlocked'* ::  $(l, t, x, m, w)$  *state*  $\Rightarrow$  *bool* **where**  
 $\text{deadlocked}' s \equiv (\forall t. \text{not-final-thread } s t \longrightarrow t \in \text{deadlocked } s)$

**lemma** *deadlocked'I*:  
 $(\bigwedge t. \text{not-final-thread } s t \Longrightarrow t \in \text{deadlocked } s) \Longrightarrow \text{deadlocked}' s$   
**by** *(auto simp add: deadlocked'-def)*

**lemma** *deadlocked'D2*:  
 $\llbracket \text{deadlocked}' s; \text{not-final-thread } s t; t \in \text{deadlocked } s \Longrightarrow \text{thesis} \rrbracket \Longrightarrow \text{thesis}$   
**by** *(auto simp add: deadlocked'-def)*

**lemma** *not-deadlocked'I*:  
 $\llbracket \text{not-final-thread } s t; t \notin \text{deadlocked } s \rrbracket \Longrightarrow \neg \text{deadlocked}' s$   
**by** *(auto dest: deadlocked'D2)*

**lemma** *deadlocked'-intro*:  
 $\llbracket \forall t. \text{not-final-thread } s t \longrightarrow t \in \text{deadlocked } s \rrbracket \Longrightarrow \text{deadlocked}' s$   
**by** *(rule deadlocked'I)(blast)+*

**lemma** *deadlocked-thread-exists*:  
**assumes**  $t \in \text{deadlocked } s$   
**and**  $\bigwedge x \text{ ln. } \text{thr } s t = \lfloor (x, \text{ln}) \rfloor \Longrightarrow \text{thesis}$   
**shows** *thesis*  
**using** *assms*  
**by** *cases blast+*

**end**

**context** *multithreaded* **begin**

**lemma** *red-no-deadlock*:  
**assumes**  $P: s \rightarrow ta \rightarrow s'$   
**and** *dead*:  $t \in \text{deadlocked } s$   
**shows** *False*  
**proof** –  
**from**  $P$  **show** *False*  
**proof** *(cases)*  
**case** *(redT-normal x x' m')*  
**note**  $\text{red} = \langle t \vdash \langle x, \text{shr } s \rangle \rightarrow ta \rightarrow \langle x', m' \rangle \rangle$   
**note**  $\text{tst} = \langle \text{thr } s t = \lfloor (x, \text{no-wait-locks}) \rfloor \rangle$   
**note**  $\text{aok} = \langle \text{actions-ok } s t ta \rangle$   
**show** *False*  
**proof** *(cases  $\exists w. \text{wset } s t = \lfloor \text{InWS } w \rfloor$ )*  
**case** *True with aok show* *?thesis by (auto simp add: wset-actions-ok-def split: if-split-asm)*  
**next**  
**case** *False*  
**with** *dead tst*  
**have**  $\text{mle}: t \vdash \langle x, \text{shr } s \rangle \wr$   
**and**  $\text{clead}: \forall LT. t \vdash \langle x, \text{shr } s \rangle LT \wr \longrightarrow (\exists lt \in LT. \text{must-wait } s t lt (\text{deadlocked } s \cup \text{final-threads } s))$   
**s)**

```

  by(cases, auto simp add: waiting-def)+
let ?LT = collect-waits ta
from red have t ⊢ ⟨x, shr s⟩ ?LT ∷ by(auto intro: can-syncI)
then obtain lt where lt: lt ∈ ?LT and mw: must-wait s t lt (deadlocked s ∪ final-threads s)
  by(blast dest: clead[rule-format])
from mw show False
proof(cases rule: must-wait-elim)
  case (lock l t')
  from ⟨lt = Inl l⟩ lt have l ∈ collect-locks {ta}_l by(auto)
  with aok have may-lock (locks s $ l) t
    by(auto elim!: collect-locksE lock-ok-las-may-lock)
  with ⟨has-lock (locks s $ l) t'⟩ have t' = t
    by(auto dest: has-lock-may-lock-t-eq)
  with ⟨t' ≠ t⟩ show False by contradiction
next
  case (join t')
  from ⟨lt = Inr (Inl t')⟩ lt have Join t' ∈ set {ta}_c by auto
  from ⟨not-final-thread s t'⟩ obtain x'' ln''
    where thr s t' = [(x'', ln'')] by(rule not-final-thread-existsE)
  moreover with ⟨Join t' ∈ set {ta}_c⟩ aok
  have final x'' ln'' = no-wait-locks wset s t' = None
    by(auto dest: cond-action-oks-Join)
  ultimately show False using ⟨not-final-thread s t'⟩ by(auto)
next
  case (interrupt t')
  from aok lt ⟨lt = Inr (Inr t')⟩
  have t' ∈ interrupts s
    by(auto intro: collect-interrupts-interrupted)
  with ⟨t' ∉ interrupts s⟩ show False by contradiction
qed
qed
next
  case (redT-acquire x n ln)
  show False
  proof(cases ∃ w. wset s t = [InWS w])
    case True with ⟨¬ waiting (wset s t)⟩ show ?thesis
      by(auto simp add: not-waiting-iff)
  next
    case False
    with dead ⟨thr s t = [(x, ln)]⟩ ⟨0 < ln $ n⟩
    obtain l t' where 0 < ln $ l t ≠ t'
      and has-lock (locks s $ l) t'
      by(cases)(fastforce simp add: waiting-def)+
    hence ¬ may-acquire-all (locks s) t ln
      by(auto elim: may-acquire-allE dest: has-lock-may-lock-t-eq)
    with ⟨may-acquire-all (locks s) t ln⟩ show ?thesis by contradiction
  qed
qed
qed
lemma deadlocked'-no-red:
  [ s -t>ta→ s'; deadlocked' s ] ⇒ False
apply(rule red-no-deadlock)
apply(assumption)

```

**apply**(erule deadlocked'D2)  
**by**(rule red-not-final-thread)

**lemma** not-final-thread-deadlocked-final-thread [iff]:  
 $thr\ s\ t = \lfloor xln \rfloor \implies not\_final\_thread\ s\ t \vee t \in deadlocked\ s \vee final\_thread\ s\ t$   
**by**(auto simp add: not-final-thread-final-thread-conv[symmetric])

**lemma** all-waiting-deadlocked:  
**assumes** not-final-thread  $s\ t$   
**and** lock-thread-ok (locks  $s$ ) (thr  $s$ )  
**and** normal:  $\bigwedge t\ x. \llbracket thr\ s\ t = \lfloor (x, no\_wait\_locks) \rfloor; \neg final\ x; wset\ s\ t = None \rrbracket$   
 $\implies t \vdash \langle x, shr\ s \rangle \wr \wedge (\forall LT. t \vdash \langle x, shr\ s \rangle LT \wr \longrightarrow (\exists lt \in LT. must\_wait\ s\ t\ lt\ (final\_threads\ s)))$   
**and** acquire:  $\bigwedge t\ x\ ln\ l. \llbracket thr\ s\ t = \lfloor (x, ln) \rfloor; \neg waiting\ (wset\ s\ t); ln\ \$\ l > 0 \rrbracket$   
 $\implies \exists l'. ln\ \$\ l' > 0 \wedge \neg may\_lock\ (locks\ s\ \$\ l')\ t$   
**and** wakeup:  $\bigwedge t\ x\ w. thr\ s\ t = \lfloor (x, no\_wait\_locks) \rfloor \implies wset\ s\ t \neq \lfloor PostWS\ w \rfloor$   
**shows**  $t \in deadlocked\ s$

**proof** –

**from**  $\langle not\_final\_thread\ s\ t \rangle$   
**have**  $t \in \{t. not\_final\_thread\ s\ t\}$  **by** simp  
**thus** ?thesis  
**proof**(coinduct)  
**case** (deadlocked  $z$ )  
**hence** not-final-thread  $s\ z$  **by** simp  
**then obtain**  $x'\ ln'$  **where**  $thr\ s\ z = \lfloor (x', ln') \rfloor$  **by**(fastforce elim!: not-final-thread-existsE)  
**{**  
**assume**  $wset\ s\ z = None \neg final\ x'$   
**and** [simp]:  $ln' = no\_wait\_locks$   
**with**  $\langle thr\ s\ z = \lfloor (x', ln') \rfloor \rangle$   
**have**  $z \vdash \langle x', shr\ s \rangle \wr \wedge (\forall LT. z \vdash \langle x', shr\ s \rangle LT \wr \longrightarrow (\exists lt \in LT. must\_wait\ s\ z\ lt\ (final\_threads\ s)))$   
**by**(auto dest: normal)  
**then obtain**  $z \vdash \langle x', shr\ s \rangle \wr$   
**and** clnml:  $\bigwedge LT. z \vdash \langle x', shr\ s \rangle LT \wr \implies \exists lt \in LT. must\_wait\ s\ z\ lt\ (final\_threads\ s)$  **by**(blast)  
**{ fix**  $LT$   
**assume**  $z \vdash \langle x', shr\ s \rangle LT \wr$   
**then obtain**  $lt$  **where**  $mw: must\_wait\ s\ z\ lt\ (final\_threads\ s)$  **and**  $lt: lt \in LT$   
**by**(blast dest: clnml)  
**from**  $mw$  **have**  $must\_wait\ s\ z\ lt\ (\{t. not\_final\_thread\ s\ t\} \cup deadlocked\ s \cup final\_threads\ s)$   
**by**(blast intro: must-wait-mono')  
**with**  $lt$  **have**  $\exists lt \in LT. must\_wait\ s\ z\ lt\ (\{t. not\_final\_thread\ s\ t\} \cup deadlocked\ s \cup final\_threads\ s)$   
**by** blast }  
**with**  $\langle z \vdash \langle x', shr\ s \rangle \wr \rangle \langle thr\ s\ z = \lfloor (x', ln') \rfloor \rangle \langle wset\ s\ z = None \rangle$  **have** ?case **by**(simp) }  
**note** c1 = this

**{**  
**assume**  $wsz: \neg waiting\ (wset\ s\ z)$   
**and**  $ln' \neq no\_wait\_locks$   
**from**  $\langle ln' \neq no\_wait\_locks \rangle$  **obtain**  $l$  **where**  $0 < ln'\ \$\ l$   
**by**(auto simp add: neq-no-wait-locks-conv)  
**with**  $wsz\ \langle thr\ s\ z = \lfloor (x', ln') \rfloor \rangle$   
**obtain**  $l'$  **where**  $0 < ln'\ \$\ l' \neg may\_lock\ (locks\ s\ \$\ l')\ z$   
**by**(blast dest: acquire)  
**then obtain**  $t''$  **where**  $t'' \neq z$  has-lock (locks  $s\ \$\ l')$   $t''$



```

  unfolding not-may-lock-conv by blast
with ⟨lock-thread-ok (locks s) (thr s)⟩
obtain  $x''$   $ln''$  where  $thr\ s\ t'' = \lfloor (x'', ln'') \rfloor$ 
  by(auto elim!: lock-thread-ok-has-lockE)
hence  $(not-final-thread\ s\ t'' \vee t'' \in deadlocked\ s) \vee final-thread\ s\ t''$ 
  by(clarsimp simp add: not-final-thread-iff final-thread-def)
with  $wsz\ \langle 0 < ln'\ \$\ l' \rangle\ \langle thr\ s\ z = \lfloor (x', ln') \rfloor \rangle\ \langle t'' \neq z \rangle\ \langle has-lock\ (locks\ s\ \$\ l')\ t'' \rangle$ 
have ?Acquire by simp blast
hence ?case by simp }
note c2 = this
{ fix w
  assume waiting (wset s z)
  with ⟨thr s z =  $\lfloor (x', ln') \rfloor$ ⟩
  have ?Wait by(clarsimp simp add: all-final-except-def)
  hence ?case by simp }
note c3 = this
from ⟨not-final-thread s z⟩ ⟨thr s z =  $\lfloor (x', ln') \rfloor$ ⟩ show ?case
proof(cases rule: not-final-thread-cases2)
  case final show ?thesis
  proof(cases wset s z)
    case None show ?thesis
    proof(cases  $ln' = no-wait-locks$ )
      case True with None final show ?thesis by(rule c1)
    next
      case False
      from None have  $\neg waiting\ (wset\ s\ z)$  by(simp add: not-waiting-iff)
      thus ?thesis using False by(rule c2)
    qed
  next
    case (Some w)
    show ?thesis
    proof(cases w)
      case (InWS w')
      with Some have waiting (wset s z) by(simp add: waiting-def)
      thus ?thesis by(rule c3)
    next
      case (PostWS w')
      with Some have  $\neg waiting\ (wset\ s\ z)$  by(simp add: not-waiting-iff)
      moreover from PostWS ⟨thr s z =  $\lfloor (x', ln') \rfloor$ ⟩ Some
      have  $ln' \neq no-wait-locks$  by(auto dest: wakeup)
      ultimately show ?thesis by(rule c2)
    qed
  qed
next
  case wait-locks show ?thesis
  proof(cases wset s z)
    case None
    hence  $\neg waiting\ (wset\ s\ z)$  by(simp add: not-waiting-iff)
    thus ?thesis using wait-locks by(rule c2)
  next
    case (Some w)
    show ?thesis
    proof(cases w)
      case (InWS w')

```

```

    with Some have waiting (wset s z) by(simp add: waiting-def)
    thus ?thesis by(rule c3)
  next
    case (PostWS w')
    with Some have  $\neg$  waiting (wset s z) by(simp add: not-waiting-iff)
    moreover from PostWS  $\langle \text{thr } s \ z = \lfloor (x', \text{ln}') \rfloor \rangle$  Some
    have  $\text{ln}' \neq \text{no-wait-locks}$  by(auto dest: wakeup)
    ultimately show ?thesis by(rule c2)
  qed
qed
next
  case (wait-set w)
  show ?thesis
  proof(cases w)
    case (InWS w')
    with wait-set have waiting (wset s z) by(simp add: waiting-def)
    thus ?thesis by(rule c3)
  next
    case (PostWS w')
    with wait-set have  $\neg$  waiting (wset s z) by(simp add: not-waiting-iff)
    moreover from PostWS  $\langle \text{thr } s \ z = \lfloor (x', \text{ln}') \rfloor \rangle$  wait-set
    have  $\text{ln}' \neq \text{no-wait-locks}$  by(auto dest: wakeup[simplified])
    ultimately show ?thesis by(rule c2)
  qed
qed
qed
qed

```

Equivalence proof for both notions of deadlock

```

lemma deadlock-implies-deadlocked':
  assumes dead: deadlock s
  shows deadlock' s
proof -
  show ?thesis
  proof(rule deadlock'I)
    fix t
    assume not-final-thread s t
    hence  $t \in \{t. \text{not-final-thread } s \ t\}$  ..
    thus  $t \in \text{deadlocked } s$ 
    proof(coinduct)
      case (deadlocked t'')
      hence not-final-thread s t'' ..
      then obtain  $x'' \text{ ln}''$  where  $\text{tst}'': \text{thr } s \ t'' = \lfloor (x'', \text{ln}'') \rfloor$ 
        by(rule not-final-thread-existsE)
      { assume waiting (wset s t'')
        moreover
        with tst'' have nfine: not-final-thread s t''
          unfolding waiting-def
          by(blast intro: not-final-thread.intros)
        ultimately have ?case using tst''
          by(blast intro: all-final-exceptI not-final-thread-final) }
      note c1 = this
    {
      assume wst'':  $\neg$  waiting (wset s t'')
    }
  }

```

```

    and  $ln'' \neq \text{no-wait-locks}$ 
  then obtain  $l$  where  $l: ln'' \$ l > 0$ 
    by(auto simp add: neq-no-wait-locks-conv)
  with  $dead\ wst''\ tst''$  obtain  $l' T$ 
    where  $ln'' \$ l' > 0\ t'' \neq T$ 
    and  $hl: \text{has-lock } (locks\ s\ \$\ l')\ T$ 
    and  $tsT: thr\ s\ T \neq \text{None}$ 
    by - (erule deadlockD2)
  moreover from  $\langle thr\ s\ T \neq \text{None} \rangle$ 
  obtain  $xln$  where  $tsT: thr\ s\ T = \lfloor xln \rfloor$  by auto
  then obtain  $X\ LN$  where  $thr\ s\ T = \lfloor (X, LN) \rfloor$ 
    by(cases  $xln$ , auto)
  moreover hence  $\text{not-final-thread } s\ T \vee \text{final-thread } s\ T$ 
    by(auto simp add: final-thread-def not-final-thread-iff)
  ultimately have ?case using  $wst''\ tst''$  by blast }
note  $c2 = \text{this}$ 
{ assume  $wset\ s\ t'' = \text{None}$ 
  and  $[simp]: ln'' = \text{no-wait-locks}$ 
  moreover
  with  $\langle \text{not-final-thread } s\ t'' \rangle\ tst''$ 
  have  $\neg \text{final } x''$  by(auto)
  ultimately obtain  $t'' \vdash \langle x'', shr\ s \rangle$ 
    and  $clnml: \bigwedge LT. t'' \vdash \langle x'', shr\ s \rangle\ LT \wr \implies \exists t'. thr\ s\ t' \neq \text{None} \wedge (\exists lt \in LT. \text{must-wait } s\ t'')$ 
  lt (dom (thr s))
    using  $\langle thr\ s\ t'' = \lfloor (x'', ln'') \rfloor \rangle\ \langle \text{deadlock } s \rangle$ 
    by(blast elim: deadlockD1)
  { fix  $LT$ 
    assume  $t'' \vdash \langle x'', shr\ s \rangle\ LT \wr$ 
    then obtain  $lt$  where  $lt: lt \in LT$ 
      and  $mw: \text{must-wait } s\ t''\ lt\ (\text{dom } (thr\ s))$ 
      by(blast dest: clnml)
    note  $mw$ 
    also have  $\text{dom } (thr\ s) = \{t. \text{not-final-thread } s\ t\} \cup \text{deadlocked } s \cup \text{final-threads } s$ 
      by(auto simp add: not-final-thread-conv dest: deadlocked-thread-exists elim: final-threadE)
    finally have  $\exists lt \in LT. \text{must-wait } s\ t''\ lt\ (\{t. \text{not-final-thread } s\ t\} \cup \text{deadlocked } s \cup \text{final-threads } s)$ 
  }
s)
  using  $lt$  by blast }
  with  $\langle t'' \vdash \langle x'', shr\ s \rangle \wr \rangle\ tst''\ \langle wset\ s\ t'' = \text{None} \rangle$  have ?case by(simp) }
note  $c3 = \text{this}$ 
from  $\langle \text{not-final-thread } s\ t'' \rangle\ tst''$  show ?case
proof(cases rule: not-final-thread-cases2)
  case final show ?thesis
  proof(cases  $wset\ s\ t''$ )
    case None show ?thesis
    proof(cases  $ln'' = \text{no-wait-locks}$ )
      case True with None show ?thesis by(rule c3)
    next
      case False
      from None have  $\neg \text{waiting } (wset\ s\ t'')$  by(simp add: not-waiting-iff)
      thus ?thesis using False by(rule c2)
    qed
  next
  case (Some w)
  show ?thesis

```

```

    proof(cases w)
      case (InWS w')
        with Some have waiting (wset s t'') by(simp add: waiting-def)
        thus ?thesis by(rule c1)
      next
        case (PostWS w')
        hence  $\neg$  waiting (wset s t'') using Some by(simp add: not-waiting-iff)
        moreover from PostWS Some tst''
        have  $ln'' \neq$  no-wait-locks by(auto dest: deadlockD3[OF dead])
        ultimately show ?thesis by(rule c2)
    qed
  qed
next
  case wait-locks show ?thesis
  proof(cases waiting (wset s t''))
    case False
    thus ?thesis using wait-locks by(rule c2)
  next
    case True thus ?thesis by(rule c1)
  qed
next
  case (wait-set w)
  show ?thesis
  proof(cases w)
    case InWS
    with wait-set have waiting (wset s t'') by(simp add: waiting-def)
    thus ?thesis by(rule c1)
  next
    case (PostWS w')
    hence  $\neg$  waiting (wset s t'') using wait-set
      by(simp add: not-waiting-iff)
    moreover from PostWS wait-set tst''
    have  $ln'' \neq$  no-wait-locks by(auto dest: deadlockD3[OF dead])
    ultimately show ?thesis by(rule c2)
  qed
qed
qed
qed
qed
qed

```

lemma *deadlocked'-implies-deadlock*:

assumes *dead*: *deadlocked' s*

shows *deadlock s*

proof –

have *deadlocked*:  $\bigwedge t. \text{not-final-thread } s \ t \implies t \in \text{deadlocked } s$

using *dead* by(rule *deadlocked'D2*)

show ?thesis

proof(rule *deadlockI*)

fix *t' x'*

assume *thr s t' = [(x', no-wait-locks)]*

and  $\neg$  *final x'*

and *wset s t' = None*

hence *not-final-thread s t'* by(auto intro: not-final-thread-final)

hence *t' ∈ deadlocked s* by(rule *deadlocked*)

```

thus  $t' \vdash \langle x', shr\ s \rangle \wr \wedge (\forall LT. t' \vdash \langle x', shr\ s \rangle LT \wr \longrightarrow (\exists lt \in LT. must\ wait\ s\ t'\ lt\ (dom\ (thr\ s))))$ 
proof(cases rule: deadlocked-elim)
  case (lock  $x''$ )
    note  $lock = \langle \bigwedge LT. t' \vdash \langle x'', shr\ s \rangle LT \wr \implies \exists lt \in LT. must\ wait\ s\ t'\ lt\ (deadlocked\ s \cup final\ threads\ s) \rangle$ 
    from  $\langle thr\ s\ t' = \lfloor (x'', no\ wait\ locks) \rfloor \rangle \langle thr\ s\ t' = \lfloor (x', no\ wait\ locks) \rfloor \rangle$ 
    have [simp]:  $x' = x''$  by auto
    { fix  $LT$ 
      assume  $t' \vdash \langle x'', shr\ s \rangle LT \wr$ 
      from lock[OF this] obtain  $lt$  where  $lt: lt \in LT$ 
      and  $mw: must\ wait\ s\ t'\ lt\ (deadlocked\ s \cup final\ threads\ s)$  by blast
      have  $deadlocked\ s \cup final\ threads\ s \subseteq dom\ (thr\ s)$ 
      by(auto elim: final-threadE dest: deadlocked-thread-exists)
      with  $mw$  have  $must\ wait\ s\ t'\ lt\ (dom\ (thr\ s))$  by(rule must-wait-mono')
      with  $lt$  have  $\exists lt \in LT. must\ wait\ s\ t'\ lt\ (dom\ (thr\ s))$  by blast }
    with  $\langle t' \vdash \langle x'', shr\ s \rangle \wr$  show ?thesis by(auto)
  next
    case (wait  $x''\ ln''$ )
    from  $\langle wset\ s\ t' = None \rangle \langle waiting\ (wset\ s\ t') \rangle$ 
    have False by(simp add: waiting-def)
    thus ?thesis ..
  next
    case (acquire  $x''\ ln''\ l''\ T$ )
    from  $\langle thr\ s\ t' = \lfloor (x'', ln'') \rfloor \rangle \langle thr\ s\ t' = \lfloor (x', no\ wait\ locks) \rfloor \rangle \langle 0 < ln''\ \$\ l'' \rangle$ 
    have False by(auto)
    thus ?thesis ..
  qed
next
  fix  $t'\ x'\ ln'\ l$ 
  assume  $thr\ s\ t' = \lfloor (x', ln') \rfloor$ 
  and  $0 < ln'\ \$\ l$ 
  and  $wst': \neg waiting\ (wset\ s\ t')$ 
  hence not-final-thread  $s\ t'$  by(auto intro: not-final-thread-wait-locks)
  hence  $t' \in deadlocked\ s$  by(rule deadlocked)
  thus  $\exists l\ T. 0 < ln'\ \$\ l \wedge t' \neq T \wedge thr\ s\ T \neq None \wedge has\ lock\ (locks\ s\ \$\ l)\ T$ 
  proof(cases rule: deadlocked-elim)
    case (lock  $x''$ )
    from  $\langle thr\ s\ t' = \lfloor (x', ln') \rfloor \rangle \langle thr\ s\ t' = \lfloor (x'', no\ wait\ locks) \rfloor \rangle \langle 0 < ln'\ \$\ l \rangle$ 
    have False by auto
    thus ?thesis ..
  next
    case (wait  $x'\ ln'$ )
    from  $wst' \langle waiting\ (wset\ s\ t') \rangle$ 
    have False by contradiction
    thus ?thesis ..
  next
    case (acquire  $x''\ ln''\ l''\ t''$ )
    from  $\langle thr\ s\ t' = \lfloor (x'', ln'') \rfloor \rangle \langle thr\ s\ t' = \lfloor (x', ln') \rfloor \rangle$ 
    have [simp]:  $x' = x''\ ln' = ln''$  by auto
    moreover from  $\langle t'' \in deadlocked\ s \vee final\ thread\ s\ t'' \rangle$ 
    have  $thr\ s\ t'' \neq None$ 
    by(auto elim: deadlocked-thread-exists simp add: final-thread-def)
    with  $\langle 0 < ln''\ \$\ l'' \rangle \langle has\ lock\ (locks\ s\ \$\ l'')\ t'' \rangle \langle t' \neq t'' \rangle \langle thr\ s\ t' = \lfloor (x'', ln'') \rfloor \rangle$ 

```

```

    show ?thesis by auto
  qed
next
  fix t x w
  assume tst: thr s t = [(x, no-wait-locks)]
  show wset s t ≠ [PostWS w]
  proof
    assume wset s t = [PostWS w]
    moreover with tst have not-final-thread s t
      by(auto simp add: not-final-thread-iff)
    hence t ∈ deadlocked s by(rule deadlocked)
    ultimately show False using tst
      by(auto elim: deadlocked.cases simp add: waiting-def)
  qed
qed
qed
qed

lemma deadlock-eq-deadlocked':
  deadlock = deadlocked'
by(rule ext)(auto intro: deadlock-implies-deadlocked' deadlocked'-implies-deadlock)

lemma deadlock-no-red:
  [ s -t>ta→ s'; deadlock s ] ⇒ False
unfolding deadlock-eq-deadlocked'
by(rule deadlocked'-no-red)

lemma deadlock-no-active-threads:
  assumes dead: deadlock s
  shows active-threads s = {}
proof(rule equals0I)
  fix t
  assume active: t ∈ active-threads s
  then obtain ta s' where s -t>ta→ s' by(auto dest: active-thread-ex-red)
  thus False using dead by(rule deadlock-no-red)
qed

end

locale preserve-deadlocked = multithreaded final r convert-RA
  for final :: 'x ⇒ bool
  and r :: ('l,'t,'x,'m,'w,'o) semantics (- ⊢ - -> - [50,0,0,50] 80)
  and convert-RA :: 'l released-locks ⇒ 'o list
  +
  fixes wf-state :: ('l,'t,'x,'m,'w) state set
  assumes invariant3p-wf-state: invariant3p redT wf-state
  assumes can-lock-preserved:
    [ s ∈ wf-state; s -t'>ta'→ s';
      thr s t = [(x, no-wait-locks)]; t ⊢ ⟨x, shr s⟩ ⊔ ]
    ⇒ t ⊢ ⟨x, shr s'⟩ ⊔
  and can-lock-devreserp:
    [ s ∈ wf-state; s -t'>ta'→ s';
      thr s t = [(x, no-wait-locks)]; t ⊢ ⟨x, shr s'⟩ L ⊔ ]
    ⇒ ∃ L' ⊆ L. t ⊢ ⟨x, shr s⟩ L' ⊔
begin

```

**lemma** *redT-deadlocked-subset*:

**assumes** *wfs*:  $s \in \text{wf-state}$

**and** *Red*:  $s \xrightarrow{t} \text{ta} \rightarrow s'$

**shows**  $\text{deadlocked } s \subseteq \text{deadlocked } s'$

**proof**

**fix**  $t'$

**assume**  $t'\text{dead}$ :  $t' \in \text{deadlocked } s$

**from** *Red* **have**  $t\text{dead}$ :  $t \notin \text{deadlocked } s$

**by**(*auto dest: red-no-deadlock*)

**with**  $t'\text{dead}$  **have**  $t't$ :  $t' \neq t$  **by** *auto*

{ **fix**  $t'$

**assume** *final-thread*  $s \ t'$

**then obtain**  $x' \ ln'$  **where**  $tst'$ :  $\text{thr } s \ t' = \lfloor (x', \ln') \rfloor$  **by**(*auto elim!: final-threadE*)

**with**  $\langle \text{final-thread } s \ t' \rangle$  **have** *final*  $x'$

**and**  $wset \ s \ t' = \text{None}$  **and** [*simp*]:  $\ln' = \text{no-wait-locks}$

**by**(*auto elim: final-threadE*)

**with** *Red*  $tst'$  **have**  $t \neq t'$  **by** *cases*(*auto dest: final-no-red*)

**with** *Red*  $tst'$  **have**  $\text{thr } s' \ t' = \lfloor (x', \ln') \rfloor$

**by** *cases*(*auto intro: redT-updTs-Some*)

**moreover from** *Red*  $\langle t \neq t' \rangle \langle wset \ s \ t' = \text{None} \rangle$

**have**  $wset \ s' \ t' = \text{None}$  **by** *cases*(*auto simp: redT-updWs-None-implies-None*)

**ultimately have** *final-thread*  $s' \ t'$  **using**  $tst'$   $\langle \text{final } x' \rangle$

**by**(*auto simp add: final-thread-def*) }

**hence** *subset*:  $\text{deadlocked } s \cup \text{final-threads } s \subseteq \text{deadlocked } s \cup \text{deadlocked } s' \cup \text{final-threads } s'$  **by**(*auto*)

**from** *Red* **show**  $t' \in \text{deadlocked } s'$

**proof**(*cases*)

**case** (*redT-normal*  $x \ x' \ m'$ )

**note**  $\text{red} = \langle t \vdash \langle x, \text{shr } s \rangle \xrightarrow{\text{ta}} \langle x', m' \rangle \rangle$

**and**  $tst = \langle \text{thr } s \ t = \lfloor (x, \text{no-wait-locks}) \rfloor \rangle$

**and**  $\text{aok} = \langle \text{actions-ok } s \ t \ \text{ta} \rangle$

**and**  $s' = \langle \text{redT-upd } s \ t \ \text{ta} \ x' \ m' \ s' \rangle$

**from** *red* **have**  $\neg \text{final } x$  **by**(*auto dest: final-no-red*)

**with**  $t\text{dead}$   $tst$  **have**  $\text{nafe}$ :  $\neg \text{all-final-except } s \ (\text{deadlocked } s)$

**by**(*fastforce simp add: all-final-except-def not-final-thread-iff*)

**from**  $t'\text{dead}$  **show** ?thesis

**proof**(*coinduct*)

**case** ( $\text{deadlocked } t''$ )

**note**  $t''\text{dead} = \text{this}$

**with** *Red* **have**  $t''t$ :  $t'' \neq t$

**by**(*auto dest: red-no-deadlock*)

**from**  $t''\text{dead}$  **show** ?case

**proof**(*cases rule: deadlocked-elim*s)

**case** (*lock*  $X$ )

**hence**  $\text{est}''$ :  $\text{thr } s \ t'' = \lfloor (X, \text{no-wait-locks}) \rfloor$

**and**  $\text{msE}$ :  $t'' \vdash \langle X, \text{shr } s \rangle \wr$

**and**  $\text{csexdead}$ :  $\bigwedge LT. t'' \vdash \langle X, \text{shr } s \rangle \ LT \wr \implies \exists lt \in LT. \text{must-wait } s \ t'' \ lt \ (\text{deadlocked } s \cup \text{final-threads } s)$

**by** *auto*

**from**  $t''t$  *Red*  $\text{est}''$

**have**  $\text{es}''t''$ :  $\text{thr } s' \ t'' = \lfloor (X, \text{no-wait-locks}) \rfloor$

**by**(*cases*  $s$ )(*cases*  $s'$ , *auto elim!: redT-ts-Some-inv*)

**note**  $\text{es}''t''$  **moreover**

```

from wfs Red est'' msE have msE': t'' ⊢ ⟨X, shr s⟩ ∷ by(rule can-lock-preserved)
moreover
{ fix LT
  assume clL'': t'' ⊢ ⟨X, shr s⟩ LT ∷
  with est'' have ∃ LT' ⊆ LT. t'' ⊢ ⟨X, shr s⟩ LT' ∷
    by(rule can-lock-devreserp[OF wfs Red])
  then obtain LT' where clL': t'' ⊢ ⟨X, shr s⟩ LT' ∷
    and LL': LT' ⊆ LT by blast
  with csexdead obtain lt
    where lt: lt ∈ LT and mw: must-wait s t'' lt (deadlocked s ∪ final-threads s)
    by blast
  from mw have must-wait s' t'' lt (deadlocked s ∪ deadlocked s' ∪ final-threads s')
  proof(cases rule: must-wait-elim)
    case (lock l t')
    from ⟨t' ∈ deadlocked s ∪ final-threads s⟩ Red have tt': t ≠ t'
      by(auto dest: red-no-deadlock final-no-redT elim: final-threadE)
    from aok have lock-actions-ok (locks s $ l) t (⟦ta⟧l $ l)
      by(auto simp add: lock-ok-las-def)
    with tt' ⟨has-lock (locks s $ l) t'⟩ s'
    have hl't': has-lock (locks s' $ l) t' by(auto)
    moreover note ⟨t' ≠ t''⟩
    moreover from ⟨t' ∈ deadlocked s ∪ final-threads s⟩
    have t' ∈ (deadlocked s ∪ deadlocked s' ∪ final-threads s')
      using subset by blast
    ultimately show ?thesis unfolding ⟨lt = Inl l⟩ ..
  next
    case (join t')
    note t'dead = ⟨t' ∈ deadlocked s ∪ final-threads s⟩
    with Red have tt': t ≠ t'
      by(auto dest: red-no-deadlock final-no-redT elim: final-threadE)
    note nftt' = ⟨not-final-thread s t'⟩
    from t'dead Red aok s' tt' have ts't': thr s' t' = thr s t'
      by(auto elim!: deadlocked-thread-exists final-threadE intro: redT-updTs-Some)
    from nftt' have thr s t' ≠ None by auto
    with nftt' t'dead have t' ∈ deadlocked s
      by(simp add: not-final-thread-final-thread-conv[symmetric])
    hence not-final-thread s' t'
  proof(cases rule: deadlocked-elim)
    case (lock x'')
    from ⟨t' ⊢ ⟨x'', shr s⟩ ∷⟩ have ¬ final x''
      by(auto elim: must-syncE dest: final-no-red)
    with ⟨thr s t' = [(x'', no-wait-locks)]⟩ ts't' show ?thesis
      by(auto intro: not-final-thread.intros)
  next
    case (wait x'' ln'')
    from ⟨¬ final x⟩ tst ⟨all-final-except s (deadlocked s)⟩
    have t ∈ deadlocked s by(fastforce dest: all-final-exceptD simp add: not-final-thread-iff)
    with Red have False by(auto dest: red-no-deadlock)
    thus ?thesis ..
  next
    case (acquire x'' ln'' l'' T'')
    from ⟨thr s t' = [(x'', ln'')]⟩ ⟨0 < ln'' $ l''⟩ ts't'
    show ?thesis by(auto intro: not-final-thread.intros(2))
  qed

```



**moreover from**  $t' \text{dead subset}$  **have**  $t' \in \text{deadlocked } s \cup \text{deadlocked } s' \cup \text{final-threads } s' ..$   
**ultimately show**  $?thesis \text{ unfolding } \langle lt = \text{Inr } (\text{Inl } t') \rangle ..$   
**next**  
**case**  $(\text{interrupt } t')$   
**from**  $tst \text{ red aok}$  **have**  $\text{not-final-thread } s \ t$   
**by** $(\text{auto simp add: wset-actions-ok-def not-final-thread-iff split: if-split-asm dest: final-no-red})$   
**with**  $\langle \text{all-final-except } s \ (\text{deadlocked } s \cup \text{final-threads } s) \rangle$   
**have**  $t \in \text{deadlocked } s \cup \text{final-threads } s$  **by** $(\text{rule all-final-exceptD})$   
**moreover have**  $t \notin \text{deadlocked } s$  **using**  $\text{Red}$  **by** $(\text{blast dest: red-no-deadlock})$   
**moreover have**  $\neg \text{final-thread } s \ t$  **using**  $\text{red } tst$  **by** $(\text{auto simp add: final-thread-def dest: final-no-red})$   
**ultimately have**  $\text{False}$  **by**  $\text{blast}$   
**thus**  $?thesis ..$   
**qed**  
**with**  $lt$  **have**  $\exists lt \in LT. \text{ must-wait } s' \ t'' \ lt \ (\text{deadlocked } s \cup \text{deadlocked } s' \cup \text{final-threads } s')$  **by**  
 $\text{blast}$  }  
**moreover have**  $\text{wset } s' \ t'' = \text{None}$  **using**  $s' \ t'' \ t \ (\text{wset } s \ t'' = \text{None})$   
**by** $(\text{auto intro: redT-updWs-None-implies-None})$   
**ultimately show**  $?thesis$  **by** $(\text{auto})$   
**next**  
**case**  $(\text{wait } x \ ln)$   
**from**  $\langle \text{all-final-except } s \ (\text{deadlocked } s) \rangle \text{ nafe}$  **have**  $\text{False}$  **by**  $\text{simp}$   
**thus**  $?thesis$  **by**  $\text{simp}$   
**next**  
**case**  $(\text{acquire } X \ ln \ l \ T)$   
**from**  $t'' \ t \ \text{Red} \ \langle \text{thr } s \ t'' = \lfloor (X, \ln) \rfloor \rangle \ s'$   
**have**  $es' t'': \text{thr } s' \ t'' = \lfloor (X, \ln) \rfloor$   
**by** $(\text{cases } s)(\text{auto dest: redT-ts-Some-inv})$   
**moreover**  
**from**  $\langle T \in \text{deadlocked } s \vee \text{final-thread } s \ T \rangle$   
**have**  $T \neq t$   
**proof** $(\text{rule disjE})$   
**assume**  $T \in \text{deadlocked } s$   
**with**  $\text{Red}$  **show**  $?thesis$  **by** $(\text{auto dest: red-no-deadlock})$   
**next**  
**assume**  $\text{final-thread } s \ T$   
**with**  $\text{Red}$  **show**  $?thesis$   
**by** $(\text{auto dest!: final-no-redT simp add: final-thread-def})$   
**qed**  
**with**  $s' \ tst \ \text{Red} \ \langle \text{has-lock } (\text{locks } s \ \$ \ l) \ T \rangle$  **have**  $\text{has-lock } (\text{locks } s' \ \$ \ l) \ T$   
**by**  $\neg(\text{cases } s, \text{auto dest: redT-has-lock-inv}[THEN \text{iffD2}])$   
**moreover**  
**from**  $s' \ \langle T \neq t \rangle$  **have**  $\text{wset: wset } s \ T = \text{None} \implies \text{wset } s' \ T = \text{None}$   
**by** $(\text{auto intro: redT-updWs-None-implies-None})$   
**{ fix } x**  
**assume**  $\text{thr } s \ T = \lfloor (x, \text{no-wait-locks}) \rfloor$   
**with**  $\langle T \neq t \rangle \ \text{Red } s' \ \text{aok } tst$  **have**  $\text{thr } s' \ T = \lfloor (x, \text{no-wait-locks}) \rfloor$   
**by** $(\text{auto intro: redT-updTs-Some})$  }  
**moreover**  
**hence**  $\text{final-thread } s \ T \implies \text{final-thread } s' \ T$   
**by** $(\text{auto simp add: final-thread-def intro: wset})$   
**moreover from**  $\langle \neg \text{waiting } (\text{wset } s \ t'') \rangle \ s' \ t'' \ t$   
**have**  $\neg \text{waiting } (\text{wset } s' \ t'')$   
**by** $(\text{auto simp add: redT-updWs-None-implies-None redT-updWs-PostWS-imp-PostWS not-waiting-iff})$

```

ultimately have ?Acquire
  using  $\langle 0 < \ln \$ l \rangle \langle t'' \neq T \rangle \langle T \in \text{deadlocked } s \vee \text{final-thread } s \ T \rangle$  by(auto)
thus ?thesis by simp
qed
qed
next
case (redT-acquire x n ln)
hence [simp]:  $ta = (K\$ \ [], \ [], \ [], \ [], \ [], \ \text{convert-RA } \ln)$ 
and  $s': s' = (\text{acquire-all } (\text{locks } s) \ t \ \ln, ((\text{thr } s)(t \mapsto (x, \text{no-wait-locks})), \text{shr } s), \text{wset } s, \text{interrupts } s)$ 
and  $tst: \text{thr } s \ t = \lfloor (x, \ln) \rfloor$ 
and  $wst: \neg \text{waiting } (\text{wset } s \ t)$  by auto
from  $t'\text{dead}$  show ?thesis
proof(coinduct)
case (deadlocked  $t''$ )
note  $t''\text{dead} = \text{this}$ 
with Red have  $t''t: t'' \neq t$ 
by(auto dest: red-no-deadlock)
from  $t''\text{dead}$  show ?case
proof(cases rule: deadlocked-elim)
case (lock X)
note  $\text{clnml} = \langle \bigwedge LT. t'' \vdash \langle X, \text{shr } s \rangle LT \rangle \implies \exists lt \in LT. \text{must-wait } s \ t'' \ lt \ (\text{deadlocked } s \cup \text{final-threads } s)$ 
note  $tst'' = \langle \text{thr } s \ t'' = \lfloor (X, \text{no-wait-locks}) \rfloor \rangle$ 
with  $s' \ t''t$  have  $ts't'': \text{thr } s' \ t'' = \lfloor (X, \text{no-wait-locks}) \rfloor$  by simp
moreover
{ fix LT
  assume  $t'' \vdash \langle X, \text{shr } s' \rangle LT$ 
  hence  $t'' \vdash \langle X, \text{shr } s \rangle LT$  using  $s'$  by simp
  then obtain  $lt$  where  $lt: lt \in LT$  and  $\text{hlnft}: \text{must-wait } s \ t'' \ lt \ (\text{deadlocked } s \cup \text{final-threads } s)$ 
  by(blast dest: clnml)
  from hlnft have  $\text{must-wait } s' \ t'' \ lt \ (\text{deadlocked } s \cup \text{deadlocked } s' \cup \text{final-threads } s')$ 
  proof(cases rule: must-wait-elim)
  case (lock  $l' \ T$ )
  from  $\langle \text{has-lock } (\text{locks } s \ \$ \ l') \ T \rangle s'$ 
  have  $\text{has-lock } (\text{locks } s' \ \$ \ l') \ T$ 
  by(auto intro: has-lock-has-lock-acquire-locks)
  moreover note  $\langle T \neq t'' \rangle$ 
  moreover from  $\langle T \in \text{deadlocked } s \cup \text{final-threads } s \rangle$ 
  have  $T \in \text{deadlocked } s \cup \text{deadlocked } s' \cup \text{final-threads } s'$  using subset by blast
  ultimately show ?thesis unfolding  $\langle lt = \text{Inl } l' \rangle$  ..
}
next
case (join T)
from  $\langle \text{not-final-thread } s \ T \rangle$  have  $\text{thr } s \ T \neq \text{None}$ 
by(auto simp add: not-final-thread-iff)
moreover
from  $\langle T \in \text{deadlocked } s \cup \text{final-threads } s \rangle$ 
have  $T \neq t$ 
proof
  assume  $T \in \text{deadlocked } s$ 
  with Red show ?thesis by(auto dest: red-no-deadlock)
next
  assume  $T \in \text{final-threads } s$ 
  with  $\langle 0 < \ln \$ n \rangle tst$  show ?thesis

```

```

    by(auto simp add: final-thread-def)
  qed
  ultimately have not-final-thread s' T using ⟨not-final-thread s T⟩ s'
    by(auto simp add: not-final-thread-iff)
  moreover from ⟨T ∈ deadlocked s ∪ final-threads s⟩
  have T ∈ deadlocked s ∪ deadlocked s' ∪ final-threads s' using subset by blast
  ultimately show ?thesis unfolding ⟨lt = Inr (Inl T)⟩ ..
next
  case (interrupt T)
  from tst wst ⟨0 < ln $ n⟩ have not-final-thread s t
    by(auto simp add: waiting-def not-final-thread-iff)
  with ⟨all-final-except s (deadlocked s ∪ final-threads s)⟩
  have t ∈ deadlocked s ∪ final-threads s by(rule all-final-exceptD)
  moreover have t ∉ deadlocked s using Red by(blast dest: red-no-deadlock)
  moreover have ¬ final-thread s t using tst ⟨0 < ln $ n⟩ by(auto simp add: final-thread-def)
  ultimately have False by blast
  thus ?thesis ..
qed
with lt have ∃ lt ∈ LT. must-wait s' t'' lt (deadlocked s ∪ deadlocked s' ∪ final-threads s') by
blast }
moreover from ⟨wset s t'' = None⟩ s' have wset s' t'' = None by simp
ultimately show ?thesis using ⟨thr s t'' = [(X, no-wait-locks)]⟩ ⟨t'' ⊢ ⟨X, shr s⟩⟩ s' by
fastforce
next
  case (wait X LN)
  have all-final-except s' (deadlocked s)
  proof(rule all-final-exceptI)
    fix T
    assume not-final-thread s' T
    hence not-final-thread s T using wst tst s'
      by(auto simp add: not-final-thread-iff split: if-split-asm)
    with ⟨all-final-except s (deadlocked s)⟩ ⟨thr s t = [(x, ln)]⟩
    show T ∈ deadlocked s by-(erule all-final-exceptD)
  qed
  hence all-final-except s' (deadlocked s ∪ deadlocked s')
    by(rule all-final-except-mono') blast
  with t''t ⟨thr s t'' = [(X, LN)]⟩ ⟨waiting (wset s t'')⟩ s'
  have ?Wait by simp
  thus ?thesis by simp
next
  case (acquire X LN l T)
  from ⟨thr s t'' = [(X, LN)]⟩ t''t s'
  have thr s' t'' = [(X, LN)] by(simp)
  moreover from ⟨T ∈ deadlocked s ∨ final-thread s T⟩ s' tst
  have T ∈ deadlocked s ∨ final-thread s' T
    by(clarsimp simp add: final-thread-def)
  moreover from ⟨has-lock (locks s $ l) T⟩ s'
  have has-lock (locks s' $ l) T
    by(auto intro: has-lock-has-lock-acquire-locks)
  moreover have ¬ waiting (wset s' t'') using ⟨¬ waiting (wset s t'')⟩ s' by simp
  ultimately show ?thesis using ⟨0 < LN $ l⟩ ⟨t'' ≠ T⟩ by blast
qed
qed
qed

```

qed

**corollary** *RedT-deadlocked-subset:*

**assumes** *wfs*:  $s \in \text{wf-state}$

**and** *Red*:  $s \multimap \text{ttas} \rightarrow^* s'$

**shows** *deadlocked*  $s \subseteq \text{deadlocked } s'$

**using** *Red*

**apply**(*induct rule: RedT-induct'*)

**apply**(*unfold RedT-def*)

**apply**(*blast dest: invariant3p-rtrancl3p[OF invariant3p-wf-state - wfs] redT-deadlocked-subset*) +  
**done**

**end**

**end**

### 1.13 Progress theorem for the multithreaded semantics

**theory** *FWProgress*

**imports**

*FWDeadlock*

**begin**

**locale** *progress* = *multithreaded final r convert-RA*

**for** *final* ::  $'x \Rightarrow \text{bool}$

**and** *r* ::  $(\text{'l}, \text{'t}, \text{'x}, \text{'m}, \text{'w}, \text{'o}) \text{ semantics } (- \vdash - \multimap - [50, 0, 0, 50] \ 80)$

**and** *convert-RA* ::  $\text{'l released-locks} \Rightarrow \text{'o list}$

+

**fixes** *wf-state* ::  $(\text{'l}, \text{'t}, \text{'x}, \text{'m}, \text{'w}) \text{ state set}$

**assumes** *wf-stateD*:  $s \in \text{wf-state} \implies \text{lock-thread-ok } (\text{locks } s) \ (\text{thr } s) \wedge \text{wset-final-ok } (\text{wset } s) \ (\text{thr } s)$

**and** *wf-red*:

$\llbracket s \in \text{wf-state}; \text{thr } s \ t = \lfloor (x, \text{no-wait-locks}) \rfloor;$

$t \vdash (x, \text{shr } s) -ta \rightarrow (x', m'); \neg \text{waiting } (\text{wset } s \ t) \rrbracket$

$\implies \exists ta' x' m'. t \vdash (x, \text{shr } s) -ta' \rightarrow (x', m') \wedge (\text{actions-ok } s \ t \ ta' \vee \text{actions-ok}' s \ t \ ta' \wedge \text{actions-subset } ta' \ ta)$

**and** *red-wait-set-not-final*:

$\llbracket s \in \text{wf-state}; \text{thr } s \ t = \lfloor (x, \text{no-wait-locks}) \rfloor;$

$t \vdash (x, \text{shr } s) -ta \rightarrow (x', m'); \neg \text{waiting } (\text{wset } s \ t); \text{Suspend } w \in \text{set } \{ta\}_w \rrbracket$

$\implies \neg \text{final } x'$

**and** *wf-progress*:

$\llbracket s \in \text{wf-state}; \text{thr } s \ t = \lfloor (x, \text{no-wait-locks}) \rfloor; \neg \text{final } x \rrbracket$

$\implies \exists ta \ x' m'. t \vdash \langle x, \text{shr } s \rangle -ta \rightarrow \langle x', m' \rangle$

**and** *ta-Wakeup-no-join-no-lock-no-interrupt*:

$\llbracket s \in \text{wf-state}; \text{thr } s \ t = \lfloor (x, \text{no-wait-locks}) \rfloor; t \vdash xm -ta \rightarrow xm'; \text{Notified} \in \text{set } \{ta\}_w \vee \text{WokenUp} \in \text{set } \{ta\}_w \rrbracket$

$\implies \text{collect-waits } ta = \{\}$

**and** *ta-satisfiable*:

$\llbracket s \in \text{wf-state}; \text{thr } s \ t = \lfloor (x, \text{no-wait-locks}) \rfloor; t \vdash \langle x, \text{shr } s \rangle -ta \rightarrow \langle x', m' \rangle \rrbracket$

$\implies \exists s'. \text{actions-ok } s' \ t \ ta$

**begin**

**lemma** *wf-redE*:

**assumes**  $s \in \text{wf-state}$  *thr s t* =  $\lfloor (x, \text{no-wait-locks}) \rfloor$   
**and**  $t \vdash \langle x, \text{shr } s \rangle -ta \rightarrow \langle x'', m'' \rangle \neg \text{waiting } (\text{wset } s \ t)$   
**obtains**  $ta' \ x' \ m'$   
**where**  $t \vdash \langle x, \text{shr } s \rangle -ta' \rightarrow \langle x', m' \rangle \text{actions-ok}' \ s \ t \ ta' \text{actions-subset } ta' \ ta$   
 $| \ ta' \ x' \ m' \text{ where } t \vdash \langle x, \text{shr } s \rangle -ta' \rightarrow \langle x', m' \rangle \text{actions-ok } s \ t \ ta'$   
**using** *wf-red[OF assms]* **by** *blast*

**lemma** *wf-progressE*:

**assumes**  $s \in \text{wf-state}$   
**and** *thr s t* =  $\lfloor (x, \text{no-wait-locks}) \rfloor \neg \text{final } x$   
**obtains**  $ta \ x' \ m' \text{ where } t \vdash \langle x, \text{shr } s \rangle -ta \rightarrow \langle x', m' \rangle$   
**using** *assms*  
**by** (*blast dest: wf-progress*)

**lemma** *wf-progress-satisfiable*:

$\llbracket s \in \text{wf-state}; \text{thr } s \ t = \lfloor (x, \text{no-wait-locks}) \rfloor; \neg \text{final } x \rrbracket$   
 $\implies \exists ta \ x' \ m' \ s'. \ t \vdash \langle x, \text{shr } s \rangle -ta \rightarrow \langle x', m' \rangle \wedge \text{actions-ok } s' \ t \ ta$   
**apply** (*frule (2) wf-progress*)  
**apply** (*blast dest: ta-satisfiable*)  
**done**

**theorem** *redT-progress*:

**assumes** *wfs*:  $s \in \text{wf-state}$   
**and** *ndead*:  $\neg \text{deadlock } s$   
**shows**  $\exists t' \ ta' \ s'. \ s -t' \triangleright ta' \rightarrow s'$   
**proof** –  
**from** *wfs* **have** *lok*: *lock-thread-ok* (*locks s*) (*thr s*)  
**and** *wfin*: *wset-final-ok* (*wset s*) (*thr s*)  
**by** (*auto dest: wf-stateD*)  
**from** *ndead*  
**have**  $\exists t \ x \ ln \ l. \ \text{thr } s \ t = \lfloor (x, ln) \rfloor \wedge$   
 $(\text{wset } s \ t = \text{None} \wedge ln = \text{no-wait-locks} \wedge \neg \text{final } x \wedge (\exists LT. \ t \vdash \langle x, \text{shr } s \rangle \ LT \wr \wedge (\forall lt \in LT. \neg \text{must-wait } s \ t \ lt \ (\text{dom } (\text{thr } s)))) \vee$   
 $\neg \text{waiting } (\text{wset } s \ t) \wedge ln \ \$ \ l > 0 \wedge (\forall l. \ ln \ \$ \ l > 0 \implies \text{may-lock } (\text{locks } s \ \$ \ l) \ t) \vee$   
 $(\exists w. \ ln = \text{no-wait-locks} \wedge \text{wset } s \ t = \lfloor \text{PostWS } w \rfloor))$   
**by** (*rule contrapos-np*) (*blast intro!: all-waiting-implies-deadlock[OF lok] intro: must-syncI[OF wf-progress-satisfiable[OF wfs]]*)  
**then obtain**  $t \ x \ ln \ l$   
**where** *tst*:  $\text{thr } s \ t = \lfloor (x, ln) \rfloor$   
**and** *a*:  $\text{wset } s \ t = \text{None} \wedge ln = \text{no-wait-locks} \wedge \neg \text{final } x \wedge$   
 $(\exists LT. \ t \vdash \langle x, \text{shr } s \rangle \ LT \wr \wedge (\forall lt \in LT. \neg \text{must-wait } s \ t \ lt \ (\text{dom } (\text{thr } s)))) \vee$   
 $\neg \text{waiting } (\text{wset } s \ t) \wedge ln \ \$ \ l > 0 \wedge (\forall l. \ ln \ \$ \ l > 0 \implies \text{may-lock } (\text{locks } s \ \$ \ l) \ t) \vee$   
 $(\exists w. \ ln = \text{no-wait-locks} \wedge \text{wset } s \ t = \lfloor \text{PostWS } w \rfloor)$   
**by** *blast*  
**from** *a* **have** *cases*[*case-names normal acquire wakeup*]:  
 $\bigwedge \text{thesis.}$   
 $\llbracket \bigwedge LT. \llbracket \text{wset } s \ t = \text{None}; \ ln = \text{no-wait-locks}; \neg \text{final } x; \ t \vdash \langle x, \text{shr } s \rangle \ LT \wr;$   
 $\bigwedge lt. \ lt \in LT \implies \neg \text{must-wait } s \ t \ lt \ (\text{dom } (\text{thr } s)) \rrbracket \implies \text{thesis};$   
 $\llbracket \neg \text{waiting } (\text{wset } s \ t); \ ln \ \$ \ l > 0; \bigwedge l. \ ln \ \$ \ l > 0 \implies \text{may-lock } (\text{locks } s \ \$ \ l) \ t \rrbracket \implies \text{thesis};$   
 $\bigwedge w. \llbracket ln = \text{no-wait-locks}; \text{wset } s \ t = \lfloor \text{PostWS } w \rfloor \rrbracket \implies \text{thesis} \rrbracket \implies \text{thesis}$   
**by** *auto*

```

show ?thesis
proof(cases rule: cases)
  case (normal LT)
    note [simp] = ⟨ln = no-wait-locks⟩
    and nfine' = ⟨¬ final x⟩
    and cl' = ⟨t ⊢ ⟨x, shr s⟩ LT ⟩
    and mw = ⟨∧ lt. lt ∈ LT ⇒ ¬ must-wait s t lt (dom (thr s))⟩
    from tst nfine' obtain x'' m'' ta'
    where red: t ⊢ ⟨x, shr s⟩ -ta' → ⟨x'', m''⟩
    by(auto intro: wf-progressE[OF wfs])
    from cl'
    have ∃ ta''' x''' m'''. t ⊢ ⟨x, shr s⟩ -ta''' → ⟨x''', m'''⟩ ∧
      LT = collect-waits ta'''
    by (fastforce elim!: can-syncE)
    then obtain ta''' x''' m'''
    where red'': t ⊢ ⟨x, shr s⟩ -ta''' → ⟨x''', m'''⟩
    and L: LT = collect-waits ta'''
    by blast
    from ⟨wset s t = None⟩ have ¬ waiting (wset s t) by(simp add: not-waiting-iff)
    with tst obtain ta'' x'' m''
    where red': t ⊢ ⟨x, shr s⟩ -ta'' → ⟨x'', m''⟩
    and aok': actions-ok s t ta'' ∨ actions-ok' s t ta'' ∧ actions-subset ta'' ta'''
    by -(rule wf-redE[OF wfs - red''], auto)
    from aok' have actions-ok s t ta''
    proof
      assume actions-ok' s t ta'' ∧ actions-subset ta'' ta'''
      hence aok': actions-ok' s t ta'' and aos: actions-subset ta'' ta''' by simp-all

    { fix l
      assume Inl l ∈ LT
      { fix t'
        assume t ≠ t'
        have ¬ has-lock (locks s $ l) t'
        proof
          assume has-lock (locks s $ l) t'
          moreover with lok have thr s t' ≠ None by(auto dest: lock-thread-okD)
          ultimately have must-wait s t (Inl l) (dom (thr s)) using ⟨t ≠ t'⟩ by(auto)
          moreover from ⟨Inl l ∈ LT⟩ have ¬ must-wait s t (Inl l) (dom (thr s)) by(rule mw)
          ultimately show False by contradiction
        qed }
      hence may-lock (locks s $ l) t
      by-(rule classical, auto simp add: not-may-lock-conv) }
    note mayl = this
    { fix t'
      assume t'LT: Inr (Inl t') ∈ LT
      hence ¬ not-final-thread s t' ∧ t' ≠ t
      proof(cases t' = t)
        case False with t'LT mw L show ?thesis by(fastforce)
      next
        case True with tst mw[OF t'LT] nfine' L have False
        by(auto intro!: must-wait.intros simp add: not-final-thread-iff)
        thus ?thesis ..
      qed }
    note mayj = this

```

```

{ fix t'
  assume t': Inr (Inr t') ∈ LT
  from t' have ¬ must-wait s t (Inr (Inr t')) (dom (thr s)) by(rule mw)
  hence t' ∈ interrupts s
    by(rule contrapos-np)(fastforce intro: all-final-exceptI simp add: not-final-thread-iff) }
note interrupt = this
from aos L mayl
have ∧l. l ∈ collect-locks' {ta''}_l ⇒ may-lock (locks s $ l) t by auto
with aok' have lock-ok-las (locks s) t {ta''}_l by(auto intro: lock-ok-las'-into-lock-on-las)
moreover
from mayj aos L
have cond-action-oks s t {ta''}_c
  by(fastforce intro: may-join-cond-action-oks)
moreover
from ta-satisfiable[OF wfs tst[simplified] red']
obtain is' where interrupt-actions-ok is' {ta''}_i by auto
with interrupt aos aok' L have interrupt-actions-ok (interrupts s) {ta''}_i
  by(auto 5 2 intro: interrupt-actions-ok'-collect-interrupts-imp-interrupt-actions-ok)
ultimately show actions-ok s t ta'' using aok' by auto
qed
moreover obtain ws'' where redT-updWs t (wset s) {ta''}_w ws''
  using redT-updWs-total[of t wset s {ta''}_w] ..
then obtain s' where redT-upd s t ta'' x'' m'' s' by fastforce
ultimately have s -t>ta''→ s'
  using red' tst ⟨wset s t = None⟩ by(auto intro: redT-normal)
thus ?thesis by blast
next
case acquire
hence may-acquire-all (locks s) t ln by(auto)
with tst ⟨¬ waiting (wset s t)⟩ ⟨0 < ln $ l⟩
show ?thesis by(fastforce intro: redT-acquire)
next
case (wakeup w)
from ⟨wset s t = [PostWS w]⟩
have ¬ waiting (wset s t) by(simp add: not-waiting-iff)
from tst wakeup have tst: thr s t = [(x, no-wait-locks)] by simp
from wakeup tst wfin have ¬ final x by(auto dest: wset-final-okD)
from wf-progress[OF wfs tst this]
obtain ta x' m' where red: t ⊢ ⟨x, shr s⟩ -ta→ ⟨x', m'⟩ by auto
from wf-red[OF wfs tst red ⟨¬ waiting (wset s t)⟩]
obtain ta' x'' m''
  where red': t ⊢ ⟨x, shr s⟩ -ta'→ ⟨x'', m'⟩
  and aok': actions-ok s t ta' ∨ actions-ok' s t ta' ∧ actions-subset ta' ta by blast
from aok' have actions-ok s t ta'
proof
  assume actions-ok' s t ta' ∧ actions-subset ta' ta
  hence aok': actions-ok' s t ta'
    and subset: actions-subset ta' ta by simp-all
  from wakeup aok' have Notified ∈ set {ta''}_w ∨ WokenUp ∈ set {ta''}_w
    by(auto simp add: wset-actions-ok-def split: if-split-asm)
  from ta-Wakeup-no-join-no-lock-no-interrupt[OF wfs tst red' this]
  have no-join: collect-cond-actions {ta''}_c = {}
    and no-lock: collect-locks {ta''}_l = {}
    and no-interrupt: collect-interrupts {ta''}_i = {} by auto

```

```

from no-lock have no-lock': collect-locks'  $\llbracket ta' \rrbracket_l = \{\}$ 
  using collect-locks'-subset-collect-locks[of  $\llbracket ta' \rrbracket_l$ ] by auto
from aok' have lock-ok-las' (locks s) t  $\llbracket ta' \rrbracket_l$  by auto
hence lock-ok-las (locks s) t  $\llbracket ta' \rrbracket_l$ 
  by(rule lock-ok-las'-into-lock-on-las)(simp add: no-lock')
moreover from subset aok' no-join have cond-action-oks s t  $\llbracket ta' \rrbracket_c$ 
  by(auto intro: may-join-cond-action-oks)
moreover from ta-satisfiable[OF wfs tst[simplified] red']
obtain is' where interrupt-actions-ok is'  $\llbracket ta' \rrbracket_i$  by auto
with aok' no-interrupt have interrupt-actions-ok (interrupts s)  $\llbracket ta' \rrbracket_i$ 
  by(auto intro: interrupt-actions-ok'-collect-interrupts-imp-interrupt-actions-ok)
ultimately show actions-ok s t ta' using aok' by auto
qed
moreover obtain ws'' where redT-updWs t (wset s)  $\llbracket ta' \rrbracket_w$  ws''
  using redT-updWs-total[of t wset s  $\llbracket ta' \rrbracket_w$ ] ..
then obtain s' where redT-upd s t ta' x'' m'' s' by fastforce
ultimately have s  $\rightarrow$  ta'  $\rightarrow$  s' using tst red' wakeup
  by(auto intro: redT-normal)
thus ?thesis by blast
qed
qed
end
end

```

## 1.14 Lifting of thread-local properties to the multithreaded case

```

theory FWLifting
imports
  FWWellform
begin

```

Lifting for properties that only involve thread-local state information and the shared memory.

### definition

```

ts-ok :: ('t  $\Rightarrow$  'x  $\Rightarrow$  'm  $\Rightarrow$  bool)  $\Rightarrow$  ('l, 't, 'x) thread-info  $\Rightarrow$  'm  $\Rightarrow$  bool
where
 $\bigwedge ln. ts-ok P ts m \equiv \forall t. case (ts t) of None \Rightarrow True \mid \lfloor (x, ln) \rfloor \Rightarrow P t x m$ 

```

### lemma ts-okI:

```

 $\llbracket \bigwedge t x ln. ts t = \lfloor (x, ln) \rfloor \implies P t x m \rrbracket \implies ts-ok P ts m$ 
by(auto simp add: ts-ok-def)

```

### lemma ts-okE:

```

 $\llbracket ts-ok P ts m; \llbracket \bigwedge t x ln. ts t = \lfloor (x, ln) \rfloor \implies P t x m \rrbracket \implies Q \rrbracket \implies Q$ 
by(auto simp add: ts-ok-def)

```

### lemma ts-okD:

```

 $\bigwedge ln. \llbracket ts-ok P ts m; ts t = \lfloor (x, ln) \rfloor \rrbracket \implies P t x m$ 
by(auto simp add: ts-ok-def)

```



**lemma** *ts-ok-True* [simp]:  
 $ts\text{-}ok (\lambda t\ m\ x.\ True)\ ts\ m$   
**by**(*auto intro: ts-okI*)

**lemma** *ts-ok-conj*:  
 $ts\text{-}ok (\lambda t\ x\ m.\ P\ t\ x\ m \wedge Q\ t\ x\ m) = (\lambda ts\ m.\ ts\text{-}ok\ P\ ts\ m \wedge ts\text{-}ok\ Q\ ts\ m)$   
**by**(*auto intro: ts-okI intro!: ext dest: ts-okD*)

**lemma** *ts-ok-mono*:  
 $\llbracket ts\text{-}ok\ P\ ts\ m; \bigwedge t\ x.\ P\ t\ x\ m \implies Q\ t\ x\ m \rrbracket \implies ts\text{-}ok\ Q\ ts\ m$   
**by**(*auto intro!: ts-okI dest: ts-okD*)

Lifting for properites, that also require additional data that does not change during execution

**definition**

$ts\text{-}inv :: ('i \Rightarrow 't \Rightarrow 'x \Rightarrow 'm \Rightarrow bool) \Rightarrow ('t \multimap 'i) \Rightarrow ('l, 't, 'x)\ thread\text{-}info \Rightarrow 'm \Rightarrow bool$

**where**

$\bigwedge ln.\ ts\text{-}inv\ P\ I\ ts\ m \equiv \forall t.\ case\ (ts\ t)\ of\ None \Rightarrow True \mid \lfloor (x, ln) \rfloor \Rightarrow \exists i.\ I\ t = \lfloor i \rfloor \wedge P\ i\ t\ x\ m$

**lemma** *ts-invI*:  
 $\llbracket \bigwedge t\ x\ ln.\ ts\ t = \lfloor (x, ln) \rfloor \implies \exists i.\ I\ t = \lfloor i \rfloor \wedge P\ i\ t\ x\ m \rrbracket \implies ts\text{-}inv\ P\ I\ ts\ m$   
**by**(*simp add: ts-inv-def*)

**lemma** *ts-invE*:  
 $\llbracket ts\text{-}inv\ P\ I\ ts\ m; \forall t\ x\ ln.\ ts\ t = \lfloor (x, ln) \rfloor \longrightarrow (\exists i.\ I\ t = \lfloor i \rfloor \wedge P\ i\ t\ x\ m) \implies R \rrbracket \implies R$   
**by**(*auto simp add: ts-inv-def*)

**lemma** *ts-invD*:  
 $\bigwedge ln.\ \llbracket ts\text{-}inv\ P\ I\ ts\ m; ts\ t = \lfloor (x, ln) \rfloor \rrbracket \implies \exists i.\ I\ t = \lfloor i \rfloor \wedge P\ i\ t\ x\ m$   
**by**(*auto simp add: ts-inv-def*)

Wellformedness properties for lifting

**definition**

$ts\text{-}inv\text{-}ok :: ('l, 't, 'x)\ thread\text{-}info \Rightarrow ('t \multimap 'i) \Rightarrow bool$

**where**

$ts\text{-}inv\text{-}ok\ ts\ I \equiv \forall t.\ ts\ t = None \longleftrightarrow I\ t = None$

**lemma** *ts-inv-okI*:  
 $(\bigwedge t.\ ts\ t = None \longleftrightarrow I\ t = None) \implies ts\text{-}inv\text{-}ok\ ts\ I$   
**by**(*clarsimp simp add: ts-inv-ok-def*)

**lemma** *ts-inv-okI2*:  
 $(\bigwedge t.\ (\exists v.\ ts\ t = \lfloor v \rfloor) \longleftrightarrow (\exists v.\ I\ t = \lfloor v \rfloor)) \implies ts\text{-}inv\text{-}ok\ ts\ I$   
**by**(*force simp add: ts-inv-ok-def*)

**lemma** *ts-inv-okE*:  
 $\llbracket ts\text{-}inv\text{-}ok\ ts\ I; \forall t.\ ts\ t = None \longleftrightarrow I\ t = None \implies P \rrbracket \implies P$   
**by**(*force simp add: ts-inv-ok-def*)

**lemma** *ts-inv-okE2*:  
 $\llbracket ts\text{-}inv\text{-}ok\ ts\ I; \forall t.\ (\exists v.\ ts\ t = \lfloor v \rfloor) \longleftrightarrow (\exists v.\ I\ t = \lfloor v \rfloor) \implies P \rrbracket \implies P$   
**by**(*force simp add: ts-inv-ok-def*)

**lemma** *ts-inv-okD*:

$ts\text{-}inv\text{-}ok\ ts\ I \implies (ts\ t = None) \longleftrightarrow (I\ t = None)$   
**by**(*erule ts-inv-okE, blast*)

**lemma** *ts-inv-okD2*:

$ts\text{-}inv\text{-}ok\ ts\ I \implies (\exists v. ts\ t = \lfloor v \rfloor) \longleftrightarrow (\exists v. I\ t = \lfloor v \rfloor)$   
**by**(*erule ts-inv-okE2, blast*)

**lemma** *ts-inv-ok-conv-dom-eq*:

$ts\text{-}inv\text{-}ok\ ts\ I \longleftrightarrow (dom\ ts = dom\ I)$

**proof** –

**have**  $ts\text{-}inv\text{-}ok\ ts\ I \longleftrightarrow (\forall t. ts\ t = None \longleftrightarrow I\ t = None)$

**unfolding** *ts-inv-ok-def* **by** *blast*

**also have**  $\dots \longleftrightarrow (\forall t. t \in -\ dom\ ts \longleftrightarrow t \in -\ dom\ I)$  **by**(*force*)

**also have**  $\dots \longleftrightarrow dom\ ts = dom\ I$  **by** *auto*

**finally show** *?thesis* .

**qed**

**lemma** *ts-inv-ok-upd-ts*:

$\llbracket ts\ t = \lfloor x \rfloor; ts\text{-}inv\text{-}ok\ ts\ I \rrbracket \implies ts\text{-}inv\text{-}ok\ (ts(t \mapsto x'))\ I$   
**by**(*auto dest!: ts-inv-okD intro!: ts-inv-okI split: if-splits*)

**lemma** *ts-inv-upd-map-option*:

**assumes**  $ts\text{-}inv\ P\ I\ ts\ m$

**and**  $\bigwedge x\ ln. ts\ t = \lfloor (x, ln) \rfloor \implies P\ (the\ (I\ t))\ t\ (fst\ (f\ (x, ln)))\ m$

**shows**  $ts\text{-}inv\ P\ I\ (ts(t := (map\ option\ f\ (ts\ t))))\ m$

**using** *assms*

**by**(*fastforce intro!: ts-invI split: if-split-asm dest: ts-invD*)

**fun** *upd-inv* ::  $('t \rightarrow 'i) \Rightarrow ('i \Rightarrow 't \Rightarrow 'x \Rightarrow 'm \Rightarrow bool) \Rightarrow ('t, 'x, 'm)\ new\text{-}thread\text{-}action \Rightarrow ('t \rightarrow 'i)$   
**where**

$upd\text{-}inv\ I\ P\ (NewThread\ t\ x\ m) = I(t \mapsto SOME\ i. P\ i\ t\ x\ m)$

|  $upd\text{-}inv\ I\ P\ - = I$

**fun** *upd-invs* ::  $('t \rightarrow 'i) \Rightarrow ('i \Rightarrow 't \Rightarrow 'x \Rightarrow 'm \Rightarrow bool) \Rightarrow ('t, 'x, 'm)\ new\text{-}thread\text{-}action\ list \Rightarrow ('t \rightarrow 'i)$

**where**

$upd\text{-}invs\ I\ P\ [] = I$

|  $upd\text{-}invs\ I\ P\ (ta \# tas) = upd\text{-}invs\ (upd\text{-}inv\ I\ P\ ta)\ P\ tas$

**lemma** *upd-invs-append [simp]*:

$upd\text{-}invs\ I\ P\ (xs\ @\ ys) = upd\text{-}invs\ (upd\text{-}invs\ I\ P\ xs)\ P\ ys$

**by**(*induct xs arbitrary: I*)(*auto*)

**lemma** *ts-inv-ok-upd-inv'*:

$ts\text{-}inv\text{-}ok\ ts\ I \implies ts\text{-}inv\text{-}ok\ (redT\text{-}updT'\ ts\ ta)\ (upd\text{-}inv\ I\ P\ ta)$

**by**(*cases ta*)(*auto intro!: ts-inv-okI elim: ts-inv-okD del: iffI*)

**lemma** *ts-inv-ok-upd-invs'*:

$ts\text{-}inv\text{-}ok\ ts\ I \implies ts\text{-}inv\text{-}ok\ (redT\text{-}updTs'\ ts\ tas)\ (upd\text{-}invs\ I\ P\ tas)$

**proof**(*induct tas arbitrary: ts I*)

**case** *Nil* **thus** *?case* **by** *simp*

**next**

**case** (*Cons TA TAS TS I*)

**note**  $IH = \langle \bigwedge ts\ I. ts\text{-}inv\text{-}ok\ ts\ I \implies ts\text{-}inv\text{-}ok\ (redT\text{-}updTs'\ ts\ TAS)\ (upd\text{-}invs\ I\ P\ TAS) \rangle$

**note**  $esok = \langle ts\text{-}inv\text{-}ok\ TS\ I \rangle$   
**from**  $esok$  **have**  $ts\text{-}inv\text{-}ok\ (redT\text{-}updT'\ TS\ TA)\ (upd\text{-}inv\ I\ P\ TA)$   
**by**  $-(rule\ ts\text{-}inv\text{-}ok\text{-}upd\text{-}inv')$   
**hence**  $ts\text{-}inv\text{-}ok\ (redT\text{-}updTs'\ (redT\text{-}updT'\ TS\ TA)\ TAS)\ (upd\text{-}invs\ (upd\text{-}inv\ I\ P\ TA)\ P\ TAS)$   
**by**  $(rule\ IH)$   
**thus**  $?case$  **by**  $simp$   
**qed**

**lemma**  $ts\text{-}inv\text{-}ok\text{-}upd\text{-}inv$ :  
 $ts\text{-}inv\text{-}ok\ ts\ I \implies ts\text{-}inv\text{-}ok\ (redT\text{-}updT\ ts\ ta)\ (upd\text{-}inv\ I\ P\ ta)$   
**apply** $(cases\ ta)$   
**apply** $(auto\ intro!:\ ts\text{-}inv\text{-}okI\ elim:\ ts\text{-}inv\text{-}okD\ del:\ iffI)$   
**done**

**lemma**  $ts\text{-}inv\text{-}ok\text{-}upd\text{-}invs$ :  
 $ts\text{-}inv\text{-}ok\ ts\ I \implies ts\text{-}inv\text{-}ok\ (redT\text{-}updTs\ ts\ tas)\ (upd\text{-}invs\ I\ P\ tas)$   
**proof** $(induct\ tas\ arbitrary:\ ts\ I)$   
**case**  $Nil$  **thus**  $?case$  **by**  $simp$   
**next**  
**case**  $(Cons\ TA\ TAS\ TS\ I)$   
**note**  $IH = \langle \bigwedge ts\ I. ts\text{-}inv\text{-}ok\ ts\ I \implies ts\text{-}inv\text{-}ok\ (redT\text{-}updTs\ ts\ TAS)\ (upd\text{-}invs\ I\ P\ TAS) \rangle$   
**note**  $esok = \langle ts\text{-}inv\text{-}ok\ TS\ I \rangle$   
**from**  $esok$  **have**  $ts\text{-}inv\text{-}ok\ (redT\text{-}updT\ TS\ TA)\ (upd\text{-}inv\ I\ P\ TA)$   
**by**  $-(rule\ ts\text{-}inv\text{-}ok\text{-}upd\text{-}inv)$   
**hence**  $ts\text{-}inv\text{-}ok\ (redT\text{-}updTs\ (redT\text{-}updT\ TS\ TA)\ TAS)\ (upd\text{-}invs\ (upd\text{-}inv\ I\ P\ TA)\ P\ TAS)$   
**by**  $(rule\ IH)$   
**thus**  $?case$  **by**  $simp$   
**qed**

**lemma**  $ts\text{-}inv\text{-}ok\text{-}inv\text{-}ext\text{-}upd\text{-}inv$ :  
 $\llbracket ts\text{-}inv\text{-}ok\ ts\ I; thread\text{-}ok\ ts\ ta \rrbracket \implies I \subseteq_m upd\text{-}inv\ I\ P\ ta$   
**by** $(cases\ ta)(auto\ intro!:\ map\text{-}le\text{-}same\text{-}upd\ dest:\ ts\text{-}inv\text{-}okD)$

**lemma**  $ts\text{-}inv\text{-}ok\text{-}inv\text{-}ext\text{-}upd\text{-}invs$ :  
 $\llbracket ts\text{-}inv\text{-}ok\ ts\ I; thread\text{-}oks\ ts\ tas \rrbracket$   
 $\implies I \subseteq_m upd\text{-}invs\ I\ P\ tas$   
**proof** $(induct\ tas\ arbitrary:\ ts\ I)$   
**case**  $Nil$  **thus**  $?case$  **by**  $simp$   
**next**  
**case**  $(Cons\ TA\ TAS\ TS\ I)$   
**note**  $IH = \langle \bigwedge ts\ I. \llbracket ts\text{-}inv\text{-}ok\ ts\ I; thread\text{-}oks\ ts\ TAS \rrbracket \implies I \subseteq_m upd\text{-}invs\ I\ P\ TAS \rangle$   
**note**  $esinv = \langle ts\text{-}inv\text{-}ok\ TS\ I \rangle$   
**note**  $cct = \langle thread\text{-}oks\ TS\ (TA\ \# \ TAS) \rangle$   
**from**  $esinv\ cct$  **have**  $I \subseteq_m upd\text{-}inv\ I\ P\ TA$   
**by** $(auto\ intro:\ ts\text{-}inv\text{-}ok\text{-}inv\text{-}ext\text{-}upd\text{-}inv)$   
**also from**  $esinv\ cct$  **have**  $ts\text{-}inv\text{-}ok\ (redT\text{-}updT'\ TS\ TA)\ (upd\text{-}inv\ I\ P\ TA)$   
**by** $(auto\ intro:\ ts\text{-}inv\text{-}ok\text{-}upd\text{-}inv')$   
**with**  $cct$  **have**  $upd\text{-}inv\ I\ P\ TA \subseteq_m upd\text{-}invs\ (upd\text{-}inv\ I\ P\ TA)\ P\ TAS$   
**by** $(auto\ intro:\ IH)$   
**finally show**  $?case$  **by**  $simp$   
**qed**

**lemma**  $upd\text{-}invs\text{-}Some$ :  
 $\llbracket thread\text{-}oks\ ts\ tas; I\ t = \lfloor i \rfloor; ts\ t = \lfloor x \rfloor \rrbracket \implies upd\text{-}invs\ I\ Q\ tas\ t = \lfloor i \rfloor$

```

proof(induct tas arbitrary: ts I)
  case Nil thus ?case by simp
next
  case (Cons TA TAS TS I)
  note  $IH = \langle \bigwedge ts I. \llbracket thread\text{-}oks\ ts\ TAS; I\ t = \lfloor i \rfloor; ts\ t = \lfloor x \rfloor \rrbracket \implies upd\text{-}invs\ I\ Q\ TAS\ t = \lfloor i \rfloor \rangle$ 
  note  $cct = \langle thread\text{-}oks\ TS\ (TA \# TAS) \rangle$ 
  note  $it = \langle I\ t = \lfloor i \rfloor \rangle$ 
  note  $est = \langle TS\ t = \lfloor x \rfloor \rangle$ 
  from cct have cctta: thread-ok TS TA
    and ccttas: thread-oks (redT-updT' TS TA) TAS by auto
  from cctta it est have upd-inv I Q TA t = ⌊i⌋
    by(cases TA, auto)
  moreover
  have redT-updT' TS TA t = ⌊x⌋ using cctta est
    by - (rule redT-updT'-Some)
  ultimately have upd-invs (upd-inv I Q TA) Q TAS t = ⌊i⌋ using ccttas
    by -(erule IH)
  thus ?case by simp
qed

```

**lemma** *upd-inv-Some-eq*:

```

 $\llbracket thread\text{-}ok\ ts\ ta; ts\ t = \lfloor x \rfloor \rrbracket \implies upd\text{-}inv\ I\ Q\ ta\ t = I\ t$ 
by(cases ta, auto)

```

**lemma** *upd-invs-Some-eq*:  $\llbracket thread\text{-}oks\ ts\ tas; ts\ t = \lfloor x \rfloor \rrbracket \implies upd\text{-}invs\ I\ Q\ tas\ t = I\ t$

```

proof(induct tas arbitrary: ts I)
  case Nil thus ?case by simp
next
  case (Cons TA TAS TS I)
  note  $IH = \langle \bigwedge ts I. \llbracket thread\text{-}oks\ ts\ TAS; ts\ t = \lfloor x \rfloor \rrbracket \implies upd\text{-}invs\ I\ Q\ TAS\ t = I\ t \rangle$ 
  note  $cct = \langle thread\text{-}oks\ TS\ (TA \# TAS) \rangle$ 
  note  $est = \langle TS\ t = \lfloor x \rfloor \rangle$ 
  from cct est have upd-invs (upd-inv I Q TA) Q TAS t = upd-inv I Q TA t
    apply(clarsimp)
    apply(erule IH)
    by(rule redT-updT'-Some)
  also from cct est have  $\dots = I\ t$ 
    by(auto elim: upd-inv-Some-eq)
  finally show ?case by simp
qed

```

**lemma** *SOME-new-thread-upd-invs*:

```

assumes Qsome:  $Q\ (SOME\ i. Q\ i\ t\ x\ m)\ t\ x\ m$ 
and nt: NewThread t x m ∈ set tas
and cct: thread-oks ts tas
shows  $\exists i. upd\text{-}invs\ I\ Q\ tas\ t = \lfloor i \rfloor \wedge Q\ i\ t\ x\ m$ 
proof(rule exI[where x=SOME i. Q i t x m])
  from nt cct have upd-invs I Q tas t = ⌊SOME i. Q i t x m⌋
  proof(induct tas arbitrary: ts I)
    case Nil thus ?case by simp
  next
    case (Cons TA TAS TS I)
    note  $IH = \langle \bigwedge ts I. \llbracket NewThread\ t\ x\ m \in set\ TAS; thread\text{-}oks\ ts\ TAS \rrbracket \implies upd\text{-}invs\ I\ Q\ TAS\ t = \lfloor SOME\ i. Q\ i\ t\ x\ m \rfloor \rangle$ 

```

```

note  $nt = \langle \text{NewThread } t \ x \ m \in \text{set } (TA \ \# \ TAS) \rangle$ 
note  $cct = \langle \text{thread-oks } TS \ (TA \ \# \ TAS) \rangle$ 
{ assume  $nt'$ :  $\text{NewThread } t \ x \ m \in \text{set } TAS$ 
  from  $cct$  have  $?case$ 
    apply( $clarsimp$ )
    by( $\text{rule } IH[OF \ nt']$ ) }
moreover
{ assume  $ta$ :  $TA = \text{NewThread } t \ x \ m$ 
  with  $cct$  have  $rup$ :  $\text{redT-updT}' \ TS \ TA \ t = \lfloor (\text{undefined}, \text{no-wait-locks}) \rfloor$ 
    by( $simp$ )
  from  $cct$  have  $cctta$ :  $\text{thread-oks } (\text{redT-updT}' \ TS \ TA) \ TAS$  by  $simp$ 
  from  $ta$  have  $upd\text{-}inv \ I \ Q \ TA \ t = \lfloor \text{SOME } i. \ Q \ i \ t \ x \ m \rfloor$ 
    by( $simp$ )
  hence  $?case$ 
    by( $clarsimp \ simp \ add$ :  $\text{upd-invs-Some-eq}[OF \ cctta, \ OF \ rup]$ ) }
  ultimately show  $?case$  using  $nt$  by  $auto$ 
qed
with  $Q_{some}$  show  $\text{upd-invs } I \ Q \ tas \ t = \lfloor \text{SOME } i. \ Q \ i \ t \ x \ m \rfloor \wedge Q \ (\text{SOME } i. \ Q \ i \ t \ x \ m) \ t \ x \ m$ 
  by( $simp$ )
qed

```

**lemma**  $ts\text{-ok-into-ts-inv-const}$ :

**assumes**  $ts\text{-ok } P \ ts \ m$

**obtains**  $I$  **where**  $ts\text{-inv } (\lambda\cdot. \ P) \ I \ ts \ m$

**proof** –

**from**  $assms$  **have**  $ts\text{-inv } (\lambda\cdot. \ P) \ (\lambda t. \ \text{if } t \in \text{dom } ts \text{ then } \text{Some } \text{undefined} \text{ else } \text{None}) \ ts \ m$

**by**( $\text{auto intro!} \ ts\text{-invI dest: } ts\text{-okD}$ )

**thus thesis** **by**( $\text{rule that}$ )

**qed**

**lemma**  $ts\text{-inv-const-into-ts-ok}$ :

$ts\text{-inv } (\lambda\cdot. \ P) \ I \ ts \ m \implies ts\text{-ok } P \ ts \ m$

**by**( $\text{auto intro!} \ ts\text{-okI dest: } ts\text{-invD}$ )

**lemma**  $ts\text{-inv-into-ts-ok-Ex}$ :

$ts\text{-inv } Q \ I \ ts \ m \implies ts\text{-ok } (\lambda t \ x \ m. \ \exists i. \ Q \ i \ t \ x \ m) \ ts \ m$

**by**( $\text{rule } ts\text{-okI}(\text{blast dest: } ts\text{-invD})$ )

**lemma**  $ts\text{-ok-Ex-into-ts-inv}$ :

$ts\text{-ok } (\lambda t \ x \ m. \ \exists i. \ Q \ i \ t \ x \ m) \ ts \ m \implies \exists I. \ ts\text{-inv } Q \ I \ ts \ m$

**by**( $\text{rule } exI[\text{where } x=\lambda t. \ \lfloor \text{SOME } i. \ Q \ i \ t \ (\text{fst } (\text{the } (ts \ t))) \ m \rfloor](\text{auto } 4 \ 4 \text{ dest: } ts\text{-okD intro: someI intro: } ts\text{-invI})$ )

**lemma**  $Ex\text{-ts-inv-conv-ts-ok}$ :

$(\exists I. \ ts\text{-inv } Q \ I \ ts \ m) \longleftrightarrow (ts\text{-ok } (\lambda t \ x \ m. \ \exists i. \ Q \ i \ t \ x \ m) \ ts \ m)$

**by**( $\text{auto dest: } ts\text{-inv-into-ts-ok-Ex } ts\text{-ok-Ex-into-ts-inv}$ )

**end**

## 1.15 Labelled transition systems

**theory**  $LTS$

**imports**

../Basic/Auxiliary  
Coinductive.TLList

**begin**

**no-notation** *floor* ( $\lfloor \_ \rfloor$ )

**lemma** *rel-option-mono*:

$\llbracket \text{rel-option } R \ x \ y; \bigwedge x \ y. R \ x \ y \implies R' \ x \ y \rrbracket \implies \text{rel-option } R' \ x \ y$   
**by**(*cases* *x*)(*case-tac* [*!*] *y*, *auto*)

**lemma** *nth-concat-conv*:

$n < \text{length } (\text{concat } xss)$   
 $\implies \exists m \ n'. \text{concat } xss ! n = (xss ! m) ! n' \wedge n' < \text{length } (xss ! m) \wedge$   
 $m < \text{length } xss \wedge n = (\sum i < m. \text{length } (xss ! i)) + n'$

**using** *lnth-lconcat-conv*[*of* *n* *l*list-of (*map* *l*list-of *xss*)]  
*sum-comp-morphism*[**where** *h* = *enat* **and** *g* =  $\lambda i. \text{length } (xss ! i)$ ]  
**by**(*clarsimp* *simp* *add*: *lconcat-l*list-of *zero-enat-def*[*symmetric*]) *blast*

**definition** *flip* ::  $('a \Rightarrow 'b \Rightarrow 'c) \Rightarrow 'b \Rightarrow 'a \Rightarrow 'c$

**where** *flip* *f* =  $(\lambda b \ a. f \ a \ b)$

Create a dynamic list *flip-simps* of theorems for *flip*

**ML** <

*structure* *FlipSimpRules* = *Named-Thms*

(  
  *val* *name* = @{*binding* *flip-simps*}  
  *val* *description* = *Simplification rules for flip in bisimulations*  
)  
>

**setup** <*FlipSimpRules.setup*>

**lemma** *flip-conv* [*flip-simps*]: *flip* *f* *b* *a* = *f* *a* *b*

**by**(*simp* *add*: *flip-def*)

**lemma** *flip-flip* [*flip-simps*, *simp*]: *flip* (*flip* *f*) = *f*

**by**(*simp* *add*: *flip-def*)

**lemma** *list-all2-flip* [*flip-simps*]: *list-all2* (*flip* *P*) *xs* *ys* = *list-all2* *P* *ys* *xs*

**unfolding** *flip-def* *list-all2-conv-all-nth* **by** *auto*

**lemma** *l*list-all2-flip [*flip-simps*]: *l*list-all2 (*flip* *P*) *xs* *ys* = *l*list-all2 *P* *ys* *xs*

**unfolding** *flip-def* *l*list-all2-conv-all-lnth **by** *auto*

**lemma** *rtranclp-flipD*:

**assumes**  $(\text{flip } r)^{\hat{**}} \ x \ y$

**shows**  $r^{\hat{**}} \ y \ x$

**using** *assms*

**by**(*induct* *rule*: *rtranclp-induct*)(*auto* *intro*: *rtranclp.rtrancl-into-rtrancl* *simp* *add*: *flip-conv*)

**lemma** *rtranclp-flip* [*flip-simps*]:

$(\text{flip } r)^{\hat{**}} = \text{flip } r^{\hat{**}}$

**by**(*auto* *intro*!: *ext* *simp* *add*: *flip-conv* *intro*: *rtranclp-flipD*)

**lemma** *rel-prod-flip* [*flip-simps*]:  
 $rel\_prod (flip R) (flip S) = flip (rel\_prod R S)$   
**by**(*auto intro!*: *ext simp add: flip-def*)

**lemma** *rel-option-flip* [*flip-simps*]:  
 $rel\_option (flip R) = flip (rel\_option R)$   
**by**(*simp add: fun-eq-iff rel-option-iff flip-def*)

**lemma** *tllist-all2-flip* [*flip-simps*]:  
 $tllist\_all2 (flip P) (flip Q) xs\ ys \longleftrightarrow tllist\_all2 P Q ys\ xs$   
**proof**  
**assume**  $tllist\_all2 (flip P) (flip Q) xs\ ys$   
**thus**  $tllist\_all2 P Q ys\ xs$   
**by**(*coinduct rule: tllist-all2-coinduct*)(*auto dest: tllist-all2-is-TNilD tllist-all2-tfinite2-terminalD tllist-all2-thdD intro: tllist-all2-ttlI simp add: flip-def*)  
**next**  
**assume**  $tllist\_all2 P Q ys\ xs$   
**thus**  $tllist\_all2 (flip P) (flip Q) xs\ ys$   
**by**(*coinduct rule: tllist-all2-coinduct*)(*auto dest: tllist-all2-is-TNilD tllist-all2-tfinite2-terminalD tllist-all2-thdD intro: tllist-all2-ttlI simp add: flip-def*)  
**qed**

### 1.15.1 Labelled transition systems

**type-synonym** (*'a*, *'b*) *trsys* = *'a*  $\Rightarrow$  *'b*  $\Rightarrow$  *'a*  $\Rightarrow$  *bool*

**locale** *trsys* =  
**fixes** *trsys* :: (*'s*, *'tl*) *trsys* (*-*  $\dashrightarrow$  *-* [*50*, *0*, *50*] *60*)  
**begin**

**abbreviation** *Trsys* :: (*'s*, *'tl list*) *trsys* (*-*  $\dashrightarrow^*$  *-* [*50*,*0*,*50*] *60*)  
**where**  $\bigwedge tl. s \dashrightarrow^* s' \equiv rtrancl3p\ trsys\ s\ tl\ s'$

**coinductive** *inf-step* :: *'s*  $\Rightarrow$  *'tl llist*  $\Rightarrow$  *bool* (*-*  $\dashrightarrow^*$   $\infty$  [*50*, *0*] *80*)  
**where** *inf-stepI*:  $\llbracket trsys\ a\ b\ a';\ a' \dashrightarrow^* \infty \rrbracket \Longrightarrow a \dashrightarrow^* \infty$

**coinductive** *inf-step-table* :: *'s*  $\Rightarrow$  (*'s*  $\times$  *'tl*  $\times$  *'s*) *llist*  $\Rightarrow$  *bool* (*-*  $\dashrightarrow^*$  *t*  $\infty$  [*50*, *0*] *80*)  
**where**

*inf-step-tableI*:  
 $\bigwedge tl. \llbracket trsys\ s\ tl\ s';\ s' \dashrightarrow^* t \infty \rrbracket$   
 $\Longrightarrow s \dashrightarrow^* LCons\ (s,\ tl,\ s')\ stls \dashrightarrow^* t \infty$

**definition** *inf-step2inf-step-table* :: *'s*  $\Rightarrow$  *'tl llist*  $\Rightarrow$  (*'s*  $\times$  *'tl*  $\times$  *'s*) *llist*  
**where**

*inf-step2inf-step-table* *s* *tls* =  
*unfold-llist*  
 $(\lambda(s,\ tls). lnull\ tls)$   
 $(\lambda(s,\ tls). (s,\ lhd\ tls,\ SOME\ s'.\ trsys\ s\ (lhd\ tls)\ s' \wedge s' \dashrightarrow^* \infty))$   
 $(\lambda(s,\ tls). (SOME\ s'.\ trsys\ s\ (lhd\ tls)\ s' \wedge s' \dashrightarrow^* \infty,\ ltl\ tls))$   
 $(s,\ tls)$

**coinductive** *Rtrancl3p* :: *'s*  $\Rightarrow$  (*'tl*, *'s*) *tllist*  $\Rightarrow$  *bool*

**where**  
*Rtrancl3p-stop*:  $(\bigwedge tl\ s'. \neg s \dashrightarrow tl\ s') \Longrightarrow Rtrancl3p\ s\ (TNil\ s)$

| *Rtranc3p-into-Rtranc3p*:  $\bigwedge tl. \llbracket s - tl \rightarrow s'; Rtranc3p\ s'\ tlss \rrbracket \implies Rtranc3p\ s\ (TCons\ tl\ tlss)$

**inductive-simps** *Rtranc3p-simps*:

*Rtranc3p* *s* (*TNil* *s'*)  
*Rtranc3p* *s* (*TCons* *tl'* *tlss*)

**inductive-cases** *Rtranc3p-cases*:

*Rtranc3p* *s* (*TNil* *s'*)  
*Rtranc3p* *s* (*TCons* *tl'* *tlss*)

**coinductive** *Runs* :: '*s*  $\Rightarrow$  'tl llist  $\Rightarrow$  bool

**where**

*Stuck*:  $(\bigwedge tl\ s'. \neg s - tl \rightarrow s') \implies Runs\ s\ LNil$   
| *Step*:  $\bigwedge tl. \llbracket s - tl \rightarrow s'; Runs\ s'\ tls \rrbracket \implies Runs\ s\ (LCons\ tl\ tls)$

**coinductive** *Runs-table* :: '*s*  $\Rightarrow$  ('s  $\times$  'tl  $\times$  's) llist  $\Rightarrow$  bool

**where**

*Stuck*:  $(\bigwedge tl\ s'. \neg s - tl \rightarrow s') \implies Runs-table\ s\ LNil$   
| *Step*:  $\bigwedge tl. \llbracket s - tl \rightarrow s'; Runs-table\ s'\ stlss \rrbracket \implies Runs-table\ s\ (LCons\ (s, tl, s')\ stlss)$

**inductive-simps** *Runs-table-simps*:

*Runs-table* *s* *LNil*  
*Runs-table* *s* (*LCons* *stls* *stlss*)

**lemma** *inf-step-not-finite-llist*:

**assumes** *r*: *s*  $\rightarrow^* \infty$

**shows**  $\neg lfinite\ bs$

**proof**

**assume** *lfinite* *bs* **thus** *False* **using** *r*

**by**(*induct* *arbitrary*: *s* *rule*: *lfinite.induct*)(*auto* *elim*: *inf-step.cases*)

**qed**

**lemma** *inf-step2inf-step-table-LNil* [*simp*]: *inf-step2inf-step-table* *s* *LNil* = *LNil*

**by**(*simp* *add*: *inf-step2inf-step-table-def*)

**lemma** *inf-step2inf-step-table-LCons* [*simp*]:

**fixes** *tl* **shows**

*inf-step2inf-step-table* *s* (*LCons* *tl* *tls*) =  
*LCons* (*s*, *tl*, *SOME* *s'*. *trsys* *s* *tl* *s'*  $\wedge$  *s'*  $\rightarrow^* \infty$ )  
(*inf-step2inf-step-table* (*SOME* *s'*. *trsys* *s* *tl* *s'*  $\wedge$  *s'*  $\rightarrow^* \infty$ ) *tls*)

**by**(*simp* *add*: *inf-step2inf-step-table-def*)

**lemma** *lnull-inf-step2inf-step-table* [*simp*]:

*lnull* (*inf-step2inf-step-table* *s* *tls*)  $\longleftrightarrow$  *lnull* *tls*

**by**(*simp* *add*: *inf-step2inf-step-table-def*)

**lemma** *inf-step2inf-step-table-eq-LNil*:

*inf-step2inf-step-table* *s* *tls* = *LNil*  $\longleftrightarrow$  *tls* = *LNil*

**using** *lnull-inf-step2inf-step-table* **unfolding** *lnull-def* .

**lemma** *lhd-inf-step2inf-step-table* [*simp*]:

$\neg$  *lnull* *tls*

$\implies$  *lhd* (*inf-step2inf-step-table* *s* *tls*) =

(*s*, *lhd* *tls*, *SOME* *s'*. *trsys* *s* (*lhd* *tls*) *s'*  $\wedge$  *s'*  $\rightarrow^* \infty$ )



by(*simp add: inf-step2inf-step-table-def*)

**lemma** *ltl-inf-step2inf-step-table* [*simp*]:

*ltl (inf-step2inf-step-table s tls) =  
inf-step2inf-step-table (SOME s'. trsys s (lhd tls) s'  $\wedge$  s'  $\rightarrow$  tls  $\rightarrow^*$   $\infty$ ) (ltl tls)*

by(*cases tls simp-all*)

**lemma** *lmap-inf-step2inf-step-table*: *lmap (fst  $\circ$  snd) (inf-step2inf-step-table s tls) = tls*

by(*coinduction arbitrary: s tls auto*)

**lemma** *inf-step-imp-inf-step-table*:

**assumes** *s  $\rightarrow$  tls  $\rightarrow^*$   $\infty$*

**shows**  $\exists stls. s \rightarrow stls \rightarrow^* t \infty \wedge tls = lmap (fst \circ snd) stls$

**proof** –

**from** *assms* **have** *s  $\rightarrow$  inf-step2inf-step-table s tls  $\rightarrow^*$   $\infty$*

**proof**(*coinduction arbitrary: s tls*)

**case** (*inf-step-table s tls*)

**thus** *?case*

**proof** *cases*

**case** (*inf-stepI tl s' tls'*)

**let** *?s' = SOME s'. trsys s tl s'  $\wedge$  s'  $\rightarrow$  tls'  $\rightarrow^*$   $\infty$*

**have** *trsys s tl ?s'  $\wedge$  ?s'  $\rightarrow$  tls'  $\rightarrow^*$   $\infty$*  **by**(*rule someI*)(*blast intro: inf-stepI*)

**thus** *?thesis* **using**  $\langle tls = LCons tl tls' \rangle$  **by** *auto*

**qed**

**qed**

**moreover** **have** *tls = lmap (fst  $\circ$  snd) (inf-step2inf-step-table s tls)*

**by**(*simp only: lmap-inf-step2inf-step-table*)

**ultimately** **show** *?thesis* **by** *blast*

**qed**

**lemma** *inf-step-table-imp-inf-step*:

*s  $\rightarrow$  stls  $\rightarrow^*$   $\infty \implies s \rightarrow lmap (fst \circ snd) stls \rightarrow^*$   $\infty$*

**proof**(*coinduction arbitrary: s stls rule: inf-step.coinduct*)

**case** (*inf-step s tls*)

**thus** *?case* **by** *cases auto*

**qed**

**lemma** *Runs-table-into-Runs*:

*Runs-table s stlss  $\implies$  Runs s (lmap ( $\lambda(s, tl, s'). tl$ ) stlss)*

**proof**(*coinduction arbitrary: s stlss*)

**case** (*Runs s tls*)

**thus** *?case* **by** (*cases*)*auto*

**qed**

**lemma** *Runs-into-Runs-table*:

**assumes** *Runs s tls*

**obtains** *stlss*

**where** *tls = lmap ( $\lambda(s, tl, s'). tl$ ) stlss*

**and** *Runs-table s stlss*

**proof** –

**define** *stlss* **where** *stlss s tls = unfold-llist*

*( $\lambda(s, tls). lnull tls$ )*

*( $\lambda(s, tls). (s, lhd tls, SOME s'. s \rightarrow lhd tls \rightarrow s' \wedge Runs s' (ltl tls))$ )*

*( $\lambda(s, tls). (SOME s'. s \rightarrow lhd tls \rightarrow s' \wedge Runs s' (ltl tls), ltl tls)$ )*

```

  (s, tls)
  for s tls
  have [simp]:
     $\bigwedge s. \text{stlss } s \text{ LNil} = \text{LNil}$ 
     $\bigwedge s \text{ tl } \text{tls}. \text{stlss } s (\text{LCons } \text{tl } \text{tls}) = \text{LCons } (s, \text{tl}, \text{SOME } s'. s - \text{tl} \rightarrow s' \wedge \text{Runs } s' \text{ tls}) (\text{stlss } (\text{SOME } s'. s - \text{tl} \rightarrow s' \wedge \text{Runs } s' \text{ tls}) \text{ tls})$ 
     $\bigwedge s \text{ tls}. \text{lnull } (\text{stlss } s \text{ tls}) \longleftrightarrow \text{lnull } \text{tls}$ 
     $\bigwedge s \text{ tls}. \neg \text{lnull } \text{tls} \implies \text{lhs } (\text{stlss } s \text{ tls}) = (s, \text{lhs } \text{tls}, \text{SOME } s'. s - \text{lhs } \text{tls} \rightarrow s' \wedge \text{Runs } s' (\text{lhs } \text{tls}))$ 
     $\bigwedge s \text{ tls}. \neg \text{lnull } \text{tls} \implies \text{lhs } (\text{stlss } s \text{ tls}) = \text{stlss } (\text{SOME } s'. s - \text{lhs } \text{tls} \rightarrow s' \wedge \text{Runs } s' (\text{lhs } \text{tls})) (\text{lhs } \text{tls})$ 
    by (simp-all add: stlss-def)

  from assms have tls = lmap ( $\lambda(s, \text{tl}, s'). \text{tl}$ ) (stlss s tls)
  proof (coinduction arbitrary: s tls)
    case Eq-list
    thus ?case by cases (auto 4 3 intro: someI2)
  qed
  moreover
  from assms have Runs-table s (stlss s tls)
  proof (coinduction arbitrary: s tls)
    case (Runs-table s stlss')
    thus ?case
    proof (cases)
      case (Step s' tls' tl)
      let ?P =  $\lambda s'. s - \text{tl} \rightarrow s' \wedge \text{Runs } s' \text{ tls'}$ 
      from  $\langle s - \text{tl} \rightarrow s' \rangle \langle \text{Runs } s' \text{ tls'} \rangle$  have ?P s' ..
      hence ?P (Eps ?P) by (rule someI)
      with Step have ?Step by auto
      thus ?thesis ..
    qed simp
  qed
  ultimately show ?thesis by (rule that)
qed

lemma Runs-lappendE:
  assumes Runs  $\sigma$  (lappend tls tls')
  and lfinite tls
  obtains  $\sigma'$  where  $\sigma - \text{list-of } \text{tls} \rightarrow^* \sigma'$ 
  and Runs  $\sigma' \text{ tls'}$ 
proof (atomize-elim)
  from  $\langle \text{lfinite } \text{tls} \rangle \langle \text{Runs } \sigma (\text{lappend } \text{tls } \text{tls}') \rangle$ 
  show  $\exists \sigma'. \sigma - \text{list-of } \text{tls} \rightarrow^* \sigma' \wedge \text{Runs } \sigma' \text{ tls'}$ 
  proof (induct arbitrary:  $\sigma$ )
    case lfinite-LNil thus ?case by (auto)
  next
    case (lfinite-LConsI tls tl)
    from  $\langle \text{Runs } \sigma (\text{lappend } (\text{LCons } \text{tl } \text{tls}) \text{tls}') \rangle$ 
    show ?case unfolding lappend-code
    proof (cases)
      case (Step  $\sigma'$ )
      from  $\langle \text{Runs } \sigma' (\text{lappend } \text{tls } \text{tls}') \implies \exists \sigma''. \sigma' - \text{list-of } \text{tls} \rightarrow^* \sigma'' \wedge \text{Runs } \sigma'' \text{ tls'} \rangle \langle \text{Runs } \sigma' (\text{lappend } \text{tls } \text{tls}') \rangle$ 
      obtain  $\sigma''$  where  $\sigma' - \text{list-of } \text{tls} \rightarrow^* \sigma''$  Runs  $\sigma'' \text{ tls'}$  by blast
      from  $\langle \sigma - \text{tl} \rightarrow \sigma' \rangle \langle \sigma' - \text{list-of } \text{tls} \rightarrow^* \sigma'' \rangle$ 
      have  $\sigma - \text{tl} \# \text{list-of } \text{tls} \rightarrow^* \sigma''$  by (rule rtrancl3p-step-converse)
    qed
  qed

```

```

    with ⟨lfinite tls⟩ have  $\sigma \text{--list-of } (LCons\ tl\ tls) \rightarrow^* \sigma''$  by (simp)
    with ⟨Runs  $\sigma''\ tls'$ ⟩ show ?thesis by blast
qed
qed
qed

lemma Trsys-into-Runs:
  assumes  $s \text{--} tls \rightarrow^* s'$ 
  and  $Runs\ s'\ tls'$ 
  shows  $Runs\ s\ (lappend\ (l\text{list-of } tls)\ tls')$ 
using assms
by (induct rule: rtrancl3p-converse-induct) (auto intro: Runs.Step)

lemma rtrancl3p-into-Rtrancl3p:
   $\llbracket rtrancl3p\ trsys\ a\ bs\ a'; \bigwedge b\ a''. \neg a' \text{--} b \rightarrow a'' \rrbracket \implies Rtrancl3p\ a\ (tl\text{list-of-}l\text{list } a'\ (l\text{list-of } bs))$ 
  by (induct rule: rtrancl3p-converse-induct) (auto intro: Rtrancl3p.intros)

lemma Rtrancl3p-into-Runs:
   $Rtrancl3p\ s\ tlss \implies Runs\ s\ (l\text{list-of-}tl\text{list } tlss)$ 
by (coinduction arbitrary: s tlss rule: Runs.coinduct) (auto elim: Rtrancl3p.cases)

lemma Runs-into-Rtrancl3p:
  assumes  $Runs\ s\ tls$ 
  obtains  $tlss$  where  $tlss = l\text{list-of-}tl\text{list } tlss\ Rtrancl3p\ s\ tlss$ 
proof
  let ?Q =  $\lambda s\ tls\ s'. s \text{--} lhd\ tls \rightarrow s' \wedge Runs\ s'\ (l\text{tl } tls)$ 
  define tlss where  $tlss = corec\text{-}tl\text{list}$ 
    ( $\lambda (s, tls). lnull\ tls$ ) ( $\lambda (s, tls). s$ )
    ( $\lambda (s, tls). lhd\ tls$ )
    ( $\lambda \_. False$ ) undefined ( $\lambda (s, tls). (SOME\ s'. ?Q\ s\ tls\ s', l\text{tl } tls)$ )
  have [simp]:
     $tlss\ (s, LNil) = TNil\ s$ 
     $tlss\ (s, LCons\ tl\ tls) = TCons\ tl\ (tlss\ (SOME\ s'. ?Q\ s\ (LCons\ tl\ tls)\ s', tls))$ 
  for s tl tls by (auto simp add: tlss-def intro: tl\text{list}.expand)

  show  $tlss = l\text{list-of-}tl\text{list } (tlss\ (s, tls))$  using assms
  by (coinduction arbitrary: s tlss) (erule Runs.cases; fastforce intro: someI2)

  show  $Rtrancl3p\ s\ (tlss\ (s, tls))$  using assms
  by (coinduction arbitrary: s tlss) (erule Runs.cases; simp; iprover intro: someI2 [where Q = trsys - -]
    someI2 [where Q =  $\lambda s'. Runs\ s'\ -$ ])
qed

lemma fixes tl
  assumes  $Rtrancl3p\ s\ tlss\ tfinite\ tlss$ 
  shows  $Rtrancl3p\text{-into-}Trsys: Trsys\ s\ (l\text{list-of } (l\text{list-of-}tl\text{list } tlss))\ (terminal\ tlss)$ 
  and  $terminal\text{-}Rtrancl3p\text{-final}: \neg terminal\ tlss \text{--} tl \rightarrow s'$ 
using assms (2,1) by (induction arbitrary: s rule: tfinite-induct) (auto simp add: Rtrancl3p-simps intro:
  rtrancl3p-step-converse)

end

```

### 1.15.2 Labelled transition systems with internal actions

**locale**  $\tau trsys = trsys +$   
**constrains**  $trsys :: ('s, 'tl) trsys$   
**fixes**  $\tau move :: ('s, 'tl) trsys$   
**begin**

**inductive**  $silent-move :: 's \Rightarrow 's \Rightarrow bool \ (- \ -\tau \rightarrow - \ [50, 50] \ 60)$   
**where**  $[intro]: !!tl. \llbracket trsys \ s \ tl \ s'; \tau move \ s \ tl \ s' \rrbracket \Longrightarrow s \ -\tau \rightarrow s'$

**declare**  $silent-move.cases \ [elim]$

**lemma**  $silent-move-iff: silent-move = (\lambda s \ s'. (\exists tl. trsys \ s \ tl \ s' \wedge \tau move \ s \ tl \ s'))$   
**by**( $auto \ simp \ add: fun-eq-iff$ )

**abbreviation**  $silent-moves :: 's \Rightarrow 's \Rightarrow bool \ (- \ -\tau \rightarrow * \ - \ [50, 50] \ 60)$   
**where**  $silent-moves == silent-move \hat{\ } **$

**abbreviation**  $silent-movet :: 's \Rightarrow 's \Rightarrow bool \ (- \ -\tau \rightarrow + \ - \ [50, 50] \ 60)$   
**where**  $silent-movet == silent-move \hat{\ } ++$

**coinductive**  $\tau diverge :: 's \Rightarrow bool \ (- \ -\tau \rightarrow \infty \ [50] \ 60)$   
**where**  
 $\tau diverge I: \llbracket s \ -\tau \rightarrow s'; \ s' \ -\tau \rightarrow \infty \rrbracket \Longrightarrow s \ -\tau \rightarrow \infty$

**coinductive**  $\tau inf-step :: 's \Rightarrow 'tl \ llist \Rightarrow bool \ (- \ -\tau \dashrightarrow * \ \infty \ [50, 0] \ 60)$   
**where**  
 $\tau inf-step-Cons: \bigwedge tl. \llbracket s \ -\tau \rightarrow * \ s'; \ s' \ -tl \rightarrow s''; \neg \tau move \ s' \ tl \ s''; \ s'' \ -\tau \dashrightarrow * \ \infty \rrbracket \Longrightarrow s \ -\tau \dashrightarrow * \ LCons \ tl \ tls \rightarrow * \ \infty$   
 $\mid \tau inf-step-Nil: s \ -\tau \rightarrow \infty \Longrightarrow s \ -\tau \dashrightarrow * \ LNil \rightarrow * \ \infty$

**coinductive**  $\tau inf-step-table :: 's \Rightarrow ('s \times 's \times 'tl \times 's) \ llist \Rightarrow bool \ (- \ -\tau \dashrightarrow * \ t \ \infty \ [50, 0] \ 80)$   
**where**  
 $\tau inf-step-table-Cons: \bigwedge tl. \llbracket s \ -\tau \rightarrow * \ s'; \ s' \ -tl \rightarrow s''; \neg \tau move \ s' \ tl \ s''; \ s'' \ -\tau \dashrightarrow * \ t \ \infty \rrbracket \Longrightarrow s \ -\tau \dashrightarrow * \ LCons \ (s, s', tl, s'') \ tls \rightarrow * \ t \ \infty$

$\mid \tau inf-step-table-Nil: s \ -\tau \rightarrow \infty \Longrightarrow s \ -\tau \dashrightarrow * \ LNil \rightarrow * \ t \ \infty$

**definition**  $\tau inf-step2\tau inf-step-table :: 's \Rightarrow 'tl \ llist \Rightarrow ('s \times 's \times 'tl \times 's) \ llist$   
**where**

$\tau inf-step2\tau inf-step-table \ s \ tls =$   
 $unfold-llist$   
 $(\lambda (s, tls). lnull \ tls)$   
 $(\lambda (s, tls). let (s', s'') = SOME \ (s', s''). \ s \ -\tau \rightarrow * \ s' \wedge s' \ -lhd \ tls \rightarrow s'' \wedge \neg \tau move \ s' \ (lhd \ tls) \ s'' \wedge$   
 $s'' \ -\tau \dashrightarrow * \ tls \rightarrow * \ \infty$   
 $in \ (s, s', lhd \ tls, s''))$   
 $(\lambda (s, tls). let (s', s'') = SOME \ (s', s''). \ s \ -\tau \rightarrow * \ s' \wedge s' \ -lhd \ tls \rightarrow s'' \wedge \neg \tau move \ s' \ (lhd \ tls) \ s'' \wedge$   
 $s'' \ -\tau \dashrightarrow * \ tls \rightarrow * \ \infty$   
 $in \ (s'', ltl \ tls))$   
 $(s, tls)$

**definition**  $silent-move-from :: 's \Rightarrow 's \Rightarrow 's \Rightarrow bool$

**where** *silent-move-from*  $s0\ s1\ s2 \longleftrightarrow \text{silent-moves } s0\ s1 \wedge \text{silent-move } s1\ s2$

**inductive**  $\tau\text{rtrancl3p} :: 's \Rightarrow 'tl\ list \Rightarrow 's \Rightarrow \text{bool} \ (-\ \neg\tau\text{---}\rightarrow* \ -\ [50, 0, 50]\ 60)$

**where**

$\tau\text{rtrancl3p-refl} : \tau\text{rtrancl3p } s \ []\ s$   
 $| \tau\text{rtrancl3p-step} : \bigwedge tl. \llbracket s -tl\rightarrow s'; \neg \tau\text{move } s\ tl\ s'; \tau\text{rtrancl3p } s'\ tl\ s'' \rrbracket \Longrightarrow \tau\text{rtrancl3p } s\ (tl \#\ tl\ s'')\ s''$   
 $| \tau\text{rtrancl3p-}\tau\text{step} : \bigwedge tl. \llbracket s -tl\rightarrow s'; \tau\text{move } s\ tl\ s'; \tau\text{rtrancl3p } s'\ tl\ s'' \rrbracket \Longrightarrow \tau\text{rtrancl3p } s\ tl\ s''$

**coinductive**  $\tau\text{Runs} :: 's \Rightarrow ('tl, 's\ \text{option})\ tl\text{list} \Rightarrow \text{bool} \ (-\ \Downarrow \ -\ [50, 50]\ 51)$

**where**

*Terminate*:  $\llbracket s -\tau\rightarrow* s'; \bigwedge tl\ s''. \neg s' -tl\rightarrow s'' \rrbracket \Longrightarrow s \Downarrow\ TNil\ [s']$   
 $| \text{Diverge} : s -\tau\rightarrow \infty \Longrightarrow s \Downarrow\ TNil\ None$   
 $| \text{Proceed} : \bigwedge tl. \llbracket s -\tau\rightarrow* s'; s' -tl\rightarrow s''; \neg \tau\text{move } s'\ tl\ s''; s'' \Downarrow\ tl\ s \rrbracket \Longrightarrow s \Downarrow\ TCons\ tl\ tl\ s$

**inductive-simps**  $\tau\text{Runs-simps}$ :

$s \Downarrow\ TNil\ (\text{Some } s')$   
 $s \Downarrow\ TNil\ None$   
 $s \Downarrow\ TCons\ tl'\ tl\ s$

**coinductive**  $\tau\text{Runs-table} :: 's \Rightarrow ('tl \times 's, 's\ \text{option})\ tl\text{list} \Rightarrow \text{bool}$

**where**

*Terminate*:  $\llbracket s -\tau\rightarrow* s'; \bigwedge tl\ s''. \neg s' -tl\rightarrow s'' \rrbracket \Longrightarrow \tau\text{Runs-table } s\ (TNil\ [s'])$   
 $| \text{Diverge} : s -\tau\rightarrow \infty \Longrightarrow \tau\text{Runs-table } s\ (TNil\ None)$   
 $| \text{Proceed} :$   
 $\bigwedge tl. \llbracket s -\tau\rightarrow* s'; s' -tl\rightarrow s''; \neg \tau\text{move } s'\ tl\ s''; \tau\text{Runs-table } s''\ tl\ s \rrbracket$   
 $\Longrightarrow \tau\text{Runs-table } s\ (TCons\ (tl, s'')\ tl\ s)$

**definition** *silent-move2*  $:: 's \Rightarrow 'tl \Rightarrow 's \Rightarrow \text{bool}$

**where**  $\bigwedge tl. \text{silent-move2 } s\ tl\ s' \longleftrightarrow s -tl\rightarrow s' \wedge \tau\text{move } s\ tl\ s'$

**abbreviation** *silent-moves2*  $:: 's \Rightarrow 'tl\ list \Rightarrow 's \Rightarrow \text{bool}$

**where** *silent-moves2*  $\equiv \text{rtrancl3p } \text{silent-move2}$

**coinductive**  $\tau\text{Runs-table2} :: 's \Rightarrow ('tl\ list \times 's \times 'tl \times 's, ('tl\ list \times 's) + 'tl\ llist)\ tl\text{list} \Rightarrow \text{bool}$

**where**

*Terminate*:  $\llbracket \text{silent-moves2 } s\ tl\ s'; \bigwedge tl\ s''. \neg s' -tl\rightarrow s'' \rrbracket \Longrightarrow \tau\text{Runs-table2 } s\ (TNil\ (Inl\ (tl\ s, s')))$   
 $| \text{Diverge} : \text{trsys.inf-step } \text{silent-move2 } s\ tl\ s \Longrightarrow \tau\text{Runs-table2 } s\ (TNil\ (Inr\ tl\ s))$   
 $| \text{Proceed} :$   
 $\bigwedge tl. \llbracket \text{silent-moves2 } s\ tl\ s'; s' -tl\rightarrow s''; \neg \tau\text{move } s'\ tl\ s''; \tau\text{Runs-table2 } s''\ tl\ s\ tl\ s \rrbracket$   
 $\Longrightarrow \tau\text{Runs-table2 } s\ (TCons\ (tl\ s, s', tl, s'')\ tl\ s\ tl\ s)$

**inductive-simps**  $\tau\text{Runs-table2-simps}$ :

$\tau\text{Runs-table2 } s\ (TNil\ tl\ s)$   
 $\tau\text{Runs-table2 } s\ (TCons\ tl\ s\ tl\ s\ tl\ s)$

**lemma** *inf-step-table-all- $\tau$ -into- $\tau$ diverge*:

$\llbracket s -stls\rightarrow* t\ \infty; \forall (s, tl, s') \in \text{lset } stls. \tau\text{move } s\ tl\ s' \rrbracket \Longrightarrow s -\tau\rightarrow \infty$

**proof**(*coinduction arbitrary: s stls*)

**case** ( $\tau\text{diverge } s$ )

**thus** ?*case* **by** *cases (auto simp add: silent-move-iff, blast)*

**qed**

**lemma** *inf-step-table-lappend-llist-ofD*:

$s -lappend\ (llist\text{-of } stls)\ (LCons\ (x, tl', x')\ xs)\rightarrow* t\ \infty$

$\implies (s - \text{map } (\text{fst} \circ \text{snd}) \text{ stls} \rightarrow^* x) \wedge (x - \text{LCons } (x, \text{tl}', x') \text{ xs} \rightarrow^* t \infty)$   
**proof**(*induct stls arbitrary: s*)  
**case Nil thus** ?case **by**(*auto elim: inf-step-table.cases intro: inf-step-table.intros rtrancl3p-refl*)  
**next**  
**case (Cons st stls)**  
**note**  $IH = \langle \bigwedge s. s - \text{lappend } (\text{llist-of stls}) (\text{LCons } (x, \text{tl}', x') \text{ xs}) \rightarrow^* t \infty \implies$   
 $s - \text{map } (\text{fst} \circ \text{snd}) \text{ stls} \rightarrow^* x \wedge x - \text{LCons } (x, \text{tl}', x') \text{ xs} \rightarrow^* t \infty \rangle$   
**from**  $\langle s - \text{lappend } (\text{llist-of } (st \# \text{stls})) (\text{LCons } (x, \text{tl}', x') \text{ xs}) \rightarrow^* t \infty \rangle$   
**show** ?case  
**proof cases**  
**case (inf-step-tableI s' stls' tl)**  
**hence** [*simp*]:  $st = (s, \text{tl}, s') \text{ stls}' = \text{lappend } (\text{llist-of stls}) (\text{LCons } (x, \text{tl}', x') \text{ xs})$   
**and**  $s - \text{tl} \rightarrow s' \text{ s}' - \text{lappend } (\text{llist-of stls}) (\text{LCons } (x, \text{tl}', x') \text{ xs}) \rightarrow^* t \infty$  **by** *simp-all*  
**from**  $IH[OF \langle s' - \text{lappend } (\text{llist-of stls}) (\text{LCons } (x, \text{tl}', x') \text{ xs}) \rightarrow^* t \infty \rangle]$   
**have**  $s' - \text{map } (\text{fst} \circ \text{snd}) \text{ stls} \rightarrow^* x \text{ x} - \text{LCons } (x, \text{tl}', x') \text{ xs} \rightarrow^* t \infty$  **by** *auto*  
**with**  $\langle s - \text{tl} \rightarrow s' \rangle$  **show** ?thesis **by**(*auto simp add: o-def intro: rtrancl3p-step-converse*)  
**qed**  
**qed**

**lemma** *inf-step-table-lappend-llist-of- $\tau$ -into- $\tau$ moves:*  
**assumes** *lfinite stls*  
**shows**  $\llbracket s - \text{lappend stls } (\text{LCons } (x, \text{tl}' x') \text{ xs}) \rightarrow^* t \infty; \forall (s, \text{tl}, s') \in \text{lset stls}. \tau \text{move } s \text{ tl } s' \rrbracket \implies s$   
 $- \tau \rightarrow^* x$   
**using** *assms*  
**proof**(*induct arbitrary: s rule: lfinite.induct*)  
**case lfinite-LNil thus** ?case **by**(*auto elim: inf-step-table.cases*)  
**next**  
**case (lfinite-LConsI stls st)**  
**note**  $IH = \langle \bigwedge s. \llbracket s - \text{lappend stls } (\text{LCons } (x, \text{tl}' x') \text{ xs}) \rightarrow^* t \infty; \forall (s, \text{tl}, s') \in \text{lset stls}. \tau \text{move } s \text{ tl } s' \rrbracket$   
 $\implies s - \tau \rightarrow^* x \rangle$   
**obtain**  $s1 \text{ tl1 } s1'$  **where** [*simp*]:  $st = (s1, \text{tl1}, s1')$  **by**(*cases st*)  
**from**  $\langle s - \text{lappend } (\text{LCons } st \text{ stls}) (\text{LCons } (x, \text{tl}' x') \text{ xs}) \rightarrow^* t \infty \rangle$   
**show** ?case  
**proof cases**  
**case (inf-step-tableI X' STLS TL)**  
**hence** [*simp*]:  $s1 = s \text{ TL} = \text{tl1 } X' = s1' \text{ STLS} = \text{lappend stls } (\text{LCons } (x, \text{tl}' x') \text{ xs})$   
**and**  $s - \text{tl1} \rightarrow s1' \text{ and } s1' - \text{lappend stls } (\text{LCons } (x, \text{tl}' x') \text{ xs}) \rightarrow^* t \infty$  **by** *simp-all*  
**from**  $\langle \forall (s, \text{tl}, s') \in \text{lset } (\text{LCons } st \text{ stls}). \tau \text{move } s \text{ tl } s' \rangle$  **have**  $\tau \text{move } s \text{ tl1 } s1'$  **by** *simp*  
**moreover**  
**from**  $IH[OF \langle s1' - \text{lappend stls } (\text{LCons } (x, \text{tl}' x') \text{ xs}) \rightarrow^* t \infty \rangle] \langle \forall (s, \text{tl}, s') \in \text{lset } (\text{LCons } st \text{ stls}).$   
 $\tau \text{move } s \text{ tl } s' \rangle$   
**have**  $s1' - \tau \rightarrow^* x$  **by** *simp*  
**ultimately show** ?thesis **using**  $\langle s - \text{tl1} \rightarrow s1' \rangle$  **by**(*auto intro: converse-rtranclp-into-rtranclp*)  
**qed**  
**qed**

**lemma** *inf-step-table-into- $\tau$ inf-step:*

$s - \text{stls} \rightarrow^* t \infty \implies s - \tau - \text{lmap } (\text{fst} \circ \text{snd}) (\text{lfilter } (\lambda(s, \text{tl}, s'). \neg \tau \text{move } s \text{ tl } s') \text{ stls}) \rightarrow^* t \infty$   
**proof**(*coinduction arbitrary: s stls*)  
**case** ( *$\tau$ inf-step s stls*)  
**let** ?P =  $\lambda(s, \text{tl}, s'). \neg \tau \text{move } s \text{ tl } s'$   
**show** ?case  
**proof**(*cases lfilter ?P stls*)

```

case LNil
with  $\tau$ inf-step have ? $\tau$ inf-step-Nil
  by(auto intro: inf-step-table-all- $\tau$ -into- $\tau$ diverge simp add: lfilter-eq-LNil)
thus ?thesis ..
next
case (LCons stls' xs)
obtain  $x$   $tl$   $x'$  where  $stls' = (x, tl, x')$  by(cases stls')
with LCons have stls: lfilter ? $P$  stls = LCons  $(x, tl, x')$  xs by simp
from lfilter-eq-LConsD[OF this] obtain stls1 stls2
  where stls1: stls = lappend stls1 (LCons  $(x, tl, x')$  stls2)
  and lfinite stls1
  and  $\tau s: \forall (s, tl, s') \in \text{lset } stls1. \tau \text{move } s \text{ tl } s'$ 
  and  $n\tau: \neg \tau \text{move } x \text{ tl } x'$  and xs: xs = lfilter ? $P$  stls2 by blast
from  $\langle \text{lfinite } stls1 \rangle \tau$ inf-step  $\tau s$  have  $s \rightarrow^* x$  unfolding stls1
  by(rule inf-step-table-lappend-llist-of- $\tau$ -into- $\tau$ moves)
moreover from  $\langle \text{lfinite } stls1 \rangle$  have llist-of (list-of stls1) = stls1 by(simp add: llist-of-list-of)
with  $\tau$ inf-step stls1 have  $s \rightarrow^* \text{lappend (llist-of (list-of stls1)) (LCons } (x, tl, x') \text{ stls2)} \rightarrow^* t \infty$  by
simp
  from inf-step-table-lappend-llist-ofD[OF this]
  have  $x \rightarrow^* \text{LCons } (x, tl, x') \text{ stls2} \rightarrow^* t \infty$  ..
  hence  $x \rightarrow^* x' \text{ stls2} \rightarrow^* t \infty$  by(auto elim: inf-step-table.cases)
  ultimately have ? $\tau$ inf-step-Cons using xs  $n\tau$  by(auto simp add: stls o-def)
  thus ?thesis ..
qed
qed

lemma inf-step-into- $\tau$ inf-step:
  assumes  $s \rightarrow^* t \infty$ 
  shows  $\exists A. s \rightarrow^* \text{lnths } t \text{ } A \rightarrow^* \infty$ 
proof -
  from inf-step-imp-inf-step-table[OF assms]
  obtain stls where  $s \rightarrow^* t \infty$  and tls: tls = lmap (fst  $\circ$  snd) stls by blast
  from  $\langle s \rightarrow^* t \infty \rangle$  have  $s \rightarrow^* \text{lmap (fst } \circ \text{ snd) (lfilter } (\lambda(s, tl, s'). \neg \tau \text{move } s \text{ tl } s') \text{ tls)} \rightarrow^* \infty$ 
  by(rule inf-step-table-into- $\tau$ inf-step)
  hence  $s \rightarrow^* \text{lnths } t \text{ } \{n. \text{enat } n < \text{llength } stls \wedge (\lambda(s, tl, s'). \neg \tau \text{move } s \text{ tl } s') (\text{lnth } stls \text{ } n)\} \rightarrow^* \infty$ 
  unfolding lfilter-conv-lnths tls by simp
  thus ?thesis by blast
qed

lemma silent-moves-into- $\tau$ rtrancl3p:
   $s \rightarrow^* s' \implies s \rightarrow^* \Box \rightarrow^* s'$ 
by(induct rule: converse-rtranclp-induct)(blast intro:  $\tau$ rtrancl3p.intros)+

lemma  $\tau$ rtrancl3p-into-silent-moves:
   $s \rightarrow^* \Box \rightarrow^* s' \implies s \rightarrow^* s'$ 
apply(induct s tls  $\equiv \Box :: \text{'tl list } s'$  rule:  $\tau$ rtrancl3p.induct)
apply(auto intro: converse-rtranclp-into-rtranclp)
done

lemma  $\tau$ rtrancl3p-Nil-eq- $\tau$ moves:
   $s \rightarrow^* \Box \rightarrow^* s' \iff s \rightarrow^* s'$ 
by(blast intro: silent-moves-into- $\tau$ rtrancl3p  $\tau$ rtrancl3p-into-silent-moves)

lemma  $\tau$ rtrancl3p-trans [trans]:

```

$\llbracket s -\tau -tls \rightarrow^* s'; s' -\tau -tls' \rightarrow^* s'' \rrbracket \implies s -\tau -tls @ tls' \rightarrow^* s''$   
**apply**(*induct rule:  $\tau rtrancl3p.induct$* )  
**apply**(*auto intro:  $\tau rtrancl3p.intros$* )  
**done**

**lemma**  $\tau rtrancl3p\text{-}SingletonE$ :

**fixes**  $tl$   
**assumes**  $red: s -\tau -[tl] \rightarrow^* s'''$   
**obtains**  $s' s''$  **where**  $s -\tau \rightarrow^* s' s' -tl \rightarrow s'' \neg \tau move s' tl s'' s'' -\tau \rightarrow^* s'''$   
**proof**(*atomize-elim*)  
**from**  $red$  **show**  $\exists s' s''. s -\tau \rightarrow^* s' \wedge s' -tl \rightarrow s'' \wedge \neg \tau move s' tl s'' \wedge s'' -\tau \rightarrow^* s'''$   
**proof**(*induct  $s tls \equiv [tl] s'''$* )  
**case** ( $\tau rtrancl3p\text{-}step s s' s''$ )  
**from**  $\langle s -tl \rightarrow s' \rangle \langle \neg \tau move s tl s' \rangle \langle s' -\tau -[] \rightarrow^* s'' \rangle$  **show**  $?case$   
**by**(*auto simp add:  $\tau rtrancl3p\text{-}Nil\text{-}eq\text{-}\tau moves$* )  
**next**  
**case** ( $\tau rtrancl3p\text{-}\tau step s s' s'' tl'$ )  
**then obtain**  $t' t''$  **where**  $s' -\tau \rightarrow^* t' t' -tl \rightarrow t'' \neg \tau move t' tl t'' t'' -\tau \rightarrow^* s''$  **by** *auto*  
**moreover**  
**from**  $\langle s -tl' \rightarrow s' \rangle \langle \tau move s tl' s' \rangle$  **have**  $s -\tau \rightarrow^* s'$  **by** *blast*  
**ultimately show**  $?case$  **by**(*auto intro:  $rtranclp\text{-}trans$* )  
**qed**  
**qed**

**lemma**  $\tau rtrancl3p\text{-}snocI$ :

$\bigwedge tl. \llbracket \tau rtrancl3p s tls s''; s'' -\tau \rightarrow^* s'''; s''' -tl \rightarrow s'; \neg \tau move s''' tl s' \rrbracket$   
 $\implies \tau rtrancl3p s (tls @ [tl]) s'$   
**apply**(*erule  $\tau rtrancl3p\text{-}trans$* )  
**apply**(*fold  $\tau rtrancl3p\text{-}Nil\text{-}eq\text{-}\tau moves$* )  
**apply**(*drule  $\tau rtrancl3p\text{-}trans$* )  
**apply**(*erule (1)  $\tau rtrancl3p\text{-}step$* )  
**apply**(*rule  $\tau rtrancl3p\text{-}refl$* )  
**apply** *simp*  
**done**

**lemma**  $\tau diverge\text{-}rtranclp\text{-}silent\text{-}move$ :

$\llbracket silent\text{-}move^{\wedge**} s s'; s' -\tau \rightarrow \infty \rrbracket \implies s -\tau \rightarrow \infty$   
**by**(*induct rule:  $converse\text{-}rtranclp\text{-}induct$* )(*auto intro:  $\tau divergeI$* )

**lemma**  $\tau diverge\text{-}trancl\text{-}coinduct$  [*consumes 1, case-names  $\tau diverge$* ]:

**assumes**  $X: X s$   
**and** *step*:  $\bigwedge s. X s \implies \exists s'. silent\text{-}move^{\wedge++} s s' \wedge (X s' \vee s' -\tau \rightarrow \infty)$   
**shows**  $s -\tau \rightarrow \infty$   
**proof** –  
**from**  $X$  **have**  $\exists s'. silent\text{-}move^{\wedge**} s s' \wedge X s'$  **by** *blast*  
**thus**  $?thesis$   
**proof**(*coinduct*)  
**case** ( $\tau diverge s$ )  
**then obtain**  $s'$  **where**  $silent\text{-}move^{\wedge**} s s' X s'$  **by** *blast*  
**from** *step*[*OF*  $\langle X s' \rangle$ ] **obtain**  $s'''$   
**where**  $silent\text{-}move^{\wedge++} s' s''' X s''' \vee s''' -\tau \rightarrow \infty$  **by** *blast*  
**from**  $\langle silent\text{-}move^{\wedge**} s s' \rangle$  **show**  $?case$   
**proof**(*cases rule:  $converse\text{-}rtranclpE$  [consumes 1, case-names  $refl step$ ]*)  
**case** *refl*



```

moreover from  $\text{trancplD}[OF \langle \text{silent-move}^{++} s' s''' \rangle]$  obtain  $s''$ 
  where  $\text{silent-move} s' s'' \text{ silent-move}^{**} s'' s'''$  by blast
ultimately show  $?thesis$  using  $\langle \text{silent-move}^{**} s'' s''' \rangle \langle X s''' \vee s''' - \tau \rightarrow \infty \rangle$ 
  by  $(\text{auto intro: } \tau \text{diverge-rtrancpl-silent-move})$ 
next
  case (step S)
    moreover from  $\langle \text{silent-move}^{**} S s' \rangle \langle \text{silent-move}^{++} s' s''' \rangle$ 
    have  $\text{silent-move}^{**} S s'''$  by  $(\text{rule rtrancpl-trans}[OF - \text{trancpl-into-rtrancpl}])$ 
    ultimately show  $?thesis$  using  $\langle X s''' \vee s''' - \tau \rightarrow \infty \rangle$  by  $(\text{auto intro: } \tau \text{diverge-rtrancpl-silent-move})$ 
  qed
qed
qed

```

**lemma**  $\tau \text{diverge-trancl-measure-coinduct}$  [*consumes 2, case-names  $\tau \text{diverge}$* ]:

**assumes** *major*:  $X s t \text{ wfP } \mu$   
**and** *step*:  $\bigwedge s t. X s t \implies \exists s' t'. (\mu t' t \wedge s' = s \vee \text{silent-move}^{++} s s') \wedge (X s' t' \vee s' - \tau \rightarrow \infty)$   
**shows**  $s - \tau \rightarrow \infty$

**proof** –

```

{ fix  $s t$ 
  assume  $X s t$ 
  with  $\langle \text{wfP } \mu \rangle$  have  $\exists s' t'. \text{silent-move}^{++} s s' \wedge (X s' t' \vee s' - \tau \rightarrow \infty)$ 
  proof (induct arbitrary: s rule: wfP-induct[consumes 1])
    case (1 t)
    hence IH:  $\bigwedge s' t'. \llbracket \mu t' t; X s' t' \rrbracket \implies$ 
       $\exists s'' t''. \text{silent-move}^{++} s' s'' \wedge (X s'' t'' \vee s'' - \tau \rightarrow \infty)$  by blast
    from  $\text{step}[OF \langle X s t \rangle]$  obtain  $s' t'$ 
    where  $\mu t' t \wedge s' = s \vee \text{silent-move}^{++} s s' X s' t' \vee s' - \tau \rightarrow \infty$  by blast
    from  $\langle \mu t' t \wedge s' = s \vee \text{silent-move}^{++} s s' \rangle$  show  $?case$ 
  proof
    assume  $\mu t' t \wedge s' = s$ 
    hence  $\mu t' t$  and [simp]:  $s' = s$  by simp-all
    from  $\langle X s' t' \vee s' - \tau \rightarrow \infty \rangle$  show  $?thesis$ 
  proof
    assume  $X s' t'$ 
    from  $\text{IH}[OF \langle \mu t' t \rangle \text{ this}]$  show  $?thesis$  by simp
  next
    assume  $s' - \tau \rightarrow \infty$  thus  $?thesis$ 
    by cases(auto simp add: silent-move-iff)
  qed
next
  assume  $\text{silent-move}^{++} s s'$ 
  thus  $?thesis$  using  $\langle X s' t' \vee s' - \tau \rightarrow \infty \rangle$  by blast
qed
qed }
note  $X = \text{this}$ 
from  $\langle X s t \rangle$  have  $\exists t. X s t ..$ 
thus  $?thesis$ 
proof (coinduct rule:  $\tau \text{diverge-trancl-coinduct}$ )
  case ( $\tau \text{diverge } s$ )
  then obtain  $t$  where  $X s t ..$ 
  from  $X[OF \text{ this}]$  show  $?case$  by blast
qed
qed

```

**lemma**  $\tau\text{inf-step2}\tau\text{inf-step-table-LNil}$  [simp]:  $\tau\text{inf-step2}\tau\text{inf-step-table } s \text{ LNil} = \text{LNil}$   
**by**(simp add:  $\tau\text{inf-step2}\tau\text{inf-step-table-def}$ )

**lemma**  $\tau\text{inf-step2}\tau\text{inf-step-table-LCons}$  [simp]:  
**fixes**  $s \text{ tl } ss \text{ tls}$   
**defines**  $ss \equiv \text{SOME } (s', s''). s -\tau \rightarrow^* s' \wedge s' -\text{tl} \rightarrow s'' \wedge \neg \tau\text{move } s' \text{ tl } s'' \wedge s'' -\tau -\text{tls} \rightarrow^* \infty$   
**shows**  
 $\tau\text{inf-step2}\tau\text{inf-step-table } s \text{ (LCons tl tls)} =$   
 $\text{LCons } (s, \text{fst } ss, \text{tl}, \text{snd } ss) (\tau\text{inf-step2}\tau\text{inf-step-table } (\text{snd } ss) \text{ tls})$   
**by**(simp add: ss-def  $\tau\text{inf-step2}\tau\text{inf-step-table-def}$  split-beta)

**lemma**  $\text{lnull-}\tau\text{inf-step2}\tau\text{inf-step-table}$  [simp]:  
 $\text{lnull } (\tau\text{inf-step2}\tau\text{inf-step-table } s \text{ tls}) \longleftrightarrow \text{lnull } \text{tls}$   
**by**(simp add:  $\tau\text{inf-step2}\tau\text{inf-step-table-def}$ )

**lemma**  $\text{lhs-}\tau\text{inf-step2}\tau\text{inf-step-table}$  [simp]:  
 $\neg \text{lnull } \text{tls} \implies \text{lhs } (\tau\text{inf-step2}\tau\text{inf-step-table } s \text{ tls}) =$   
 $(\text{let } (s', s'') = \text{SOME } (s', s''). s -\tau \rightarrow^* s' \wedge s' -\text{lhs } \text{tls} \rightarrow s'' \wedge \neg \tau\text{move } s' (\text{lhs } \text{tls}) s'' \wedge s'' -\tau -\text{lhs } \text{tls} \rightarrow^* \infty$   
 $\text{in } (s, s', \text{lhs } \text{tls}, s''))$   
**unfolding**  $\tau\text{inf-step2}\tau\text{inf-step-table-def}$  *Let-def* **by** simp

**lemma**  $\text{lhs-}\tau\text{inf-step2}\tau\text{inf-step-table}$  [simp]:  
 $\neg \text{lnull } \text{tls} \implies \text{lhs } (\tau\text{inf-step2}\tau\text{inf-step-table } s \text{ tls}) =$   
 $(\text{let } (s', s'') = \text{SOME } (s', s''). s -\tau \rightarrow^* s' \wedge s' -\text{lhs } \text{tls} \rightarrow s'' \wedge \neg \tau\text{move } s' (\text{lhs } \text{tls}) s'' \wedge s'' -\tau -\text{lhs } \text{tls} \rightarrow^* \infty$   
 $\text{in } \tau\text{inf-step2}\tau\text{inf-step-table } s'' (\text{lhs } \text{tls}))$   
**unfolding**  $\tau\text{inf-step2}\tau\text{inf-step-table-def}$  *Let-def*  
**by**(simp add: split-beta)

**lemma**  $\text{lmap-}\tau\text{inf-step2}\tau\text{inf-step-table}$ :  $\text{lmap } (\text{fst} \circ \text{snd} \circ \text{snd}) (\tau\text{inf-step2}\tau\text{inf-step-table } s \text{ tls}) = \text{tls}$   
**by**(coinduction arbitrary:  $s \text{ tls}$ )(auto simp add: split-beta)

**lemma**  $\tau\text{inf-step-into-}\tau\text{inf-step-table}$ :  
 $s -\tau -\text{tls} \rightarrow^* \infty \implies s -\tau -\tau\text{inf-step2}\tau\text{inf-step-table } s \text{ tls} \rightarrow^* t \infty$   
**proof**(coinduction arbitrary:  $s \text{ tls}$ )  
**case**  $(\tau\text{inf-step-table } s \text{ tls})$   
**thus** ?case  
**proof**(cases)  
**case**  $(\tau\text{inf-step-Cons } s' s'' \text{ tls}' \text{ tl})$   
**let** ?ss =  $\text{SOME } (s', s''). s -\tau \rightarrow^* s' \wedge s' -\text{tl} \rightarrow s'' \wedge \neg \tau\text{move } s' \text{ tl } s'' \wedge s'' -\tau -\text{tls}' \rightarrow^* \infty$   
**from**  $\tau\text{inf-step-Cons}$  **have**  $\text{tls} = \text{LCons } \text{tl } \text{tls}'$  **and**  $s -\tau \rightarrow^* s' s' -\text{tl} \rightarrow s''$   
 $\neg \tau\text{move } s' \text{ tl } s'' s'' -\tau -\text{tls}' \rightarrow^* \infty$  **by** simp-all  
**hence**  $(\lambda(s', s''). s -\tau \rightarrow^* s' \wedge s' -\text{tl} \rightarrow s'' \wedge \neg \tau\text{move } s' \text{ tl } s'' \wedge s'' -\tau -\text{tls}' \rightarrow^* \infty) (s', s'')$  **by** simp  
**hence**  $(\lambda(s', s''). s -\tau \rightarrow^* s' \wedge s' -\text{tl} \rightarrow s'' \wedge \neg \tau\text{move } s' \text{ tl } s'' \wedge s'' -\tau -\text{tls}' \rightarrow^* \infty) ?ss$  **by**(rule someI)  
**with**  $\text{tls}$  **have** ? $\tau\text{inf-step-table-Cons}$  **by** auto  
**thus** ?thesis ..  
**next**  
**case**  $\tau\text{inf-step-Nil}$   
**then** **have** ? $\tau\text{inf-step-table-Nil}$  **by** simp  
**thus** ?thesis ..  
**qed**

qed

**lemma**  $\tau$ inf-step-imp- $\tau$ inf-step-table:

**assumes**  $s \rightarrow^* \infty$

**shows**  $\exists sstls. s \rightarrow^* sstls \wedge tls = \text{lmap } (fst \circ snd \circ snd) \ sstls$

**using**  $\tau$ inf-step-into- $\tau$ inf-step-table[OF assms]

**by**(auto simp only: lmap- $\tau$ inf-step2 $\tau$ inf-step-table)

**lemma**  $\tau$ inf-step-table-into- $\tau$ inf-step:

$s \rightarrow^* \infty \implies s \rightarrow^* \text{lmap } (fst \circ snd \circ snd) \ sstls \rightarrow^* \infty$

**proof**(coinduction arbitrary: s sstls)

**case** ( $\tau$ inf-step s tls)

**thus** ?case **by** cases(auto simp add: o-def)

qed

**lemma** silent-move-fromI [intro]:

$\llbracket \text{silent-moves } s0 \ s1; \text{silent-move } s1 \ s2 \rrbracket \implies \text{silent-move-from } s0 \ s1 \ s2$

**by**(simp add: silent-move-from-def)

**lemma** silent-move-fromE [elim]:

**assumes** silent-move-from s0 s1 s2

**obtains** silent-moves s0 s1 silent-move s1 s2

**using** assms **by**(auto simp add: silent-move-from-def)

**lemma** rtrancplp-silent-move-from-imp-silent-moves:

**assumes**  $s'x: \text{silent-move}^{**} \ s' \ x$

**shows**  $(\text{silent-move-from } s')^{**} \ x \ z \implies \text{silent-moves } s' \ z$

**by**(induct rule: rtrancplp-induct)(auto intro: s'x)

**lemma**  $\tau$ diverge-not-wfP-silent-move-from:

**assumes**  $s \rightarrow^* \infty$

**shows**  $\neg \text{wfP } (\text{flip } (\text{silent-move-from } s))$

**proof**

**assume**  $\text{wfP } (\text{flip } (\text{silent-move-from } s))$

**moreover** **define**  $Q$  **where**  $Q = \{s'. \text{silent-moves } s \ s' \wedge s' \rightarrow^* \infty\}$

**hence**  $s \in Q$  **using**  $\langle s \rightarrow^* \infty \rangle$  **by**(auto)

**ultimately** **have**  $\exists z \in Q. \forall y. \text{silent-move-from } s \ z \ y \longrightarrow y \notin Q$

**unfolding** wfP-eq-minimal flip-simps **by** blast

**then** **obtain**  $z$  **where**  $z \in Q$

**and**  $\min: \bigwedge y. \text{silent-move-from } s \ z \ y \implies y \notin Q$  **by** blast

**from**  $\langle z \in Q \rangle$  **have**  $\text{silent-moves } s \ z \ z \rightarrow^* \infty$  **unfolding** Q-def **by** auto

**from**  $\langle z \rightarrow^* \infty \rangle$  **obtain**  $y$  **where**  $\text{silent-move } z \ y \rightarrow^* \infty$  **by** cases auto

**from**  $\langle \text{silent-moves } s \ z \rangle \ \langle \text{silent-move } z \ y \rangle$  **have**  $\text{silent-move-from } s \ z \ y \dots$

**hence**  $y \notin Q$  **by**(rule min)

**moreover** **from**  $\langle \text{silent-moves } s \ z \rangle \ \langle \text{silent-move } z \ y \rangle \ \langle y \rightarrow^* \infty \rangle$

**have**  $y \in Q$  **unfolding** Q-def **by** auto

**ultimately** **show** False **by** contradiction

qed

**lemma** wfP-silent-move-from-unroll:

**assumes**  $\text{wfPs}': \bigwedge s'. s \rightarrow^* s' \implies \text{wfP } (\text{flip } (\text{silent-move-from } s'))$

**shows**  $\text{wfP } (\text{flip } (\text{silent-move-from } s))$

**unfolding** wfP-eq-minimal flip-conv

**proof**(intro allI impI)

```

fix Q and x :: 's
assume x ∈ Q
show ∃ z ∈ Q. ∀ y. silent-move-from s z y ⟶ y ∉ Q
proof(cases ∃ s'. s -τ→ s' ∧ (∃ x'. silent-moves s' x' ∧ x' ∈ Q))
  case False
    hence ∀ y. silent-move-from s x y ⟶ ¬ y ∈ Q
    by(cases x=s)(auto, blast elim: converse-rtranclpE intro: rtranclp.rtrancl-into-rtrancl)
  with ⟨x ∈ Q⟩ show ?thesis by blast
next
case True
then obtain s' x' where s -τ→ s' and silent-moves s' x' and x' ∈ Q
  by auto
from ⟨s -τ→ s'⟩ have wfP (flip (silent-move-from s')) by(rule wfPs')
from this ⟨x' ∈ Q⟩ obtain z where z ∈ Q and min: ∧ y. silent-move-from s' z y ⟹ ¬ y ∈ Q
  and (silent-move-from s')** x' z
  by (rule wfP-minimalE) (unfold flip-simps, blast)
{ fix y
  assume silent-move-from s z y
  with ⟨(silent-move-from s')** x' z⟩ ⟨silent-move** s' x'⟩
  have silent-move-from s' z y
    by(blast intro: rtranclp-silent-move-from-imp-silent-moves)
  hence ¬ y ∈ Q by(rule min) }
with ⟨z ∈ Q⟩ show ?thesis by(auto simp add: intro!: bexI)
qed
qed

lemma not-wfP-silent-move-from-τ-diverge:
  assumes ¬ wfP (flip (silent-move-from s))
  shows s -τ→ ∞
using assms
proof(coinduct)
  case (τ-diverge s)
  { assume wfPs': ∧ s'. s -τ→ s' ⟹ wfP (flip (silent-move-from s'))
    hence wfP (flip (silent-move-from s)) by(rule wfP-silent-move-from-unroll) }
  with τ-diverge have ∃ s'. s -τ→ s' ∧ ¬ wfP (flip (silent-move-from s')) by auto
  thus ?case by blast
qed

lemma τ-diverge-neq-wfP-silent-move-from:
  s -τ→ ∞ ≠ wfP (flip (silent-move-from s))
by(auto intro: not-wfP-silent-move-from-τ-diverge dest: τ-diverge-not-wfP-silent-move-from)

lemma not-τ-diverge-to-no-τ-move:
  assumes ¬ s -τ→ ∞
  shows ∃ s'. s -τ→* s' ∧ (∀ s''. ¬ s' -τ→ s'')
proof -
  define S where S = s
  from ⟨¬ τ-diverge s⟩ have wfP (flip (silent-move-from S)) unfolding S-def
    using τ-diverge-neq-wfP-silent-move-from[of s] by simp
  moreover have silent-moves S s unfolding S-def ..
  ultimately show ?thesis
  proof(induct rule: wfP-induct')
    case (wfP s)
    note IH = ⟨∧ y. [flip (silent-move-from S) y s; S -τ→* y]⟩

```

```

     $\implies \exists s'. y \rightarrow^* s' \wedge (\forall s''. \neg s' \rightarrow s'')$ 
  show ?case
  proof(cases  $\exists s'. \text{silent-move } s \ s'$ )
    case False thus ?thesis by auto
  next
    case True
    then obtain  $s'$  where  $s \rightarrow s'$  ..
    with  $\langle S \rightarrow^* s \rangle$  have flip (silent-move-from  $S$ )  $s' \ s$ 
      unfolding flip-conv by(rule silent-move-fromI)
    moreover from  $\langle S \rightarrow^* s \rangle \langle s \rightarrow s' \rangle$  have  $S \rightarrow^* s' ..$ 
    ultimately have  $\exists s''. s' \rightarrow^* s'' \wedge (\forall s'''. \neg s'' \rightarrow s''')$  by(rule IH)
    then obtain  $s''$  where  $s' \rightarrow^* s'' \forall s'''. \neg s'' \rightarrow s'''$  by blast
    from  $\langle s \rightarrow s' \rangle \langle s' \rightarrow^* s'' \rangle$  have  $s \rightarrow^* s''$  by(rule converse-rtranclp-into-rtranclp)
    with  $\langle \forall s'''. \neg s'' \rightarrow s''' \rangle$  show ?thesis by blast
  qed
qed
qed

```

**lemma**  $\tau \text{diverge-conv-}\tau \text{Runs}$ :

$s \rightarrow \infty \iff s \Downarrow \text{TNil None}$

by(auto intro:  $\tau \text{Runs.Diverge elim: } \tau \text{Runs.cases}$ )

**lemma**  $\tau \text{inf-step-into-}\tau \text{Runs}$ :

$s \rightarrow \text{tls} \rightarrow^* \infty \implies s \Downarrow \text{tlist-of-llist None } \text{tls}$

**proof**(coinduction arbitrary:  $s \ \text{tls}$ )

case ( $\tau \text{Runs } s \ \text{tls}'$ )

thus ?case by cases(auto simp add:  $\tau \text{diverge-conv-}\tau \text{Runs}$ )

qed

**lemma**  $\tau \text{-into-}\tau \text{Runs}$ :

$\llbracket s \rightarrow s'; s' \Downarrow \text{tls} \rrbracket \implies s \Downarrow \text{tls}$

by(blast elim:  $\tau \text{Runs.cases intro: } \tau \text{Runs.intros } \tau \text{diverge.intros converse-rtranclp-into-rtranclp}$ )

**lemma**  $\tau \text{rtrancl3p-into-}\tau \text{Runs}$ :

assumes  $s \rightarrow \text{tls} \rightarrow^* s'$

and  $s' \Downarrow \text{tls}'$

shows  $s \Downarrow \text{lappendt (llist-of } \text{tls}) } \text{tls}'$

using assms

by induct(auto intro:  $\tau \text{Runs.Proceed } \tau \text{-into-}\tau \text{Runs}$ )

**lemma**  $\tau \text{Runs-table-into-}\tau \text{Runs}$ :

$\tau \text{Runs-table } s \ \text{stlsss} \implies s \Downarrow \text{tmap fst id stlsss}$

**proof**(coinduction arbitrary:  $s \ \text{stlsss}$ )

case ( $\tau \text{Runs } s \ \text{tls}$ )

thus ?case by cases(auto simp add: o-def id-def)

qed

**definition**  $\tau \text{Runs2}\tau \text{Runs-table} :: 's \Rightarrow ('tl, 's \text{ option}) \text{tlist} \Rightarrow ('tl \times 's, 's \text{ option}) \text{tlist}$

where

$\tau \text{Runs2}\tau \text{Runs-table } s \ \text{tls} = \text{unfold-tlist}$

$(\lambda(s, \text{tls}). \text{is-TNil } \text{tls})$

$(\lambda(s, \text{tls}). \text{terminal } \text{tls})$

$(\lambda(s, \text{tls}). (\text{thd } \text{tls}, \text{SOME } s''. \exists s'. s \rightarrow^* s' \wedge s' \rightarrow \text{thd } \text{tls} \rightarrow s'' \wedge \neg \tau \text{move } s' (\text{thd } \text{tls}) s'' \wedge s'' \Downarrow \text{ttl } \text{tls}))$

$(\lambda(s, tls). (SOME\ s''. \exists s'. s \rightarrow^* s' \wedge s' \text{thd } tls \rightarrow s'' \wedge \neg \tau move\ s' (thd\ tls)\ s'' \wedge s'' \Downarrow ttl\ tls, \\ ttl\ tls))$   
 $(s, tls)$

**lemma** *is-TNil- $\tau$ Runs2 $\tau$ Runs-table* [simp]:  
 $is\text{-}TNil\ (\tauRuns2\tauRuns\text{-}table\ s\ tls) \longleftrightarrow is\text{-}TNil\ tls$   
**thm** *unfold-tllist.disc*  
**by**(simp add:  $\tauRuns2\tauRuns\text{-}table\text{-}def$ )

**lemma** *thd- $\tau$ Runs2 $\tau$ Runs-table* [simp]:  
 $\neg is\text{-}TNil\ tls \implies$   
 $thd\ (\tauRuns2\tauRuns\text{-}table\ s\ tls) =$   
 $(thd\ tls, SOME\ s''. \exists s'. s \rightarrow^* s' \wedge s' \text{thd } tls \rightarrow s'' \wedge \neg \tau move\ s' (thd\ tls)\ s'' \wedge s'' \Downarrow ttl\ tls)$   
**by**(simp add:  $\tauRuns2\tauRuns\text{-}table\text{-}def$ )

**lemma** *ttl- $\tau$ Runs2 $\tau$ Runs-table* [simp]:  
 $\neg is\text{-}TNil\ tls \implies$   
 $ttl\ (\tauRuns2\tauRuns\text{-}table\ s\ tls) =$   
 $\tauRuns2\tauRuns\text{-}table\ (SOME\ s''. \exists s'. s \rightarrow^* s' \wedge s' \text{thd } tls \rightarrow s'' \wedge \neg \tau move\ s' (thd\ tls)\ s'' \wedge s''$   
 $\Downarrow ttl\ tls)\ (ttl\ tls)$   
**by**(simp add:  $\tauRuns2\tauRuns\text{-}table\text{-}def$ )

**lemma** *terminal- $\tau$ Runs2 $\tau$ Runs-table* [simp]:  
 $is\text{-}TNil\ tls \implies terminal\ (\tauRuns2\tauRuns\text{-}table\ s\ tls) = terminal\ tls$   
**by**(simp add:  $\tauRuns2\tauRuns\text{-}table\text{-}def$ )

**lemma**  *$\tau$ Runs2 $\tau$ Runs-table-simps* [simp, nitpick-simp]:  
 $\tauRuns2\tauRuns\text{-}table\ s\ (TNil\ so) = TNil\ so$   
 $\bigwedge^{tl.}$   
 $\tauRuns2\tauRuns\text{-}table\ s\ (TCons\ tl\ tls) =$   
 $(let\ s'' = SOME\ s''. \exists s'. s \rightarrow^* s' \wedge s' \text{thd } tls \rightarrow s'' \wedge \neg \tau move\ s' tl\ s'' \wedge s'' \Downarrow tls$   
 $in\ TCons\ (tl, s''))\ (\tauRuns2\tauRuns\text{-}table\ s''\ tls))$   
**apply**(simp add:  $\tauRuns2\tauRuns\text{-}table\text{-}def$ )  
**apply**(rule *tllist.expand*)  
**apply**(simp-all)  
**done**

**lemma**  *$\tau$ Runs2 $\tau$ Runs-table-inverse*:  
 $tmap\ fst\ id\ (\tauRuns2\tauRuns\text{-}table\ s\ tls) = tls$   
**by**(coinduction arbitrary:  $s\ tls$ ) auto

**lemma**  *$\tau$ Runs-into- $\tau$ Runs-table*:  
**assumes**  $s \Downarrow tls$   
**shows**  $\exists stlsss. tls = tmap\ fst\ id\ stlsss \wedge \tauRuns\text{-}table\ s\ stlsss$   
**proof**(intro *exI conjI*)  
**from** *assms* **show**  $\tauRuns\text{-}table\ s\ (\tauRuns2\tauRuns\text{-}table\ s\ tls)$   
**proof**(coinduction arbitrary:  $s\ tls$ )  
**case**  $(\tauRuns\text{-}table\ s\ tls)$   
**thus** ?case  
**proof** cases  
**case** (*Terminate*  $s'$ )  
**hence** ?*Terminate* **by** *simp*  
**thus** ?thesis ..  
**next**

```

case Diverge
hence ?Diverge by simp
thus ?thesis by simp
next
case (Proceed s' s'' tls' tl)
let ?P =  $\lambda s''. \exists s'. s -\tau \rightarrow^* s' \wedge s' -tl \rightarrow s'' \wedge \neg \tau \text{move } s' \text{ tl } s'' \wedge s'' \Downarrow tls'$ 
from Proceed have ?P s'' by auto
hence ?P (Eps ?P) by (rule someI)
hence ?Proceed using  $\langle tls = TCons \text{ tl } tls' \rangle$ 
  by (auto simp add: split-beta)
thus ?thesis by simp
qed
qed
qed (simp add:  $\tau \text{Runs2} \tau \text{Runs-table-inverse}$ )

lemma  $\tau \text{Runs-lappendE}$ :
  assumes  $\sigma \Downarrow \text{lappendt } tls \ tls'$ 
  and  $lfinite \ tls$ 
  obtains  $\sigma'$  where  $\sigma -\tau \text{-list-of } tls \rightarrow^* \sigma'$ 
  and  $\sigma' \Downarrow tls'$ 
proof (atomize-elim)
  from  $\langle lfinite \ tls \rangle \langle \sigma \Downarrow \text{lappendt } tls \ tls' \rangle$ 
  show  $\exists \sigma'. \sigma -\tau \text{-list-of } tls \rightarrow^* \sigma' \wedge \sigma' \Downarrow tls'$ 
  proof (induct arbitrary:  $\sigma$ )
    case lfinite-LNil thus ?case by (auto intro:  $\tau \text{tranc13p-refl}$ )
  next
    case (lfinite-LConsI tls tl)
    from  $\langle \sigma \Downarrow \text{lappendt } (LCons \text{ tl } tls) \ tls' \rangle$ 
    show ?case unfolding lappendt-LCons
    proof (cases)
      case (Proceed  $\sigma' \sigma''$ )
      from  $\langle \sigma'' \Downarrow \text{lappendt } tls \ tls' \rangle \implies \exists \sigma'''. \sigma'' -\tau \text{-list-of } tls \rightarrow^* \sigma''' \wedge \sigma''' \Downarrow tls' \rangle \langle \sigma'' \Downarrow \text{lappendt } tls \ tls' \rangle$ 
      obtain  $\sigma'''$  where  $\sigma'' -\tau \text{-list-of } tls \rightarrow^* \sigma''' \wedge \sigma''' \Downarrow tls'$  by blast
      from  $\langle \sigma' -tl \rightarrow \sigma'' \rangle \langle \neg \tau \text{move } \sigma' \text{ tl } \sigma'' \rangle \langle \sigma'' -\tau \text{-list-of } tls \rightarrow^* \sigma''' \rangle$ 
      have  $\sigma' -\tau \text{-tl} \# \text{list-of } tls \rightarrow^* \sigma'''$  by (rule  $\tau \text{tranc13p-step}$ )
      with  $\langle \sigma -\tau \rightarrow^* \sigma' \rangle$  have  $\sigma -\tau \rightarrow^* \sigma'''$  @ (tl # list-of tls)  $\rightarrow^* \sigma'''$ 
      unfolding  $\tau \text{tranc13p-Nil-eq-}\tau \text{moves[symmetric]}$  by (rule  $\tau \text{tranc13p-trans}$ )
      with  $\langle lfinite \ tls \rangle$  have  $\sigma -\tau \text{-list-of } (LCons \text{ tl } tls) \rightarrow^* \sigma'''$  by (simp add: list-of-LCons)
      with  $\langle \sigma''' \Downarrow tls' \rangle$  show ?thesis by blast
    qed
  qed
qed
qed

lemma  $\tau \text{Runs-total}$ :
   $\exists tls. \sigma \Downarrow tls$ 
proof
  let ? $\tau \text{halt}$  =  $\lambda \sigma \sigma'. \sigma -\tau \rightarrow^* \sigma' \wedge (\forall \text{tl } \sigma''. \neg \sigma' -tl \rightarrow \sigma'')$ 
  let ? $\tau \text{diverge}$  =  $\lambda \sigma. \sigma -\tau \rightarrow \infty$ 
  let ? $\text{proceed}$  =  $\lambda \sigma \text{ (tl, } \sigma''). \exists \sigma'. \sigma -\tau \rightarrow^* \sigma' \wedge \sigma' -tl \rightarrow \sigma'' \wedge \neg \tau \text{move } \sigma' \text{ tl } \sigma''$ 

  define tls where  $tls = \text{unfold-tllist}$ 
    ( $\lambda \sigma. (\exists \sigma'. ?\tau \text{halt } \sigma \sigma') \vee ?\tau \text{diverge } \sigma$ )
    ( $\lambda \sigma. \text{if } \exists \sigma'. ?\tau \text{halt } \sigma \sigma' \text{ then Some (SOME } \sigma'. ?\tau \text{halt } \sigma \sigma') \text{ else None}$ )

```

```

  ( $\lambda\sigma. \text{fst } (\text{SOME } tl\sigma'. \text{?proceed } \sigma \text{ } tl\sigma')$ )
  ( $\lambda\sigma. \text{snd } (\text{SOME } tl\sigma'. \text{?proceed } \sigma \text{ } tl\sigma')$ )  $\sigma$ 
then show  $\sigma \Downarrow tls$ 
proof(coinduct  $\sigma$  tls rule:  $\tau$ Runs.coinduct)
  case ( $\tau$ Runs  $\sigma$  tls)
  show ?case
  proof(cases  $\exists \sigma'. \text{?}\tau\text{halt } \sigma \text{ } \sigma'$ )
    case True
    hence  $\text{?}\tau\text{halt } \sigma$  (SOME  $\sigma'. \text{?}\tau\text{halt } \sigma \text{ } \sigma'$ ) by(rule someI-ex)
    hence ?Terminate using True unfolding  $\tau$ Runs by simp
    thus ?thesis ..
  next
  case False
  note  $\tau\text{halt} = \text{this}$ 
  show ?thesis
  proof(cases  $\text{?}\tau\text{diverge } \sigma$ )
    case True
    hence ?Diverge using False unfolding  $\tau$ Runs by simp
    thus ?thesis by simp
  next
  case False
  from not- $\tau$ diverge-to-no- $\tau$ move[OF this]
  obtain  $\sigma'$  where  $\sigma\text{-}\sigma': \sigma \text{--}\tau\text{--}\rightarrow^* \sigma'$ 
    and  $\text{no-}\tau: \bigwedge \sigma''. \neg \sigma' \text{--}\tau\text{--}\rightarrow \sigma''$  by blast
  from  $\sigma\text{-}\sigma' \tau\text{halt}$  obtain  $tl \sigma''$  where  $\sigma' \text{--}tl\text{--}\rightarrow \sigma''$  by auto
  moreover with  $\text{no-}\tau$ [of  $\sigma''$ ] have  $\neg \tau\text{move } \sigma' \text{ } tl \sigma''$  by auto
  ultimately have  $\text{?proceed } \sigma$  ( $tl, \sigma''$ ) using  $\sigma\text{-}\sigma'$  by auto
  hence  $\text{?proceed } \sigma$  (SOME  $tl\sigma. \text{?proceed } \sigma \text{ } tl\sigma$ ) by(rule someI)
  hence ?Proceed using False  $\tau\text{halt}$  unfolding  $\tau$ Runs
    by(subst unfold-tllist.code) fastforce
  thus ?thesis by simp
  qed
qed
qed
qed

lemma silent-move2-into-silent-move:
  fixes tl
  assumes silent-move2  $s \text{ } tl \text{ } s'$ 
  shows  $s \text{--}\tau\text{--}\rightarrow s'$ 
using assms by(auto simp add: silent-move2-def)

lemma silent-move-into-silent-move2:
  assumes  $s \text{--}\tau\text{--}\rightarrow s'$ 
  shows  $\exists tl. \text{silent-move2 } s \text{ } tl \text{ } s'$ 
using assms by(auto simp add: silent-move2-def)

lemma silent-moves2-into-silent-moves:
  assumes silent-moves2  $s \text{ } tls \text{ } s'$ 
  shows  $s \text{--}\tau\text{--}\rightarrow^* s'$ 
using assms
by(induct)(blast intro: silent-move2-into-silent-move rtranclp.rtrancl-into-rtrancl)+

lemma silent-moves-into-silent-moves2:

```



```

assumes  $s \rightarrow^* s'$ 
shows  $\exists tls. \text{silent-moves2 } s \ tls \ s'$ 
using assms
by(induct)(blast dest: silent-move-into-silent-move2 intro: rtrancl3p-step)+

lemma inf-step-silent-move2-into- $\tau$  diverge:
  trsys.inf-step silent-move2 s tls  $\implies s \rightarrow^* \infty$ 
proof(coinduction arbitrary: s tls)
  case ( $\tau$  diverge  $s$ )
  thus ?case
    by(cases rule: trsys.inf-step.cases[consumes 1])(auto intro: silent-move2-into-silent-move)
qed

lemma  $\tau$  diverge-into-inf-step-silent-move2:
  assumes  $s \rightarrow^* \infty$ 
  obtains  $tls$  where trsys.inf-step silent-move2 s tls
proof -
  define  $tls$  where  $tls = \text{unfold-llist}$ 
    ( $\lambda -. \text{False}$ )
    ( $\lambda s. \text{fst } (\text{SOME } (tl, s'). \text{silent-move2 } s \ tl \ s' \wedge s' \rightarrow^* \infty)$ )
    ( $\lambda s. \text{snd } (\text{SOME } (tl, s'). \text{silent-move2 } s \ tl \ s' \wedge s' \rightarrow^* \infty)$ )
     $s \ (\text{is } - = ?tls \ s)$ 

  with assms have  $s \rightarrow^* \infty \wedge tls = ?tls \ s$  by simp
  hence trsys.inf-step silent-move2 s tls
  proof(coinduct rule: trsys.inf-step.coinduct[consumes 1, case-names inf-step, case-conclusion inf-step step])
    case (inf-step  $s \ tls$ )
    let ? $P = \lambda(tl, s'). \text{silent-move2 } s \ tl \ s' \wedge s' \rightarrow^* \infty$ 
    from inf-step obtain  $s \rightarrow^* \infty$  and  $tls: tls = ?tls \ s ..$ 
    from  $\langle s \rightarrow^* \infty \rangle$  obtain  $s'$  where  $s \rightarrow^* s' \wedge s' \rightarrow^* \infty$  by cases
    from  $\langle s \rightarrow^* s' \rangle$  obtain  $tl$  where silent-move2 s tl s'
    by(blast dest: silent-move-into-silent-move2)
    with  $\langle s' \rightarrow^* \infty \rangle$  have ? $P \ (tl, s')$  by simp
    hence ? $P \ (Eps \ ?P)$  by(rule someI)
    thus ?case using  $tls$ 
    by(subst (asm) unfold-llist.code)(auto)
  qed
  thus thesis by(rule that)
qed

lemma  $\tau$ Runs-into- $\tau$ rtrancl3p:
  assumes runs: s  $\Downarrow$  tlss
  and fin: tfinite tlss
  and terminal: terminal tlss = Some s'
  shows  $\tau\text{rtrancl3p } s \ (\text{list-of } (\text{llist-of-tlss } tlss)) \ s'$ 
using fin runs terminal
proof(induct arbitrary: s rule: tfinite-induct)
  case TNil thus ?case by cases(auto intro: silent-moves-into- $\tau$ rtrancl3p)
next
  case (TCons  $tl \ tlss$ )
  from  $\langle s \Downarrow TCons \ tl \ tlss \rangle$  obtain  $s'' \ s'''$ 
  where step: s  $\rightarrow^* s''$ 
  and step2: s''  $\rightarrow tl \rightarrow s''' \wedge \tau\text{move } s'' \ tl \ s'''$ 

```

and  $s''' \Downarrow \text{tlss}$  **by cases**  
**from**  $\langle \text{terminal } (TCons \text{ tl tlss}) = [s'] \rangle$  **have**  $\text{terminal tlss} = [s']$  **by simp**  
**with**  $\langle s''' \Downarrow \text{tlss} \rangle$  **have**  $s''' -\tau - \text{list-of } (l\text{list-of-tlss tlss}) \rightarrow^* s'$  **by** (rule  $TCons$ )  
**with step2** **have**  $s'' -\tau - \text{tl} \# \text{list-of } (l\text{list-of-tlss tlss}) \rightarrow^* s'$  **by** (rule  $\tau\text{rtrancl3p-step}$ )  
**with step** **have**  $s -\tau - [] @ \text{tl} \# \text{list-of } (l\text{list-of-tlss tlss}) \rightarrow^* s'$   
**by** (rule  $\tau\text{rtrancl3p-trans}[OF \text{ silent-moves-into-}\tau\text{rtrancl3p}]$ )  
**thus**  $?case$  **using**  $\langle \text{tfinite tlss} \rangle$  **by simp**  
**qed**

**lemma**  $\tau\text{Runs-terminal-stuck}$ :

**assumes**  $\text{Runs}: s \Downarrow \text{tlss}$   
**and**  $\text{fin}: \text{tfinite tlss}$   
**and**  $\text{terminal}: \text{terminal tlss} = \text{Some } s'$   
**and**  $\text{proceed}: s' -\text{tls} \rightarrow s''$   
**shows**  $\text{False}$   
**using**  $\text{fin Runs terminal}$   
**proof** (induct arbitrary:  $s$  rule:  $\text{tfinite-induct}$ )  
**case**  $TNil$  **thus**  $?case$  **using**  $\text{proceed}$  **by cases auto**  
**next**  
**case**  $TCons$  **thus**  $?case$  **by** (fastforce elim:  $\tau\text{Runs.cases}$ )  
**qed**

**lemma**  $\text{Runs-table-silent-diverge}$ :

$$\llbracket \text{Runs-table } s \text{ stlss}; \forall (s, \text{tl}, s') \in \text{lset stlss}. \tau\text{move } s \text{ tl } s'; \neg \text{lfinite stlss} \rrbracket$$
  

$$\implies s -\tau \rightarrow \infty$$
  
**proof** (coinduction arbitrary:  $s \text{ stlss}$ )  
**case**  $(\tau\text{diverge } s)$   
**thus**  $?case$  **by cases** (auto 5 2)  
**qed**

**lemma**  $\text{Runs-table-silent-rtrancl}$ :

**assumes**  $\text{lfinite stlss}$   
**and**  $\text{Runs-table } s \text{ stlss}$   
**and**  $\forall (s, \text{tl}, s') \in \text{lset stlss}. \tau\text{move } s \text{ tl } s'$   
**shows**  $s -\tau \rightarrow^* \text{llast } (LCons \text{ s } (lmap (\lambda(s, \text{tl}, s'). s') \text{ stlss}))$  (**is**  $?thesis1$ )  
**and**  $\text{llast } (LCons \text{ s } (lmap (\lambda(s, \text{tl}, s'). s') \text{ stlss})) -\text{tl}' \rightarrow s'' \implies \text{False}$  (**is**  $PROP ?thesis2$ )  
**proof** –  
**from**  $\text{assms}$  **have**  $?thesis1 \wedge (\text{llast } (LCons \text{ s } (lmap (\lambda(s, \text{tl}, s'). s') \text{ stlss})) -\text{tl}' \rightarrow s'' \longrightarrow \text{False})$   
**proof** (induct arbitrary:  $s$ )  
**case**  $\text{lfinite-LNil}$  **thus**  $?case$  **by** (auto elim:  $\text{Runs-table.cases}$ )  
**next**  
**case**  $(\text{lfinite-LConsI stlss stls})$   
**from**  $\langle \text{Runs-table } s (LCons \text{ stls stlss}) \rangle$   
**obtain**  $\text{tl } s'$  **where**  $[\text{simp}]: \text{stls} = (s, \text{tl}, s')$   
**and**  $s -\text{tl} \rightarrow s'$  **and**  $\text{Run}': \text{Runs-table } s' \text{ stlss}$  **by cases**  
**from**  $\langle \forall (s, \text{tl}, s') \in \text{lset } (LCons \text{ stls stlss}). \tau\text{move } s \text{ tl } s' \rangle$   
**have**  $\tau\text{move } s \text{ tl } s'$  **and**  $\text{silent}': \forall (s, \text{tl}, s') \in \text{lset stlss}. \tau\text{move } s \text{ tl } s'$  **by simp-all**  
**from**  $\langle s -\text{tl} \rightarrow s' \rangle \langle \tau\text{move } s \text{ tl } s' \rangle$  **have**  $s -\tau \rightarrow s'$  **by auto**  
**moreover from**  $\text{Run}' \text{ silent}'$   
**have**  $s' -\tau \rightarrow^* \text{llast } (LCons \text{ s}' (lmap (\lambda(s, \text{tl}, s'). s') \text{ stlss})) \wedge$   

$$(\text{llast } (LCons \text{ s}' (lmap (\lambda(s, \text{tl}, s'). s') \text{ stlss})) -\text{tl}' \rightarrow s'' \longrightarrow \text{False})$$
  
**by** (rule  $\text{lfinite-LConsI}$ )  
**ultimately show**  $?case$  **by** (auto)  
**qed**

**thus** *?thesis1 PROP ?thesis2* **by** *blast+*  
**qed**

**lemma** *Runs-table-silent-lappendD*:

**fixes** *s stlss*  
**defines**  $s' \equiv \text{llast } (LCons\ s\ (\text{lmap } (\lambda(s, tl, s').\ s')\ stlss))$   
**assumes** *Runs: Runs-table s (lappend stlss stlss')*  
**and** *fin: lfinite stlss*  
**and** *silent:  $\forall (s, tl, s') \in \text{lset stlss}. \tau\text{move } s\ tl\ s'$*   
**shows**  $s - \tau \rightarrow^* s'$  (**is** *?thesis1*)  
**and** *Runs-table s' stlss'* (**is** *?thesis2*)  
**and**  $stlss' \neq LNil \implies s' = \text{fst } (\text{lhs } stlss')$  (**is** *PROP ?thesis3*)  
**proof** –  
**from** *fin Runs silent*  
**have**  $?thesis1 \wedge ?thesis2 \wedge (stlss' \neq LNil \longrightarrow s' = \text{fst } (\text{lhs } stlss'))$   
**unfolding** *s'-def*  
**proof**(*induct arbitrary: s*)  
**case** *lfinite-LNil* **thus** *?case*  
**by**(*auto simp add: neq-LNil-conv Runs-table-simps*)  
**next**  
**case** *lfinite-LConsI* **thus** *?case*  
**by**(*clarsimp simp add: neq-LNil-conv Runs-table-simps*)(*blast intro: converse-rtrancpl-into-rtrancpl*)  
**qed**  
**thus** *?thesis1 ?thesis2 PROP ?thesis3* **by** *simp-all*  
**qed**

**lemma** *Runs-table-into- $\tau$ Runs*:

**fixes** *s stlss*  
**defines**  $tls \equiv \text{tmap } (\lambda(s, tl, s').\ tl)\ id\ (\text{tfilter } None\ (\lambda(s, tl, s').\ \neg \tau\text{move } s\ tl\ s'))\ (\text{tlist-of-llist } (\text{Some } (\text{llast } (LCons\ s\ (\text{lmap } (\lambda(s, tl, s').\ s')\ stlss))))\ stlss)$   
**(is -  $\equiv ?conv\ s\ stlss$ )**  
**assumes** *Runs-table s stlss*  
**shows**  $\tau\text{Runs } s\ tls$   
**using** *assms*  
**proof**(*coinduction arbitrary: s tls stlss*)  
**case**  $(\tau\text{Runs } s\ tls\ stlss)$   
**note**  $tls = \langle tls = ?conv\ s\ stlss \rangle$   
**and**  $Run = \langle \text{Runs-table } s\ stlss \rangle$   
**show** *?case*  
**proof**(*cases tls*)  
**case** [*simp*]: (*TNil so*)  
**from** *tls*  
**have** *silent:  $\forall (s, tl, s') \in \text{lset stlss}. \tau\text{move } s\ tl\ s'$*   
**by**(*auto simp add: TNil-eq-tmap-conv tfilter-empty-conv*)  
**show** *?thesis*  
**proof**(*cases lfinite stlss*)  
**case** *False*  
**with** *Run silent* **have**  $s - \tau \rightarrow \infty$  **by**(*rule Runs-table-silent-diverge*)  
**hence** *?Diverge* **using** *False tls* **by**(*simp add: TNil-eq-tmap-conv tfilter-empty-conv*)  
**thus** *?thesis* **by** *simp*  
**next**  
**case** *True*  
**with** *Runs-table-silent-rtrancpl*[*OF this Run silent*]  
**have** *?Terminate* **using** *tls*

```

    by(auto simp add: TNil-eq-tmap-conv tfilter-empty-conv terminal-tllist-of-llist split-def)
  thus ?thesis by simp
qed
next
case [simp]: (TCons tl tls')
from tls obtain s' s'' stlss'
  where tl': tfilter None ( $\lambda(s, tl, s'). \neg \tau \text{move } s \text{ } tl \text{ } s'$ ) (tllist-of-llist [llast (LCons s (lmap ( $\lambda(s, tl, s'). s')$ ) stlss))]) stlss = TCons (s', tl, s'') stlss'
  and tls': tls' = tmap ( $\lambda(s, tl, s'). tl$ ) id stlss'
  by(simp add: TCons-eq-tmap-conv split-def id-def split-paired-Ex) blast
from tfilter-eq-TConsD[OF tl']
obtain stls $\tau$  rest
  where stlss-eq: tllist-of-llist [llast (LCons s (lmap ( $\lambda(s, tl, s'). s')$ ) stlss))]) stlss = lappendt stls $\tau$  (TCons (s', tl, s'') rest)
  and fin: lfinite stls $\tau$ 
  and silent:  $\forall (s, tl, s') \in \text{lset } stls\tau. \tau \text{move } s \text{ } tl \text{ } s'$ 
  and  $\neg \tau \text{move } s' \text{ } tl \text{ } s''$ 
  and stlss': stlss' = tfilter None ( $\lambda(s, tl, s'). \neg \tau \text{move } s \text{ } tl \text{ } s')$  rest
  by(auto simp add: split-def)
from stlss-eq fin obtain rest'
  where stlss: stlss = lappend stls $\tau$  rest'
  and rest': tllist-of-llist [llast (LCons s (lmap ( $\lambda(s, tl, s'). s')$ ) stlss))]) rest' = TCons (s', tl, s'')
rest
  unfolding tllist-of-llist-eq-lappendt-conv by auto
hence rest'  $\neq$  LNil by clarsimp
from Run[unfolded stlss] fin silent
have s  $\neg \tau \rightarrow^*$  llast (LCons s (lmap ( $\lambda(s, tl, s'). s')$ ) stls $\tau$ ))
  and Runs-table (llast (LCons s (lmap ( $\lambda(s, tl, s'). s')$ ) stls $\tau$ ))) rest'
  and llast (LCons s (lmap ( $\lambda(s, tl, s'). s')$ ) stls $\tau$ )) = fst (lhd rest')
  by(rule Runs-table-silent-lappendD)+(simp add:  $\langle \text{rest}' \neq \text{LNil} \rangle$ )
moreover with rest'  $\langle \text{rest}' \neq \text{LNil} \rangle$  stlss fin obtain rest''
  where rest': rest' = LCons (s', tl, s'') rest''
  and rest: rest = tllist-of-llist [llast (LCons s'' (lmap ( $\lambda(s, tl, s'). s')$ ) rest''))]) rest''
  by(clarsimp simp add: neq-LNil-conv llast-LCons lmap-lappend-distrib)
ultimately have s  $\neg \tau \rightarrow^*$  s' s' -tl $\rightarrow$  s'' Runs-table s'' rest''
  by(simp-all add: Runs-table-simps)
hence ?Proceed using  $\langle \neg \tau \text{move } s' \text{ } tl \text{ } s'' \rangle$  tls' stlss' rest
  by(auto simp add: id-def)
thus ?thesis by simp
qed
qed

lemma  $\tau$ Runs-table2-into- $\tau$ Runs:
   $\tau$ Runs-table2 s tlstlss
 $\implies s \Downarrow \text{tmap } (\lambda(tls, s', tl, s''). tl) (\lambda x. \text{case } x \text{ of } \text{Inl } (tls, s') \Rightarrow \text{Some } s' \mid \text{Inr } - \Rightarrow \text{None}) \text{ tlstlss}$ 
proof(coinduction arbitrary: s tlstlss)
  case ( $\tau$ Runs s tlstlss)
  thus ?case by cases(auto intro: silent-moves2-into-silent-moves inf-step-silent-move2-into- $\tau$ diverge)
qed

lemma  $\tau$ Runs-into- $\tau$ Runs-table2:
  assumes s  $\Downarrow$  tls
  obtains tlstlss
  where  $\tau$ Runs-table2 s tlstlss

```

**and**  $tls = tmap (\lambda(tls, s', tl, s''). tl) (\lambda x. case x of Inl (tls, s') \Rightarrow Some s' \mid Inr - \Rightarrow None) tlstlss$   
**proof** –  
**let**  $?terminal = \lambda s\ tls. case terminal\ tls\ of$   
      $None \Rightarrow Inr (SOME\ tls'. trsys.inf-step\ silent-move2\ s\ tls')$   
      $\mid Some\ s' \Rightarrow let\ tls' = SOME\ tls'. silent-moves2\ s\ tls'\ s' in Inl (tls', s')$   
**let**  $?P = \lambda s\ tls\ (tls'', s', s''). silent-moves2\ s\ tls''\ s' \wedge s' - thd\ tls \rightarrow s'' \wedge \neg \tau move\ s' (thd\ tls)\ s'' \wedge$   
 $s'' \Downarrow ttl\ tls$   
**define**  $tlstlss$  **where**  $tlstlss\ s\ tls = unfold-tllist$   
      $(\lambda(s, tls). is-TNil\ tls)$   
      $(\lambda(s, tls). ?terminal\ s\ tls)$   
      $(\lambda(s, tls). let\ (tls'', s', s'') = Eps\ (?P\ s\ tls)\ in\ (tls'', s', thd\ tls, s''))$   
      $(\lambda(s, tls). let\ (tls'', s', s'') = Eps\ (?P\ s\ tls)\ in\ (s'', ttl\ tls))$   
      $(s, tls)$   
**for**  $s\ tls$   
  
**have**  $[simp]:$   
      $\bigwedge s\ tls. is-TNil\ (tlstlss\ s\ tls) \longleftrightarrow is-TNil\ tls$   
      $\bigwedge s\ tls. is-TNil\ tls \implies terminal\ (tlstlss\ s\ tls) = ?terminal\ s\ tls$   
      $\bigwedge s\ tls. \neg is-TNil\ tls \implies thd\ (tlstlss\ s\ tls) = (let\ (tls'', s', s'') = Eps\ (?P\ s\ tls)\ in\ (tls'', s', thd\ tls,$   
 $s''))$   
      $\bigwedge s\ tls. \neg is-TNil\ tls \implies ttl\ (tlstlss\ s\ tls) = (let\ (tls'', s', s'') = Eps\ (?P\ s\ tls)\ in\ tlstlss\ s''\ (ttl\ tls))$   
     **by**  $(simp-all\ add: tlstlss-def\ split-beta)$   
  
**have**  $[simp]:$   
      $\bigwedge s. tlstlss\ s\ (TNil\ None) = TNil\ (Inr\ (SOME\ tls'. trsys.inf-step\ silent-move2\ s\ tls'))$   
      $\bigwedge s\ s'. tlstlss\ s\ (TNil\ (Some\ s')) = TNil\ (Inl\ (SOME\ tls'. silent-moves2\ s\ tls'\ s', s'))$   
     **unfolding**  $tlstlss-def$  **by**  $simp-all$   
  
**let**  $?conv = tmap (\lambda(tls, s', tl, s''). tl) (\lambda x. case x of Inl (tls, s') \Rightarrow Some s' \mid Inr - \Rightarrow None)$   
**from**  $assms$  **have**  $\tauRuns-table2\ s\ (tlstlss\ s\ tls)$   
**proof**  $(coinduction\ arbitrary: s\ tls)$   
     **case**  $(\tauRuns-table2\ s\ tls)$   
     **thus**  $?case$   
     **proof**  $(cases)$   
         **case**  $(Terminate\ s')$   
         **let**  $?P = \lambda tls'. silent-moves2\ s\ tls'\ s'$   
         **from**  $\langle s - \tau \rightarrow^* s' \rangle$  **obtain**  $tls'$  **where**  $?P\ tls'$  **by**  $(blast\ dest: silent-moves-into-silent-moves2)$   
         **hence**  $?P\ (Eps\ ?P)$  **by**  $(rule\ someI)$   
         **with**  $Terminate$  **have**  $?Terminate$  **by**  $auto$   
         **thus**  $?thesis$  **by**  $simp$   
     **next**  
         **case**  $Diverge$   
         **let**  $?P = \lambda tls'. trsys.inf-step\ silent-move2\ s\ tls'$   
         **from**  $\langle s - \tau \rightarrow^* \infty \rangle$  **obtain**  $tls'$  **where**  $?P\ tls'$  **by**  $(rule\ \tau diverge-into-inf-step-silent-move2)$   
         **hence**  $?P\ (Eps\ ?P)$  **by**  $(rule\ someI)$   
         **hence**  $?Diverge$  **using**  $\langle tls = TNil\ None \rangle$  **by**  $simp$   
         **thus**  $?thesis$  **by**  $simp$   
     **next**  
         **case**  $(Proceed\ s'\ s''\ tls'\ tl)$   
         **from**  $\langle s - \tau \rightarrow^* s' \rangle$  **obtain**  $tls''$  **where**  $silent-moves2\ s\ tls''\ s'$   
         **by**  $(blast\ dest: silent-moves-into-silent-moves2)$   
         **with**  $Proceed$  **have**  $?P\ s\ tls\ (tls'', s', s'')$  **by**  $simp$   
         **hence**  $?P\ s\ tls\ (Eps\ (?P\ s\ tls))$  **by**  $(rule\ someI)$   
         **hence**  $?Proceed$  **using**  $Proceed$  **unfolding**  $tlstlss-def$

```

    by(subst unfold-tllist.code)(auto simp add: split-def)
  thus ?thesis by simp
qed
qed
moreover
from assms have tls = ?conv (tlstlss s tls)
proof(coinduction arbitrary: s tls)
  case (Eq-tllist s tls)
  thus ?case
  proof(cases)
    case (Proceed s' s'' tls' tl)
    from ⟨s -τ→* s'⟩ obtain tls'' where silent-moves2 s tls'' s'
    by(blast dest: silent-moves-into-silent-moves2)
    with Proceed have ?P s tls (tls'', s', s'') by simp
    hence ?P s tls (Eps (?P s tls)) by(rule someI)
    thus ?thesis using ⟨tls = TCons tl tls'⟩ by auto
  qed auto
qed
ultimately show thesis by(rule that)
qed

lemma τRuns-table2-into-Runs:
  assumes τRuns-table2 s tlstlss
  shows Runs s (lconcat (lappend (lmap (λ(tls, s, tl, s'). llist-of (tls @ [tl])) (llist-of-tllist tlstlss))
  (LCons (case terminal tlstlss of Inl (tls, s') ⇒ llist-of tls | Inr tls ⇒ tls) LNil)))
  (is Runs - (?conv tlstlss))
using assms
proof(coinduction arbitrary: s tlstlss)
  case (Runs s tlstlss)
  thus ?case
  proof(cases)
    case (Terminate tls' s')
    from ⟨silent-moves2 s tls' s'⟩ show ?thesis
    proof(cases rule: rtrancl3p-converseE)
      case refl
      hence ?Stuck using Terminate by simp
      thus ?thesis ..
    next
      case (step tls'' tl s'')
      from ⟨silent-moves2 s'' tls'' s'⟩ ⟨tl s''. ¬ s' -tl→ s''⟩
      have τRuns-table2 s'' (TNil (Inl (tls'', s'))) ..
      with ⟨tls' = tl # tls''⟩ ⟨silent-move2 s tl s''⟩ ⟨tlstlss = TNil (Inl (tls', s'))⟩
      have ?Step by(auto simp add: silent-move2-def intro!: exI)
      thus ?thesis ..
    qed
  next
    case (Diverge tls')
    from ⟨trsys.inf-step silent-move2 s tls'⟩
    obtain tl tls'' s' where silent-move2 s tl s'
    and tls' = LCons tl tls'' trsys.inf-step silent-move2 s' tls''
    by(cases rule: trsys.inf-step.cases[consumes 1]) auto
    from ⟨trsys.inf-step silent-move2 s' tls''⟩
    have τRuns-table2 s' (TNil (Inr tls'')) ..
    hence ?Step using ⟨tlstlss = TNil (Inr tls')⟩ ⟨tls' = LCons tl tls''⟩ ⟨silent-move2 s tl s'⟩

```

```

    by(auto simp add: silent-move2-def intro!: exI)
  thus ?thesis ..
next
  case (Proceed tls' s' s'' tlstlss' tl)
  from ⟨silent-moves2 s tls' s'⟩ have ?Step
  proof(cases rule: rtrancl3p-converseE)
    case refl with Proceed show ?thesis by auto
  next
    case (step tls'' tl' s''')
    from ⟨silent-moves2 s''' tls'' s'⟩ ⟨s' -tl→ s''⟩ ⟨¬ τmove s' tl s''⟩ ⟨τRuns-table2 s'' tlstlss'⟩
    have τRuns-table2 s''' (TCons (tls'', s', tl, s'') tlstlss') ..
    with ⟨tls' = tl' # tls''⟩ ⟨silent-move2 s tl' s'''⟩ ⟨tlstlss = TCons (tls', s', tl, s'') tlstlss'⟩
    show ?thesis by(auto simp add: silent-move2-def intro!: exI)
  qed
  thus ?thesis ..
qed
qed

```

```

lemma τRuns-table2-silentsD:
  fixes tl
  assumes Runs: τRuns-table2 s tlstlss
  and tset: (tls, s', tl', s'') ∈ tset tlstlss
  and set: tl ∈ set tls
  shows ∃ s''' s'''. silent-move2 s''' tl s'''
using tset Runs
proof(induct arbitrary: s rule: tset-induct)
  case (find tlstlss')
  from ⟨τRuns-table2 s (TCons (tls, s', tl', s'') tlstlss')⟩
  have silent-moves2 s tls s' by cases
  thus ?case using set by induct auto
next
  case step thus ?case by(auto simp add: τRuns-table2-simps)
qed

```

```

lemma τRuns-table2-terminal-silentsD:
  assumes Runs: τRuns-table2 s tlstlss
  and fin: lfinite (llist-of-tllist tlstlss)
  and terminal: terminal tlstlss = Inl (tls, s'')
  shows ∃ s'. silent-moves2 s' tls s''
using fin Runs terminal
proof(induct llist-of-tllist tlstlss arbitrary: tlstlss s)
  case lfinite-LNil thus ?case
    by(cases tlstlss)(auto simp add: τRuns-table2-simps)
next
  case (lfinite-LConsI xs tlstls)
  thus ?case by(cases tlstlss)(auto simp add: τRuns-table2-simps)
qed

```

```

lemma τRuns-table2-terminal-inf-stepD:
  assumes Runs: τRuns-table2 s tlstlss
  and fin: lfinite (llist-of-tllist tlstlss)
  and terminal: terminal tlstlss = Inr tls
  shows ∃ s'. trsys.inf-step silent-move2 s' tls
using fin Runs terminal

```

```

proof(induct llist-of-tlist tlstlss arbitrary: s tlstlss)
  case lfinite-LNil thus ?case
    by(cases tlstlss)(auto simp add:  $\tau$ Runs-table2-simps)
next
  case (lfinite-LConsI xs tlstls)
  thus ?case by(cases tlstlss)(auto simp add:  $\tau$ Runs-table2-simps)
qed

lemma  $\tau$ Runs-table2-lappendtD:
  assumes Runs:  $\tau$ Runs-table2 s (lappendt tlstlss tlstlss')
  and fin: lfinite tlstlss
  shows  $\exists s'. \tau$ Runs-table2 s' tlstlss'
using fin Runs
by(induct arbitrary: s)(auto simp add:  $\tau$ Runs-table2-simps)

end

lemma  $\tau$ moves-False:  $\tau$ trsys.silent-move r ( $\lambda s$  ta s'. False) = ( $\lambda s$  s'. False)
by(auto simp add:  $\tau$ trsys.silent-move-iff)

lemma  $\tau$ rtrancl3p-False-eq-rtrancl3p:  $\tau$ trsys. $\tau$ rtrancl3p r ( $\lambda s$  tl s'. False) = rtrancl3p r
proof(intro ext iffI)
  fix s tls s'
  assume  $\tau$ trsys. $\tau$ rtrancl3p r ( $\lambda s$  tl s'. False) s tls s'
  thus rtrancl3p r s tls s' by(rule  $\tau$ trsys. $\tau$ rtrancl3p.induct)(blast intro: rtrancl3p-step-converse)+
next
  fix s tls s'
  assume rtrancl3p r s tls s'
  thus  $\tau$ trsys. $\tau$ rtrancl3p r ( $\lambda s$  tl s'. False) s tls s'
    by(induct rule: rtrancl3p-converse-induct)(auto intro:  $\tau$ trsys. $\tau$ rtrancl3p.intros)
qed

lemma  $\tau$ diverge-empty- $\tau$ move:
   $\tau$ trsys. $\tau$ diverge r ( $\lambda s$  ta s'. False) = ( $\lambda s$ . False)
by(auto intro!: ext elim:  $\tau$ trsys. $\tau$ diverge.cases  $\tau$ trsys.silent-move.cases)

end

```

## 1.16 The multithreaded semantics as a labelled transition system

```

theory FWLTS
imports
  FWProgressAux
  FWLifting
  LTS
begin

```

```

sublocale multithreaded-base < trsys r t for t .
sublocale multithreaded-base < mthr: trsys redT .

```

— Move to FWSemantics?

```

definition redT-upd- $\varepsilon$  :: ('l, 't, 'x, 'm, 'w) state  $\Rightarrow$  't  $\Rightarrow$  'x  $\Rightarrow$  'm  $\Rightarrow$  ('l, 't, 'x, 'm, 'w) state

```



**where**  $[simp]: redT\text{-}upd\text{-}\varepsilon\ s\ t\ x'\ m' = (locks\ s,\ ((thr\ s)(t \mapsto (x',\ snd\ (the\ (thr\ s\ t))))),\ m'),\ wset\ s,\ interrupts\ s)$

**lemma**  $redT\text{-}upd\text{-}\varepsilon\text{-}redT\text{-}upd$ :

$redT\text{-}upd\ s\ t\ \varepsilon\ x'\ m'\ (redT\text{-}upd\text{-}\varepsilon\ s\ t\ x'\ m')$

**by**( $auto\ simp\ add: redT\text{-}updLns\text{-}def\ redT\text{-}updWs\text{-}def$ )

**context**  $multithreaded$  **begin**

**sublocale**  $trsys\ r\ t$  **for**  $t$  .

**sublocale**  $mthr: trsys\ redT$  .

**end**

### 1.16.1 The multithreaded semantics with internal actions

**type-synonym**

$(l, t, x, m, w, o)\ \tau moves =$   
 $x \times m \Rightarrow (l, t, x, m, w, o)\ thread\text{-}action \Rightarrow x \times m \Rightarrow bool$

pretty printing for  $\tau moves$

**print-translation**  $\langle$

$let$   
 $fun\ tr'\ [(Const\ (@\{type\text{-}syntax\ prod\},\ -)\ \$\ x1\ \$\ m1),$   
 $(Const\ (@\{type\text{-}syntax\ fun\},\ -)\ \$$   
 $(Const\ (@\{type\text{-}syntax\ prod\},\ -)\ \$$   
 $(Const\ (@\{type\text{-}syntax\ finfun\},\ -)\ \$\ l\ \$$   
 $(Const\ (@\{type\text{-}syntax\ list\},\ -)\ \$\ Const\ (@\{type\text{-}syntax\ lock\text{-}action\},\ -)))\ \$$   
 $(Const\ (@\{type\text{-}syntax\ prod\},\ -)\ \$$   
 $(Const\ (@\{type\text{-}syntax\ list\},\ -)\ \$\ (Const\ (@\{type\text{-}syntax\ new\text{-}thread\text{-}action\},\ -)\ \$\ t1\ \$$   
 $x2\ \$\ m2)))\ \$$   
 $(Const\ (@\{type\text{-}syntax\ prod\},\ -)\ \$$   
 $(Const\ (@\{type\text{-}syntax\ list\},\ -)\ \$\ (Const\ (@\{type\text{-}syntax\ conditional\text{-}action\},\ -)\ \$\ t2)))$   
 $\$$   
 $(Const\ (@\{type\text{-}syntax\ prod\},\ -)\ \$$   
 $(Const\ (@\{type\text{-}syntax\ list\},\ -)\ \$\ (Const\ (@\{type\text{-}syntax\ wait\text{-}set\text{-}action\},\ -)\ \$\ t3\ \$$   
 $w)))\ \$$   
 $(Const\ (@\{type\text{-}syntax\ prod\},\ -)\ \$$   
 $(Const\ (@\{type\text{-}syntax\ list\},\ -)\ \$\ (Const\ (@\{type\text{-}syntax\ interrupt\text{-}action\},\ -)\ \$$   
 $t4)))\ \$$   
 $(Const\ (@\{type\text{-}syntax\ list\},\ -)\ \$\ o1))))))\ \$$   
 $(Const\ (@\{type\text{-}syntax\ fun\},\ -)\ \$$   
 $(Const\ (@\{type\text{-}syntax\ prod\},\ -)\ \$\ x3\ \$\ m3)\ \$$   
 $(Const\ (@\{type\text{-}syntax\ bool\},\ -))))\ =$   
 $if\ x1 = x2\ andalso\ x1 = x3\ andalso\ m1 = m2\ andalso\ m1 = m3\ andalso\ t1 = t2\ andalso\ t2 =$   
 $t3\ andalso\ t3 = t4$   
 $then\ Syntax.const\ (@\{type\text{-}syntax\ \tau moves\})\ \$\ l\ \$\ t1\ \$\ x1\ \$\ m1\ \$\ w\ \$\ o1$   
 $else\ raise\ Match;$   
 $in\ [(@\{type\text{-}syntax\ fun\},\ K\ tr')$   
 $end$   
 $\rangle$   
**typ**  $(l, t, x, m, w, o)\ \tau moves$

**locale**  $\tau\text{multithreaded} = \text{multithreaded-base} +$   
**constrains**  $\text{final} :: 'x \Rightarrow \text{bool}$   
**and**  $r :: ('l, 't, 'x, 'm, 'w, 'o) \text{ semantics}$   
**and**  $\text{convert-RA} :: 'l \text{ released-locks} \Rightarrow 'o \text{ list}$   
**fixes**  $\tau\text{move} :: ('l, 't, 'x, 'm, 'w, 'o) \tau\text{moves}$

**sublocale**  $\tau\text{multithreaded} < \tau\text{trsys } r \ t \ \tau\text{move} \text{ for } t .$

**context**  $\tau\text{multithreaded} \text{ begin}$

**inductive**  $m\tau\text{move} :: (('l, 't, 'x, 'm, 'w) \text{ state}, 't \times ('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action}) \text{ trsys}$   
**where**  
 $\llbracket \text{thr } s \ t = \lfloor (x, \text{no-wait-locks}) \rfloor; \text{thr } s' \ t = \lfloor (x', \text{ln}') \rfloor; \tau\text{move } (x, \text{shr } s) \text{ ta } (x', \text{shr } s') \rrbracket$   
 $\implies m\tau\text{move } s \ (t, \text{ta}) \ s'$

**end**

**sublocale**  $\tau\text{multithreaded} < \text{mthr}: \tau\text{trsys } \text{redT} \ m\tau\text{move} .$

**context**  $\tau\text{multithreaded} \text{ begin}$

**abbreviation**  $\tau\text{mredT} :: ('l, 't, 'x, 'm, 'w) \text{ state} \Rightarrow ('l, 't, 'x, 'm, 'w) \text{ state} \Rightarrow \text{bool}$   
**where**  $\tau\text{mredT} == \text{mthr.silent-move}$

**end**

**lemma** (**in**  $\text{multithreaded-base}$ )  $\tau\text{rtranc}3p\text{-redT-thread-not-disappear}$ :

**assumes**  $\tau\text{trsys}.\tau\text{rtranc}3p \ \text{redT} \ \tau\text{move } s \ \text{ttas } s' \ \text{thr } s \ t \neq \text{None}$   
**shows**  $\text{thr } s' \ t \neq \text{None}$

**proof** –

**interpret**  $T: \tau\text{trsys } \text{redT} \ \tau\text{move} .$

**show**  $?thesis$

**proof**

**assume**  $\text{thr } s' \ t = \text{None}$

**with**  $\langle \tau\text{trsys}.\tau\text{rtranc}3p \ \text{redT} \ \tau\text{move } s \ \text{ttas } s' \rangle$  **have**  $\text{thr } s \ t = \text{None}$

**by** (*induct rule: T. $\tau\text{rtranc}3p$ .induct*) (*auto simp add: split-paired-all dest: redT-thread-not-disappear*)

**with**  $\langle \text{thr } s \ t \neq \text{None} \rangle$  **show**  $\text{False}$  **by** *contradiction*

**qed**

**qed**

**lemma**  $m\tau\text{move-False}: \tau\text{multithreaded}.m\tau\text{move } (\lambda s \text{ ta } s'. \text{False}) = (\lambda s \text{ ta } s'. \text{False})$

**by** (*auto intro!: ext elim:  $\tau\text{multithreaded}.m\tau\text{move}.\text{cases}$* )

**declare**  $\text{split-paired-Ex} \ [simp \ del]$

**locale**  $\tau\text{multithreaded-wf} =$

$\tau\text{multithreaded} - - \tau\text{move} +$

$\text{multithreaded } \text{final } r \ \text{convert-RA}$

**for**  $\tau\text{move} :: ('l, 't, 'x, 'm, 'w, 'o) \tau\text{moves} +$

**assumes**  $\tau\text{move-heap}: \llbracket t \vdash (x, m) -\text{ta} \rightarrow (x', m'); \tau\text{move } (x, m) \text{ ta } (x', m') \rrbracket \implies m = m'$

**assumes**  $\text{silent-tl}: \tau\text{move } s \text{ ta } s' \implies \text{ta} = \varepsilon$

**begin**

**lemma**  $m\tau\text{move-silentD}: m\tau\text{move } s \ (t, \text{ta}) \ s' \implies \text{ta} = (K\$ \ [], [], [], [], [], [])$

by(auto elim!: m $\tau$ move.cases dest: silent-tl)

**lemma** m $\tau$ move-heap:

assumes redT: redT s (t, ta) s'  
and m $\tau$ move: m $\tau$ move s (t, ta) s'  
shows shr s' = shr s

using m $\tau$ move redT

by cases(auto dest:  $\tau$ move-heap elim!: redT.cases)

**lemma**  $\tau$ mredT-thread-preserved:

$\tau$ mredT s s'  $\implies$  thr s t = None  $\longleftrightarrow$  thr s' t = None

by(auto simp add: mthr.silent-move-iff elim!: redT.cases dest!: m $\tau$ move-silentD split: if-split-asm)

**lemma**  $\tau$ mRedT-thread-preserved:

$\tau$ mredT<sup>\*\*</sup> s s'  $\implies$  thr s t = None  $\longleftrightarrow$  thr s' t = None

by(induct rule: rtrancpl.induct)(auto dest:  $\tau$ mredT-thread-preserved[where t=t])

**lemma**  $\tau$ mtRedT-thread-preserved:

$\tau$ mredT<sup>++</sup> s s'  $\implies$  thr s t = None  $\longleftrightarrow$  thr s' t = None

by(induct rule: trancpl.induct)(auto dest:  $\tau$ mredT-thread-preserved[where t=t])

**lemma**  $\tau$ mredT-add-thread-inv:

assumes  $\tau$ red:  $\tau$ mredT s s' and tst: thr s t = None

shows  $\tau$ mredT (locks s, ((thr s)(t  $\mapsto$  xln), shr s), wset s, interrupts s) (locks s', ((thr s')(t  $\mapsto$  xln), shr s'), wset s', interrupts s')

**proof** –

obtain ls ts m ws is where s: s = (ls, (ts, m), ws, is) by(cases s) fastforce

obtain ls' ts' m' ws' is' where s': s' = (ls', (ts', m'), ws', is') by(cases s') fastforce

from  $\tau$ red s s' obtain t' where red: (ls, (ts, m), ws, is)  $\rightarrow$  t'  $\vdash \varepsilon \rightarrow$  (ls', (ts', m'), ws', is')

and  $\tau$ : m $\tau$ move (ls, (ts, m), ws, is) (t',  $\varepsilon$ ) (ls', (ts', m'), ws', is')

by(auto simp add: mthr.silent-move-iff dest: m $\tau$ move-silentD)

from red have (ls, (ts(t  $\mapsto$  xln), m), ws, is)  $\rightarrow$  t'  $\vdash \varepsilon \rightarrow$  (ls', (ts'(t  $\mapsto$  xln), m'), ws', is')

**proof**(cases rule: redT-elim)

case (normal x x' m') with tst s show ?thesis

by–(rule redT-normal, auto simp add: fun-upd-twist elim!: rtrancpl3p-cases)

next

case (acquire x ln n)

with tst s show ?thesis

unfolding  $\langle \varepsilon = (K\$ \square, \square, \square, \square, \square, \text{convert-RA } \text{ln}) \rangle$

by –(rule redT-acquire, auto intro: fun-upd-twist)

qed

moreover from red tst s have tt': t  $\neq$  t' by(cases) auto

have  $(\lambda t''. (ts(t \mapsto xln)) t'' \neq \text{None} \wedge (ts(t \mapsto xln)) t'' \neq (ts'(t \mapsto xln)) t'') =$

$(\lambda t''. ts t'' \neq \text{None} \wedge ts t'' \neq ts' t'')$  using tst s by(auto simp add: fun-eq-iff)

with  $\tau$  tst tt' have m $\tau$ move (ls, (ts(t  $\mapsto$  xln), m), ws, is) (t',  $\varepsilon$ ) (ls', (ts'(t  $\mapsto$  xln), m'), ws', is')

by cases(rule m $\tau$ move.intros, auto)

ultimately show ?thesis unfolding s s' by auto

qed

**lemma**  $\tau$ mRedT-add-thread-inv:

$\llbracket \tau$ mredT<sup>\*\*</sup> s s'; thr s t = None  $\rrbracket$

$\implies \tau$ mredT<sup>\*\*</sup> (locks s, ((thr s)(t  $\mapsto$  xln), shr s), wset s, interrupts s) (locks s', ((thr s')(t  $\mapsto$  xln), shr s'), wset s', interrupts s')

apply(induct rule: rtrancpl-induct)

**apply**(blast dest:  $\tau mRedT$ -thread-preserved[**where**  $t=t$ ]  $\tau mredT$ -add-thread-inv[**where**  $xln=xln$ ] intro:  $rtranclp.rtrancl$ -into- $rtrancl$ ) +  
**done**

**lemma**  $\tau mtRed$ -add-thread-inv:

$\llbracket \tau mredT^{++} s s'; thr s t = None \rrbracket$   
 $\implies \tau mredT^{++} (locks s, ((thr s)(t \mapsto xln), shr s), wset s, interrupts s) (locks s', ((thr s')(t \mapsto xln), shr s'), wset s', interrupts s')$

**apply**(induct rule:  $trancpl$ -induct)

**apply**(blast dest:  $\tau mtRedT$ -thread-preserved[**where**  $t=t$ ]  $\tau mredT$ -add-thread-inv[**where**  $xln=xln$ ] intro:  $trancpl.trancpl$ -into- $trancpl$ ) +  
**done**

**lemma**  $silent$ -move-into- $RedT$ - $\tau$ -inv:

**assumes**  $move$ :  $silent$ -move  $t (x, shr s) (x', m')$   
**and**  $state$ :  $thr s t = \llbracket (x, no-wait-locks) \rrbracket$   $wset s t = None$   
**shows**  $\tau mredT s (redT\text{-}upd\text{-}\varepsilon s t x' m')$

**proof** –

**from**  $move$  **obtain**  $red$ :  $t \vdash (x, shr s) \text{--}\varepsilon \rightarrow (x', m')$  **and**  $\tau$ :  $\tau move (x, shr s) \varepsilon (x', m')$

**by**(auto simp add:  $silent$ -move-iff dest:  $silent$ -tl)

**from**  $red$  **state** **have**  $s \text{--} t \vdash \varepsilon \rightarrow redT\text{-}upd\text{-}\varepsilon s t x' m'$

**by** –(rule  $redT$ -normal, auto simp add:  $redT\text{-}updLns$ -def o-def finfun-Diag-const2  $redT\text{-}updWs$ -def)

**moreover from**  $\tau$   $red$  **state** **have**  $m\tau move s (t, \varepsilon) (redT\text{-}upd\text{-}\varepsilon s t x' m')$

**by** –(rule  $m\tau move$ .intros, auto dest:  $\tau move$ -heap simp add:  $redT\text{-}updLns$ -def)

**ultimately show** ?thesis **by** auto

**qed**

**lemma**  $silent$ -moves-into- $RedT$ - $\tau$ -inv:

**assumes**  $major$ :  $silent$ -moves  $t (x, shr s) (x', m')$   
**and**  $state$ :  $thr s t = \llbracket (x, no-wait-locks) \rrbracket$   $wset s t = None$   
**shows**  $\tau mredT^{**} s (redT\text{-}upd\text{-}\varepsilon s t x' m')$

**using**  $major$

**proof**(induct rule:  $rtranclp$ -induct2)

**case**  $refl$  **with**  $state$  **show** ?case **by**(cases  $s$ )(auto simp add:  $fun$ -upd-idem)

**next**

**case** (step  $x' m' x'' m''$ )

**from**  $\langle silent\text{-}move t (x', m') (x'', m'') \rangle state$

**have**  $\tau mredT (redT\text{-}upd\text{-}\varepsilon s t x' m') (redT\text{-}upd\text{-}\varepsilon (redT\text{-}upd\text{-}\varepsilon s t x' m') t x'' m'')$

**by** –(rule  $silent$ -move-into- $RedT$ - $\tau$ -inv, auto)

**hence**  $\tau mredT (redT\text{-}upd\text{-}\varepsilon s t x' m') (redT\text{-}upd\text{-}\varepsilon s t x'' m'')$  **by**(simp)

**with**  $\langle \tau mredT^{**} s (redT\text{-}upd\text{-}\varepsilon s t x' m') \rangle$  **show** ?case ..

**qed**

**lemma**  $red$ - $rtrancl$ - $\tau$ -heapD-inv:

$\llbracket silent\text{-}moves t s s'; wfs t s \rrbracket \implies snd s' = snd s$

**proof**(induct rule:  $rtranclp$ -induct)

**case**  $base$  **show** ?case ..

**next**

**case** (step  $s' s''$ )

**thus** ?case **by**(cases  $s$ , cases  $s'$ , cases  $s''$ )(auto dest:  $\tau move$ -heap)

**qed**

**lemma**  $red$ - $trancpl$ - $\tau$ -heapD-inv:

$\llbracket silent\text{-}movet t s s'; wfs t s \rrbracket \implies snd s' = snd s$

```

proof(induct rule: trancpl-induct)
  case (base s') thus ?case by(cases s')(cases s, auto simp add: silent-move-iff dest:  $\tau$ move-heap)
next
  case (step s' s'')
  thus ?case by(cases s, cases s', cases s'')(auto simp add: silent-move-iff dest:  $\tau$ move-heap)
qed

```

**lemma** *red-trancl- $\tau$ -into-RedT- $\tau$ -inv*:

```

  assumes major: silent-movet t (x, shr s) (x', m')
  and state: thr s t =  $\lfloor (x, \text{no-wait-locks}) \rfloor$  wset s t = None
  shows  $\tau mredT^{++} s (\text{redT-upd-}\varepsilon s t x' m')$ 
using major
proof(induct rule: trancpl-induct2)
  case (base x' m')
  thus ?case using state
    by  $-(\text{rule } \text{trancpl.r-into-trancl}, \text{rule } \text{silent-move-into-RedT-}\tau\text{-inv}, \text{auto})$ 
next

```

```

  case (step x' m' x'' m'')
  hence  $\tau mredT^{++} s (\text{redT-upd-}\varepsilon s t x' m')$  by blast
  moreover from  $\langle \text{silent-move } t (x', m') (x'', m'') \rangle \text{ state}$ 
  have  $\tau mredT (\text{redT-upd-}\varepsilon s t x' m') (\text{redT-upd-}\varepsilon (\text{redT-upd-}\varepsilon s t x' m') t x'' m'')$ 
    by  $-(\text{rule } \text{silent-move-into-RedT-}\tau\text{-inv}, \text{auto simp add: redT-updLns-def})$ 
  hence  $\tau mredT (\text{redT-upd-}\varepsilon s t x' m') (\text{redT-upd-}\varepsilon s t x'' m'')$ 
    by(simp add: redT-updLns-def)
  ultimately show ?case ..
qed

```

**lemma**  *$\tau$ diverge-into- $\tau mredT$* :

```

  assumes  $\tau \text{diverge } t (x, \text{shr } s)$ 
  and thr s t =  $\lfloor (x, \text{no-wait-locks}) \rfloor$  wset s t = None
  shows  $mthr.\tau \text{diverge } s$ 
using assms
proof(coinduction arbitrary: s x)
  case ( $\tau \text{diverge } s x$ )
  note  $tst = \langle \text{thr } s t = \lfloor (x, \text{no-wait-locks}) \rfloor \rangle$ 
  from  $\langle \tau \text{diverge } t (x, \text{shr } s) \rangle$  obtain  $x' m'$  where  $\text{silent-move } t (x, \text{shr } s) (x', m')$ 
    and  $\tau \text{diverge } t (x', m')$  by cases auto
  from  $\langle \text{silent-move } t (x, \text{shr } s) (x', m') \rangle$   $tst \langle \text{wset } s t = \text{None} \rangle$ 
  have  $\tau mredT s (\text{redT-upd-}\varepsilon s t x' m')$  by(rule silent-move-into-RedT- $\tau$ -inv)
  moreover have  $\text{thr } (\text{redT-upd-}\varepsilon s t x' m') t = \lfloor (x', \text{no-wait-locks}) \rfloor$ 
    using  $tst$  by(auto simp add: redT-updLns-def)
  moreover have  $\text{wset } (\text{redT-upd-}\varepsilon s t x' m') t = \text{None}$  using  $\langle \text{wset } s t = \text{None} \rangle$  by simp
  moreover from  $\langle \tau \text{diverge } t (x', m') \rangle$  have  $\tau \text{diverge } t (x', \text{shr } (\text{redT-upd-}\varepsilon s t x' m'))$  by simp
  ultimately show ?case using  $\langle \tau \text{diverge } t (x', m') \rangle$  by blast
qed

```

**lemma**  *$\tau$ diverge- $\tau mredTD$* :

```

  assumes div: mthr. $\tau$ diverge s
  and fin: finite (dom (thr s))
  shows  $\exists t x. \text{thr } s t = \lfloor (x, \text{no-wait-locks}) \rfloor \wedge \text{wset } s t = \text{None} \wedge \tau \text{diverge } t (x, \text{shr } s)$ 
using fin div
proof(induct  $A \equiv \text{dom } (\text{thr } s)$  arbitrary: s rule: finite-induct)
  case empty
  from  $\langle mthr.\tau \text{diverge } s \rangle$  obtain  $s'$  where  $\tau mredT s s'$  by cases auto

```

```

with  $\langle \{ \} = \text{dom } (\text{thr } s) \rangle [\text{symmetric}]$  have False by (auto elim!: mthr.silent-move.cases redT.cases)
thus ?case ..
next
case (insert t A)
note  $IH = \langle \bigwedge s. \llbracket A = \text{dom } (\text{thr } s); \text{mthr}.\tau \text{diverge } s \rrbracket$ 
       $\implies \exists t x. \text{thr } s \ t = \llbracket (x, \text{no-wait-locks}) \rrbracket \wedge \text{wset } s \ t = \text{None} \wedge \tau \text{diverge } t \ (x, \text{shr } s) \rangle$ 
from  $\langle \text{insert } t \ A = \text{dom } (\text{thr } s) \rangle$ 
obtain  $x \ \text{ln}$  where  $\text{tst}: \text{thr } s \ t = \llbracket (x, \text{ln}) \rrbracket$  by (fastforce simp add: dom-def)
define  $s'$  where  $s' = (\text{locks } s, ((\text{thr } s)(t := \text{None}), \text{shr } s), \text{wset } s, \text{interrupts } s)$ 
show ?case
proof (cases ln = no-wait-locks  $\wedge$   $\tau \text{diverge } t \ (x, \text{shr } s) \wedge \text{wset } s \ t = \text{None}$ )
  case True
    with  $\text{tst}$  show ?thesis by blast
  next
    case False
      define  $xm$  where  $xm = (x, \text{shr } s)$ 
      define  $xm'$  where  $xm' = (x, \text{shr } s)$ 
      have  $A = \text{dom } (\text{thr } s')$  using  $\langle t \notin A \rangle \langle \text{insert } t \ A = \text{dom } (\text{thr } s) \rangle$ 
      unfolding  $s'\text{-def}$  by auto
      moreover {
        from  $xm'\text{-def}$   $\text{tst}$   $\langle \text{mthr}.\tau \text{diverge } s \rangle$  False
        have  $\exists s \ x. \text{thr } s \ t = \llbracket (x, \text{ln}) \rrbracket \wedge (\text{ln} \neq \text{no-wait-locks} \vee \text{wset } s \ t \neq \text{None} \vee \neg \tau \text{diverge } t \ xm') \wedge$ 
           $s' = (\text{locks } s, ((\text{thr } s)(t := \text{None}), \text{shr } s), \text{wset } s, \text{interrupts } s) \wedge xm = (x, \text{shr } s) \wedge$ 
           $\text{mthr}.\tau \text{diverge } s \wedge \text{silent-moves } t \ xm' \ xm$ 
        unfolding  $s'\text{-def}$   $xm\text{-def}$  by blast
      }
      moreover
        from False have  $\text{wfP}$  (if  $\tau \text{diverge } t \ xm'$  then  $(\lambda s \ s'. \text{False})$  else flip (silent-move-from  $t \ xm'$ ))
        using  $\tau \text{diverge-neq-wfP-silent-move-from}$  [of  $t \ (x, \text{shr } s)$ ] unfolding  $xm'\text{-def}$  by (auto)
        ultimately have  $\text{mthr}.\tau \text{diverge } s'$ 
      proof (coinduct  $s' \ xm$  rule: mthr. $\tau$ diverge-trancl-measure-coinduct)
        case ( $\tau \text{diverge } s' \ xm$ )
          then obtain  $s \ x$  where  $\text{tst}: \text{thr } s \ t = \llbracket (x, \text{ln}) \rrbracket$ 
            and  $\text{blocked}: \text{ln} \neq \text{no-wait-locks} \vee \text{wset } s \ t \neq \text{None} \vee \neg \tau \text{diverge } t \ xm'$ 
            and  $s'\text{-def}: s' = (\text{locks } s, ((\text{thr } s)(t := \text{None}), \text{shr } s), \text{wset } s, \text{interrupts } s)$ 
            and  $xm\text{-def}: xm = (x, \text{shr } s)$ 
            and  $xm \ x m': \text{silent-moves } t \ xm' \ (x, \text{shr } s)$ 
            and  $\text{mthr}.\tau \text{diverge } s$  by blast
          from  $\langle \text{mthr}.\tau \text{diverge } s \rangle$  obtain  $s''$  where  $\tau \text{mredT } s \ s'' \ \text{mthr}.\tau \text{diverge } s''$  by cases auto
          from  $\langle \tau \text{mredT } s \ s'' \rangle$  obtain  $t' \ ta$  where  $s - t' \triangleright ta \rightarrow s''$  and  $m\tau \text{move } s \ (t', ta) \ s''$  by auto
          then obtain  $x' \ x'' \ m''$  where  $\text{red}: t' \vdash \langle x', \text{shr } s \rangle - ta \rightarrow \langle x'', m'' \rangle$ 
            and  $\text{tst}': \text{thr } s \ t' = \llbracket (x', \text{no-wait-locks}) \rrbracket$ 
            and  $\text{aoe}: \text{actions-ok } s \ t' \ ta$ 
            and  $s'': \text{redT-upd } s \ t' \ ta \ x'' \ m'' \ s''$ 
            by cases (fastforce elim: m $\tau$ move.cases) +
          from  $\langle m\tau \text{move } s \ (t', ta) \ s'' \rangle$  have [simp]:  $ta = \varepsilon$ 
            by (auto elim!: m $\tau$ move.cases dest!: silent-tl)
          hence  $\text{wst}': \text{wset } s \ t' = \text{None}$  using aoe by auto
          from  $\langle m\tau \text{move } s \ (t', ta) \ s'' \rangle$   $\text{tst}' \ s''$ 
          have  $\tau \text{move } (x', \text{shr } s) \in (x'', m'')$  by (auto elim: m $\tau$ move.cases)
          show ?case
          proof (cases  $t' = t$ )
            case False
              with  $\text{tst}' \ \text{wst}'$  have  $\text{thr } s' \ t' = \llbracket (x', \text{no-wait-locks}) \rrbracket$ 
                 $\text{wset } s' \ t' = \text{None}$   $\text{shr } s' = \text{shr } s$  unfolding  $s'\text{-def}$  by auto

```

with  $\text{red have } s' - t \triangleright \varepsilon \rightarrow \text{redT-upd-}\varepsilon \ s' \ t' \ x'' \ m''$   
 by  $-(\text{rule redT-normal, auto simp add: redT-updLns-def o-def finfun-Diag-const2 redT-updWs-def})$   
 moreover from  $\langle \tau \text{move } (x', \text{shr } s) \varepsilon (x'', m'') \rangle \langle \text{thr } s' \ t' = \lfloor (x', \text{no-wait-locks}) \rfloor \rangle \langle \text{shr } s' =$   
*shr s*  $\rangle$   
 have  $m\tau \text{move } s' (t', ta) (\text{redT-upd-}\varepsilon \ s' \ t' \ x'' \ m'')$   
 by  $-(\text{rule } m\tau \text{move.intros, auto})$   
 ultimately have  $\tau m\text{redT } s' (\text{redT-upd-}\varepsilon \ s' \ t' \ x'' \ m'')$   
 unfolding  $\langle ta = \varepsilon \rangle$  by  $(\text{rule } m\text{thr.silent-move.intros})$   
 hence  $\tau m\text{redT}^{++} s' (\text{redT-upd-}\varepsilon \ s' \ t' \ x'' \ m'')$  ..  
 moreover have  $\text{thr } s'' \ t = \lfloor (x, \text{ln}) \rfloor$   
 using  $tst \langle t' \neq t \rangle s''$  by *auto*  
 moreover from  $\langle \tau \text{move } (x', \text{shr } s) \varepsilon (x'', m'') \rangle \text{red}$   
 have  $[simp]: m'' = \text{shr } s$  by  $(\text{auto dest: } \tau \text{move-heap})$   
 hence  $\text{shr } s = \text{shr } s''$  using  $s''$  by *auto*  
 have  $\text{ln} \neq \text{no-wait-locks} \vee \text{wset } s'' \ t \neq \text{None} \vee \neg \tau \text{diverge } t \ x m'$   
 using  $\text{blocked } s''$  by  $(\text{auto simp add: redT-updWs-def elim!: rtrancl3p-cases})$   
 moreover have  $\text{redT-upd-}\varepsilon \ s' \ t' \ x'' \ m'' = (\text{locks } s'', ((\text{thr } s'')(t := \text{None}), \text{shr } s''), \text{wset } s'',$   
*interrupts s'')*  
 unfolding  $s'\text{-def}$  using  $tst \ s'' \langle t' \neq t \rangle$   
 by  $(\text{auto intro: ext elim!: rtrancl3p-cases simp add: redT-updLns-def redT-updWs-def})$   
 ultimately show  $?thesis$  using  $\langle m\text{thr}.\tau \text{diverge } s'' \rangle \ x m x m'$   
 unfolding  $\langle \text{shr } s = \text{shr } s'' \rangle$  by *blast*  
 next  
 case *True*  
 with  $tst \ tst' \ wst' \ \text{blocked have } \neg \tau \text{diverge } t \ x m'$   
 and  $[simp]: x' = x$  by *auto*  
 moreover from  $\text{red } \langle \tau \text{move } (x', \text{shr } s) \varepsilon (x'', m'') \rangle \text{True}$   
 have  $\text{silent-move } t (x, \text{shr } s) (x'', m'') \text{ by auto}$   
 with  $x m x m'$  have  $\text{silent-move-from } t \ x m' (x, \text{shr } s) (x'', m'')$   
 by  $(\text{rule silent-move-fromI})$   
 ultimately have  $(\text{if } \tau \text{diverge } t \ x m' \text{ then } \lambda s \ s'. \text{False else flip } (\text{silent-move-from } t \ x m')) (x'',$   
*m'') x m*  
 by  $(\text{auto simp add: flip-conv } x m\text{-def})$   
 moreover have  $\text{thr } s'' \ t = \lfloor (x'', \text{ln}) \rfloor$  using  $tst \ \text{True } s''$   
 by  $(\text{auto simp add: redT-updLns-def})$   
 moreover from  $\langle \tau \text{move } (x', \text{shr } s) \varepsilon (x'', m'') \rangle \text{red}$   
 have  $[simp]: m'' = \text{shr } s$  by  $(\text{auto dest: } \tau \text{move-heap})$   
 hence  $\text{shr } s = \text{shr } s''$  using  $s''$  by *auto*  
 have  $s' = (\text{locks } s'', ((\text{thr } s'')(t := \text{None}), \text{shr } s''), \text{wset } s'', \text{interrupts } s'')$   
 using  $\text{True } s''$  unfolding  $s'\text{-def}$   
 by  $(\text{auto intro: ext elim!: rtrancl3p-cases simp add: redT-updLns-def redT-updWs-def})$   
 moreover have  $(x'', m'') = (x'', \text{shr } s'')$  using  $s''$  by *auto*  
 moreover from  $x m x m' \langle \text{silent-move } t (x, \text{shr } s) (x'', m'') \rangle$   
 have  $\text{silent-moves } t \ x m' (x'', \text{shr } s'')$   
 unfolding  $\langle m'' = \text{shr } s \rangle \langle \text{shr } s = \text{shr } s'' \rangle$  by *auto*  
 ultimately show  $?thesis$  using  $\langle \neg \tau \text{diverge } t \ x m' \rangle \langle m\text{thr}.\tau \text{diverge } s'' \rangle$  by *blast*  
 qed  
 qed }  
 ultimately have  $\exists t \ x. \text{thr } s' \ t = \lfloor (x, \text{no-wait-locks}) \rfloor \wedge \text{wset } s' \ t = \text{None} \wedge \tau \text{diverge } t (x, \text{shr } s')$   
 by  $(\text{rule IH})$   
 then obtain  $t' \ x'$  where  $\text{thr } s' \ t' = \lfloor (x', \text{no-wait-locks}) \rfloor$   
 and  $\text{wset } s' \ t' = \text{None}$  and  $\tau \text{diverge } t' (x', \text{shr } s')$  by *blast*  
 moreover with *False* have  $t' \neq t$  by  $(\text{auto simp add: } s'\text{-def})$   
 ultimately have  $\text{thr } s \ t' = \lfloor (x', \text{no-wait-locks}) \rfloor \wedge \text{wset } s \ t' = \text{None} \wedge \tau \text{diverge } t' (x', \text{shr } s)$

```

      unfolding s'-def by auto
    thus ?thesis by blast
  qed
qed

```

**lemma**  $\tau mredT$ -preserves-final-thread:

```

   $\llbracket \tau mredT\ s\ s';\ final\text{-thread}\ s\ t \rrbracket \implies final\text{-thread}\ s'\ t$ 
  by(auto elim: mthr.silent-move.cases intro: redT-preserves-final-thread)

```

**lemma**  $\tau mRedT$ -preserves-final-thread:

```

   $\llbracket \tau mredT^{**}\ s\ s';\ final\text{-thread}\ s\ t \rrbracket \implies final\text{-thread}\ s'\ t$ 
  by(induct rule: rtranclp.induct)(blast intro:  $\tau mredT$ -preserves-final-thread)+

```

**lemma** *silent-moves2-silentD*:

```

  assumes rtrancl3p mthr.silent-move2 s ttas s'
  and (t, ta)  $\in$  set ttas
  shows ta =  $\varepsilon$ 
  using assms
  by(induct)(auto simp add: mthr.silent-move2-def dest: m $\tau$ move-silentD)

```

**lemma** *inf-step-silentD*:

```

  assumes step: trsys.inf-step mthr.silent-move2 s ttas
  and lset: (t, ta)  $\in$  lset ttas
  shows ta =  $\varepsilon$ 
  using lset step
  by(induct arbitrary: s rule: lset-induct)(fastforce elim: trsys.inf-step.cases simp add: mthr.silent-move2-def
  dest: m $\tau$ move-silentD)+

```

end

### 1.16.2 The multithreaded semantics with a well-founded relation on states

**locale** *multithreaded-base-measure* = *multithreaded-base* +

```

  constrains final :: 'x  $\Rightarrow$  bool
  and r :: ('l, 't, 'x, 'm, 'w, 'o) semantics
  and convert-RA :: 'l released-locks  $\Rightarrow$  'o list
  fixes  $\mu$  :: ('x  $\times$  'm)  $\Rightarrow$  ('x  $\times$  'm)  $\Rightarrow$  bool
begin

```

**inductive**  $m\mu t$  :: 'm  $\Rightarrow$  ('l, 't, 'x) thread-info  $\Rightarrow$  ('l, 't, 'x) thread-info  $\Rightarrow$  bool

**for**  $m$  **and**  $ts$  **and**  $ts'$

**where**

```

   $m\mu tI$ :
   $\bigwedge ln. \llbracket finite\ (dom\ ts); ts\ t = \lfloor (x, ln) \rfloor; ts'\ t = \lfloor (x', ln') \rfloor; \mu\ (x, m)\ (x', m); \bigwedge t'. t' \neq t \implies ts\ t' = ts'\ t' \rrbracket$ 
   $\implies m\mu t\ m\ ts\ ts'$ 

```

**definition**  $m\mu$  :: ('l, 't, 'x, 'm, 'w) state  $\Rightarrow$  ('l, 't, 'x, 'm, 'w) state  $\Rightarrow$  bool

**where**  $m\mu\ s\ s' \longleftrightarrow shr\ s = shr\ s' \wedge m\mu t\ (shr\ s)\ (thr\ s)\ (thr\ s')$

**lemma**  $m\mu t$ -thr-dom-eq:  $m\mu t\ m\ ts\ ts' \implies dom\ ts = dom\ ts'$

**apply**(erule  $m\mu t$ .cases)

**apply**(rule equalityI)

**apply**(rule subsetI)



```

apply(case-tac xa = t)
  apply(auto)[2]
apply(rule subsetI)
apply(case-tac xa = t)
apply auto
done

```

```

lemma mμ-finite-thrD:
  assumes mμt m ts ts'
  shows finite (dom ts) finite (dom ts')
using assms
by(simp-all add: mμt-thr-dom-eq[symmetric])(auto elim: mμt.cases)

end

```

```

locale multithreaded-base-measure-wf = multithreaded-base-measure +
  constrains final :: 'x ⇒ bool'
  and r :: ('l, 't, 'x, 'm, 'w, 'o) semantics
  and convert-RA :: 'l released-locks ⇒ 'o list'
  and μ :: ('x × 'm) ⇒ ('x × 'm) ⇒ bool'
  assumes wf-μ: wfP μ
begin

```

```

lemma wf-mμt: wfP (mμt m)
unfolding wfP-eq-minimal
proof(intro strip)
  fix Q :: ('l, 't, 'x) thread-info set and ts
  assume ts ∈ Q
  show  $\exists z \in Q. \forall y. m\mu t\ m\ y\ z \longrightarrow y \notin Q$ 
  proof(cases finite (dom ts))
    case False
    hence  $\forall y. m\mu t\ m\ y\ ts \longrightarrow y \notin Q$  by(auto dest: mμ-finite-thrD)
    thus ?thesis using  $\langle ts \in Q \rangle$  by blast
  next
    case True
    thus ?thesis using  $\langle ts \in Q \rangle$ 
    proof(induct A ≡ dom ts arbitrary: ts Q rule: finite-induct)
      case empty
      hence dom ts = {} by simp
      with  $\langle ts \in Q \rangle$  show ?case by(auto elim: mμt.cases)
    next
      case (insert t A)
      note IH =  $\langle \bigwedge ts\ Q. \llbracket A = \text{dom } ts; ts \in Q \rrbracket \implies \exists z \in Q. \forall y. m\mu t\ m\ y\ z \longrightarrow y \notin Q \rangle$ 
      define Q' where Q' = {ts. ts t = None ∧ (∃ xln. ts(t ↦ xln) ∈ Q)}
      let ?ts' = ts(t := None)
      from  $\langle \text{insert } t\ A = \text{dom } ts \rangle \langle t \notin A \rangle$  have A = dom ?ts' by auto
      moreover from  $\langle \text{insert } t\ A = \text{dom } ts \rangle$  obtain xln where ts t = [xln] by(cases ts t) auto
      hence ts(t ↦ xln) = ts by(auto simp add: fun-eq-iff)
      with  $\langle ts \in Q \rangle$  have ts(t ↦ xln) ∈ Q by(auto)
      hence ?ts' ∈ Q' unfolding Q'-def by(auto simp del: split-paired-Ex)
      ultimately have  $\exists z \in Q'. \forall y. m\mu t\ m\ y\ z \longrightarrow y \notin Q'$  by(rule IH)
      then obtain ts' where ts' ∈ Q'
      and min:  $\bigwedge ts''. m\mu t\ m\ ts''\ ts' \implies ts'' \notin Q'$  by blast
      from  $\langle ts' \in Q' \rangle$  obtain x' ln' where ts' t = None ts'(t ↦ (x', ln')) ∈ Q

```

```

    unfolding Q'-def by auto
    define Q'' where Q'' = {(x, m) | x. ∃ ln. ts'(t ↦ (x, ln)) ∈ Q}
    from ⟨ts'(t ↦ (x', ln')) ∈ Q⟩ have (x', m) ∈ Q'' unfolding Q''-def by blast
    hence ∃ xm'' ∈ Q''. ∀ xm'''. μ xm''' xm'' → xm''' ∉ Q'' by (rule wf-μ[unfolded wfP-eq-minimal,
rule-format])
    then obtain xm'' where xm'' ∈ Q'' and min': ∧ xm'''. μ xm''' xm'' ⇒ xm''' ∉ Q'' by blast
    from ⟨xm'' ∈ Q''⟩ obtain x'' ln'' where xm'' = (x'', m) ts'(t ↦ (x'', ln'')) ∈ Q unfolding
Q''-def by blast
    moreover {
      fix ts''
      assume mμt m ts'' (ts'(t ↦ (x'', ln'')))
      then obtain T X'' LN'' X' LN'
        where finite (dom ts'') ts'' T = [(X'', LN'')]
        and (ts'(t ↦ (x'', ln''))) T = [(X', LN')] μ (X'', m) (X', m)
        and eq: ∧ t'. t' ≠ T ⇒ ts'' t' = (ts'(t ↦ (x'', ln''))) t' by (cases) blast
      have ts'' ∉ Q
      proof (cases T = t)
        case True
        from True ⟨ts'(t ↦ (x'', ln''))) T = [(X', LN')]⟩ have X' = x'' by simp
        with ⟨μ (X'', m) (X', m)⟩ have (X'', m) ∉ Q'' by (auto dest: min'[unfolded ⟨xm'' = (x'',
m)⟩])
        hence ∀ ln. ts'(t ↦ (X'', ln)) ∉ Q by (simp add: Q''-def)
        moreover from ⟨ts' t = None⟩ eq True
        have ts''(t := None) = ts' by (auto simp add: fun-eq-iff)
        with ⟨ts'' T = [(X'', LN'')]⟩ True
        have ts'': ts'' = ts'(t ↦ (X'', LN'')) by (auto intro!: ext)
        ultimately show ?thesis by blast
      next
        case False
        from ⟨finite (dom ts'')⟩ have finite (dom (ts''(t := None))) by simp
        moreover from ⟨ts'' T = [(X'', LN'')]⟩ False
        have (ts''(t := None)) T = [(X'', LN'')] by simp
        moreover from ⟨(ts'(t ↦ (x'', ln''))) T = [(X', LN')]⟩ False
        have ts' T = [(X', LN')] by simp
        ultimately have mμt m (ts''(t := None)) ts' using ⟨μ (X'', m) (X', m)⟩
        proof (rule mμtI)
          fix t'
          assume t' ≠ T
          with eq[OF False[symmetric]] eq[OF this] ⟨ts' t = None⟩
          show (ts''(t := None)) t' = ts' t' by auto
        qed
        hence ts''(t := None) ∉ Q' by (rule min)
        thus ?thesis
        proof (rule contrapos-nn)
          assume ts'' ∈ Q
          from eq[OF False[symmetric]] have ts'' t = [(x'', ln'')] by simp
          hence ts'': ts''(t ↦ (x'', ln'')) = ts'' by (auto simp add: fun-eq-iff)
          from ⟨ts'' ∈ Q⟩ have ts''(t ↦ (x'', ln'')) ∈ Q unfolding ts''.
          thus ts''(t := None) ∈ Q' unfolding Q'-def by auto
        qed
      qed
    }
    ultimately show ?case by blast
  qed

```

```

qed
qed

lemma wf-mμ: wfP mμ
proof -
  have wf (inv-image (same-fst (λm. True) (λm. {(ts, ts'). mμt m ts ts'})) (λs. (shr s, thr s)))
    by(rule wf-inv-image)(rule wf-same-fst, rule wf-mμt[unfolded wfP-def])
  also have inv-image (same-fst (λm. True) (λm. {(ts, ts'). mμt m ts ts'})) (λs. (shr s, thr s)) = {(s,
    s'). mμ s s'}
    by(auto simp add: mμ-def same-fst-def)
  finally show ?thesis by(simp add: wfP-def)
qed

end

end

```

## 1.17 Various notions of bisimulation

theory *Bisimulation*

imports

*LTS*

begin

type-synonym ('a, 'b) *bisim* = 'a  $\Rightarrow$  'b  $\Rightarrow$  bool

### 1.17.1 Strong bisimulation

```

locale bisimulation-base = r1: trsys trsys1 + r2: trsys trsys2
  for trsys1 :: ('s1, 'tl1) trsys (-/ -1-->/ - [50,0,50] 60)
  and trsys2 :: ('s2, 'tl2) trsys (-/ -2-->/ - [50,0,50] 60) +
  fixes bisim :: ('s1, 's2) bisim (-/  $\approx$  - [50, 50] 60)
  and tlim :: ('tl1, 'tl2) bisim (-/  $\sim$  - [50, 50] 60)
begin

```

notation

*r1.Trsys* (-/ -1-->\* / - [50,0,50] 60) and  
*r2.Trsys* (-/ -2-->\* / - [50,0,50] 60)

notation

*r1.inf-step* (- -1-->\*  $\infty$  [50, 0] 80) and  
*r2.inf-step* (- -2-->\*  $\infty$  [50, 0] 80)

notation

*r1.inf-step-table* (- -1-->\*t  $\infty$  [50, 0] 80) and  
*r2.inf-step-table* (- -2-->\*t  $\infty$  [50, 0] 80)

abbreviation *Tlsim* :: ('tl1 list, 'tl2 list) bisim (-/ [ $\sim$ ] - [50, 50] 60)

where *Tlsim* *tl1* *tl2*  $\equiv$  list-all2 *tlsim* *tl1* *tl2*

abbreviation *Tlsiml* :: ('tl1 llist, 'tl2 llist) bisim (-/ [[ $\sim$ ]] - [50, 50] 60)

where *Tlsiml* *tl1* *tl2*  $\equiv$  llist-all2 *tlsim* *tl1* *tl2*

end

**locale** *bisimulation* = *bisimulation-base* +  
**constrains** *trsyst1* :: ('s1, 'tl1) *trsyst1*  
**and** *trsyst2* :: ('s2, 'tl2) *trsyst2*  
**and** *bisim* :: ('s1, 's2) *bisim*  
**and** *tlsim* :: ('tl1, 'tl2) *bisim*  
**assumes** *simulation1*:  $\llbracket s1 \approx s2; s1 -1-tl1 \rightarrow s1' \rrbracket \implies \exists s2' \, tl2. s2 -2-tl2 \rightarrow s2' \wedge s1' \approx s2' \wedge tl1 \sim tl2$   
**and** *simulation2*:  $\llbracket s1 \approx s2; s2 -2-tl2 \rightarrow s2' \rrbracket \implies \exists s1' \, tl1. s1 -1-tl1 \rightarrow s1' \wedge s1' \approx s2' \wedge tl1 \sim tl2$   
**begin**

**lemma** *bisimulation-flip*:

*bisimulation trsyst2 trsyst1 (flip bisim) (flip tlsim)*  
**by**(*unfold-locales*)(*unfold flip-simps*,(*blast intro: simulation1 simulation2*))+)

**end**

**lemma** *bisimulation-flip-simps [flip-simps]*:

*bisimulation trsyst2 trsyst1 (flip bisim) (flip tlsim)* = *bisimulation trsyst1 trsyst2 bisim tlsim*  
**by**(*auto dest: bisimulation.bisimulation-flip simp only: flip-flip*)

**context** *bisimulation* **begin**

**lemma** *simulation1-rtrancl*:

$\llbracket s1 -1-tls1 \rightarrow^* s1'; s1 \approx s2 \rrbracket$   
 $\implies \exists s2' \, tls2. s2 -2-tls2 \rightarrow^* s2' \wedge s1' \approx s2' \wedge tls1 [\sim] tls2$

**proof**(*induct rule: rtrancl3p.induct*)

**case** *rtrancl3p-refl* **thus** ?case **by**(*auto intro: rtrancl3p.rtrancl3p-refl*)

**next**

**case** (*rtrancl3p-step s1 tls1 s1' tl1 s1''*)

**from**  $\langle s1 \approx s2 \implies \exists s2' \, tls2. s2 -2-tls2 \rightarrow^* s2' \wedge s1' \approx s2' \wedge tls1 [\sim] tls2 \rangle \langle s1 \approx s2 \rangle$

**obtain** *s2' tls2* **where**  $s2 -2-tls2 \rightarrow^* s2' \wedge s1' \approx s2' \wedge tls1 [\sim] tls2$  **by** *blast*

**moreover from**  $\langle s1' -1-tl1 \rightarrow s1'' \rangle \langle s1' \approx s2' \rangle$

**obtain** *s2'' tl2* **where**  $s2' -2-tl2 \rightarrow s2'' \wedge s1'' \approx s2'' \wedge tl1 \sim tl2$  **by**(*auto dest: simulation1*)

**ultimately have**  $s2 -2-tls2 @ [tl2] \rightarrow^* s2'' \wedge s1' @ [tl1] [\sim] tls2 @ [tl2]$

**by**(*auto intro: rtrancl3p.rtrancl3p-step list-all2-appendI*)

**with**  $\langle s1'' \approx s2'' \rangle$  **show** ?case **by**(*blast*)

**qed**

**lemma** *simulation2-rtrancl*:

$\llbracket s2 -2-tls2 \rightarrow^* s2'; s1 \approx s2 \rrbracket$   
 $\implies \exists s1' \, tls1. s1 -1-tls1 \rightarrow^* s1' \wedge s1' \approx s2' \wedge s1' \approx s2' \wedge tls1 [\sim] tls2$

**using** *bisimulation.simulation1-rtrancl*[*OF bisimulation-flip*]

**unfolding** *flip-simps* .

**lemma** *simulation1-inf-step*:

**assumes** *red1*:  $s1 -1-tls1 \rightarrow^* \infty$  **and** *bisim*:  $s1 \approx s2$

**shows**  $\exists s2'. s2 -2-tls2 \rightarrow^* \infty \wedge s1' @ [tl1] [\sim] tls2$

**proof** –

**from** *r1.inf-step-imp-inf-step-table*[*OF red1*]

**obtain** *stls1* **where**  $red1': s1 -1-stls1 \rightarrow^* \infty$

**and** *tls1*:  $stls1 = lmap (fst \circ snd) stls1$  **by** *blast*

**define** *tl1-to-tl2* **where** *tl1-to-tl2 s2 stls1* = *unfold-llist*

```

( $\lambda(s2, stls1). \text{lnull } stls1$ )
( $\lambda(s2, stls1). \text{let } (s1, tl1, s1') = \text{lhs } stls1;$ 
  ( $tl2, s2' = \text{SOME } (tl2, s2'). \text{trsys2 } s2 \text{ } tl2 \text{ } s2' \wedge s1' \approx s2' \wedge tl1 \sim tl2$ 
    in ( $s2, tl2, s2'$ ))
( $\lambda(s2, stls1). \text{let } (s1, tl1, s1') = \text{lhs } stls1;$ 
  ( $tl2, s2' = \text{SOME } (tl2, s2'). \text{trsys2 } s2 \text{ } tl2 \text{ } s2' \wedge s1' \approx s2' \wedge tl1 \sim tl2$ 
    in ( $s2', \text{lhs } stls1$ ))
( $s2, stls1$ )
for  $s2 :: 's2$  and  $stls1 :: ('s1 \times 'tl1 \times 's1) \text{ llist}$ 

```

**have**  $tl1\text{-to-}tl2\text{-simps}$  [simp]:

```

 $\bigwedge s2 \text{ } stls1. \text{lnull } (tl1\text{-to-}tl2 \text{ } s2 \text{ } stls1) \longleftrightarrow \text{lnull } stls1$ 
 $\bigwedge s2 \text{ } stls1. \neg \text{lnull } stls1 \implies \text{lhs } (tl1\text{-to-}tl2 \text{ } s2 \text{ } stls1) =$ 
( $\text{let } (s1, tl1, s1') = \text{lhs } stls1;$ 
  ( $tl2, s2' = \text{SOME } (tl2, s2'). \text{trsys2 } s2 \text{ } tl2 \text{ } s2' \wedge s1' \approx s2' \wedge tl1 \sim tl2$ 
    in ( $s2, tl2, s2'$ ))
 $\bigwedge s2 \text{ } stls1. \neg \text{lnull } stls1 \implies \text{lhs } (tl1\text{-to-}tl2 \text{ } s2 \text{ } stls1) =$ 
( $\text{let } (s1, tl1, s1') = \text{lhs } stls1;$ 
  ( $tl2, s2' = \text{SOME } (tl2, s2'). \text{trsys2 } s2 \text{ } tl2 \text{ } s2' \wedge s1' \approx s2' \wedge tl1 \sim tl2$ 
    in  $tl1\text{-to-}tl2 \text{ } s2' \text{ } (\text{lhs } stls1)$ ))
 $\bigwedge s2. tl1\text{-to-}tl2 \text{ } s2 \text{ } LNil = LNil$ 
 $\bigwedge s2 \text{ } s1 \text{ } tl1 \text{ } s1' \text{ } stls1'. tl1\text{-to-}tl2 \text{ } s2 \text{ } (LCons (s1, tl1, s1') \text{ } stls1') =$ 
   $LCons (s2, \text{SOME } (tl2, s2'). \text{trsys2 } s2 \text{ } tl2 \text{ } s2' \wedge s1' \approx s2' \wedge tl1 \sim tl2)$ 
  ( $tl1\text{-to-}tl2 \text{ } (\text{snd } (\text{SOME } (tl2, s2'). \text{trsys2 } s2 \text{ } tl2 \text{ } s2' \wedge s1' \approx s2' \wedge tl1 \sim tl2)) \text{ } stls1'$ )
by (simp-all add:  $tl1\text{-to-}tl2\text{-def}$  split-beta)

```

**have** [simp]:  $\text{length } (tl1\text{-to-}tl2 \text{ } s2 \text{ } stls1) = \text{length } stls1$

**by** (coinduction arbitrary:  $s2 \text{ } stls1$  rule:  $\text{enat-coinduct}$ ) (auto simp add:  $\text{epred-length}$  split-beta)

**from**  $\text{red1}' \text{ bisim}$  **have**  $s2 \text{ } -2\text{-}tl1\text{-to-}tl2 \text{ } s2 \text{ } stls1 \rightarrow^* \infty$

**proof** (coinduction arbitrary:  $s2 \text{ } s1 \text{ } stls1$ )

**case** ( $\text{inf-step-table } s2 \text{ } s1 \text{ } stls1$ )

**note**  $\text{red1}' = \langle s1 \text{ } -1\text{-}stls1 \rightarrow^* \infty \rangle$  **and**  $\text{bisim} = \langle s1 \approx s2 \rangle$

**from**  $\text{red1}'$  **show** ?case

**proof** (cases)

**case** ( $\text{inf-step-tableI } s1' \text{ } stls1' \text{ } tl1$ )

**hence**  $stls1: stls1 = LCons (s1, tl1, s1') \text{ } stls1'$

**and**  $r: s1 \text{ } -1\text{-}tl1 \rightarrow s1'$  **and**  $\text{reds1}: s1' \text{ } -1\text{-}stls1' \rightarrow^* \infty$  **by** simp-all

**let**  $?tl2s2' = \text{SOME } (tl2, s2'). s2 \text{ } -2\text{-}tl2 \rightarrow s2' \wedge s1' \approx s2' \wedge tl1 \sim tl2$

**let**  $?tl2 = \text{fst } ?tl2s2' \text{ let } ?s2' = \text{snd } ?tl2s2'$

**from**  $\text{simulationI} [OF \text{ bisim } r]$  **obtain**  $s2' \text{ } tl2$

**where**  $s2 \text{ } -2\text{-}tl2 \rightarrow s2' \text{ } s1' \approx s2' \text{ } tl1 \sim tl2$  **by** blast

**hence** ( $\lambda(tl2, s2'). s2 \text{ } -2\text{-}tl2 \rightarrow s2' \wedge s1' \approx s2' \wedge tl1 \sim tl2$ ) ( $tl2, s2'$ ) **by** simp

**hence** ( $\lambda(tl2, s2'). s2 \text{ } -2\text{-}tl2 \rightarrow s2' \wedge s1' \approx s2' \wedge tl1 \sim tl2$ )  $?tl2s2'$  **by** (rule someI)

**hence**  $s2 \text{ } -2\text{-}?tl2 \rightarrow ?s2' \text{ } s1' \approx ?s2' \text{ } tl1 \sim ?tl2$  **by** (simp-all add: split-beta)

**then show** ?thesis **using**  $\text{reds1 } stls1$  **by** (fastforce intro: prod-eqI)

**qed**

**qed**

**hence**  $s2 \text{ } -2\text{-}lmap (\text{fst} \circ \text{snd}) (tl1\text{-to-}tl2 \text{ } s2 \text{ } stls1) \rightarrow^* \infty$

**by** (rule  $r2.\text{inf-step-table-imp-inf-step}$ )

**moreover have**  $stls1 [[\sim]] lmap (\text{fst} \circ \text{snd}) (tl1\text{-to-}tl2 \text{ } s2 \text{ } stls1)$

**proof** (rule llist-all2-all-lnthI)

**show**  $\text{length } stls1 = \text{length } (lmap (\text{fst} \circ \text{snd}) (tl1\text{-to-}tl2 \text{ } s2 \text{ } stls1))$

**using**  $stls1$  **by** simp

```

next
  fix n
  assume enat n < llength tls1
  thus lnth tls1 n ~ lnth (lmap (fst o snd) (tl1-to-tl2 s2 stls1)) n
    using red1' bisim unfolding tls1
  proof(induct n arbitrary: s1 s2 stls1 rule: nat-less-induct)
    case (1 n)
    hence IH:  $\bigwedge m$  s1 s2 stls1.  $\llbracket m < n; \text{enat } m < \text{llength } (\text{lmap } (\text{fst} \circ \text{snd}) \text{ stls1});$ 
       $s1 - 1 - \text{stls1} \rightarrow^* t \infty; s1 \approx s2 \rrbracket$ 
       $\implies \text{lnth } (\text{lmap } (\text{fst} \circ \text{snd}) \text{ stls1}) m \sim \text{lnth } (\text{lmap } (\text{fst} \circ \text{snd}) (\text{tl1-to-tl2 } s2 \text{ stls1})) m$ 
    by blast
  from  $\langle s1 - 1 - \text{stls1} \rightarrow^* t \infty \rangle$  show ?case
  proof cases
    case (inf-step-tableI s1' stls1' tl1)
    hence stls1: stls1 = LCons (s1, tl1, s1') stls1'
      and r: s1 - 1 - tl1  $\rightarrow$  s1' and reds: s1' - 1 - stls1'  $\rightarrow^* t \infty$  by simp-all
    let ?tl2s2' = SOME (tl2, s2'). s2 - 2 - tl2  $\rightarrow$  s2'  $\wedge$  s1'  $\approx$  s2'  $\wedge$  tl1  $\sim$  tl2
    let ?tl2 = fst ?tl2s2' let ?s2' = snd ?tl2s2'
    from simulation1[OF  $\langle s1 \approx s2 \rangle$  r] obtain s2' tl2
      where s2 - 2 - tl2  $\rightarrow$  s2'  $\wedge$  s1'  $\approx$  s2'  $\wedge$  tl1  $\sim$  tl2 by blast
    hence  $(\lambda(tl2, s2'). s2 - 2 - tl2 \rightarrow s2' \wedge s1' \approx s2' \wedge tl1 \sim tl2) (tl2, s2')$  by simp
    hence  $(\lambda(tl2, s2'). s2 - 2 - tl2 \rightarrow s2' \wedge s1' \approx s2' \wedge tl1 \sim tl2) ?tl2s2'$  by(rule someI)
    hence bisim': s1'  $\approx$  ?s2' and tlsim: tl1  $\sim$  ?tl2 by(simp-all add: split-beta)
    show ?thesis
  proof(cases n)
    case 0
    with stls1 tlsim show ?thesis by simp
  next
    case (Suc m)
    hence m < n by simp
    moreover have enat m < llength (lmap (fst o snd) stls1')
      using stls1  $\langle \text{enat } n < \text{llength } (\text{lmap } (\text{fst} \circ \text{snd}) \text{ stls1}) \rangle$  Suc by(simp add: Suc-ile-eq)
    ultimately have lnth (lmap (fst o snd) stls1') m ~ lnth (lmap (fst o snd) (tl1-to-tl2 ?s2'
stls1')) m
      using reds bisim' by(rule IH)
    with Suc stls1 show ?thesis by(simp del: o-apply)
  qed
qed
qed
qed
ultimately show ?thesis by blast
qed

lemma simulation2-inf-step:
   $\llbracket s2 - 2 - \text{tls2} \rightarrow^* \infty; s1 \approx s2 \rrbracket \implies \exists \text{tls1}. s1 - 1 - \text{tls1} \rightarrow^* \infty \wedge \text{tls1} \llbracket [\sim] \rrbracket \text{tls2}$ 
using bisimulation.simulation1-inf-step[OF bisimulation-flip]
unfolding flip-simps .

end

locale bisimulation-final-base =
  bisimulation-base +
  constrains trsys1 :: ('s1, 'tl1) trsys
  and trsys2 :: ('s2, 'tl2) trsys

```

```

and bisim :: ('s1, 's2) bisim
and tlsim :: ('tl1, 'tl2) bisim
fixes final1 :: 's1  $\Rightarrow$  bool
and final2 :: 's2  $\Rightarrow$  bool

locale bisimulation-final = bisimulation-final-base + bisimulation +
  constrains trsys1 :: ('s1, 'tl1) trsys
  and trsys2 :: ('s2, 'tl2) trsys
  and bisim :: ('s1, 's2) bisim
  and tlsim :: ('tl1, 'tl2) bisim
  and final1 :: 's1  $\Rightarrow$  bool
  and final2 :: 's2  $\Rightarrow$  bool
  assumes bisim-final:  $s1 \approx s2 \implies \text{final1 } s1 \longleftrightarrow \text{final2 } s2$ 

begin

lemma bisimulation-final-flip:
  bisimulation-final trsys2 trsys1 (flip bisim) (flip tlsim) final2 final1
apply(intro-locales)
apply(rule bisimulation-flip)
apply(unfold-locales)
by(unfold flip-simps, rule bisim-final[symmetric])

end

lemma bisimulation-final-flip-simps [flip-simps]:
  bisimulation-final trsys2 trsys1 (flip bisim) (flip tlsim) final2 final1 =
    bisimulation-final trsys1 trsys2 bisim tlsim final1 final2
by(auto dest: bisimulation-final.bisimulation-final-flip simp only: flip-flip)

context bisimulation-final begin

lemma final-simulation1:
   $\llbracket s1 \approx s2; s1 -1-tls1 \rightarrow^* s1'; \text{final1 } s1' \rrbracket$ 
 $\implies \exists s2' \text{tls2}. s2 -2-tls2 \rightarrow^* s2' \wedge s1' \approx s2' \wedge \text{final2 } s2' \wedge \text{tls1 } [\sim] \text{tls2}$ 
by(auto dest: bisim-final dest!: simulation1-rtranc1)

lemma final-simulation2:
   $\llbracket s1 \approx s2; s2 -2-tls2 \rightarrow^* s2'; \text{final2 } s2' \rrbracket$ 
 $\implies \exists s1' \text{tls1}. s1 -1-tls1 \rightarrow^* s1' \wedge s1' \approx s2' \wedge \text{final1 } s1' \wedge \text{tls1 } [\sim] \text{tls2}$ 
by(auto dest: bisim-final dest!: simulation2-rtranc1)

end

```

### 1.17.2 Delay bisimulation

```

locale delay-bisimulation-base =
  bisimulation-base +
  trsys1?:  $\tau \text{trsys trsys1 } \tau \text{move1}$  +
  trsys2?:  $\tau \text{trsys trsys2 } \tau \text{move2}$ 
  for  $\tau \text{move1 } \tau \text{move2}$  +
  constrains trsys1 :: ('s1, 'tl1) trsys
  and trsys2 :: ('s2, 'tl2) trsys
  and bisim :: ('s1, 's2) bisim

```

```

and tlsim :: ('tl1, 'tl2) bisim
and τmove1 :: ('s1, 'tl1) trsys
and τmove2 :: ('s2, 'tl2) trsys
begin

```

**notation**

```

trsys1.silent-move (-/ -τ1→ - [50, 50] 60) and
trsys2.silent-move (-/ -τ2→ - [50, 50] 60)

```

**notation**

```

trsys1.silent-moves (-/ -τ1→* - [50, 50] 60) and
trsys2.silent-moves (-/ -τ2→* - [50, 50] 60)

```

**notation**

```

trsys1.silent-movet (-/ -τ1→+ - [50, 50] 60) and
trsys2.silent-movet (-/ -τ2→+ - [50, 50] 60)

```

**notation**

```

trsys1.τrtrancl3p (- -τ1---→* - [50, 0, 50] 60) and
trsys2.τrtrancl3p (- -τ2---→* - [50, 0, 50] 60)

```

**notation**

```

trsys1.τinf-step (- -τ1---→* ∞ [50, 0] 80) and
trsys2.τinf-step (- -τ2---→* ∞ [50, 0] 80)

```

**notation**

```

trsys1.τdiverge (- -τ1→ ∞ [50] 80) and
trsys2.τdiverge (- -τ2→ ∞ [50] 80)

```

**notation**

```

trsys1.τinf-step-table (- -τ1---→*t ∞ [50, 0] 80) and
trsys2.τinf-step-table (- -τ2---→*t ∞ [50, 0] 80)

```

**notation**

```

trsys1.τRuns (- ↓1 - [50, 50] 51) and
trsys2.τRuns (- ↓2 - [50, 50] 51)

```

**lemma** *simulation-silent1I'*:

```

assumes ∃ s2'. (if μ1 s1' s1 then trsys2.silent-moves else trsys2.silent-movet) s2 s2' ∧ s1' ≈ s2'
shows s1' ≈ s2 ∧ μ1^++ s1' s1 ∨ (∃ s2'. s2 -τ2→+ s2' ∧ s1' ≈ s2')

```

**proof** –

```

from assms obtain s2' where red: (if μ1 s1' s1 then trsys2.silent-moves else trsys2.silent-movet)
s2 s2'

```

```

and bisim: s1' ≈ s2' by blast

```

```

show ?thesis

```

```

proof(cases μ1 s1' s1)

```

```

case True

```

```

with red have s2 -τ2→* s2' by simp

```

```

thus ?thesis using bisim True by cases(blast intro: rtranclp-into-tranclp1)+

```

```

next

```

```

case False

```

```

with red bisim show ?thesis by auto

```

```

qed

```

```

qed

```



```

lemma simulation-silent2I':
  assumes  $\exists s1'. (if \mu2\ s2'\ s2\ then\ trsys1.silent-moves\ else\ trsys1.silent-movet)\ s1\ s1' \wedge s1' \approx s2'$ 
  shows  $s1 \approx s2' \wedge \mu2^{++}\ s2'\ s2 \vee (\exists s1'. s1 \rightarrow \tau 1 \rightarrow^+ s1' \wedge s1' \approx s2')$ 
using assms
by(rule delay-bisimulation-base.simulation-silent1I')

end

locale delay-bisimulation-obs = delay-bisimulation-base - - -  $\tau move1\ \tau move2$ 
  for  $\tau move1 :: 's1 \Rightarrow 'tl1 \Rightarrow 's1 \Rightarrow bool$ 
  and  $\tau move2 :: 's2 \Rightarrow 'tl2 \Rightarrow 's2 \Rightarrow bool +$ 
  assumes simulation1:
     $\llbracket s1 \approx s2; s1 \rightarrow \tau 1 \rightarrow s1'; \neg \tau move1\ s1\ tl1\ s1' \rrbracket$ 
     $\implies \exists s2'\ s2''\ tl2. s2 \rightarrow \tau 2 \rightarrow^* s2' \wedge s2' \rightarrow \tau 2 \rightarrow s2'' \wedge \neg \tau move2\ s2'\ tl2\ s2'' \wedge s1' \approx s2'' \wedge tl1$ 
     $\sim tl2$ 
  and simulation2:
     $\llbracket s1 \approx s2; s2 \rightarrow \tau 2 \rightarrow s2'; \neg \tau move2\ s2\ tl2\ s2' \rrbracket$ 
     $\implies \exists s1'\ s1''\ tl1. s1 \rightarrow \tau 1 \rightarrow^* s1' \wedge s1' \rightarrow \tau 1 \rightarrow s1'' \wedge \neg \tau move1\ s1'\ tl1\ s1'' \wedge s1'' \approx s2' \wedge tl1$ 
     $\sim tl2$ 
begin

lemma delay-bisimulation-obs-flip: delay-bisimulation-obs trsys2 trsys1 (flip bisim) (flip tlsim)  $\tau move2$ 
 $\tau move1$ 
apply(unfold-locales)
apply(unfold flip-simps)
by(blast intro: simulation1 simulation2)+

end

lemma delay-bisimulation-obs-flip-simps [flip-simps]:
  delay-bisimulation-obs trsys2 trsys1 (flip bisim) (flip tlsim)  $\tau move2\ \tau move1$  =
delay-bisimulation-obs trsys1 trsys2 bisim tlsim  $\tau move1\ \tau move2$ 
by(auto dest: delay-bisimulation-obs.delay-bisimulation-obs-flip simp only: flip-flip)

locale delay-bisimulation-diverge = delay-bisimulation-obs - - -  $\tau move1\ \tau move2$ 
  for  $\tau move1 :: 's1 \Rightarrow 'tl1 \Rightarrow 's1 \Rightarrow bool$ 
  and  $\tau move2 :: 's2 \Rightarrow 'tl2 \Rightarrow 's2 \Rightarrow bool +$ 
  assumes simulation-silent1:
     $\llbracket s1 \approx s2; s1 \rightarrow \tau 1 \rightarrow s1' \rrbracket \implies \exists s2'. s2 \rightarrow \tau 2 \rightarrow^* s2' \wedge s1' \approx s2'$ 
  and simulation-silent2:
     $\llbracket s1 \approx s2; s2 \rightarrow \tau 2 \rightarrow s2' \rrbracket \implies \exists s1'. s1 \rightarrow \tau 1 \rightarrow^* s1' \wedge s1' \approx s2'$ 
  and  $\tau diverge-bisim-inv: s1 \approx s2 \implies s1 \rightarrow \tau 1 \rightarrow \infty \longleftrightarrow s2 \rightarrow \tau 2 \rightarrow \infty$ 
begin

lemma delay-bisimulation-diverge-flip: delay-bisimulation-diverge trsys2 trsys1 (flip bisim) (flip tlsim)
 $\tau move2\ \tau move1$ 
apply(rule delay-bisimulation-diverge.intro)
apply(rule delay-bisimulation-obs-flip)
apply(unfold-locales)
apply(unfold flip-simps)
by(blast intro: simulation-silent1 simulation-silent2  $\tau diverge-bisim-inv[symmetric]$  del: iffI)+

end

```

**lemma** *delay-bisimulation-diverge-flip-simps* [*flip-simps*]:  
 $\text{delay-bisimulation-diverge trsys2 trsys1 (flip bisim) (flip tlsim) } \tau\text{move2 } \tau\text{move1} =$   
 $\text{delay-bisimulation-diverge trsys1 trsys2 bisim tlsim } \tau\text{move1 } \tau\text{move2}$   
**by**(*auto dest: delay-bisimulation-diverge.delay-bisimulation-diverge-flip simp only: flip-flip*)

**context** *delay-bisimulation-diverge* **begin**

**lemma** *simulation-silents1*:  
**assumes** *bisim*:  $s1 \approx s2$  **and** *moves*:  $s1 \rightarrow^* s1'$   
**shows**  $\exists s2'. s2 \rightarrow^* s2' \wedge s1' \approx s2'$   
**using** *moves bisim*  
**proof** *induct*  
**case** *base* **thus** ?*case* **by**(*blast*)  
**next**  
**case** (*step*  $s1' s1''$ )  
**from**  $\langle s1 \approx s2 \implies \exists s2'. s2 \rightarrow^* s2' \wedge s1' \approx s2' \rangle \langle s1 \approx s2 \rangle$   
**obtain**  $s2'$  **where**  $s2 \rightarrow^* s2' \wedge s1' \approx s2'$  **by** *blast*  
**from** *simulation-silent1*[*OF*  $\langle s1' \approx s2' \rangle \langle s1' \rightarrow s1'' \rangle$ ]  
**obtain**  $s2''$  **where**  $s2' \rightarrow^* s2'' \wedge s1'' \approx s2''$  **by** *blast*  
**from**  $\langle s2 \rightarrow^* s2' \rangle \langle s2' \rightarrow^* s2'' \rangle$  **have**  $s2 \rightarrow^* s2''$  **by**(*rule rtranclp-trans*)  
**with**  $\langle s1'' \approx s2'' \rangle$  **show** ?*case* **by** *blast*  
**qed**

**lemma** *simulation-silents2*:  
 $\llbracket s1 \approx s2; s2 \rightarrow^* s2' \rrbracket \implies \exists s1'. s1 \rightarrow^* s1' \wedge s1' \approx s2'$   
**using** *delay-bisimulation-diverge.simulation-silents1*[*OF* *delay-bisimulation-diverge-flip*]  
**unfolding** *flip-simps* .

**lemma** *simulation1- $\tau$ rtrancl3p*:  
 $\llbracket s1 \rightarrow^* s1'; s1 \approx s2 \rrbracket \implies \exists t1s2 s2'. s2 \rightarrow^* s2' \wedge s1' \approx s2' \wedge t1s1 [\sim] t1s2$   
**proof**(*induct arbitrary: s2 rule: trsys1. $\tau$ rtrancl3p.induct*)  
**case** ( *$\tau$ rtrancl3p-refl* *s*)  
**thus** ?*case* **by**(*auto intro:  $\tau$ trsys. $\tau$ rtrancl3p.intros*)  
**next**  
**case** ( *$\tau$ rtrancl3p-step*  $s1 s1' t1s1 s1'' t1$ )  
**from** *simulation1*[*OF*  $\langle s1 \approx s2 \rangle \langle s1 \rightarrow s1' \rangle \langle \neg \tau\text{move1 } s1 t1 s1' \rangle$ ]  
**obtain**  $s2' s2'' t1s2$  **where**  $s2 \rightarrow^* s2'$   
**and**  $s2' \rightarrow^* s2''$  **and**  $\neg \tau\text{move2 } s2' t1s2 s2''$   
**and** *bisim'*:  $s1' \approx s2''$  **and** *tlsim*:  $t1s1 \sim t1s2$  **by** *blast*  
**from** *bisim'*  $\langle s1' \approx s2'' \rangle \implies \exists t1s2 s2'. s2'' \rightarrow^* s2' \wedge s1'' \approx s2' \wedge t1s1 [\sim] t1s2$   
**obtain**  $t1s2 s2'''$  **where** *IH*:  $s2'' \rightarrow^* s2''' \wedge s1'' \approx s2''' \wedge t1s1 [\sim] t1s2$  **by** *blast*  
**from**  $\tau\text{red}$  **have**  $s2 \rightarrow^* s2'$  **by**(*rule trsys2.silent-moves-into- $\tau$ rtrancl3p*)  
**also from**  $\tau\text{red}$  *IH*(1) **have**  $s2' \rightarrow^* s2'''$  **by**(*rule  $\tau$ rtrancl3p. $\tau$ rtrancl3p-step*)  
**finally show** ?*case* **using** *IH tlsim* **by** *fastforce*  
**next**  
**case** ( *$\tau$ rtrancl3p- $\tau$ step*  $s1 s1' t1s1 s1'' t1$ )  
**from**  $\langle s1 \rightarrow s1' \rangle \langle \tau\text{move1 } s1 t1 s1' \rangle$  **have**  $s1 \rightarrow s1'$  ..  
**from** *simulation-silent1*[*OF*  $\langle s1 \approx s2 \rangle$  *this*]  
**obtain**  $s2'$  **where**  $s2 \rightarrow^* s2'$  **and** *bisim'*:  $s1' \approx s2'$  **by** *blast*  
**from**  $\tau\text{red}$  **have**  $s2 \rightarrow^* s2'$  **by**(*rule trsys2.silent-moves-into- $\tau$ rtrancl3p*)  
**also from** *bisim'*  $\langle s1' \approx s2' \rangle \implies \exists t1s2 s2''. s2' \rightarrow^* s2'' \wedge s1'' \approx s2'' \wedge t1s1 [\sim] t1s2$

**obtain**  $tls2\ s2''$  **where**  $IH: s2' -\tau 2 -tls2 \rightarrow^* s2''\ s1'' \approx s2''\ tls1\ [\sim]\ tls2$  **by** *blast*  
**note**  $\langle s2' -\tau 2 -tls2 \rightarrow^* s2'' \rangle$   
**finally show**  $?case$  **using**  $IH$  **by** *auto*  
**qed**

**lemma** *simulation2- $\tau$ tranc13p*:

$\llbracket s2 -\tau 2 -tls2 \rightarrow^* s2'; s1 \approx s2 \rrbracket$   
 $\implies \exists\ tls1\ s1'. s1 -\tau 1 -tls1 \rightarrow^* s1' \wedge s1' \approx s2' \wedge tls1\ [\sim]\ tls2$

**using** *delay-bisimulation-diverge.simulation1- $\tau$ tranc13p*[*OF delay-bisimulation-diverge-flip*]  
**unfolding** *flip-simps* .

**lemma** *simulation1- $\tau$ inf-step*:

**assumes**  $\tau inf1: s1 -\tau 1 -tls1 \rightarrow^* \infty$  **and** *bisim*:  $s1 \approx s2$

**shows**  $\exists\ tls2. s2 -\tau 2 -tls2 \rightarrow^* \infty \wedge tls1\ [[\sim]]\ tls2$

**proof** –

**from** *trsysts1. $\tau$ inf-step-imp- $\tau$ inf-step-table*[*OF  $\tau inf1$* ]

**obtain**  $sstls1$  **where**  $\tau inf1': s1 -\tau 1 -sstls1 \rightarrow^* t\ \infty$

**and**  $tls1: tls1 = lmap\ (fst \circ snd \circ snd)\ sstls1$  **by** *blast*

**define**  $tl1\text{-}to\text{-}tl2$  **where**  $tl1\text{-}to\text{-}tl2\ s2\ sstls1 = unfold\ llist$

$(\lambda(s2, sstls1). lnull\ sstls1)$

$(\lambda(s2, sstls1).$

$\text{let } (s1, s1', tl1, s1'') = lhd\ sstls1;$

$(s2', tl2, s2'') = SOME\ (s2', tl2, s2'').\ s2 -\tau 2 \rightarrow^* s2' \wedge trsys2\ s2'\ tl2\ s2'' \wedge$   
 $\neg \tau move2\ s2'\ tl2\ s2'' \wedge s1'' \approx s2'' \wedge tl1 \sim tl2$

$\text{in } (s2, s2', tl2, s2''))$

$(\lambda(s2, sstls1).$

$\text{let } (s1, s1', tl1, s1'') = lhd\ sstls1;$

$(s2', tl2, s2'') = SOME\ (s2', tl2, s2'').\ s2 -\tau 2 \rightarrow^* s2' \wedge trsys2\ s2'\ tl2\ s2'' \wedge$   
 $\neg \tau move2\ s2'\ tl2\ s2'' \wedge s1'' \approx s2'' \wedge tl1 \sim tl2$

$\text{in } (s2'', ltl\ sstls1))$

$(s2, sstls1)$

**for**  $s2 :: 's2$  **and**  $sstls1 :: ('s1 \times 's1 \times 'tl1 \times 's1)\ llist$

**have** [*simp*]:

$\bigwedge s2\ sstls1. lnull\ (tl1\text{-}to\text{-}tl2\ s2\ sstls1) \longleftrightarrow lnull\ sstls1$

$\bigwedge s2\ sstls1. \neg lnull\ sstls1 \implies lhd\ (tl1\text{-}to\text{-}tl2\ s2\ sstls1) =$

$(\text{let } (s1, s1', tl1, s1'') = lhd\ sstls1;$

$(s2', tl2, s2'') = SOME\ (s2', tl2, s2'').\ s2 -\tau 2 \rightarrow^* s2' \wedge trsys2\ s2'\ tl2\ s2'' \wedge$   
 $\neg \tau move2\ s2'\ tl2\ s2'' \wedge s1'' \approx s2'' \wedge tl1 \sim tl2$

$\text{in } (s2, s2', tl2, s2''))$

$\bigwedge s2\ sstls1. \neg lnull\ sstls1 \implies ltl\ (tl1\text{-}to\text{-}tl2\ s2\ sstls1) =$

$(\text{let } (s1, s1', tl1, s1'') = lhd\ sstls1;$

$(s2', tl2, s2'') = SOME\ (s2', tl2, s2'').\ s2 -\tau 2 \rightarrow^* s2' \wedge trsys2\ s2'\ tl2\ s2'' \wedge$   
 $\neg \tau move2\ s2'\ tl2\ s2'' \wedge s1'' \approx s2'' \wedge tl1 \sim tl2$

$\text{in } tl1\text{-}to\text{-}tl2\ s2''\ (ltl\ sstls1))$

$\bigwedge s2. tl1\text{-}to\text{-}tl2\ s2\ LNil = LNil$

$\bigwedge s2\ s1\ s1'\ tl1\ s1''\ stls1'. tl1\text{-}to\text{-}tl2\ s2\ (LCons\ (s1, s1', tl1, s1'')\ stls1') =$

$LCons\ (s2, SOME\ (s2', tl2, s2'').\ s2 -\tau 2 \rightarrow^* s2' \wedge trsys2\ s2'\ tl2\ s2'' \wedge$   
 $\neg \tau move2\ s2'\ tl2\ s2'' \wedge s1'' \approx s2'' \wedge tl1 \sim tl2)$

$(tl1\text{-}to\text{-}tl2\ (snd\ (snd\ (SOME\ (s2', tl2, s2'').\ s2 -\tau 2 \rightarrow^* s2' \wedge trsys2\ s2'\ tl2\ s2'' \wedge$   
 $\neg \tau move2\ s2'\ tl2\ s2'' \wedge s1'' \approx s2'' \wedge tl1 \sim tl2))))$

$stls1')$

**by** (*simp-all add: tl1-to-tl2-def split-beta*)

**have** [*simp*]:  $llength\ (tl1\text{-}to\text{-}tl2\ s2\ sstls1) = llength\ sstls1$

```

by(coinduction arbitrary: s2 sstls1 rule: enat-coinduct)(auto simp add: epred-llength split-beta)

define sstls2 where sstls2 = tl1-to-tl2 s2 sstls1
with  $\tau inf1'$  bisim have  $\exists s1 \text{ sstls1}. s1 \rightarrow \tau 1 - sstls1 \rightarrow * t \infty \wedge sstls2 = tl1-to-tl2 s2 sstls1 \wedge s1 \approx s2$ 
by blast

from  $\tau inf1'$  bisim have  $s2 \rightarrow \tau 2 - tl1-to-tl2 s2 sstls1 \rightarrow * t \infty$ 
proof(coinduction arbitrary: s2 s1 sstls1)
  case ( $\tau inf$ -step-table s2 s1 sstls1)
  note  $\tau inf' = \langle s1 \rightarrow \tau 1 - sstls1 \rightarrow * t \infty \rangle$  and bisim =  $\langle s1 \approx s2 \rangle$ 
  from  $\tau inf'$  show ?case
  proof(cases)
    case ( $\tau inf$ -step-table-Cons s1' s1'' sstls1' tl1)
    hence sstls1: sstls1 = LCons (s1, s1', tl1, s1'') sstls1'
      and  $\tau s$ :  $s1 \rightarrow \tau 1 \rightarrow * s1'$  and  $r$ :  $s1' \rightarrow 1 - tl1 \rightarrow s1''$  and  $n\tau$ :  $\neg \tau move1 s1' tl1 s1''$ 
      and reds1:  $s1'' \rightarrow \tau 1 - sstls1' \rightarrow * t \infty$  by simp-all
    let ?P =  $\lambda(s2', tl2, s2''). s2 \rightarrow \tau 2 \rightarrow * s2' \wedge trsys2 s2' tl2 s2'' \wedge \neg \tau move2 s2' tl2 s2'' \wedge s1'' \approx$ 
 $s2'' \wedge tl1 \sim tl2$ 
    let ?s2tl2s2' = Eps ?P
    let ?s2'' = snd (snd ?s2tl2s2')
    from simulation-silents1[OF  $\langle s1 \approx s2 \rangle \tau s$ ]
    obtain s2' where  $s2 \rightarrow \tau 2 \rightarrow * s2' s1' \approx s2'$  by blast
    from simulation1[OF  $\langle s1' \approx s2' \rangle r n\tau$ ] obtain s2'' s2''' tl2
      where  $s2' \rightarrow \tau 2 \rightarrow * s2''$ 
      and rest:  $s2'' \rightarrow 2 - tl2 \rightarrow s2''' \neg \tau move2 s2'' tl2 s2''' s1'' \approx s2''' tl1 \sim tl2$  by blast
    from  $\langle s2 \rightarrow \tau 2 \rightarrow * s2' \rangle \langle s2' \rightarrow \tau 2 \rightarrow * s2'' \rangle$  have  $s2 \rightarrow \tau 2 \rightarrow * s2''$  by (rule rtranclp-trans)
    with rest have ?P (s2'', tl2, s2''') by simp
    hence ?P ?s2tl2s2' by (rule someI)
    then show ?thesis using reds1 sstls1 by fastforce
  next
  case  $\tau inf$ -step-table-Nil
  hence [simp]: sstls1 = LNil and  $s1 \rightarrow \tau 1 \rightarrow \infty$  by simp-all
  from  $\langle s1 \rightarrow \tau 1 \rightarrow \infty \rangle \langle s1 \approx s2 \rangle$  have  $s2 \rightarrow \tau 2 \rightarrow \infty$  by (simp add:  $\tau$  diverge-bisim-inv)
  thus ?thesis using sstls2-def by simp
qed
qed
hence  $s2 \rightarrow \tau 2 - lmap (fst \circ snd \circ snd) (tl1-to-tl2 s2 sstls1) \rightarrow * \infty$ 
  by (rule trsys2. $\tau inf$ -step-table-into- $\tau inf$ -step)
moreover have tls1 [[ $\sim$ ]] lmap (fst  $\circ$  snd  $\circ$  snd) (tl1-to-tl2 s2 sstls1)
proof(rule llist-all2-all-lnthI)
  show llength tls1 = llength (lmap (fst  $\circ$  snd  $\circ$  snd) (tl1-to-tl2 s2 sstls1))
    using tls1 by simp
next
fix n
assume enat n < llength tls1
thus lnth tls1 n  $\sim$  lnth (lmap (fst  $\circ$  snd  $\circ$  snd) (tl1-to-tl2 s2 sstls1)) n
  using  $\tau inf1'$  bisim unfolding tls1
proof(induct n arbitrary: s1 s2 sstls1 rule: less-induct)
  case (less n)
  note IH =  $\langle \bigwedge m s1 s2 sstls1. \llbracket m < n; enat m < llength (lmap (fst \circ snd \circ snd) sstls1);$ 
 $s1 \rightarrow \tau 1 - sstls1 \rightarrow * t \infty; s1 \approx s2 \rrbracket$ 
 $\implies lnth (lmap (fst \circ snd \circ snd) sstls1) m \sim lnth (lmap (fst \circ snd \circ snd) (tl1-to-tl2 s2$ 
 $sstls1)) m \rangle$ 
  from  $\langle s1 \rightarrow \tau 1 - sstls1 \rightarrow * t \infty \rangle$  show ?case

```

**proof cases**

case  $(\tau\text{inf-step-table-Cons } s1' \ s1'' \ sstls1' \ tl1)$   
 hence  $sstls1: sstls1 = LCons (s1, s1', tl1, s1'') \ sstls1'$   
 and  $\tau s: s1 \rightarrow^* s1'$  and  $r: s1' \rightarrow tl1 \rightarrow s1''$   
 and  $n\tau: \neg \tau\text{move1 } s1' \ tl1 \ s1''$  and  $reds: s1'' \rightarrow^* t \infty$  by *simp-all*  
 let  $?P = \lambda(s2', tl2, s2''). s2 \rightarrow^* s2' \wedge \text{trsys2 } s2' \ tl2 \ s2'' \wedge \neg \tau\text{move2 } s2' \ tl2 \ s2'' \wedge s1''$   
 $\approx s2'' \wedge tl1 \sim tl2$   
 let  $?s2tl2s2' = Eps \ ?P$  let  $?tl2 = fst (snd \ ?s2tl2s2')$  let  $?s2'' = snd (snd \ ?s2tl2s2')$   
 from *simulation-silents1*  $[OF \ \langle s1 \approx s2 \rangle \ \tau s]$  obtain  $s2'$   
 where  $s2 \rightarrow^* s2' \ s1' \approx s2'$  by *blast*  
 from *simulation1*  $[OF \ \langle s1' \approx s2' \rangle \ r \ n\tau]$  obtain  $s2'' \ s2''' \ tl2$   
 where  $s2' \rightarrow^* s2''$   
 and  $rest: s2'' \rightarrow tl2 \rightarrow s2''' \neg \tau\text{move2 } s2'' \ tl2 \ s2''' \ s1'' \approx s2''' \ tl1 \sim tl2$  by *blast*  
 from  $\langle s2 \rightarrow^* s2' \rangle \ \langle s2' \rightarrow^* s2'' \rangle$  have  $s2 \rightarrow^* s2''$  by *(rule rtrancpl-trans)*  
 with *rest* have  $?P (s2'', tl2, s2''')$  by *auto*  
 hence  $?P \ ?s2tl2s2'$  by *(rule someI)*  
 hence  $s1'' \approx ?s2'' \ tl1 \sim ?tl2$  by *(simp-all add: split-beta)*  
 show *?thesis*  
 proof(cases n)  
 case 0  
 with  $sstls1 \ \langle tl1 \sim ?tl2 \rangle$  show *?thesis* by *simp*  
 next  
 case  $(Suc \ m)$   
 hence  $m < n$  by *simp*  
 moreover have  $enat \ m < llength \ (lmap \ (fst \circ \ snd \circ \ snd) \ sstls1')$   
 using  $sstls1 \ \langle enat \ n < llength \ (lmap \ (fst \circ \ snd \circ \ snd) \ sstls1) \rangle \ Suc$  by *(simp add: Suc-ile-eq)*  
 ultimately have  $lnth \ (lmap \ (fst \circ \ snd \circ \ snd) \ sstls1') \ m \sim lnth \ (lmap \ (fst \circ \ snd \circ \ snd)$   
 $(tl1\text{-to-}tl2 \ ?s2'' \ sstls1')) \ m$   
 using  $reds \ \langle s1'' \approx ?s2'' \rangle$  by *(rule IH)*  
 with  $Suc \ sstls1$  show *?thesis* by *(simp del: o-apply)*  
 qed  
 next  
 case  $\tau\text{inf-step-table-Nil}$   
 with  $\langle enat \ n < llength \ (lmap \ (fst \circ \ snd \circ \ snd) \ sstls1) \rangle$  have *False* by *simp*  
 thus *?thesis ..*  
 qed  
 qed  
 qed  
 ultimately show *?thesis* by *blast*  
 qed

**lemma simulation2- $\tau\text{inf-step}$ :**

$\llbracket s2 \rightarrow^* t \infty; s1 \approx s2 \rrbracket \implies \exists t1s1. s1 \rightarrow^* t1s1 \rightarrow^* \infty \wedge t1s1 \llbracket [\sim] \rrbracket t1s2$   
 using *delay-bisimulation-diverge.simulation1- $\tau\text{inf-step}$*   $[OF \ \text{delay-bisimulation-diverge-flip}]$   
 unfolding *flip-simps* .

**lemma no- $\tau\text{move1}$ - $\tau s$ -to-no- $\tau\text{move2}$ :**

assumes  $s1 \approx s2$   
 and  $\text{no-}\tau\text{moves1}: \bigwedge s1'. \neg s1 \rightarrow s1'$   
 shows  $\exists s2'. s2 \rightarrow^* s2' \wedge (\forall s2''. \neg s2' \rightarrow s2'') \wedge s1 \approx s2'$   
 proof -  
 have  $\neg s1 \rightarrow \infty$   
 proof  
 assume  $s1 \rightarrow \infty$

then obtain  $s1'$  where  $s1 \rightarrow \tau 1 s1'$  by cases  
 with  $no\text{-}\tau\text{-moves1}[of\ s1]$  show *False* by contradiction  
 qed  
 with  $\langle s1 \approx s2 \rangle$  have  $\neg s2 \rightarrow \tau 2 \rightarrow \infty$  by(*simp add:  $\tau$ diverge-bisim-inv*)  
 from  $trsys2.not\text{-}\tau\text{-diverge-to-no-}\tau\text{-move}[OF\ this]$   
 obtain  $s2'$  where  $s2 \rightarrow \tau 2 s2'$  and  $\bigwedge s2''. \neg s2' \rightarrow \tau 2 s2''$  by blast  
 moreover from  $simulation\text{-}silents2[OF\ \langle s1 \approx s2 \rangle \langle s2 \rightarrow \tau 2 s2' \rangle]$   
 obtain  $s1'$  where  $s1 \rightarrow \tau 1 s1'$  and  $s1' \approx s2'$  by blast  
 from  $\langle s1 \rightarrow \tau 1 s1' \rangle no\text{-}\tau\text{-moves1}$  have  $s1' = s1$   
 by(*auto elim: converse-rtranclpE*)  
 ultimately show  $?thesis$  using  $\langle s1' \approx s2' \rangle$  by blast  
 qed

lemma *no- $\tau$ move2- $\tau$ s-to-no- $\tau$ move1*:

$\llbracket s1 \approx s2; \bigwedge s2'. \neg s2 \rightarrow \tau 2 s2' \rrbracket \implies \exists s1'. s1 \rightarrow \tau 1 s1' \wedge (\forall s1''. \neg s1' \rightarrow \tau 1 s1'') \wedge s1' \approx s2$

using *delay-bisimulation-diverge.no- $\tau$ move1- $\tau$ s-to-no- $\tau$ move2*[*OF delay-bisimulation-diverge-flip*]  
 unfolding *flip-simps* .

lemma *no-move1-to-no-move2*:

assumes  $s1 \approx s2$

and *no-moves1*:  $\bigwedge tl1\ s1'. \neg s1 \rightarrow \tau 1 tl1 s1'$

shows  $\exists s2'. s2 \rightarrow \tau 2 s2' \wedge (\forall tl2\ s2''. \neg s2' \rightarrow \tau 2 tl2 s2'') \wedge s1 \approx s2'$

proof –

from *no-moves1* have  $\bigwedge s1'. \neg s1 \rightarrow \tau 1 s1'$  by(*auto*)

from *no- $\tau$ move1- $\tau$ s-to-no- $\tau$ move2*[*OF  $\langle s1 \approx s2 \rangle$  this*]

obtain  $s2'$  where  $s2 \rightarrow \tau 2 s2'$  and  $s1 \approx s2'$

and *no- $\tau$ moves2*:  $\bigwedge s2''. \neg s2' \rightarrow \tau 2 s2''$  by blast

moreover

have  $\bigwedge tl2\ s2''. \neg s2' \rightarrow \tau 2 tl2 s2''$

proof

fix  $tl2\ s2''$

assume  $s2' \rightarrow \tau 2 tl2 s2''$

with *no- $\tau$ moves2*[*of  $s2''$* ] have  $\neg \tau\text{-move2}\ s2'\ tl2\ s2''$  by(*auto*)

from *simulation2*[*OF  $\langle s1 \approx s2' \rangle \langle s2' \rightarrow \tau 2 tl2 s2'' \rangle$  this*]

obtain  $s1'\ s1''\ tl1$  where  $s1 \rightarrow \tau 1 s1'$  and  $s1' \rightarrow \tau 1 tl1 s1''$  by blast

with *no-moves1* show *False* by(*fastforce elim: converse-rtranclpE*)

qed

ultimately show  $?thesis$  by blast

qed

lemma *no-move2-to-no-move1*:

$\llbracket s1 \approx s2; \bigwedge tl2\ s2'. \neg s2 \rightarrow \tau 2 tl2 s2' \rrbracket$

$\implies \exists s1'. s1 \rightarrow \tau 1 s1' \wedge (\forall tl1\ s1''. \neg s1' \rightarrow \tau 1 tl1 s1'') \wedge s1' \approx s2$

using *delay-bisimulation-diverge.no-move1-to-no-move2*[*OF delay-bisimulation-diverge-flip*]

unfolding *flip-simps* .

lemma *simulation- $\tau$ Runs-table1*:

assumes *bisim*:  $s1 \approx s2$

and *run1*: *trsys1*. $\tau$ Runs-table  $s1\ stlsss1$

shows  $\exists stlsss2. trsys2.\tau$ Runs-table  $s2\ stlsss2 \wedge tllist\text{-}all2\ (\lambda(tl1, s1'')\ (tl2, s2'').\ tl1 \sim tl2 \wedge s1'' \approx s2'')$  (*rel-option bisim*)  $stlsss1\ stlsss2$

proof(*intro exI conjI*)

let  $?P = \lambda(s2 :: 's2)\ (stlsss1 :: ('tl1 \times 's1, 's1\ option)\ tllist)\ (tl2, s2'')$ .

$\exists s2'. s2 \rightarrow \tau 2 \rightarrow * s2' \wedge s2' \rightarrow 2 - tl2 \rightarrow s2'' \wedge \neg \tau move2 s2' tl2 s2'' \wedge snd(thd stlsss1) \approx s2'' \wedge fst(thd stlsss1) \sim tl2$

```

define tls1-to-tls2 where tls1-to-tls2 s2 stlsss1 = unfold-tl
  ( $\lambda(s2, stlsss1). is-TNil\ stlsss1$ )
  ( $\lambda(s2, stlsss1). map-option\ (\lambda s1'. SOME\ s2'. s2 \rightarrow \tau 2 \rightarrow * s2' \wedge (\forall tl\ s2''. \neg s2' \rightarrow 2 - tl \rightarrow s2'') \wedge s1' \approx s2') (terminal\ stlsss1)$ )
  ( $\lambda(s2, stlsss1). let\ (tl2, s2'') = Eps\ (?P\ s2\ stlsss1)\ in\ (tl2, s2'')$ )
  ( $\lambda(s2, stlsss1). let\ (tl2, s2'') = Eps\ (?P\ s2\ stlsss1)\ in\ (s2'', ttl\ stlsss1)$ )
  (s2, stlsss1)
for s2 stlsss1
have [simp]:
   $\bigwedge s2\ stlsss1. is-TNil\ (tls1-to-tls2\ s2\ stlsss1) \longleftrightarrow is-TNil\ stlsss1$ 
   $\bigwedge s2\ stlsss1. is-TNil\ stlsss1 \implies terminal\ (tls1-to-tls2\ s2\ stlsss1) = map-option\ (\lambda s1'. SOME\ s2'. s2 \rightarrow \tau 2 \rightarrow * s2' \wedge (\forall tl\ s2''. \neg s2' \rightarrow 2 - tl \rightarrow s2'') \wedge s1' \approx s2') (terminal\ stlsss1)$ 
   $\bigwedge s2\ stlsss1. \neg is-TNil\ stlsss1 \implies thd\ (tls1-to-tls2\ s2\ stlsss1) = (let\ (tl2, s2'') = Eps\ (?P\ s2\ stlsss1)\ in\ (tl2, s2''))$ 
   $\bigwedge s2\ stlsss1. \neg is-TNil\ stlsss1 \implies ttl\ (tls1-to-tls2\ s2\ stlsss1) = (let\ (tl2, s2'') = Eps\ (?P\ s2\ stlsss1)\ in\ tls1-to-tls2\ s2''\ (ttl\ stlsss1))$ 
   $\bigwedge s2\ os1. tls1-to-tls2\ s2\ (TNil\ os1) =$ 
     $TNil\ (map-option\ (\lambda s1'. SOME\ s2'. s2 \rightarrow \tau 2 \rightarrow * s2' \wedge (\forall tl\ s2''. \neg s2' \rightarrow 2 - tl \rightarrow s2'') \wedge s1' \approx s2') os1)$ 
by(simp-all add: tls1-to-tls2-def split-beta)
have [simp]:
   $\bigwedge s2\ s1\ s1'\ tl1\ s1''\ stlsss1.$ 
   $tls1-to-tls2\ s2\ (TCons\ (tl1, s1'')\ stlsss1) =$ 
  ( $let\ (tl2, s2'') = SOME\ (tl2, s2''). \exists s2'. s2 \rightarrow \tau 2 \rightarrow * s2' \wedge s2' \rightarrow 2 - tl2 \rightarrow s2'' \wedge$ 
     $\neg \tau move2\ s2'\ tl2\ s2'' \wedge s1'' \approx s2'' \wedge tl1 \sim tl2$ 
     $in\ TCons\ (tl2, s2'')\ (tls1-to-tls2\ s2''\ stlsss1))$ 
by(rule tllist.expand)(simp-all add: split-beta)

from bisim run1
show trsys2.τRuns-table s2 (tls1-to-tls2 s2 stlsss1)
proof(coinduction arbitrary: s2 s1 stlsss1)
  case (τRuns-table s2 s1 stlsss1)
  note bisim =  $\langle s1 \approx s2 \rangle$ 
  and run1 =  $\langle trsys1.τRuns-table\ s1\ stlsss1 \rangle$ 
from run1 show ?case
proof cases
  case (Terminate s1')
  let ?P =  $\lambda s2'. s2 \rightarrow \tau 2 \rightarrow * s2' \wedge (\forall tl2\ s2''. \neg s2' \rightarrow 2 - tl2 \rightarrow s2'') \wedge s1' \approx s2'$ 
  from simulation-silents1[OF bisim  $\langle s1 \rightarrow \tau 1 \rightarrow * s1' \rangle$ ]
  obtain s2' where  $s2 \rightarrow \tau 2 \rightarrow * s2'$  and  $s1' \approx s2'$  by blast
  moreover from no-move1-to-no-move2[OF  $\langle s1' \approx s2' \rangle \langle \bigwedge tl1\ s1''. \neg s1' \rightarrow 1 - tl1 \rightarrow s1'' \rangle$ ]
  obtain s2'' where  $s2' \rightarrow \tau 2 \rightarrow * s2''$  and  $s1' \approx s2''$ 
  and  $\bigwedge tl2\ s2'''. \neg s2'' \rightarrow 2 - tl2 \rightarrow s2'''$  by blast
  ultimately have ?P s2'' by(blast intro: rtrancpl-trans)
  hence ?P (Eps ?P) by(rule someI)
  hence ?Terminate using  $\langle stlsss1 = TNil\ [s1'] \rangle$  by simp
  thus ?thesis ..
next
  case Diverge
  with  $\tau diverge-bisim-inv[OF\ bisim]$ 
  have ?Diverge by simp
  thus ?thesis by simp

```

```

next
  case (Proceed  $s1' s1'' stlsss1' tl1$ )
    let  $?P = \lambda(tl2, s2''). \exists s2'. s2 \rightarrow^* s2' \wedge s2' - 2 - tl2 \rightarrow s2'' \wedge \neg \tau move2 s2' tl2 s2'' \wedge s1''$ 
     $\approx s2'' \wedge tl1 \sim tl2$ 
    from simulation-silents1[OF bisim  $\langle s1 \rightarrow^* s1' \rangle$ ]
    obtain  $s2'$  where  $s2 \rightarrow^* s2'$  and  $s1' \approx s2'$  by blast
    moreover from simulation1[OF  $\langle s1' \approx s2' \rangle \langle s1' - 1 - tl1 \rightarrow s1'' \rangle \langle \neg \tau move1 s1' tl1 s1'' \rangle$ ]
    obtain  $s2'' s2''' tl2$  where  $s2' \rightarrow^* s2''$ 
      and  $s2'' - 2 - tl2 \rightarrow s2'''$  and  $\neg \tau move2 s2'' tl2 s2'''$ 
      and  $s1'' \approx s2'''$  and  $tl1 \sim tl2$  by blast
    ultimately have  $?P (tl2, s2'')$  by (blast intro: rtrancpl-trans)
    hence  $?P (Eps ?P)$  by (rule someI)
    hence ?Proceed
      using  $\langle stlsss1 = TCons (tl1, s1'') stlsss1' \rangle \langle trsys1.\tau Runs-table s1'' stlsss1' \rangle$ 
      by auto blast
    thus ?thesis by simp
  qed
qed

let ?Tlsim =  $\lambda(tl1, s1'') (tl2, s2''). tl1 \sim tl2 \wedge s1'' \approx s2''$ 
let ?Bisim = rel-option bisim
from run1 bisim
show tllist-all2 ?Tlsim ?Bisim stlsss1 (tls1-to-tls2 s2 stlsss1)
proof (coinduction arbitrary:  $s1 s2 stlsss1$ )
  case (tllist-all2  $s1 s2 stlsss1$ )
  note  $Runs = \langle trsys1.\tau Runs-table s1 stlsss1 \rangle$  and  $bisim = \langle s1 \approx s2 \rangle$ 
  from Runs show ?case
  proof cases
    case (Terminate  $s1'$ )
    let  $?P = \lambda s2'. s2 \rightarrow^* s2' \wedge (\forall tl2 s2''. \neg s2' - 2 - tl2 \rightarrow s2'') \wedge s1' \approx s2'$ 
    from simulation-silents1[OF bisim  $\langle s1 \rightarrow^* s1' \rangle$ ]
    obtain  $s2'$  where  $s2 \rightarrow^* s2'$  and  $s1' \approx s2'$  by blast
    moreover
    from no-move1-to-no-move2[OF  $\langle s1' \approx s2' \rangle \langle \bigwedge tl1 s1''. \neg s1' - 1 - tl1 \rightarrow s1'' \rangle$ ]
    obtain  $s2''$  where  $s2' \rightarrow^* s2''$  and  $s1' \approx s2''$ 
      and  $\bigwedge tl2 s2'''. \neg s2'' - 2 - tl2 \rightarrow s2'''$  by blast
    ultimately have  $?P s2''$  by (blast intro: rtrancpl-trans)
    hence  $?P (Eps ?P)$  by (rule someI)
    thus ?thesis using  $\langle stlsss1 = TNil [s1'] \rangle bisim$  by (simp)
  next
  case (Proceed  $s1' s1'' stlsss1' tl1$ )
  from simulation-silents1[OF bisim  $\langle s1 \rightarrow^* s1' \rangle$ ]
  obtain  $s2'$  where  $s2 \rightarrow^* s2'$  and  $s1' \approx s2'$  by blast
  moreover from simulation1[OF  $\langle s1' \approx s2' \rangle \langle s1' - 1 - tl1 \rightarrow s1'' \rangle \langle \neg \tau move1 s1' tl1 s1'' \rangle$ ]
  obtain  $s2'' s2''' tl2$  where  $s2' \rightarrow^* s2''$ 
    and  $s2'' - 2 - tl2 \rightarrow s2'''$  and  $\neg \tau move2 s2'' tl2 s2'''$ 
    and  $s1'' \approx s2'''$  and  $tl1 \sim tl2$  by blast
  ultimately have  $?P s2 stlsss1 (tl2, s2''')$ 
    using  $\langle stlsss1 = TCons (tl1, s1'') stlsss1' \rangle$  by (auto intro: rtrancpl-trans)
  hence  $?P s2 stlsss1 (Eps (?P s2 stlsss1))$  by (rule someI)
  thus ?thesis using  $\langle stlsss1 = TCons (tl1, s1'') stlsss1' \rangle \langle trsys1.\tau Runs-table s1'' stlsss1' \rangle bisim$ 
    by auto blast
  qed simp
qed

```



qed

**lemma** *simulation- $\tau$ Runs-table2*:

**assumes**  $s1 \approx s2$

**and**  $trsys2.\tauRuns\text{-}table\ s2\ stlsss2$

**shows**  $\exists stlsss1. trsys1.\tauRuns\text{-}table\ s1\ stlsss1 \wedge tllist\text{-}all2\ (\lambda(tl1, s1'') (tl2, s2''). tl1 \sim tl2 \wedge s1'' \approx s2'')\ (rel\text{-}option\ bisim)\ stlsss1\ stlsss2$

**using** *delay-bisimulation-diverge.simulation- $\tau$ Runs-table1* [OF *delay-bisimulation-diverge-flip*, *unfolded flip-simps*, *OF assms*]

**by** (*subst tllist-all2-flip* [symmetric]) (*simp only: flip-def split-def*)

**lemma** *simulation- $\tau$ Runs1*:

**assumes** *bisim*:  $s1 \approx s2$

**and**  $run1: s1 \Downarrow_1 tls1$

**shows**  $\exists tls2. s2 \Downarrow_2 tls2 \wedge tllist\text{-}all2\ tlsim\ (rel\text{-}option\ bisim)\ tls1\ tls2$

**proof** –

**from**  $trsys1.\tauRuns\text{-}into\text{-}\tauRuns\text{-}table$  [OF *run1*]

**obtain**  $stlsss1$  **where**  $tls1: tls1 = tmap\ fst\ id\ stlsss1$

**and**  $\tauRuns1: trsys1.\tauRuns\text{-}table\ s1\ stlsss1$  **by** *blast*

**from**  $simulation\text{-}\tauRuns\text{-}table1$  [OF *bisim*  $\tauRuns1$ ]

**obtain**  $stlsss2$  **where**  $\tauRuns2: trsys2.\tauRuns\text{-}table\ s2\ stlsss2$

**and**  $tlsim: tllist\text{-}all2\ (\lambda(tl1, s1'') (tl2, s2''). tl1 \sim tl2 \wedge s1'' \approx s2'')\ (rel\text{-}option\ bisim)\ stlsss1\ stlsss2$  **by** *blast*

**from**  $\tauRuns2$  **have**  $s2 \Downarrow_2 tmap\ fst\ id\ stlsss2$

**by** (*rule*  $\tauRuns\text{-}table\text{-}into\text{-}\tauRuns$ )

**moreover have**  $tllist\text{-}all2\ tlsim\ (rel\text{-}option\ bisim)\ tls1\ (tmap\ fst\ id\ stlsss2)$

**using** *tlsim unfolding tls1*

**by** (*fastforce simp add: tllist-all2-tmap1 tllist-all2-tmap2 elim: tllist-all2-mono rel-option-mono*)

**ultimately show** *?thesis* **by** *blast*

qed

**lemma** *simulation- $\tau$ Runs2*:

$\llbracket s1 \approx s2; s2 \Downarrow_2 tls2 \rrbracket$

$\implies \exists tls1. s1 \Downarrow_1 tls1 \wedge tllist\text{-}all2\ tlsim\ (rel\text{-}option\ bisim)\ tls1\ tls2$

**using** *delay-bisimulation-diverge.simulation- $\tau$ Runs1* [OF *delay-bisimulation-diverge-flip*]

**unfolding** *flip-simps* .

end

**locale** *delay-bisimulation-final-base* =

*delay-bisimulation-base* - - -  $\tau move1\ \tau move2$  +

*bisimulation-final-base* - - - *final1 final2*

**for**  $\tau move1 :: ('s1, 'tl1)\ trsys$

**and**  $\tau move2 :: ('s2, 'tl2)\ trsys$

**and**  $final1 :: 's1 \Rightarrow bool$

**and**  $final2 :: 's2 \Rightarrow bool$  +

**assumes** *final1-simulation*:  $\llbracket s1 \approx s2; final1\ s1 \rrbracket \implies \exists s2'. s2 \text{-}\tau 2 \rightarrow^* s2' \wedge s1 \approx s2' \wedge final2\ s2'$

**and** *final2-simulation*:  $\llbracket s1 \approx s2; final2\ s2 \rrbracket \implies \exists s1'. s1 \text{-}\tau 1 \rightarrow^* s1' \wedge s1' \approx s2 \wedge final1\ s1'$

**begin**

**lemma** *delay-bisimulation-final-base-flip*:

*delay-bisimulation-final-base*  $trsys2\ trsys1\ (flip\ bisim)\ \tau move2\ \tau move1\ final2\ final1$

**apply** (*unfold-locales*)

**apply** (*unfold flip-simps*)

by(*blast intro: final1-simulation final2-simulation*)+

end

**lemma** *delay-bisimulation-final-base-flip-simps* [*flip-simps*]:

*delay-bisimulation-final-base trsys2 trsys1 (flip bisim)  $\tau$ move2  $\tau$ move1 final2 final1 =*  
*delay-bisimulation-final-base trsys1 trsys2 bisim  $\tau$ move1  $\tau$ move2 final1 final2*

by(*auto dest: delay-bisimulation-final-base.delay-bisimulation-final-base-flip simp only: flip-flip*)

**context** *delay-bisimulation-final-base* **begin**

**lemma**  *$\tau$ Runs-terminate-final1*:

**assumes** *s1  $\Downarrow$ 1 t1s1*

**and** *s2  $\Downarrow$ 2 t1s2*

**and** *t1list-all2 t1sim (rel-option bisim) t1s1 t1s2*

**and** *tfinite t1s1*

**and** *terminal t1s1 = Some s1'*

**and** *final1 s1'*

**shows**  $\exists s2'. tfinite\ t1s2 \wedge terminal\ t1s2 = Some\ s2' \wedge final2\ s2'$

**using** *assms(4) assms(1-3,5-)*

**apply**(*induct arbitrary: t1s2 s1 s2 rule: tfinite-induct*)

**apply**(*auto 4 4 simp add: t1list-all2-TCons1 t1list-all2-TNil1 option-rel-Some1 trsys1. $\tau$ Runs-simps*  
*trsys2. $\tau$ Runs-simps dest: final1-simulation elim: converse-rtrancpE*)

**done**

**lemma**  *$\tau$ Runs-terminate-final2*:

$\llbracket s1 \Downarrow 1\ t1s1; s2 \Downarrow 2\ t1s2; t1list-all2\ t1sim\ (rel-option\ bisim)\ t1s1\ t1s2;$

$tfinite\ t1s2; terminal\ t1s2 = Some\ s2'; final2\ s2' \rrbracket$

$\implies \exists s1'. tfinite\ t1s1 \wedge terminal\ t1s1 = Some\ s1' \wedge final1\ s1'$

**using** *delay-bisimulation-final-base. $\tau$ Runs-terminate-final1* [**where** *t1sim = flip t1sim, OF delay-bisimulation-final-b*]

**unfolding** *flip-simps* **by**  $-$

end

**locale** *delay-bisimulation-diverge-final* =

*delay-bisimulation-diverge +*

*delay-bisimulation-final-base +*

**constrains** *trsys1 :: ('s1, 't1) trsys*

**and** *trsys2 :: ('s2, 't2) trsys*

**and** *bisim :: ('s1, 's2) bisim*

**and** *t1sim :: ('t1, 't2) bisim*

**and**  *$\tau$ move1 :: ('s1, 't1) trsys*

**and**  *$\tau$ move2 :: ('s2, 't2) trsys*

**and** *final1 :: 's1  $\Rightarrow$  bool*

**and** *final2 :: 's2  $\Rightarrow$  bool*

**begin**

**lemma** *delay-bisimulation-diverge-final-flip*:

*delay-bisimulation-diverge-final trsys2 trsys1 (flip bisim) (flip t1sim)  $\tau$ move2  $\tau$ move1 final2 final1*

**apply**(*rule delay-bisimulation-diverge-final.intro*)

**apply**(*rule delay-bisimulation-diverge-flip*)

**apply**(*unfold-locales, unfold flip-simps*)

**apply**(*blast intro: final1-simulation final2-simulation*)+

**done**

end

**lemma** *delay-bisimulation-diverge-final-flip-simps* [flip-simps]:  
 $\text{delay-bisimulation-diverge-final trsys2 trsys1 (flip bisim) (flip tlim) } \tau\text{move2 } \tau\text{move1 final2 final1} =$   
 $\text{delay-bisimulation-diverge-final trsys1 trsys2 bisim tlim } \tau\text{move1 } \tau\text{move2 final1 final2}$   
**by**(auto dest: *delay-bisimulation-diverge-final.delay-bisimulation-diverge-final-flip simp only: flip-flip*)

**context** *delay-bisimulation-diverge-final* **begin**

**lemma** *delay-bisimulation-diverge*:  
 $\text{delay-bisimulation-diverge trsys1 trsys2 bisim tlim } \tau\text{move1 } \tau\text{move2}$   
**by**(*unfold-locales*)

**lemma** *delay-bisimulation-final-base*:  
 $\text{delay-bisimulation-final-base trsys1 trsys2 bisim } \tau\text{move1 } \tau\text{move2 final1 final2}$   
**by**(*unfold-locales*)

**lemma** *final-simulation1*:  
 $\llbracket s1 \approx s2; s1 \rightarrow \tau 1 \text{--} t1 s1 \rightarrow^* s1'; \text{final1 } s1' \rrbracket$   
 $\implies \exists s2' t1s2. s1 \rightarrow \tau 2 \text{--} t1s2 \rightarrow^* s2' \wedge s1' \approx s2' \wedge \text{final2 } s2' \wedge t1s1 \text{ } [\sim] \text{ } t1s2$   
**by**(blast dest: *simulation1- $\tau$ rtranc13p final1-simulation* intro:  $\tau$ rtranc13p-trans[*OF - silent-moves-into- $\tau$ rtranc13p, simplified*])

**lemma** *final-simulation2*:  
 $\llbracket s1 \approx s2; s2 \rightarrow \tau 2 \text{--} t1s2 \rightarrow^* s2'; \text{final2 } s2' \rrbracket$   
 $\implies \exists s1' t1s1. s1 \rightarrow \tau 1 \text{--} t1s1 \rightarrow^* s1' \wedge s1' \approx s2' \wedge \text{final1 } s1' \wedge t1s1 \text{ } [\sim] \text{ } t1s2$   
**by**(rule *delay-bisimulation-diverge-final.final-simulation1*[*OF delay-bisimulation-diverge-final-flip, unfolded flip-simps*])

end

**locale** *delay-bisimulation-measure-base* =  
 $\text{delay-bisimulation-base} +$   
**constrains**  $\text{trsys1} :: 's1 \Rightarrow 't1 \Rightarrow 's1 \Rightarrow \text{bool}$   
**and**  $\text{trsys2} :: 's2 \Rightarrow 't2 \Rightarrow 's2 \Rightarrow \text{bool}$   
**and**  $\text{bisim} :: 's1 \Rightarrow 's2 \Rightarrow \text{bool}$   
**and**  $\text{tlim} :: 't1 \Rightarrow 't2 \Rightarrow \text{bool}$   
**and**  $\tau\text{move1} :: 's1 \Rightarrow 't1 \Rightarrow 's1 \Rightarrow \text{bool}$   
**and**  $\tau\text{move2} :: 's2 \Rightarrow 't2 \Rightarrow 's2 \Rightarrow \text{bool}$   
**fixes**  $\mu1 :: 's1 \Rightarrow 's1 \Rightarrow \text{bool}$   
**and**  $\mu2 :: 's2 \Rightarrow 's2 \Rightarrow \text{bool}$

**locale** *delay-bisimulation-measure* =  
 $\text{delay-bisimulation-measure-base} - - - \tau\text{move1 } \tau\text{move2 } \mu1 \mu2 +$   
 $\text{delay-bisimulation-obs trsys1 trsys2 bisim tlim } \tau\text{move1 } \tau\text{move2}$   
**for**  $\tau\text{move1} :: 's1 \Rightarrow 't1 \Rightarrow 's1 \Rightarrow \text{bool}$   
**and**  $\tau\text{move2} :: 's2 \Rightarrow 't2 \Rightarrow 's2 \Rightarrow \text{bool}$   
**and**  $\mu1 :: 's1 \Rightarrow 's1 \Rightarrow \text{bool}$   
**and**  $\mu2 :: 's2 \Rightarrow 's2 \Rightarrow \text{bool} +$   
**assumes** *simulation-silent1*:  
 $\llbracket s1 \approx s2; s1 \rightarrow \tau 1 \rightarrow s1' \rrbracket \implies s1' \approx s2 \wedge \mu1 \hat{+} s1' s1 \vee (\exists s2'. s1 \rightarrow \tau 2 \rightarrow s2' \wedge s1' \approx s2')$   
**and** *simulation-silent2*:  
 $\llbracket s1 \approx s2; s2 \rightarrow \tau 2 \rightarrow s2' \rrbracket \implies s1 \approx s2' \wedge \mu2 \hat{+} s2' s2 \vee (\exists s1'. s1 \rightarrow \tau 1 \rightarrow s1' \wedge s1' \approx s2')$

and wf- $\mu 1$ : wfP  $\mu 1$   
 and wf- $\mu 2$ : wfP  $\mu 2$   
 begin

lemma delay-bisimulation-measure-flip:

delay-bisimulation-measure trsys2 trsys1 (flip bisim) (flip tlsim)  $\tau move2$   $\tau move1$   $\mu 2$   $\mu 1$   
 apply(rule delay-bisimulation-measure.intro)  
 apply(rule delay-bisimulation-obs-flip)  
 apply(unfold-locales)  
 apply(unfold flip-simps)  
 apply(rule simulation-silent1 simulation-silent2 wf- $\mu 1$  wf- $\mu 2$ |assumption)+  
 done

end

lemma delay-bisimulation-measure-flip-simps [flip-simps]:

delay-bisimulation-measure trsys2 trsys1 (flip bisim) (flip tlsim)  $\tau move2$   $\tau move1$   $\mu 2$   $\mu 1$  =  
 delay-bisimulation-measure trsys1 trsys2 bisim tlsim  $\tau move1$   $\tau move2$   $\mu 1$   $\mu 2$   
 by(auto dest: delay-bisimulation-measure.delay-bisimulation-measure-flip simp only: flip-simps)

context delay-bisimulation-measure begin

lemma simulation-silentst1:

assumes bisim:  $s1 \approx s2$  and moves:  $s1 \rightarrow \tau 1 \rightarrow s1'$   
 shows  $s1' \approx s2 \wedge \mu 1^{++} s1' s1 \vee (\exists s2'. s2 \rightarrow \tau 2 \rightarrow s2' \wedge s1' \approx s2')$   
 using moves bisim  
 proof induct  
 case (base  $s1'$ ) thus ?case by(auto dest: simulation-silent1)  
 next  
 case (step  $s1' s1''$ )  
 hence  $s1' \approx s2 \wedge \mu 1^{++} s1' s1 \vee (\exists s2'. s2 \rightarrow \tau 2 \rightarrow s2' \wedge s1' \approx s2')$  by blast  
 thus ?case  
 proof  
 assume  $s1' \approx s2 \wedge \mu 1^{++} s1' s1$   
 hence  $s1' \approx s2 \mu 1^{++} s1' s1$  by simp-all  
 with simulation-silent1[OF  $\langle s1' \approx s2 \rangle \langle s1' \rightarrow \tau 1 \rightarrow s1'' \rangle$ ]  
 show ?thesis by(auto)  
 next  
 assume  $\exists s2'. trsys2.silent-move^{++} s2 s2' \wedge s1' \approx s2'$   
 then obtain  $s2'$  where  $s2 \rightarrow \tau 2 \rightarrow s2' s1' \approx s2'$  by blast  
 with simulation-silent1[OF  $\langle s1' \approx s2' \rangle \langle s1' \rightarrow \tau 1 \rightarrow s1'' \rangle$ ]  
 show ?thesis by(auto intro: tranclp-trans)  
 qed  
 qed

lemma simulation-silentst2:

$\llbracket s1 \approx s2; s2 \rightarrow \tau 2 \rightarrow s2' \rrbracket \implies s1 \approx s2' \wedge \mu 2^{++} s2' s2 \vee (\exists s1'. s1 \rightarrow \tau 1 \rightarrow s1' \wedge s1' \approx s2')$   
 using delay-bisimulation-measure.simulation-silentst1[OF delay-bisimulation-measure-flip]  
 unfolding flip-simps .

lemma  $\tau$ diverge-simulation1:

assumes diverge1:  $s1 \rightarrow \tau 1 \rightarrow \infty$   
 and bisim:  $s1 \approx s2$   
 shows  $s2 \rightarrow \tau 2 \rightarrow \infty$

proof –

from *assms* have  $s1 \rightarrow \infty \wedge s1 \approx s2$  by *blast*  
 thus ?thesis using *wfP-trancl*[*OF wf-μ1*]  
 proof(*coinduct rule: trsys2.τdiverge-trancl-measure-coinduct*)  
 case (*τdiverge s2 s1*)  
 hence  $s1 \rightarrow \infty \wedge s1 \approx s2$  by *simp-all*  
 then obtain  $s1'$  where *trsys1.silent-move s1 s1' s1' → ∞*  
 by(*fastforce elim: trsys1.τdiverge.cases*)  
 from *simulation-silent1*[*OF*  $\langle s1 \approx s2 \rangle \langle \text{trsys1.silent-move s1 s1'} \rangle$ ]  $\langle s1' \rightarrow \infty \rangle$   
 show ?case by *auto*  
 qed  
 qed

lemma *τdiverge-simulation2*:

$\llbracket s2 \rightarrow \infty; s1 \approx s2 \rrbracket \implies s1 \rightarrow \infty$   
 using *delay-bisimulation-measure.τdiverge-simulation1*[*OF delay-bisimulation-measure-flip*]  
 unfolding *flip-simps* .

lemma *τdiverge-bisim-inv*:

$s1 \approx s2 \implies s1 \rightarrow \infty \iff s2 \rightarrow \infty$   
 by(*blast intro: τdiverge-simulation1 τdiverge-simulation2*)

end

sublocale *delay-bisimulation-measure* < *delay-bisimulation-diverge*

proof

fix  $s1 s2 s1'$   
 assume  $s1 \approx s2$   $s1 \rightarrow \infty$   
 from *simulation-silent1*[*OF this*]  
 show  $\exists s2'. s2 \rightarrow \infty \wedge s1' \approx s2'$  by(*auto intro: tranclp-into-rtranclp*)  
 next  
 fix  $s1 s2 s2'$   
 assume  $s1 \approx s2$   $s2 \rightarrow \infty$   
 from *simulation-silent2*[*OF this*]  
 show  $\exists s1'. s1 \rightarrow \infty \wedge s1' \approx s2'$  by(*auto intro: tranclp-into-rtranclp*)  
 next  
 fix  $s1 s2$   
 assume  $s1 \approx s2$   
 thus  $s1 \rightarrow \infty \iff s2 \rightarrow \infty$  by(*rule τdiverge-bisim-inv*)  
 qed

Counter example for *delay-bisimulation-diverge trsys1 trsys2 bisim tlim τmove1 τmove2*  
 $\implies \exists \mu1 \mu2. \text{delay-bisimulation-measure trsys1 trsys2 bisim tlim } \tau\text{move1 } \tau\text{move2 } \mu1 \mu2$   
 (only  $\tau$ moves):

```
--|
| v
--a ~ x
|   |
|   |
v   v
--b ~ y--
| ^   ^ |
--|   |--
```

```

locale delay-bisimulation-measure-final =
  delay-bisimulation-measure +
  delay-bisimulation-final-base +
  constrains trsys1 :: ('s1, 'tl1) trsys
  and trsys2 :: ('s2, 'tl2) trsys
  and bisim :: ('s1, 's2) bisim
  and tlsim :: ('tl1, 'tl2) bisim
  and  $\tau$ move1 :: ('s1, 'tl1) trsys
  and  $\tau$ move2 :: ('s2, 'tl2) trsys
  and  $\mu 1 :: 's1 \Rightarrow 's1 \Rightarrow \text{bool}$ 
  and  $\mu 2 :: 's2 \Rightarrow 's2 \Rightarrow \text{bool}$ 
  and final1 :: 's1  $\Rightarrow$  bool
  and final2 :: 's2  $\Rightarrow$  bool

```

```

sublocale delay-bisimulation-measure-final < delay-bisimulation-diverge-final
by unfold-locales

```

```

locale  $\tau$ inv = delay-bisimulation-base +
  constrains trsys1 :: ('s1, 'tl1) trsys
  and trsys2 :: ('s2, 'tl2) trsys
  and bisim :: ('s1, 's2) bisim
  and tlsim :: ('tl1, 'tl2) bisim
  and  $\tau$ move1 :: ('s1, 'tl1) trsys
  and  $\tau$ move2 :: ('s2, 'tl2) trsys
  and  $\tau$ moves1 :: 's1  $\Rightarrow$  's1  $\Rightarrow$  bool
  and  $\tau$ moves2 :: 's2  $\Rightarrow$  's2  $\Rightarrow$  bool
  assumes  $\tau$ inv:  $\llbracket s1 \approx s2; s1 \text{ --1--} tl1 \rightarrow s1'; s2 \text{ --2--} tl2 \rightarrow s2'; s1' \approx s2'; tl1 \sim tl2 \rrbracket$ 
     $\implies \tau$ move1 s1 tl1 s1'  $\longleftrightarrow \tau$ move2 s2 tl2 s2'

```

```

begin

```

```

lemma  $\tau$ inv-flip:
   $\tau$ inv trsys2 trsys1 (flip bisim) (flip tlsim)  $\tau$ move2  $\tau$ move1
by(unfold-locales)(unfold flip-simps, rule  $\tau$ inv[symmetric])

```

```

end

```

```

lemma  $\tau$ inv-flip-simps [flip-simps]:
   $\tau$ inv trsys2 trsys1 (flip bisim) (flip tlsim)  $\tau$ move2  $\tau$ move1 =  $\tau$ inv trsys1 trsys2 bisim tlsim  $\tau$ move1
   $\tau$ move2
by(auto dest:  $\tau$ inv. $\tau$ inv-flip simp only: flip-simps)

```

```

locale bisimulation-into-delay =
  bisimulation +  $\tau$ inv +
  constrains trsys1 :: ('s1, 'tl1) trsys
  and trsys2 :: ('s2, 'tl2) trsys
  and bisim :: ('s1, 's2) bisim
  and tlsim :: ('tl1, 'tl2) bisim
  and  $\tau$ move1 :: ('s1, 'tl1) trsys
  and  $\tau$ move2 :: ('s2, 'tl2) trsys
begin

```

```

lemma bisimulation-into-delay-flip:
  bisimulation-into-delay trsys2 trsys1 (flip bisim) (flip tlsim)  $\tau$ move2  $\tau$ move1
by(intro-locales)(intro bisimulation-flip  $\tau$ inv-flip)+

```

end

**lemma** *bisimulation-into-delay-flip-simps* [flip-simps]:

*bisimulation-into-delay trsys2 trsys1 (flip bisim) (flip tlsim)  $\tau$ move2  $\tau$ move1 =*  
*bisimulation-into-delay trsys1 trsys2 bisim tlsim  $\tau$ move1  $\tau$ move2*

**by**(auto dest: bisimulation-into-delay.bisimulation-into-delay-flip simp only: flip-simps)

**context** *bisimulation-into-delay* **begin**

**lemma** *simulation-silent1-aux*:

**assumes** *bisim*:  $s1 \approx s2$  **and**  $s1 \rightarrow \tau 1 \rightarrow s1'$

**shows**  $s1' \approx s2 \wedge \mu 1^{++} s1' s1 \vee (\exists s2'. s2 \rightarrow \tau 2 \rightarrow s2' \wedge s1' \approx s2')$

**proof** –

**from** *assms* **obtain** *tl1* **where**  $tr1: s1 \rightarrow \tau 1 \rightarrow s1'$

**and**  $\tau 1: \tau \text{move1 } s1 \text{ tl1 } s1'$  **by**(auto)

**from** *simulation1*[OF *bisim* *tr1*]

**obtain**  $s2' \text{ tl2}$  **where**  $tr2: s2 \rightarrow \tau 2 \rightarrow s2'$

**and** *bisim'*:  $s1' \approx s2'$  **and** *tlsim*:  $tl1 \sim tl2$  **by** blast

**from**  $\tau \text{inv}$ [OF *bisim* *tr1* *tr2* *bisim'* *tlsim*]  $\tau 1$  **have**  $\tau 2: \tau \text{move2 } s2 \text{ tl2 } s2'$  **by** simp

**from**  $tr2 \tau 2$  **have**  $s2 \rightarrow \tau 2 \rightarrow s2'$  **by**(auto)

**with** *bisim'* **show** ?thesis **by** blast

qed

**lemma** *simulation-silent2-aux*:

$\llbracket s1 \approx s2; s2 \rightarrow \tau 2 \rightarrow s2' \rrbracket \implies s1 \approx s2' \wedge \mu 2^{++} s2' s2 \vee (\exists s1'. s1 \rightarrow \tau 1 \rightarrow s1' \wedge s1' \approx s2')$

**using** *bisimulation-into-delay.simulation-silent1-aux*[OF *bisimulation-into-delay-flip*]

**unfolding** *flip-simps* .

**lemma** *simulation1-aux*:

**assumes** *bisim*:  $s1 \approx s2$  **and**  $tr1: s1 \rightarrow \tau 1 \rightarrow s1'$  **and**  $\tau 1: \neg \tau \text{move1 } s1 \text{ tl1 } s1'$

**shows**  $\exists s2' s2'' \text{ tl2}. s2 \rightarrow \tau 2 \rightarrow s2' \wedge s2' \rightarrow \tau 2 \rightarrow s2'' \wedge \neg \tau \text{move2 } s2' \text{ tl2 } s2'' \wedge s1' \approx s2'' \wedge$   
 $tl1 \sim tl2$

**proof** –

**from** *simulation1*[OF *bisim* *tr1*]

**obtain**  $s2' \text{ tl2}$  **where**  $tr2: s2 \rightarrow \tau 2 \rightarrow s2'$

**and** *bisim'*:  $s1' \approx s2'$  **and** *tlsim*:  $tl1 \sim tl2$  **by** blast

**from**  $\tau \text{inv}$ [OF *bisim* *tr1* *tr2* *bisim'* *tlsim*]  $\tau 1$  **have**  $\tau 2: \neg \tau \text{move2 } s2 \text{ tl2 } s2'$  **by** simp

**with** *bisim'* *tr2* *tlsim* **show** ?thesis **by** blast

qed

**lemma** *simulation2-aux*:

$\llbracket s1 \approx s2; s2 \rightarrow \tau 2 \rightarrow s2'; \neg \tau \text{move2 } s2 \text{ tl2 } s2' \rrbracket$

$\implies \exists s1' s1'' \text{ tl1}. s1 \rightarrow \tau 1 \rightarrow s1' \wedge s1' \rightarrow \tau 1 \rightarrow s1'' \wedge \neg \tau \text{move1 } s1' \text{ tl1 } s1'' \wedge s1'' \approx s2' \wedge$   
 $tl1 \sim tl2$

**using** *bisimulation-into-delay.simulation1-aux*[OF *bisimulation-into-delay-flip*]

**unfolding** *flip-simps* .

**lemma** *delay-bisimulation-measure*:

**assumes** *wf- $\mu 1$* : *wfP*  $\mu 1$

**and** *wf- $\mu 2$* : *wfP*  $\mu 2$

**shows** *delay-bisimulation-measure trsys1 trsys2 bisim tlsim  $\tau$ move1  $\tau$ move2  $\mu 1 \mu 2$*

**apply**(unfold-locales)

**apply**(rule *simulation-silent1-aux simulation-silent2-aux simulation1-aux simulation2-aux wf- $\mu 1$  wf- $\mu 2$ |assumption*) +

done

**lemma** *delay-bisimulation*:

*delay-bisimulation-diverge trsys1 trsys2 bisim tlim  $\tau$ move1  $\tau$ move2*

**proof** –

**interpret** *delay-bisimulation-measure trsys1 trsys2 bisim tlim  $\tau$ move1  $\tau$ move2  $\lambda s s'. False \lambda s s'. False$*

**by**(blast intro: *delay-bisimulation-measure wfP-empty*)

**show** *?thesis ..*

**qed**

end

**sublocale** *bisimulation-into-delay* < *delay-bisimulation-diverge*

**by**(rule *delay-bisimulation*)

**lemma** *delay-bisimulation-conv-bisimulation*:

*delay-bisimulation-diverge trsys1 trsys2 bisim tlim ( $\lambda s tl s'. False$ ) ( $\lambda s tl s'. False$ ) =*

*bisimulation trsys1 trsys2 bisim tlim*

(**is** *?lhs = ?rhs*)

**proof**

**assume** *?lhs*

**then interpret** *delay-bisimulation-diverge trsys1 trsys2 bisim tlim  $\lambda s tl s'. False \lambda s tl s'. False$  .*

**show** *?rhs by(unfold-locales)(fastforce simp add:  $\tau$ moves-False dest: simulation1 simulation2)+*

**next**

**assume** *?rhs*

**then interpret** *bisimulation trsys1 trsys2 bisim tlim .*

**interpret** *bisimulation-into-delay trsys1 trsys2 bisim tlim  $\lambda s tl s'. False \lambda s tl s'. False$*

**by**(unfold-locales)(rule refl)

**show** *?lhs by unfold-locales*

**qed**

**context** *bisimulation-final* **begin**

**lemma** *delay-bisimulation-final-base*:

*delay-bisimulation-final-base trsys1 trsys2 bisim  $\tau$ move1  $\tau$ move2 final1 final2*

**by**(unfold-locales)(auto simp add: *bisim-final*)

end

**sublocale** *bisimulation-final* < *delay-bisimulation-final-base*

**by**(rule *delay-bisimulation-final-base*)

### 1.17.3 Transitivity for bisimulations

**definition** *bisim-compose* :: (*'s1, 's2*) *bisim*  $\Rightarrow$  (*'s2, 's3*) *bisim*  $\Rightarrow$  (*'s1, 's3*) *bisim* (**infixr**  $\circ_B$  60)

**where** (*bisim1*  $\circ_B$  *bisim2*) *s1 s3*  $\equiv \exists s2. bisim1 s1 s2 \wedge bisim2 s2 s3$

**lemma** *bisim-composeI* [intro]:

$\llbracket bisim12 s1 s2; bisim23 s2 s3 \rrbracket \Longrightarrow (bisim12 \circ_B bisim23) s1 s3$

**by**(auto simp add: *bisim-compose-def*)

**lemma** *bisim-composeE* [elim!]:

**assumes** *bisim*: (*bisim12*  $\circ_B$  *bisim23*) *s1 s3*



**obtains**  $s2$  **where**  $bisim12\ s1\ s2\ bisim23\ s2\ s3$   
**by**(*atomize-elim*)(*rule bisim[unfolded bisim-compose-def]*)

**lemma** *bisim-compose-assoc* [*simp*]:  
 $(bisim12 \circ_B bisim23) \circ_B bisim34 = bisim12 \circ_B bisim23 \circ_B bisim34$   
**by**(*auto simp add: fun-eq-iff*)

**lemma** *bisim-compose-conv-relcomp*:  
 $case\text{-}prod\ (bisim\text{-}compose\ bisim12\ bisim23) = (\lambda x. x \in relcomp\ (Collect\ (case\text{-}prod\ bisim12))\ (Collect\ (case\text{-}prod\ bisim23)))$   
**by**(*auto simp add: relcomp-unfold*)

**lemma** *list-all2-bisim-composeI*:  
 $\llbracket list\text{-}all2\ A\ xs\ ys; list\text{-}all2\ B\ ys\ zs \rrbracket$   
 $\implies list\text{-}all2\ (A \circ_B B)\ xs\ zs$   
**by**(*rule list-all2-trans*) *auto*+

**lemma** *delay-bisimulation-diverge-compose*:  
**assumes**  $wbisim12: delay\text{-}bisimulation\text{-}diverge\ trsys1\ trsys2\ bisim12\ tlim12\ \tau move1\ \tau move2$   
**and**  $wbisim23: delay\text{-}bisimulation\text{-}diverge\ trsys2\ trsys3\ bisim23\ tlim23\ \tau move2\ \tau move3$   
**shows**  $delay\text{-}bisimulation\text{-}diverge\ trsys1\ trsys3\ (bisim12 \circ_B bisim23)\ (tlim12 \circ_B tlim23)\ \tau move1\ \tau move3$

*proof* –

**interpret**  $trsys1: \tau trsys\ trsys1\ \tau move1$  .  
**interpret**  $trsys2: \tau trsys\ trsys2\ \tau move2$  .  
**interpret**  $trsys3: \tau trsys\ trsys3\ \tau move3$  .  
**interpret**  $wb12: delay\text{-}bisimulation\text{-}diverge\ trsys1\ trsys2\ bisim12\ tlim12\ \tau move1\ \tau move2$  **by**(*auto intro: wbisim12*)  
**interpret**  $wb23: delay\text{-}bisimulation\text{-}diverge\ trsys2\ trsys3\ bisim23\ tlim23\ \tau move2\ \tau move3$  **by**(*auto intro: wbisim23*)  
**show** ?thesis

**proof**

**fix**  $s1\ s3\ s1'$   
**assume**  $bisim: (bisim12 \circ_B bisim23)\ s1\ s3$  **and**  $tr1: trsys1.silent\text{-}move\ s1\ s1'$   
**from**  $bisim$  **obtain**  $s2$  **where**  $bisim1: bisim12\ s1\ s2$  **and**  $bisim2: bisim23\ s2\ s3$  **by** *blast*  
**from**  $wb12.simulation\text{-}silent1[OF\ bisim1\ tr1]$  **obtain**  $s2'$   
**where**  $tr2: trsys2.silent\text{-}moves\ s2\ s2'$  **and**  $bisim1': bisim12\ s1'\ s2'$  **by** *blast*  
**from**  $wb23.simulation\text{-}silents1[OF\ bisim2\ tr2]$  **obtain**  $s3'$   
**where**  $trsys3.silent\text{-}moves\ s3\ s3'\ bisim23\ s2'\ s3'$  **by** *blast*  
**with**  $bisim1'$  **show**  $\exists s3'. trsys3.silent\text{-}moves\ s3\ s3' \wedge (bisim12 \circ_B bisim23)\ s1'\ s3'$   
**by**(*blast intro: bisim-composeI*)

**next**

**fix**  $s1\ s3\ s3'$   
**assume**  $bisim: (bisim12 \circ_B bisim23)\ s1\ s3$  **and**  $tr3: trsys3.silent\text{-}move\ s3\ s3'$   
**from**  $bisim$  **obtain**  $s2$  **where**  $bisim1: bisim12\ s1\ s2$  **and**  $bisim2: bisim23\ s2\ s3$  **by** *blast*  
**from**  $wb23.simulation\text{-}silent2[OF\ bisim2\ tr3]$  **obtain**  $s2'$   
**where**  $tr2: trsys2.silent\text{-}moves\ s2\ s2'$  **and**  $bisim2': bisim23\ s2'\ s3'$  **by** *blast*  
**from**  $wb12.simulation\text{-}silents2[OF\ bisim1\ tr2]$  **obtain**  $s1'$   
**where**  $trsys1.silent\text{-}moves\ s1\ s1'\ bisim12\ s1'\ s2'$  **by** *blast*  
**with**  $bisim2'$  **show**  $\exists s1'. trsys1.silent\text{-}moves\ s1\ s1' \wedge (bisim12 \circ_B bisim23)\ s1'\ s3'$   
**by**(*blast intro: bisim-composeI*)

**next**

**fix**  $s1\ s3\ tl1\ s1'$   
**assume**  $bisim: (bisim12 \circ_B bisim23)\ s1\ s3$

and  $tr1: trsys1\ s1\ tl1\ s1'$  and  $\tau1: \neg \tau move1\ s1\ tl1\ s1'$   
 from  $bisim$  obtain  $s2$  where  $bisim1: bisim12\ s1\ s2$  and  $bisim2: bisim23\ s2\ s3$  by *blast*  
 from  $wb12.simulation1[OF\ bisim1\ tr1\ \tau1]$  obtain  $s2'\ s2''\ tl2$   
 where  $tr21: trsys2.silent-moves\ s2\ s2'$  and  $tr22: trsys2\ s2'\ tl2\ s2''$  and  $\tau2: \neg \tau move2\ s2'\ tl2\ s2''$   
 and  $bisim1': bisim12\ s1'\ s2''$  and  $tlsim1: tlsim12\ tl1\ tl2$  by *blast*  
 from  $wb23.simulation-silents1[OF\ bisim2\ tr21]$  obtain  $s3'$   
 where  $tr31: trsys3.silent-moves\ s3\ s3'$  and  $bisim2': bisim23\ s2'\ s3'$  by *blast*  
 from  $wb23.simulation1[OF\ bisim2'\ tr22\ \tau2]$  obtain  $s3''\ s3'''\ tl3$   
 where  $trsys3.silent-moves\ s3'\ s3''\ trsys3\ s3''\ tl3\ s3'''$   
 $\neg \tau move3\ s3''\ tl3\ s3'''$   $bisim23\ s2''\ s3'''$   $tlsim23\ tl2\ tl3$  by *blast*  
 with  $tr31\ bisim1'\ tlsim1$   
 show  $\exists s3'\ s3''\ tl3. trsys3.silent-moves\ s3\ s3' \wedge trsys3\ s3'\ tl3\ s3'' \wedge \neg \tau move3\ s3'\ tl3\ s3'' \wedge$   
 $(bisim12 \circ_B bisim23)\ s1'\ s3'' \wedge (tlsim12 \circ_B tlsim23)\ tl1\ tl3$   
 by(*blast intro: rtranclp-trans bisim-composeI*)  
 next  
 fix  $s1\ s3\ tl3\ s3'$   
 assume  $bisim: (bisim12 \circ_B bisim23)\ s1\ s3$   
 and  $tr3: trsys3\ s3\ tl3\ s3'$  and  $\tau3: \neg \tau move3\ s3\ tl3\ s3'$   
 from  $bisim$  obtain  $s2$  where  $bisim1: bisim12\ s1\ s2$  and  $bisim2: bisim23\ s2\ s3$  by *blast*  
 from  $wb23.simulation2[OF\ bisim2\ tr3\ \tau3]$  obtain  $s2'\ s2''\ tl2$   
 where  $tr21: trsys2.silent-moves\ s2\ s2'$  and  $tr22: trsys2\ s2'\ tl2\ s2''$  and  $\tau2: \neg \tau move2\ s2'\ tl2\ s2''$   
 and  $bisim2': bisim23\ s2''\ s3'$  and  $tlsim2: tlsim23\ tl2\ tl3$  by *blast*  
 from  $wb12.simulation-silents2[OF\ bisim1\ tr21]$  obtain  $s1'$   
 where  $tr11: trsys1.silent-moves\ s1\ s1'$  and  $bisim1': bisim12\ s1'\ s2'$  by *blast*  
 from  $wb12.simulation2[OF\ bisim1'\ tr22\ \tau2]$  obtain  $s1''\ s1'''\ tl1$   
 where  $trsys1.silent-moves\ s1'\ s1''\ trsys1\ s1''\ tl1\ s1'''$   
 $\neg \tau move1\ s1''\ tl1\ s1'''$   $bisim12\ s1'''\ s2''$   $tlsim12\ tl1\ tl2$  by *blast*  
 with  $tr11\ bisim2'\ tlsim2$   
 show  $\exists s1'\ s1''\ tl1. trsys1.silent-moves\ s1\ s1' \wedge trsys1\ s1'\ tl1\ s1'' \wedge \neg \tau move1\ s1'\ tl1\ s1'' \wedge$   
 $(bisim12 \circ_B bisim23)\ s1''\ s3' \wedge (tlsim12 \circ_B tlsim23)\ tl1\ tl3$   
 by(*blast intro: rtranclp-trans bisim-composeI*)  
 next  
 fix  $s1\ s2$   
 assume  $(bisim12 \circ_B bisim23)\ s1\ s2$   
 thus  $\tau trsys.\tau diverge\ trsys1\ \tau move1\ s1 = \tau trsys.\tau diverge\ trsys3\ \tau move3\ s2$   
 by(*auto simp add: wb12. $\tau$ diverge-bisim-inv wb23. $\tau$ diverge-bisim-inv*)  
 qed  
 qed

**lemma** *bisimulation-bisim-compose*:

$\llbracket bisimulation\ trsys1\ trsys2\ bisim12\ tlsim12; bisimulation\ trsys2\ trsys3\ bisim23\ tlsim23 \rrbracket$   
 $\implies bisimulation\ trsys1\ trsys3\ (bisim-compose\ bisim12\ bisim23)\ (bisim-compose\ tlsim12\ tlsim23)$

**unfolding** *delay-bisimulation-conv-bisimulation[symmetric]*

by(*rule delay-bisimulation-diverge-compose*)

**lemma** *delay-bisimulation-diverge-final-compose*:

fixes  $\tau move1\ \tau move2$

assumes  $wbisim12: delay-bisimulation-diverge-final\ trsys1\ trsys2\ bisim12\ tlsim12\ \tau move1\ \tau move2$   
 $final1\ final2$

and  $wbisim23: delay-bisimulation-diverge-final\ trsys2\ trsys3\ bisim23\ tlsim23\ \tau move2\ \tau move3\ final2$   
 $final3$

shows  $delay-bisimulation-diverge-final\ trsys1\ trsys3\ (bisim12 \circ_B bisim23)\ (tlsim12 \circ_B tlsim23)$

```

 $\tau move1 \ \tau move3 \ final1 \ final3$ 
proof –
  interpret  $trsys1$ :  $\tau trsys \ trsys1 \ \tau move1$  .
  interpret  $trsys2$ :  $\tau trsys \ trsys2 \ \tau move2$  .
  interpret  $trsys3$ :  $\tau trsys \ trsys3 \ \tau move3$  .
  interpret  $wb12$ :  $delay\text{-}bisimulation\text{-}diverge\text{-}final \ trsys1 \ trsys2 \ bisim12 \ tlim12 \ \tau move1 \ \tau move2 \ final1$ 
 $final2$ 
    by( $auto \ intro$ :  $wbisim12$ )
  interpret  $wb23$ :  $delay\text{-}bisimulation\text{-}diverge\text{-}final \ trsys2 \ trsys3 \ bisim23 \ tlim23 \ \tau move2 \ \tau move3 \ final2$ 
 $final3$ 
    by( $auto \ intro$ :  $wbisim23$ )
  interpret  $delay\text{-}bisimulation\text{-}diverge \ trsys1 \ trsys3 \ bisim12 \ \circ_B \ bisim23 \ tlim12 \ \circ_B \ tlim23 \ \tau move1$ 
 $\tau move3$ 
    by( $rule \ delay\text{-}bisimulation\text{-}diverge\text{-}compose$ )( $unfold\text{-}locales$ )
  show  $?thesis$ 
  proof
    fix  $s1 \ s3$ 
    assume  $(bisim12 \ \circ_B \ bisim23) \ s1 \ s3 \ final1 \ s1$ 
    from  $\langle (bisim12 \ \circ_B \ bisim23) \ s1 \ s3 \rangle$  obtain  $s2$  where  $bisim12 \ s1 \ s2$  and  $bisim23 \ s2 \ s3$  ..
    from  $wb12.final1\text{-}simulation[OF \ \langle bisim12 \ s1 \ s2 \rangle \ \langle final1 \ s1 \rangle]$ 
    obtain  $s2'$  where  $trsys2.silent\text{-}moves \ s2 \ s2' \ bisim12 \ s1 \ s2' \ final2 \ s2'$  by  $blast$ 
    from  $wb23.simulation\text{-}silents1[OF \ \langle bisim23 \ s2 \ s3 \rangle \ \langle trsys2.silent\text{-}moves \ s2 \ s2' \rangle]$ 
    obtain  $s3'$  where  $trsys3.silent\text{-}moves \ s3 \ s3' \ bisim23 \ s2' \ s3'$  by  $blast$ 
    from  $wb23.final1\text{-}simulation[OF \ \langle bisim23 \ s2' \ s3' \rangle \ \langle final2 \ s2' \rangle]$ 
    obtain  $s3''$  where  $trsys3.silent\text{-}moves \ s3' \ s3'' \ bisim23 \ s2' \ s3'' \ final3 \ s3''$  by  $blast$ 
    from  $\langle trsys3.silent\text{-}moves \ s3 \ s3' \rangle \ \langle trsys3.silent\text{-}moves \ s3' \ s3'' \rangle$ 
    have  $trsys3.silent\text{-}moves \ s3 \ s3''$  by( $rule \ rtranclp\text{-}trans$ )
    moreover from  $\langle bisim12 \ s1 \ s2' \rangle \ \langle bisim23 \ s2' \ s3'' \rangle$ 
    have  $(bisim12 \ \circ_B \ bisim23) \ s1 \ s3''$  ..
    ultimately show  $\exists s3'. \ trsys3.silent\text{-}moves \ s3 \ s3' \wedge (bisim12 \ \circ_B \ bisim23) \ s1 \ s3' \wedge final3 \ s3'$ 
    using  $\langle final3 \ s3'' \rangle$  by  $iprover$ 
  next
    fix  $s1 \ s3$ 
    assume  $(bisim12 \ \circ_B \ bisim23) \ s1 \ s3 \ final3 \ s3$ 
    from  $\langle (bisim12 \ \circ_B \ bisim23) \ s1 \ s3 \rangle$  obtain  $s2$  where  $bisim12 \ s1 \ s2$  and  $bisim23 \ s2 \ s3$  ..
    from  $wb23.final2\text{-}simulation[OF \ \langle bisim23 \ s2 \ s3 \rangle \ \langle final3 \ s3 \rangle]$ 
    obtain  $s2'$  where  $trsys2.silent\text{-}moves \ s2 \ s2' \ bisim23 \ s2' \ s3 \ final2 \ s2'$  by  $blast$ 
    from  $wb12.simulation\text{-}silents2[OF \ \langle bisim12 \ s1 \ s2 \rangle \ \langle trsys2.silent\text{-}moves \ s2 \ s2' \rangle]$ 
    obtain  $s1'$  where  $trsys1.silent\text{-}moves \ s1 \ s1' \ bisim12 \ s1' \ s2'$  by  $blast$ 
    from  $wb12.final2\text{-}simulation[OF \ \langle bisim12 \ s1' \ s2' \rangle \ \langle final2 \ s2' \rangle]$ 
    obtain  $s1''$  where  $trsys1.silent\text{-}moves \ s1' \ s1'' \ bisim12 \ s1'' \ s2' \ final1 \ s1''$  by  $blast$ 
    from  $\langle trsys1.silent\text{-}moves \ s1 \ s1' \rangle \ \langle trsys1.silent\text{-}moves \ s1' \ s1'' \rangle$ 
    have  $trsys1.silent\text{-}moves \ s1 \ s1''$  by( $rule \ rtranclp\text{-}trans$ )
    moreover from  $\langle bisim12 \ s1'' \ s2' \rangle \ \langle bisim23 \ s2' \ s3 \rangle$ 
    have  $(bisim12 \ \circ_B \ bisim23) \ s1'' \ s3$  ..
    ultimately show  $\exists s1'. \ trsys1.silent\text{-}moves \ s1 \ s1' \wedge (bisim12 \ \circ_B \ bisim23) \ s1' \ s3 \wedge final1 \ s1'$ 
    using  $\langle final1 \ s1'' \rangle$  by  $iprover$ 
  qed
qed
end

```

## 1.18 Bisimulation relations for the multithreaded semantics

```
theory FWBisimulation
imports
  FWLTS
  Bisimulation
begin
```

### 1.18.1 Definitions for lifting bisimulation relations

```
primrec nta-bisim :: ('t  $\Rightarrow$  ('x1  $\times$  'm1, 'x2  $\times$  'm2) bisim)  $\Rightarrow$  (('t, 'x1, 'm1) new-thread-action,
('t, 'x2, 'm2) new-thread-action) bisim
  where
    [code del]: nta-bisim bisim (NewThread t x m) ta = ( $\exists$  x' m'. ta = NewThread t x' m'  $\wedge$  bisim t (x,
m) (x', m'))
  | nta-bisim bisim (ThreadExists t b) ta = (ta = ThreadExists t b)
```

```
lemma nta-bisim-1-code [code]:
  nta-bisim bisim (NewThread t x m) ta = (case ta of NewThread t' x' m'  $\Rightarrow$  t = t'  $\wedge$  bisim t (x, m)
(x', m') | -  $\Rightarrow$  False)
by(auto split: new-thread-action.split)
```

```
lemma nta-bisim-simps-sym [simp]:
  nta-bisim bisim ta (NewThread t x m) = ( $\exists$  x' m'. ta = NewThread t x' m'  $\wedge$  bisim t (x', m') (x, m))
  nta-bisim bisim ta (ThreadExists t b) = (ta = ThreadExists t b)
by(cases ta, auto)+
```

```
definition ta-bisim :: ('t  $\Rightarrow$  ('x1  $\times$  'm1, 'x2  $\times$  'm2) bisim)  $\Rightarrow$  (('l, 't, 'x1, 'm1, 'w, 'o) thread-action,
('l, 't, 'x2, 'm2, 'w, 'o) thread-action) bisim
  where
    ta-bisim bisim ta1 ta2  $\equiv$ 
       $\llbracket$  ta1  $\rrbracket_l = \llbracket$  ta2  $\rrbracket_l \wedge \llbracket$  ta1  $\rrbracket_w = \llbracket$  ta2  $\rrbracket_w \wedge \llbracket$  ta1  $\rrbracket_c = \llbracket$  ta2  $\rrbracket_c \wedge \llbracket$  ta1  $\rrbracket_o = \llbracket$  ta2  $\rrbracket_o \wedge \llbracket$  ta1
 $\rrbracket_i = \llbracket$  ta2  $\rrbracket_i \wedge$ 
      list-all2 (nta-bisim bisim)  $\llbracket$  ta1  $\rrbracket_t \llbracket$  ta2  $\rrbracket_t$ 
```

```
lemma ta-bisim-empty [iff]: ta-bisim bisim  $\varepsilon$   $\varepsilon$ 
by(auto simp add: ta-bisim-def)
```

```
lemma ta-bisim- $\varepsilon$  [simp]:
  ta-bisim b  $\varepsilon$  ta'  $\iff$  ta' =  $\varepsilon$  ta-bisim b ta  $\varepsilon$   $\iff$  ta =  $\varepsilon$ 
apply(cases ta', fastforce simp add: ta-bisim-def)
apply(cases ta, fastforce simp add: ta-bisim-def)
done
```

```
lemma nta-bisim-mono:
  assumes major: nta-bisim bisim ta ta'
  and mono:  $\bigwedge$  t s1 s2. bisim t s1 s2  $\implies$  bisim' t s1 s2
  shows nta-bisim bisim' ta ta'
using major by(cases ta)(auto intro: mono)
```

```
lemma ta-bisim-mono:
  assumes major: ta-bisim bisim ta1 ta2
  and mono:  $\bigwedge$  t s1 s2. bisim t s1 s2  $\implies$  bisim' t s1 s2
  shows ta-bisim bisim' ta1 ta2
```

**using** major

**by**(*auto simp add: ta-bisim-def elim!: List.list-all2-mono nta-bisim-mono intro: mono*)

**lemma** *nta-bisim-flip* [*flip-simps*]:

*nta-bisim* ( $\lambda t. \text{flip } (\text{bisim } t)$ ) = *flip* (*nta-bisim bisim*)

**by**(*rule ext*)(*case-tac x, auto simp add: flip-simps*)

**lemma** *ta-bisim-flip* [*flip-simps*]:

*ta-bisim* ( $\lambda t. \text{flip } (\text{bisim } t)$ ) = *flip* (*ta-bisim bisim*)

**by**(*auto simp add: fun-eq-iff flip-simps ta-bisim-def*)

**locale** *FWbisimulation-base* =

*r1: multithreaded-base final1 r1 convert-RA* +

*r2: multithreaded-base final2 r2 convert-RA*

**for** *final1* :: '*x1*  $\Rightarrow$  bool

**and** *r1* :: ('*l*, '*t*, '*x1*, '*m1*, '*w*, '*o*) *semantics* ( $- \vdash - - 1 \dashrightarrow -$  [50, 0, 0, 50] 80)

**and** *final2* :: '*x2*  $\Rightarrow$  bool

**and** *r2* :: ('*l*, '*t*, '*x2*, '*m2*, '*w*, '*o*) *semantics* ( $- \vdash - - 2 \dashrightarrow -$  [50, 0, 0, 50] 80)

**and** *convert-RA* :: '*l* *released-locks*  $\Rightarrow$  '*o* list

+

**fixes** *bisim* :: '*t*  $\Rightarrow$  ('*x1*  $\times$  '*m1*, '*x2*  $\times$  '*m2*) *bisim* ( $- \vdash - / \approx -$  [50, 50, 50] 60)

**and** *bisim-wait* :: ('*x1*, '*x2*) *bisim* ( $- / \approx_w -$  [50, 50] 60)

**begin**

**notation** *r1.redT-syntax1* ( $- - 1 \dashrightarrow -$  [50, 0, 0, 50] 80)

**notation** *r2.redT-syntax1* ( $- - 2 \dashrightarrow -$  [50, 0, 0, 50] 80)

**notation** *r1.RedT* ( $- - 1 \dashrightarrow - \ast -$  [50, 0, 50] 80)

**notation** *r2.RedT* ( $- - 2 \dashrightarrow - \ast -$  [50, 0, 50] 80)

**notation** *r1.must-sync* ( $- \vdash \langle -, / - \rangle / \wr 1$  [50, 0, 0] 81)

**notation** *r2.must-sync* ( $- \vdash \langle -, / - \rangle / \wr 2$  [50, 0, 0] 81)

**notation** *r1.can-sync* ( $- \vdash \langle -, / - \rangle / - / \wr 1$  [50, 0, 0, 0] 81)

**notation** *r2.can-sync* ( $- \vdash \langle -, / - \rangle / - / \wr 2$  [50, 0, 0, 0] 81)

**abbreviation** *ta-bisim-bisim-syntax* ( $- / \sim_m -$  [50, 50] 60)

**where** *ta1*  $\sim_m$  *ta2*  $\equiv$  *ta-bisim bisim ta1 ta2*

**definition** *tbisim* :: bool  $\Rightarrow$  '*t*  $\Rightarrow$  ('*x1*  $\times$  '*l* *released-locks*) *option*  $\Rightarrow$  '*m1*  $\Rightarrow$  ('*x2*  $\times$  '*l* *released-locks*)

*option*  $\Rightarrow$  '*m2*  $\Rightarrow$  bool **where**

$\bigwedge \text{ln. } \text{tbisim } \text{nw } t \text{ ts1 } m1 \text{ ts2 } m2 \longleftrightarrow$

(*case ts1 of* None  $\Rightarrow$  *ts2* = None

| [(*x1*, *ln*)]  $\Rightarrow$  ( $\exists x2. \text{ts2} = [(x2, \text{ln})] \wedge t \vdash (x1, m1) \approx (x2, m2) \wedge (\text{nw} \vee x1 \approx_w x2))$ ))

**lemma** *tbisim-NoneI*: *tbisim w t None m None m'*

**by**(*simp add: tbisim-def*)

**lemma** *tbisim-SomeI*:

$\bigwedge \text{ln. } \llbracket t \vdash (x, m) \approx (x', m'); \text{nw} \vee x \approx_w x' \rrbracket \Longrightarrow \text{tbisim } \text{nw } t \text{ (Some } (x, \text{ln})) \text{ } m \text{ (Some } (x', \text{ln})) \text{ } m'$

**by**(*simp add: tbisim-def*)

**lemma** *tbisim-cases*[*consumes 1, case-names None Some*]:

**assumes** major: *tbisim nw t ts1 m1 ts2 m2*

**and**  $\llbracket ts1 = \text{None}; ts2 = \text{None} \rrbracket \implies \text{thesis}$   
**and**  $\bigwedge x \ln x'. \llbracket ts1 = \lfloor (x, \ln) \rfloor; ts2 = \lfloor (x', \ln) \rfloor; t \vdash (x, m1) \approx (x', m2); nw \vee x \approx_w x' \rrbracket \implies \text{thesis}$   
**shows** *thesis*  
**using** *assms*  
**by**(*auto simp add: tbisim-def*)

**definition** *mbisim* ::  $((l', t, x1, m1, w) \text{ state}, (l', t, x2, m2, w) \text{ state}) \text{ bisim } (- \approx_m - [50, 50] 60)$   
**where**

$s1 \approx_m s2 \equiv$   
 $\text{finite } (\text{dom } (\text{thr } s1)) \wedge \text{locks } s1 = \text{locks } s2 \wedge \text{wset } s1 = \text{wset } s2 \wedge \text{wset-thread-ok } (\text{wset } s1) (\text{thr } s1)$   
 $\wedge$   
 $\text{interrupts } s1 = \text{interrupts } s2 \wedge$   
 $(\forall t. \text{tbisim } (\text{wset } s2 \ t = \text{None}) \ t \ (\text{thr } s1 \ t) \ (\text{shr } s1) \ (\text{thr } s2 \ t) \ (\text{shr } s2))$

**lemma** *mbisim-thrNone-eq*:  $s1 \approx_m s2 \implies \text{thr } s1 \ t = \text{None} \longleftrightarrow \text{thr } s2 \ t = \text{None}$

**unfolding** *mbisim-def tbisim-def*

**apply**(*clarify*)

**apply**(*erule allE[where x=t]*)

**apply**(*clarsimp*)

**done**

**lemma** *mbisim-thrD1*:

$\bigwedge \ln. \llbracket s1 \approx_m s2; \text{thr } s1 \ t = \lfloor (x, \ln) \rfloor \rrbracket$   
 $\implies \exists x'. \text{thr } s2 \ t = \lfloor (x', \ln) \rfloor \wedge t \vdash (x, \text{shr } s1) \approx (x', \text{shr } s2) \wedge (\text{wset } s1 \ t = \text{None} \vee x \approx_w x')$

**by**(*fastforce simp add: mbisim-def tbisim-def*)

**lemma** *mbisim-thrD2*:

$\bigwedge \ln. \llbracket s1 \approx_m s2; \text{thr } s2 \ t = \lfloor (x, \ln) \rfloor \rrbracket$   
 $\implies \exists x'. \text{thr } s1 \ t = \lfloor (x', \ln) \rfloor \wedge t \vdash (x', \text{shr } s1) \approx (x, \text{shr } s2) \wedge (\text{wset } s2 \ t = \text{None} \vee x' \approx_w x)$

**by**(*frule mbisim-thrNone-eq[where t=t](cases thr s1 t, fastforce simp add: mbisim-def tbisim-def)+*)

**lemma** *mbisim-dom-eq*:  $s1 \approx_m s2 \implies \text{dom } (\text{thr } s1) = \text{dom } (\text{thr } s2)$

**apply**(*clarsimp simp add: dom-def fun-eq-iff simp del: not-None-eq*)

**apply**(*rule Collect-cong*)

**apply**(*drule mbisim-thrNone-eq*)

**apply**(*simp del: not-None-eq*)

**done**

**lemma** *mbisim-wset-thread-ok1*:

$s1 \approx_m s2 \implies \text{wset-thread-ok } (\text{wset } s1) (\text{thr } s1)$

**by**(*clarsimp simp add: mbisim-def*)

**lemma** *mbisim-wset-thread-ok2*:

**assumes**  $s1 \approx_m s2$

**shows**  $\text{wset-thread-ok } (\text{wset } s2) (\text{thr } s2)$

**using** *assms*

**apply**(*clarsimp simp add: mbisim-def*)

**apply**(*auto intro!: wset-thread-okI simp add: mbisim-thrNone-eq[OF assms, THEN sym] dest: wset-thread-okD*)

**done**

**lemma** *mbisimI*:

$\llbracket \text{finite } (\text{dom } (\text{thr } s1)); \text{locks } s1 = \text{locks } s2; \text{wset } s1 = \text{wset } s2; \text{interrupts } s1 = \text{interrupts } s2;$

$\text{wset-thread-ok } (\text{wset } s1) (\text{thr } s1);$

$\bigwedge t. \text{thr } s1 \ t = \text{None} \implies \text{thr } s2 \ t = \text{None};$

$\bigwedge t \ x1 \ ln. \ thr \ s1 \ t = \lfloor (x1, ln) \rfloor \implies \exists x2. \ thr \ s2 \ t = \lfloor (x2, ln) \rfloor \wedge t \vdash (x1, shr \ s1) \approx (x2, shr \ s2)$   
 $\wedge (wset \ s2 \ t = None \vee x1 \approx_w x2) \rfloor$   
 $\implies s1 \approx_m s2$   
**by**(*fastforce simp add: mbisim-def tbisim-def*)

**lemma** *mbisimI2*:

$\llbracket finite \ (dom \ (thr \ s2)); \ locks \ s1 = locks \ s2; \ wset \ s1 = wset \ s2; \ interrupts \ s1 = interrupts \ s2;$   
 $wset-thread-ok \ (wset \ s2) \ (thr \ s2);$   
 $\bigwedge t. \ thr \ s2 \ t = None \implies thr \ s1 \ t = None;$   
 $\bigwedge t \ x2 \ ln. \ thr \ s2 \ t = \lfloor (x2, ln) \rfloor \implies \exists x1. \ thr \ s1 \ t = \lfloor (x1, ln) \rfloor \wedge t \vdash (x1, shr \ s1) \approx (x2, shr \ s2)$   
 $\wedge (wset \ s2 \ t = None \vee x1 \approx_w x2) \rfloor$   
 $\implies s1 \approx_m s2$

**apply**(*auto simp add: mbisim-def tbisim-def*)

**prefer** 2  
**apply**(*rule wset-thread-okI*)  
**apply**(*case-tac thr s2 t*)  
**apply**(*auto dest!: wset-thread-okD*)[1]  
**apply** *fastforce*  
**apply**(*erule back-subst*[**where**  $P=finite$ ])  
**apply**(*clarsimp simp add: dom-def fun-eq-iff simp del: not-None-eq*)  
**defer**  
**apply**(*rename-tac t*)  
**apply**(*case-tac* [!] *thr s2 t*)  
**by** *fastforce+*

**lemma** *mbisim-finite1*:

$s1 \approx_m s2 \implies finite \ (dom \ (thr \ s1))$   
**by**(*simp add: mbisim-def*)

**lemma** *mbisim-finite2*:

$s1 \approx_m s2 \implies finite \ (dom \ (thr \ s2))$   
**by**(*frule mbisim-finite1*)(*simp add: mbisim-dom-eq*)

**definition** *mta-bisim* ::  $(t \times (l, t, x1, m1, w, o) \text{ thread-action},$   
 $t \times (l, t, x2, m2, w, o) \text{ thread-action}) \text{ bisim}$

$(-/ \sim_T - [50, 50] \ 60)$

**where**  $tta1 \sim_T tta2 \equiv fst \ tta1 = fst \ tta2 \wedge snd \ tta1 \sim_m snd \ tta2$

**lemma** *mta-bisim-conv* [*simp*]:  $(t, ta1) \sim_T (t', ta2) \longleftrightarrow t = t' \wedge ta1 \sim_m ta2$

**by**(*simp add: mta-bisim-def*)

**definition** *bisim-inv* :: *bool* **where**

$bisim-inv \equiv (\forall s1 \ ta1 \ s1' \ s2 \ t. \ t \vdash s1 \approx s2 \longrightarrow t \vdash s1 \ -1-ta1 \rightarrow s1' \longrightarrow (\exists s2'. \ t \vdash s1' \approx s2')) \wedge$   
 $(\forall s2 \ ta2 \ s2' \ s1 \ t. \ t \vdash s1 \approx s2 \longrightarrow t \vdash s2 \ -2-ta2 \rightarrow s2' \longrightarrow (\exists s1'. \ t \vdash s1' \approx s2'))$

**lemma** *bisim-invI*:

$\llbracket \bigwedge s1 \ ta1 \ s1' \ s2 \ t. \llbracket t \vdash s1 \approx s2; \ t \vdash s1 \ -1-ta1 \rightarrow s1' \rrbracket \implies \exists s2'. \ t \vdash s1' \approx s2';$   
 $\bigwedge s2 \ ta2 \ s2' \ s1 \ t. \llbracket t \vdash s1 \approx s2; \ t \vdash s2 \ -2-ta2 \rightarrow s2' \rrbracket \implies \exists s1'. \ t \vdash s1' \approx s2' \rrbracket$   
 $\implies bisim-inv$

**by**(*auto simp add: bisim-inv-def*)

**lemma** *bisim-invD1*:

$\llbracket bisim-inv; \ t \vdash s1 \approx s2; \ t \vdash s1 \ -1-ta1 \rightarrow s1' \rrbracket \implies \exists s2'. \ t \vdash s1' \approx s2'$

**unfolding** *bisim-inv-def* **by** *blast*

**lemma** *bisim-invD2*:

$\llbracket \text{bisim-inv}; t \vdash s1 \approx s2; t \vdash s2 \text{ --2--} ta2 \rightarrow s2' \rrbracket \implies \exists s1'. t \vdash s1' \approx s2'$   
**unfolding** *bisim-inv-def* **by** *blast*

**lemma** *thread-oks-bisim-inv*:

$\llbracket \forall t. ts1\ t = \text{None} \longleftrightarrow ts2\ t = \text{None}; \text{list-all2}\ (\text{nta-bisim}\ \text{bisim})\ tas1\ tas2 \rrbracket$   
 $\implies \text{thread-oks}\ ts1\ tas1 \longleftrightarrow \text{thread-oks}\ ts2\ tas2$

**proof**(*induct tas2 arbitrary: tas1 ts1 ts2*)

**case** *Nil* **thus** ?*case* **by**(*simp*)

**next**

**case** (*Cons ta2 TAS2 tas1 TS1 TS2*)

**note** *IH* =  $\langle \bigwedge ts1\ tas1\ ts2. \llbracket \forall t. ts1\ t = \text{None} \longleftrightarrow ts2\ t = \text{None}; \text{list-all2}\ (\text{nta-bisim}\ \text{bisim})\ tas1\ TAS2 \rrbracket$

$\implies \text{thread-oks}\ ts1\ tas1 \longleftrightarrow \text{thread-oks}\ ts2\ TAS2 \rangle$

**note** *eqNone* =  $\langle \forall t. TS1\ t = \text{None} \longleftrightarrow TS2\ t = \text{None} \rangle$  [*rule-format*]

**hence** *fti*: *free-thread-id* *TS1* = *free-thread-id* *TS2* **by**(*auto simp add: free-thread-id-def*)

**from**  $\langle \text{list-all2}\ (\text{nta-bisim}\ \text{bisim})\ tas1\ (ta2 \# TAS2) \rangle$

**obtain** *ta1 TAS1* **where** *tas1* = *ta1* # *TAS1* *nta-bisim* *bisim* *ta1* *ta2* *list-all2* (*nta-bisim* *bisim*) *TAS1* *TAS2*

**by**(*auto simp add: list-all2-Cons2*)

**moreover**

{ **fix** *t*

**from**  $\langle \text{nta-bisim}\ \text{bisim}\ ta1\ ta2 \rangle$  **have** *redT-updT'* *TS1* *ta1* *t* = *None*  $\longleftrightarrow$  *redT-updT'* *TS2* *ta2* *t* = *None*

**by**(*cases ta1, auto split: if-split-asm simp add: eqNone*) }

**ultimately** **have** *thread-oks* (*redT-updT'* *TS1* *ta1*) *TAS1*  $\longleftrightarrow$  *thread-oks* (*redT-updT'* *TS2* *ta2*) *TAS2*

**by**  $\neg$ (*rule IH, auto*)

**moreover** **from**  $\langle \text{nta-bisim}\ \text{bisim}\ ta1\ ta2 \rangle$  *fti* **have** *thread-ok* *TS1* *ta1* = *thread-ok* *TS2* *ta2* **by**(*cases ta1, auto*)

**ultimately** **show** ?*case* **using**  $\langle tas1 = ta1 \# TAS1 \rangle$  **by** *auto*

**qed**

**lemma** *redT-updT-nta-bisim-inv*:

$\llbracket \text{nta-bisim}\ \text{bisim}\ ta1\ ta2; ts1\ T = \text{None} \longleftrightarrow ts2\ T = \text{None} \rrbracket \implies \text{redT-updT}\ ts1\ ta1\ T = \text{None}$   
 $\longleftrightarrow \text{redT-updT}\ ts2\ ta2\ T = \text{None}$

**by**(*cases ta1, auto*)

**lemma** *redT-updT-nts-nta-bisim-inv*:

$\llbracket \text{list-all2}\ (\text{nta-bisim}\ \text{bisim})\ tas1\ tas2; ts1\ T = \text{None} \longleftrightarrow ts2\ T = \text{None} \rrbracket$

$\implies \text{redT-updT}\ ts1\ tas1\ T = \text{None} \longleftrightarrow \text{redT-updT}\ ts2\ tas2\ T = \text{None}$

**proof**(*induct tas1 arbitrary: tas2 ts1 ts2*)

**case** *Nil* **thus** ?*case* **by**(*simp*)

**next**

**case** (*Cons TA1 TAS1 tas2 TS1 TS2*)

**note** *IH* =  $\langle \bigwedge tas2\ ts1\ ts2. \llbracket \text{list-all2}\ (\text{nta-bisim}\ \text{bisim})\ TAS1\ tas2; (ts1\ T = \text{None}) = (ts2\ T = \text{None}) \rrbracket$

$\implies (\text{redT-updT}\ ts1\ TAS1\ T = \text{None}) = (\text{redT-updT}\ ts2\ tas2\ T = \text{None}) \rangle$

**from**  $\langle \text{list-all2}\ (\text{nta-bisim}\ \text{bisim})\ (TA1 \# TAS1)\ tas2 \rangle$

**obtain** *TA2 TAS2* **where** *tas2* = *TA2* # *TAS2* *nta-bisim* *bisim* *TA1* *TA2* *list-all2* (*nta-bisim* *bisim*) *TAS1* *TAS2*

**by**(*auto simp add: list-all2-Cons1*)

**from**  $\langle \text{nta-bisim}\ \text{bisim}\ TA1\ TA2 \rangle$   $\langle (TS1\ T = \text{None}) = (TS2\ T = \text{None}) \rangle$



```

have redT-updT TS1 TA1 T = None  $\longleftrightarrow$  redT-updT TS2 TA2 T = None
  by(rule redT-updT-nta-bisim-inv)
with IH[OF ‹list-all2 (nta-bisim bisim) TAS1 TAS2›, of redT-updT TS1 TA1 redT-updT TS2 TA2]
‹tas2 = TA2 # TAS2›
  show ?case by simp
qed

```

**end**

**lemma** tbbisim-flip [flip-simps]:

```

FWbisimulation-base.tbbisim ( $\lambda t$ . flip (bisim t)) (flip bisim-wait) w t ts2 m2 ts1 m1 =
FWbisimulation-base.tbbisim bisim bisim-wait w t ts1 m1 ts2 m2

```

**unfolding** FWbisimulation-base.tbbisim-def flip-simps **by** auto

**lemma** mbisim-flip [flip-simps]:

```

FWbisimulation-base.mbisim ( $\lambda t$ . flip (bisim t)) (flip bisim-wait) s2 s1 =
FWbisimulation-base.mbisim bisim bisim-wait s1 s2

```

**apply**(rule iffI)

**apply**(frule FWbisimulation-base.mbisim-dom-eq)

**apply**(frule FWbisimulation-base.mbisim-wset-thread-ok2)

**apply**(fastforce simp add: FWbisimulation-base.mbisim-def flip-simps)

**apply**(frule FWbisimulation-base.mbisim-dom-eq)

**apply**(frule FWbisimulation-base.mbisim-wset-thread-ok2)

**apply**(fastforce simp add: FWbisimulation-base.mbisim-def flip-simps)

**done**

**lemma** mta-bisim-flip [flip-simps]:

```

FWbisimulation-base.mta-bisim ( $\lambda t$ . flip (bisim t)) = flip (FWbisimulation-base.mta-bisim bisim)

```

**by**(auto simp add: fun-eq-iff flip-simps FWbisimulation-base.mta-bisim-def)

**lemma** flip-const [simp]: flip ( $\lambda a$  b. c) = ( $\lambda a$  b. c)

**by**(rule flip-def)

**lemma** mbisim-K-flip [flip-simps]:

```

FWbisimulation-base.mbisim ( $\lambda t$ . flip (bisim t)) ( $\lambda x1$  x2. c) s1 s2 =
FWbisimulation-base.mbisim bisim ( $\lambda x1$  x2. c) s2 s1

```

**using** mbisim-flip[of bisim  $\lambda x1$  x2. c s1 s2]

**unfolding** flip-const .

**context** FWbisimulation-base **begin**

**lemma** mbisim-actions-ok-bisim-no-join-12:

**assumes** mbisim: mbisim s1 s2

**and** collect-cond-actions  $\{ta1\}_c = \{\}$

**and** ta-bisim bisim ta1 ta2

**and** r1.actions-ok s1 t ta1

**shows** r2.actions-ok s2 t ta2

**using** assms mbisim-thrNone-eq[OF mbisim]

**by**(auto simp add: ta-bisim-def mbisim-def intro: thread-oks-bisim-inv[THEN iffD1] r2.may-join-cond-action-oks)

**lemma** mbisim-actions-ok-bisim-no-join-21:

```

[[ mbisim s1 s2; collect-cond-actions  $\{ta2\}_c = \{\}$ ; ta-bisim bisim ta1 ta2; r2.actions-ok s2 t ta2 ]]

```

```

 $\implies$  r1.actions-ok s1 t ta1

```

**using** FWbisimulation-base.mbisim-actions-ok-bisim-no-join-12[**where** bisim= $\lambda t$ . flip (bisim t) **and**

*bisim-wait* = flip *bisim-wait*]  
**unfolding** *flip-simps* .

**lemma** *mbisim-actions-ok-bisim-no-join*:

$\llbracket \text{mbisim } s1 \ s2; \text{collect-cond-actions } \llbracket ta1 \rrbracket_c = \{\}; \text{ta-bisim bisim } ta1 \ ta2 \rrbracket$   
 $\implies r1.actions\text{-ok } s1 \ t \ ta1 = r2.actions\text{-ok } s2 \ t \ ta2$

**apply**(rule *iffI*)

**apply**(erule (3) *mbisim-actions-ok-bisim-no-join-12*)

**apply**(erule *mbisim-actions-ok-bisim-no-join-21* [where ?*ta2*.0 = *ta2*])

**apply**(simp add: *ta-bisim-def*)

**apply** *assumption*+

**done**

**end**

**locale** *FWbisimulation-base-aux* = *FWbisimulation-base* +

*r1*: *multithreaded final1 r1 convert-RA* +

*r2*: *multithreaded final2 r2 convert-RA* +

**constrains** *final1* :: '*x1*  $\Rightarrow$  bool

**and** *r1* :: ('*l*, '*t*, '*x1*, '*m1*, '*w*, '*o*) *semantics*

**and** *final2* :: '*x2*  $\Rightarrow$  bool

**and** *r2* :: ('*l*, '*t*, '*x2*, '*m2*, '*w*, '*o*) *semantics*

**and** *convert-RA* :: '*l* *released-locks*  $\Rightarrow$  '*o* *list*

**and** *bisim* :: '*t*  $\Rightarrow$  ('*x1*  $\times$  '*m1*, '*x2*  $\times$  '*m2*) *bisim*

**and** *bisim-wait* :: ('*x1*, '*x2*) *bisim*

**begin**

**lemma** *FWbisimulation-base-aux-flip*:

*FWbisimulation-base-aux final2 r2 final1 r1*

**by**(*unfold-locales*)

**end**

**lemma** *FWbisimulation-base-aux-flip-simps* [*flip-simps*]:

*FWbisimulation-base-aux final2 r2 final1 r1* = *FWbisimulation-base-aux final1 r1 final2 r2*

**by**(blast intro: *FWbisimulation-base-aux.FWbisimulation-base-aux-flip*)

**sublocale** *FWbisimulation-base-aux* < *mthr*:

*bisimulation-final-base*

*r1*.*redT*

*r2*.*redT*

*mbisim*

*mta-bisim*

*r1*.*mfinal*

*r2*.*mfinal*

.

**declare** *split-paired-Ex* [*simp del*]

### 1.18.2 Lifting for delay bisimulations

**locale** *FWdelay-bisimulation-base* =

*FWbisimulation-base* - - - *r2 convert-RA bisim bisim-wait* +

*r1*:  $\tau$ *multithreaded final1 r1 convert-RA  $\tau$ move1* +

$r2: \tau\text{multithreaded final2 } r2 \text{ convert-RA } \tau\text{move2}$   
**for**  $r2 :: ('l, 't, 'x2, 'm2, 'w, 'o) \text{ semantics } (- \vdash - \text{ } -2 \text{ } \dashrightarrow - [50, 0, 0, 50] \ 80)$   
**and**  $\text{convert-RA} :: 'l \text{ released-locks} \Rightarrow 'o \text{ list}$   
**and**  $\text{bisim} :: 't \Rightarrow ('x1 \times 'm1, 'x2 \times 'm2) \text{ bisim } (- \vdash - / \approx - [50, 50, 50] \ 60)$   
**and**  $\text{bisim-wait} :: ('x1, 'x2) \text{ bisim } (- / \approx w - [50, 50] \ 60)$   
**and**  $\tau\text{move1} :: ('l, 't, 'x1, 'm1, 'w, 'o) \tau\text{moves}$   
**and**  $\tau\text{move2} :: ('l, 't, 'x2, 'm2, 'w, 'o) \tau\text{moves}$   
**begin**

**abbreviation**  $\tau\text{mred1} :: ('l, 't, 'x1, 'm1, 'w) \text{ state} \Rightarrow ('l, 't, 'x1, 'm1, 'w) \text{ state} \Rightarrow \text{bool}$   
**where**  $\tau\text{mred1} \equiv r1.\tau\text{mredT}$

**abbreviation**  $\tau\text{mred2} :: ('l, 't, 'x2, 'm2, 'w) \text{ state} \Rightarrow ('l, 't, 'x2, 'm2, 'w) \text{ state} \Rightarrow \text{bool}$   
**where**  $\tau\text{mred2} \equiv r2.\tau\text{mredT}$

**abbreviation**  $m\tau\text{move1} :: ((l, 't, 'x1, 'm1, 'w) \text{ state}, 't \times (l, 't, 'x1, 'm1, 'w, 'o) \text{ thread-action}) \text{ trsys}$   
**where**  $m\tau\text{move1} \equiv r1.m\tau\text{move}$

**abbreviation**  $m\tau\text{move2} :: ((l, 't, 'x2, 'm2, 'w) \text{ state}, 't \times (l, 't, 'x2, 'm2, 'w, 'o) \text{ thread-action}) \text{ trsys}$   
**where**  $m\tau\text{move2} \equiv r2.m\tau\text{move}$

**abbreviation**  $\tau\text{mRed1} :: ('l, 't, 'x1, 'm1, 'w) \text{ state} \Rightarrow ('l, 't, 'x1, 'm1, 'w) \text{ state} \Rightarrow \text{bool}$   
**where**  $\tau\text{mRed1} \equiv \tau\text{mred1}^{\wedge**}$

**abbreviation**  $\tau\text{mRed2} :: ('l, 't, 'x2, 'm2, 'w) \text{ state} \Rightarrow ('l, 't, 'x2, 'm2, 'w) \text{ state} \Rightarrow \text{bool}$   
**where**  $\tau\text{mRed2} \equiv \tau\text{mred2}^{\wedge**}$

**abbreviation**  $\tau\text{mtRed1} :: ('l, 't, 'x1, 'm1, 'w) \text{ state} \Rightarrow ('l, 't, 'x1, 'm1, 'w) \text{ state} \Rightarrow \text{bool}$   
**where**  $\tau\text{mtRed1} \equiv \tau\text{mred1}^{\wedge++}$

**abbreviation**  $\tau\text{mtRed2} :: ('l, 't, 'x2, 'm2, 'w) \text{ state} \Rightarrow ('l, 't, 'x2, 'm2, 'w) \text{ state} \Rightarrow \text{bool}$   
**where**  $\tau\text{mtRed2} \equiv \tau\text{mred2}^{\wedge++}$

**lemma** *bisim-inv- $\tau$ s1-inv*:

**assumes** *inv*: *bisim-inv*

**and** *bisim*:  $t \vdash s1 \approx s2$

**and** *red*:  $r1.\text{silent-moves } t \ s1 \ s1'$

**obtains**  $s2'$  **where**  $t \vdash s1' \approx s2'$

**proof**(*atomize-elim*)

**from** *red bisim* **show**  $\exists s2'. t \vdash s1' \approx s2'$

**by**(*induct rule: rtrancpl-induct*)(*fastforce elim: bisim-invD1[OF inv]*)**+**

**qed**

**lemma** *bisim-inv- $\tau$ s2-inv*:

**assumes** *inv*: *bisim-inv*

**and** *bisim*:  $t \vdash s1 \approx s2$

**and** *red*:  $r2.\text{silent-moves } t \ s2 \ s2'$

**obtains**  $s1'$  **where**  $t \vdash s1' \approx s2'$

**proof**(*atomize-elim*)

**from** *red bisim* **show**  $\exists s1'. t \vdash s1' \approx s2'$

**by**(*induct rule: rtrancpl-induct*)(*fastforce elim: bisim-invD2[OF inv]*)**+**

**qed**

**primrec** *activate-cond-action1* ::  $(l, 't, 'x1, 'm1, 'w) \text{ state} \Rightarrow (l, 't, 'x2, 'm2, 'w) \text{ state} \Rightarrow$

't conditional-action  $\Rightarrow$  ('l,'t,'x1,'m1,'w) state

**where**

activate-cond-action1 s1 s2 (Join t) =  
 (case thr s1 t of None  $\Rightarrow$  s1  
   | [(x1, ln1)]  $\Rightarrow$  (case thr s2 t of None  $\Rightarrow$  s1  
     | [(x2, ln2)]  $\Rightarrow$   
     if final2 x2  $\wedge$  ln2 = no-wait-locks  
     then redT-upd- $\varepsilon$  s1 t  
       (SOME x1'. r1.silent-moves t (x1, shr s1) (x1', shr s1)  $\wedge$  final1 x1'  $\wedge$   
         t  $\vdash$  (x1', shr s1)  $\approx$  (x2, shr s2))  
       (shr s1)  
     else s1))  
 | activate-cond-action1 s1 s2 Yield = s1

**primrec** activate-cond-actions1 :: ('l,'t,'x1,'m1,'w) state  $\Rightarrow$  ('l,'t,'x2,'m2,'w) state  
 $\Rightarrow$  ('t conditional-action) list  $\Rightarrow$  ('l,'t,'x1,'m1,'w) state

**where**

activate-cond-actions1 s1 s2 [] = s1  
 | activate-cond-actions1 s1 s2 (ct # cts) = activate-cond-actions1 (activate-cond-action1 s1 s2 ct) s2  
 cts

**primrec** activate-cond-action2 :: ('l,'t,'x1,'m1,'w) state  $\Rightarrow$  ('l,'t,'x2,'m2,'w) state  
 't conditional-action  $\Rightarrow$  ('l,'t,'x2,'m2,'w) state

**where**

activate-cond-action2 s1 s2 (Join t) =  
 (case thr s2 t of None  $\Rightarrow$  s2  
   | [(x2, ln2)]  $\Rightarrow$  (case thr s1 t of None  $\Rightarrow$  s2  
     | [(x1, ln1)]  $\Rightarrow$   
     if final1 x1  $\wedge$  ln1 = no-wait-locks  
     then redT-upd- $\varepsilon$  s2 t  
       (SOME x2'. r2.silent-moves t (x2, shr s2) (x2', shr s2)  $\wedge$  final2 x2'  $\wedge$   
         t  $\vdash$  (x1, shr s1)  $\approx$  (x2', shr s2))  
       (shr s2)  
     else s2))  
 | activate-cond-action2 s1 s2 Yield = s2

**primrec** activate-cond-actions2 :: ('l,'t,'x1,'m1,'w) state  $\Rightarrow$  ('l,'t,'x2,'m2,'w) state  
 $\Rightarrow$  ('t conditional-action) list  $\Rightarrow$  ('l,'t,'x2,'m2,'w) state

**where**

activate-cond-actions2 s1 s2 [] = s2  
 | activate-cond-actions2 s1 s2 (ct # cts) = activate-cond-actions2 s1 (activate-cond-action2 s1 s2 ct)  
 cts

**end**

**lemma** activate-cond-action1-flip [flip-simps]:

FWdelay-bisimulation-base.activate-cond-action1 final2 r2 final1 ( $\lambda t.$  flip (bisim t))  $\tau$ move2 s2 s1 =  
 FWdelay-bisimulation-base.activate-cond-action2 final1 final2 r2 bisim  $\tau$ move2 s1 s2

**apply**(rule ext)

**apply**(case-tac x)

**apply**(simp-all only: FWdelay-bisimulation-base.activate-cond-action1.simps  
 FWdelay-bisimulation-base.activate-cond-action2.simps flip-simps)

**done**

**lemma** *activate-cond-actions1-flip* [*flip-simps*]:  
*FWdelay-bisimulation-base.activate-cond-actions1 final2 r2 final1* ( $\lambda t. \text{flip } (\text{bisim } t)$ )  $\tau\text{move2 } s2 \ s1$   
 =  
*FWdelay-bisimulation-base.activate-cond-actions2 final1 final2 r2 bisim*  $\tau\text{move2 } s1 \ s2$   
 (**is** ?*lhs* = ?*rhs*)  
**proof**(*rule ext*)  
**fix** *xs*  
**show** ?*lhs xs* = ?*rhs xs*  
**by**(*induct xs arbitrary: s2*)  
 (*simp-all only: FWdelay-bisimulation-base.activate-cond-actions1.simps*  
*FWdelay-bisimulation-base.activate-cond-actions2.simps flip-simps*)  
**qed**

**lemma** *activate-cond-action2-flip* [*flip-simps*]:  
*FWdelay-bisimulation-base.activate-cond-action2 final2 final1 r1* ( $\lambda t. \text{flip } (\text{bisim } t)$ )  $\tau\text{move1 } s2 \ s1$  =  
*FWdelay-bisimulation-base.activate-cond-action1 final1 r1 final2 bisim*  $\tau\text{move1 } s1 \ s2$   
**apply**(*rule ext*)  
**apply**(*case-tac x*)  
**apply**(*simp-all only: FWdelay-bisimulation-base.activate-cond-action1.simps*  
*FWdelay-bisimulation-base.activate-cond-action2.simps flip-simps*)  
**done**

**lemma** *activate-cond-actions2-flip* [*flip-simps*]:  
*FWdelay-bisimulation-base.activate-cond-actions2 final2 final1 r1* ( $\lambda t. \text{flip } (\text{bisim } t)$ )  $\tau\text{move1 } s2 \ s1$   
 =  
*FWdelay-bisimulation-base.activate-cond-actions1 final1 r1 final2 bisim*  $\tau\text{move1 } s1 \ s2$   
 (**is** ?*lhs* = ?*rhs*)  
**proof**(*rule ext*)  
**fix** *xs*  
**show** ?*lhs xs* = ?*rhs xs*  
**by**(*induct xs arbitrary: s1*)  
 (*simp-all only: FWdelay-bisimulation-base.activate-cond-actions1.simps*  
*FWdelay-bisimulation-base.activate-cond-actions2.simps flip-simps*)  
**qed**

**context** *FWdelay-bisimulation-base* **begin**

**lemma** *shr-activate-cond-action1* [*simp*]: *shr* (*activate-cond-action1 s1 s2 ct*) = *shr s1*  
**by**(*cases ct*) *simp-all*

**lemma** *shr-activate-cond-actions1* [*simp*]: *shr* (*activate-cond-actions1 s1 s2 cts*) = *shr s1*  
**by**(*induct cts arbitrary: s1*) *auto*

**lemma** *shr-activate-cond-action2* [*simp*]: *shr* (*activate-cond-action2 s1 s2 ct*) = *shr s2*  
**by**(*cases ct*) *simp-all*

**lemma** *shr-activate-cond-actions2* [*simp*]: *shr* (*activate-cond-actions2 s1 s2 cts*) = *shr s2*  
**by**(*induct cts arbitrary: s2*) *auto*

**lemma** *locks-activate-cond-action1* [*simp*]: *locks* (*activate-cond-action1 s1 s2 ct*) = *locks s1*  
**by**(*cases ct*) *simp-all*

**lemma** *locks-activate-cond-actions1* [*simp*]: *locks* (*activate-cond-actions1 s1 s2 cts*) = *locks s1*  
**by**(*induct cts arbitrary: s1*) *auto*

**lemma** *locks-activate-cond-action2* [simp]: *locks (activate-cond-action2 s1 s2 ct) = locks s2*  
**by**(cases ct) simp-all

**lemma** *locks-activate-cond-actions2* [simp]: *locks (activate-cond-actions2 s1 s2 cts) = locks s2*  
**by**(induct cts arbitrary: s2) auto

**lemma** *wset-activate-cond-action1* [simp]: *wset (activate-cond-action1 s1 s2 ct) = wset s1*  
**by**(cases ct) simp-all

**lemma** *wset-activate-cond-actions1* [simp]: *wset (activate-cond-actions1 s1 s2 cts) = wset s1*  
**by**(induct cts arbitrary: s1) auto

**lemma** *wset-activate-cond-action2* [simp]: *wset (activate-cond-action2 s1 s2 ct) = wset s2*  
**by**(cases ct) simp-all

**lemma** *wset-activate-cond-actions2* [simp]: *wset (activate-cond-actions2 s1 s2 cts) = wset s2*  
**by**(induct cts arbitrary: s2) auto

**lemma** *interrupts-activate-cond-action1* [simp]: *interrupts (activate-cond-action1 s1 s2 ct) = interrupts s1*  
**by**(cases ct) simp-all

**lemma** *interrupts-activate-cond-actions1* [simp]: *interrupts (activate-cond-actions1 s1 s2 cts) = interrupts s1*  
**by**(induct cts arbitrary: s1) auto

**lemma** *interrupts-activate-cond-action2* [simp]: *interrupts (activate-cond-action2 s1 s2 ct) = interrupts s2*  
**by**(cases ct) simp-all

**lemma** *interrupts-activate-cond-actions2* [simp]: *interrupts (activate-cond-actions2 s1 s2 cts) = interrupts s2*  
**by**(induct cts arbitrary: s2) auto

**end**

**locale** *FWdelay-bisimulation-lift-aux* =  
*FWdelay-bisimulation-base* - - - - -  $\tau\text{move1}$   $\tau\text{move2}$  +  
 $r1: \tau\text{multithreaded-wf final1 } r1 \text{ convert-RA } \tau\text{move1}$  +  
 $r2: \tau\text{multithreaded-wf final2 } r2 \text{ convert-RA } \tau\text{move2}$   
**for**  $\tau\text{move1} :: ('l, 't, 'x1, 'm1, 'w, 'o) \tau\text{moves}$   
**and**  $\tau\text{move2} :: ('l, 't, 'x2, 'm2, 'w, 'o) \tau\text{moves}$   
**begin**

**lemma** *FWdelay-bisimulation-lift-aux-flip*:  
*FWdelay-bisimulation-lift-aux final2 r2 final1 r1  $\tau\text{move2}$   $\tau\text{move1}$*   
**by** unfold-locales

**end**

**lemma** *FWdelay-bisimulation-lift-aux-flip-simps* [flip-simps]:  
*FWdelay-bisimulation-lift-aux final2 r2 final1 r1  $\tau\text{move2}$   $\tau\text{move1}$  =*  
*FWdelay-bisimulation-lift-aux final1 r1 final2 r2  $\tau\text{move1}$   $\tau\text{move2}$*

by(auto dest: FWdelay-bisimulation-lift-aux.FWdelay-bisimulation-lift-aux-flip simp only: flip-flip)

context FWdelay-bisimulation-lift-aux begin

lemma cond-actions-ok- $\tau$ mred1-inv:

assumes red:  $\tau$ mred1 s1 s1'

and ct: r1.cond-action-ok s1 t ct

shows r1.cond-action-ok s1' t ct

using ct

proof(cases ct)

case (Join t')

show ?thesis using red ct

proof(cases thr s1 t')

case None with red ct Join show ?thesis

by(fastforce elim!: r1.mthr.silent-move.cases r1.redT.cases r1.m $\tau$ move.cases rtrancl3p-cases  
dest: r1.silent-tl split: if-split-asm)

next

case (Some a) with red ct Join show ?thesis

by(fastforce elim!: r1.mthr.silent-move.cases r1.redT.cases r1.m $\tau$ move.cases rtrancl3p-cases  
dest: r1.silent-tl r1.final-no-red split: if-split-asm simp add: redT-updWs-def)

qed

next

case Yield thus ?thesis by simp

qed

lemma cond-actions-ok- $\tau$ mred2-inv:

$\llbracket \tau$ mred2 s2 s2'; r2.cond-action-ok s2 t ct  $\rrbracket \implies$  r2.cond-action-ok s2' t ct

using FWdelay-bisimulation-lift-aux.cond-actions-ok- $\tau$ mred1-inv[OF FWdelay-bisimulation-lift-aux-flip]

.

lemma cond-actions-ok- $\tau$ mRed1-inv:

$\llbracket \tau$ mRed1 s1 s1'; r1.cond-action-ok s1 t ct  $\rrbracket \implies$  r1.cond-action-ok s1' t ct

by(induct rule: rtranclp-induct)(blast intro: cond-actions-ok- $\tau$ mred1-inv)+

lemma cond-actions-ok- $\tau$ mRed2-inv:

$\llbracket \tau$ mRed2 s2 s2'; r2.cond-action-ok s2 t ct  $\rrbracket \implies$  r2.cond-action-ok s2' t ct

by(rule FWdelay-bisimulation-lift-aux.cond-actions-ok- $\tau$ mRed1-inv[OF FWdelay-bisimulation-lift-aux-flip])

end

locale FWdelay-bisimulation-lift =

FWdelay-bisimulation-lift-aux +

constrains final1 :: 'x1  $\Rightarrow$  bool

and r1 :: ('l, 't, 'x1, 'm1, 'w, 'o) semantics

and final2 :: 'x2  $\Rightarrow$  bool

and r2 :: ('l, 't, 'x2, 'm2, 'w, 'o) semantics

and convert-RA :: 'l released-locks  $\Rightarrow$  'o list

and bisim :: 't  $\Rightarrow$  ('x1  $\times$  'm1, 'x2  $\times$  'm2) bisim

and bisim-wait :: ('x1, 'x2) bisim

and  $\tau$ move1 :: ('l, 't, 'x1, 'm1, 'w, 'o)  $\tau$ moves

and  $\tau$ move2 :: ('l, 't, 'x2, 'm2, 'w, 'o)  $\tau$ moves

assumes  $\tau$ inv-locale:  $\tau$ inv (r1 t) (r2 t) (bisim t) (ta-bisim bisim)  $\tau$ move1  $\tau$ move2

sublocale FWdelay-bisimulation-lift <  $\tau$ inv r1 t r2 t bisim t ta-bisim bisim  $\tau$ move1  $\tau$ move2 for t

**by**(*rule*  $\tau$ inv-locale)

**context** *FWdelay-bisimulation-lift* **begin**

**lemma** *FWdelay-bisimulation-lift-flip*:

*FWdelay-bisimulation-lift final2 r2 final1 r1* ( $\lambda t. \text{flip} (\text{bisim } t)$ )  $\tau$ move2  $\tau$ move1

**apply**(*rule* *FWdelay-bisimulation-lift.intro*)

**apply**(*rule* *FWdelay-bisimulation-lift-aux-flip*)

**apply**(*rule* *FWdelay-bisimulation-lift-axioms.intro*)

**apply**(*unfold flip-simps*)

**apply**(*unfold-locales*)

**done**

**end**

**lemma** *FWdelay-bisimulation-lift-flip-simps* [*flip-simps*]:

*FWdelay-bisimulation-lift final2 r2 final1 r1* ( $\lambda t. \text{flip} (\text{bisim } t)$ )  $\tau$ move2  $\tau$ move1 =

*FWdelay-bisimulation-lift final1 r1 final2 r2 bisim*  $\tau$ move1  $\tau$ move2

**by**(*auto dest: FWdelay-bisimulation-lift.FWdelay-bisimulation-lift-flip simp only: flip-flip*)

**context** *FWdelay-bisimulation-lift* **begin**

**lemma**  $\tau$ inv-lift:  $\tau$ inv *r1.redT* *r2.redT* *mbisim* *mta-bisim* *m* $\tau$ move1 *m* $\tau$ move2

**proof**

**fix** *s1 s2 tl1 s1' tl2 s2'*

**assume** *s1*  $\approx_m$  *s2* *s1'*  $\approx_m$  *s2'* *tl1*  $\sim_T$  *tl2* *r1.redT* *s1* *tl1* *s1'* *r2.redT* *s2* *tl2* *s2'*

**moreover obtain** *t ta1* **where** *tl1*: *tl1* = (*t*, *ta1*) **by**(*cases tl1*)

**moreover obtain** *t' ta2* **where** *tl2*: *tl2* = (*t'*, *ta2*) **by**(*cases tl2*)

**moreover obtain** *ls1 ts1 ws1 m1 is1* **where** *s1*: *s1* = (*ls1*, (*ts1*, *m1*), *ws1*, *is1*) **by**(*cases s1*)  
*fastforce*

**moreover obtain** *ls2 ts2 ws2 m2 is2* **where** *s2*: *s2* = (*ls2*, (*ts2*, *m2*), *ws2*, *is2*) **by**(*cases s2*)  
*fastforce*

**moreover obtain** *ls1' ts1' ws1' m1' is1'* **where** *s1'*: *s1'* = (*ls1'*, (*ts1'*, *m1'*), *ws1'*, *is1'*) **by**(*cases s1'*) *fastforce*

**moreover obtain** *ls2' ts2' ws2' m2' is2'* **where** *s2'*: *s2'* = (*ls2'*, (*ts2'*, *m2'*), *ws2'*, *is2'*) **by**(*cases s2'*) *fastforce*

**ultimately have** *mbisim*: (*ls1*, (*ts1*, *m1*), *ws1*, *is1*)  $\approx_m$  (*ls2*, (*ts2*, *m2*), *ws2*, *is2*)

**and** *mbisim'*: (*ls1'*, (*ts1'*, *m1'*), *ws1'*, *is1'*)  $\approx_m$  (*ls2'*, (*ts2'*, *m2'*), *ws2'*, *is2'*)

**and** *mred1*: (*ls1*, (*ts1*, *m1*), *ws1*, *is1*)  $-1-t \triangleright ta1 \rightarrow$  (*ls1'*, (*ts1'*, *m1'*), *ws1'*, *is1'*)

**and** *mred2*: (*ls2*, (*ts2*, *m2*), *ws2*, *is2*)  $-2-t \triangleright ta2 \rightarrow$  (*ls2'*, (*ts2'*, *m2'*), *ws2'*, *is2'*)

**and** *tasim*: *ta1*  $\sim_m$  *ta2* **and** *tt'*: *t'* = *t* **by** *simp-all*

**from** *mbisim* **have** *ls*: *ls1* = *ls2* **and** *ws*: *ws1* = *ws2* **and** *is*: *is1* = *is2*

**and** *tbsim*:  $\bigwedge t. \text{tbsim} (\text{ws2 } t = \text{None}) \ t \ (\text{ts1 } t) \ m1 \ (\text{ts2 } t) \ m2$  **by**(*simp-all add: mbisim-def*)

**from** *mbisim'* **have** *ls'*: *ls1'* = *ls2'* **and** *ws'*: *ws1'* = *ws2'* **and** *is'*: *is1'* = *is2'*

**and** *tbsim'*:  $\bigwedge t. \text{tbsim} (\text{ws2'} \ t = \text{None}) \ t \ (\text{ts1'} \ t) \ m1' \ (\text{ts2'} \ t) \ m2'$  **by**(*simp-all add: mbisim-def*)

**from** *mred1* *r1.redT-thread-not-disappear*[*OF mred1*]

**obtain** *x1 ln1 x1' ln1'* **where** *tst1*: *ts1* *t* =  $\lfloor (x1, \text{ln1}) \rfloor$

**and** *tst1'*: *ts1'* *t* =  $\lfloor (x1', \text{ln1}') \rfloor$

**by**(*fastforce elim!: r1.redT.cases*)

**from** *mred2* *r2.redT-thread-not-disappear*[*OF mred2*]

**obtain** *x2 ln2 x2' ln2'* **where** *tst2*: *ts2* *t* =  $\lfloor (x2, \text{ln2}) \rfloor$

**and** *tst2'*: *ts2'* *t* =  $\lfloor (x2', \text{ln2}') \rfloor$  **by**(*fastforce elim!: r2.redT.cases*)

**from** *tbsim*[*of t*] *tst1* *tst2* *ws* **have** *bisim*: *t*  $\vdash (x1, m1) \approx (x2, m2)$

**and** *ln*: *ln1* = *ln2* **by**(*auto simp add: tbsim-def*)



```

from  $t\text{bisim}'[of\ t]\ tst1'\ tst2'$  have  $\text{bisim}'$ :  $t \vdash (x1', m1') \approx (x2', m2')$ 
  and  $ln'$ :  $ln1' = ln2'$  by( $\text{auto simp add: } t\text{bisim-def}$ )
show  $m\tau\text{move1}\ s1\ tl1\ s1' = m\tau\text{move2}\ s2\ tl2\ s2'$  unfolding  $s1\ s2\ s1'\ s2'\ tt'\ tl1\ tl2$ 
proof –
  show  $m\tau\text{move1}\ (ls1, (ts1, m1), ws1, is1)\ (t, ta1)\ (ls1', (ts1', m1'), ws1', is1') =$ 
     $m\tau\text{move2}\ (ls2, (ts2, m2), ws2, is2)\ (t, ta2)\ (ls2', (ts2', m2'), ws2', is2')$ 
    (is  $?lhs = ?rhs$ )
  proof
    assume  $m\tau$ :  $?lhs$ 
    with  $tst1\ tst1'$  obtain  $\tau1$ :  $\tau\text{move1}\ (x1, m1)\ ta1\ (x1', m1')$ 
      and  $ln1$ :  $ln1 = \text{no-wait-locks}$  by( $\text{fastforce elim!}:\ r1.m\tau\text{move.cases}$ )
    from  $\tau1$  have  $ta1 = \varepsilon$  by( $\text{rule } r1.\text{silent-tl}$ )
    with  $mred1\ \tau1\ tst1\ tst1'\ ln1$  have  $red1$ :  $t \vdash (x1, m1) -1-ta1 \rightarrow (x1', m1')$ 
      by( $\text{auto elim!}:\ r1.\text{redT.cases } r\text{tranc13p-cases}$ )
    from  $tasim\ \langle ta1 = \varepsilon \rangle$  have  $[simp]:\ ta2 = \varepsilon$  by( $simp$ )
    with  $mred2\ ln1\ ln\ tst2\ tst2'$  have  $red2$ :  $t \vdash (x2, m2) -2-\varepsilon \rightarrow (x2', m2')$ 
      by( $\text{fastforce elim!}:\ r2.\text{redT.cases } r\text{tranc13p-cases}$ )
    from  $\tau1\ \tau\text{inv}[OF\ \text{bisim } red1\ red2]\ \text{bisim}'\ tasim$ 
    have  $\tau2$ :  $\tau\text{move2}\ (x2, m2)\ \varepsilon\ (x2', m2')$  by  $simp$ 
    with  $tst2\ tst2'\ ln\ ln1$  show  $?rhs$  by  $-(\text{rule } r2.m\tau\text{move.intros, auto})$ 
  next
    assume  $m\tau$ :  $?rhs$ 
    with  $tst2\ tst2'$  obtain  $\tau2$ :  $\tau\text{move2}\ (x2, m2)\ ta2\ (x2', m2')$ 
      and  $ln2$ :  $ln2 = \text{no-wait-locks}$  by( $\text{fastforce elim!}:\ r2.m\tau\text{move.cases}$ )
    from  $\tau2$  have  $ta2 = \varepsilon$  by( $\text{rule } r2.\text{silent-tl}$ )
    with  $mred2\ \tau2\ tst2\ tst2'\ ln2$  have  $red2$ :  $t \vdash (x2, m2) -2-ta2 \rightarrow (x2', m2')$ 
      by( $\text{auto elim!}:\ r2.\text{redT.cases } r\text{tranc13p-cases}$ )
    from  $tasim\ \langle ta2 = \varepsilon \rangle$  have  $[simp]:\ ta1 = \varepsilon$  by  $simp$ 
    with  $mred1\ ln2\ ln\ tst1\ tst1'$  have  $red1$ :  $t \vdash (x1, m1) -1-\varepsilon \rightarrow (x1', m1')$ 
      by( $\text{fastforce elim!}:\ r1.\text{redT.cases } r\text{tranc13p-cases}$ )
    from  $\tau2\ \tau\text{inv}[OF\ \text{bisim } red1\ red2]\ \text{bisim}'\ tasim$ 
    have  $\tau1$ :  $\tau\text{move1}\ (x1, m1)\ \varepsilon\ (x1', m1')$  by  $auto$ 
    with  $tst1\ tst1'\ ln\ ln2$  show  $?lhs$  unfolding  $\langle ta1 = \varepsilon \rangle$ 
      by $-(\text{rule } r1.m\tau\text{move.intros, auto})$ 
  qed
qed
qed
end

sublocale  $FW\text{delay-bisimulation-lift} < mthr$ :  $\tau\text{inv } r1.\text{redT } r2.\text{redT } mbisim\ mta\text{-bisim } m\tau\text{move1 } m\tau\text{move2}$ 
by( $\text{rule } \tau\text{inv-lift}$ )

locale  $FW\text{delay-bisimulation-final-base} =$ 
   $FW\text{delay-bisimulation-lift-aux} +$ 
  constrains  $final1 :: 'x1 \Rightarrow \text{bool}$ 
  and  $r1 :: ('l, 't, 'x1, 'm1, 'w, 'o)\ \text{semantics}$ 
  and  $final2 :: 'x2 \Rightarrow \text{bool}$ 
  and  $r2 :: ('l, 't, 'x2, 'm2, 'w, 'o)\ \text{semantics}$ 
  and  $\text{convert-RA} :: 'l\ \text{released-locks} \Rightarrow 'o\ \text{list}$ 
  and  $\text{bisim} :: 't \Rightarrow ('x1 \times 'm1, 'x2 \times 'm2)\ \text{bisim}$ 
  and  $\text{bisim-wait} :: ('x1, 'x2)\ \text{bisim}$ 
  and  $\tau\text{move1} :: ('l, 't, 'x1, 'm1, 'w, 'o)\ \tau\text{moves}$ 
  and  $\tau\text{move2} :: ('l, 't, 'x2, 'm2, 'w, 'o)\ \tau\text{moves}$ 

```

```

assumes delay-bisim-locale:
  delay-bisimulation-final-base (r1 t) (r2 t) (bisim t)  $\tau$ move1  $\tau$ move2 ( $\lambda(x1, m). \text{final1 } x1$ ) ( $\lambda(x2, m). \text{final2 } x2$ )

sublocale FWdelay-bisimulation-final-base <
  delay-bisimulation-final-base r1 t r2 t bisim t ta-bisim bisim  $\tau$ move1  $\tau$ move2
     $\lambda(x1, m). \text{final1 } x1 \ \lambda(x2, m). \text{final2 } x2$ 
  for t
by(rule delay-bisim-locale)

context FWdelay-bisimulation-final-base begin

lemma FWdelay-bisimulation-final-base-flip:
  FWdelay-bisimulation-final-base final2 r2 final1 r1 ( $\lambda t. \text{flip } (\text{bisim } t)$ )  $\tau$ move2  $\tau$ move1
apply(rule FWdelay-bisimulation-final-base.intro)
apply(rule FWdelay-bisimulation-lift-aux-flip)
apply(rule FWdelay-bisimulation-final-base-axioms.intro)
apply(rule delay-bisimulation-final-base-flip)
done

end

lemma FWdelay-bisimulation-final-base-flip-simps [flip-simps]:
  FWdelay-bisimulation-final-base final2 r2 final1 r1 ( $\lambda t. \text{flip } (\text{bisim } t)$ )  $\tau$ move2  $\tau$ move1 =
    FWdelay-bisimulation-final-base final1 r1 final2 r2 bisim  $\tau$ move1  $\tau$ move2
by(auto dest: FWdelay-bisimulation-final-base.FWdelay-bisimulation-final-base-flip simp only: flip-flip)

context FWdelay-bisimulation-final-base begin

lemma cond-actions-ok-bisim-ex- $\tau$ 1-inv:
  fixes ls ts1 m1 ws is ts2 m2 ct
  defines s1'  $\equiv$  activate-cond-action1 (ls, (ts1, m1), ws, is) (ls, (ts2, m2), ws, is) ct
  assumes mbisim:  $\bigwedge t'. t' \neq t \implies \text{tbisim } (ws \ t' = \text{None}) \ t' \ (ts1 \ t') \ m1 \ (ts2 \ t') \ m2$ 
  and ts1t: ts1 t = Some xln
  and ts2t: ts2 t = Some xln'
  and ct: r2.cond-action-ok (ls, (ts2, m2), ws, is) t ct
  shows  $\tau$ mRed1 (ls, (ts1, m1), ws, is) s1'
  and  $\bigwedge t'. t' \neq t \implies \text{tbisim } (ws \ t' = \text{None}) \ t' \ (\text{thr } s1' \ t') \ m1 \ (ts2 \ t') \ m2$ 
  and r1.cond-action-ok s1' t ct
  and thr s1' t = Some xln
proof –
  have  $\tau$ mRed1 (ls, (ts1, m1), ws, is) s1'  $\wedge$ 
    ( $\forall t'. t' \neq t \longrightarrow \text{tbisim } (ws \ t' = \text{None}) \ t' \ (\text{thr } s1' \ t') \ m1 \ (ts2 \ t') \ m2$ )  $\wedge$ 
    r1.cond-action-ok s1' t ct  $\wedge$  thr s1' t = [xln]
  using ct
proof(cases ct)
  case (Join t')
  show ?thesis
  proof(cases ts1 t')
  case None
  with mbisim ts1t have t  $\neq$  t' by auto
  moreover from None Join have s1' = (ls, (ts1, m1), ws, is) by(simp add: s1'-def)
  ultimately show ?thesis using mbisim Join ct None ts1t by(simp add: tbisim-def)
next

```

case (Some  $xln$ )  
 moreover obtain  $x1\ ln$  where  $xln = (x1, ln)$  by (cases  $xln$ )  
 ultimately have  $ts1t'$ :  $ts1\ t' = \lfloor (x1, ln) \rfloor$  by simp  
 from Join  $ct$  Some  $ts2t$  have  $tt'$ :  $t' \neq t$  by auto  
 from mbisim[OF  $tt'$ ]  $ts1t'$  obtain  $x2$  where  $ts2t'$ :  $ts2\ t' = \lfloor (x2, ln) \rfloor$   
 and bisim:  $t' \vdash (x1, m1) \approx (x2, m2)$  by (auto simp add: tbisim-def)  
 from  $ct$  Join  $ts2t'$  have final2: final2  $x2$  and  $ln$ :  $ln = \text{no-wait-locks}$   
 and  $wst'$ :  $ws\ t' = \text{None}$  by simp-all  
 let  $?x1' = \text{SOME } x. r1.\text{silent-moves } t' (x1, m1) (x, m1) \wedge \text{final1 } x \wedge t' \vdash (x, m1) \approx (x2, m2)$   
 { from final2-simulation[OF bisim] final2 obtain  $x1'\ m1'$   
 where  $r1.\text{silent-moves } t' (x1, m1) (x1', m1')$  and  $t' \vdash (x1', m1') \approx (x2, m2)$   
 and final1  $x1'$  by auto  
 moreover hence  $m1' = m1$  using bisim by (auto dest:  $r1.\text{red-rtranc1-}\tau\text{-heapD-inv}$ )  
 ultimately have  $\exists x. r1.\text{silent-moves } t' (x1, m1) (x, m1) \wedge \text{final1 } x \wedge t' \vdash (x, m1) \approx (x2,$   
 $m2)$   
 by blast }  
 from someI-ex[OF this] have red1:  $r1.\text{silent-moves } t' (x1, m1) (?x1', m1)$   
 and final1: final1  $?x1'$  and bisim':  $t' \vdash (?x1', m1) \approx (x2, m2)$  by blast+  
 let  $?S1' = \text{redT-upd-}\epsilon (ls, (ts1, m1), ws, is)\ t'\ ?x1'\ m1$   
 from  $r1.\text{silent-moves-into-RedT-}\tau\text{-inv}$  [where  $?s = (ls, (ts1, m1), ws, is)$  and  $t = t'$ , simplified, OF  
 $\text{red1}$ ]  
 bisim  $ts1t'\ ln\ wst'$   
 have Red1:  $\tau m\text{Red1 } (ls, (ts1, m1), ws, is)\ ?S1'$  by auto  
 moreover from Join  $ln\ ts1t'$  final1  $wst'\ tt'$   
 have  $ct'$ :  $r1.\text{cond-action-ok } ?S1'\ t\ ct$  by (auto intro: finfun-ext)  
 { fix  $t''$   
 assume  $t \neq t''$   
 with Join mbisim[OF this[symmetric]] bisim'  $ts1t'\ ts2t'\ wst'\ s1'\text{-def}$   
 have tbisim ( $ws\ t'' = \text{None}$ )  $t'' (thr\ s1'\ t'')\ m1\ (ts2\ t'')\ m2$   
 by (auto simp add: tbisim-def redT-updLns-def o-def finfun-Diag-const2) }  
 moreover from Join  $ts1t'\ ts2t'$  final2  $ln$  have  $s1' = ?S1'$  by (simp add:  $s1'\text{-def}$ )  
 ultimately show  $?thesis$  using Red1  $ct'\ ts1t'\ tt'\ ts1t$  by (auto)  
 qed  
 next  
 case Yield thus  $?thesis$  using mbisim  $ts1t$  by (simp add:  $s1'\text{-def}$ )  
 qed  
 thus  $\tau m\text{Red1 } (ls, (ts1, m1), ws, is)\ s1'$   
 and  $\bigwedge t'. t' \neq t \implies tbisim (ws\ t' = \text{None})\ t' (thr\ s1'\ t')\ m1\ (ts2\ t')\ m2$   
 and  $r1.\text{cond-action-ok } s1'\ t\ ct$   
 and  $thr\ s1'\ t = \lfloor xln \rfloor$  by blast+  
 qed

lemma cond-actions-oks-bisim-ex- $\tau 1$ -inv:

fixes  $ls\ ts1\ m1\ ws\ is\ ts2\ m2\ cts$   
 defines  $s1' \equiv \text{activate-cond-actions1 } (ls, (ts1, m1), ws, is)\ (ls, (ts2, m2), ws, is)\ cts$   
 assumes tbisim:  $\bigwedge t'. t' \neq t \implies tbisim (ws\ t' = \text{None})\ t' (ts1\ t')\ m1\ (ts2\ t')\ m2$   
 and  $ts1t$ :  $ts1\ t = \text{Some } xln$   
 and  $ts2t$ :  $ts2\ t = \text{Some } xln'$   
 and  $ct$ :  $r2.\text{cond-action-oks } (ls, (ts2, m2), ws, is)\ t\ cts$   
 shows  $\tau m\text{Red1 } (ls, (ts1, m1), ws, is)\ s1'$   
 and  $\bigwedge t'. t' \neq t \implies tbisim (ws\ t' = \text{None})\ t' (thr\ s1'\ t')\ m1\ (ts2\ t')\ m2$   
 and  $r1.\text{cond-action-oks } s1'\ t\ cts$   
 and  $thr\ s1'\ t = \text{Some } xln$   
 using tbisim  $ts1t\ ct$  unfolding  $s1'\text{-def}$

```

proof(induct cts arbitrary: ts1)
  case (Cons ct cts)
    note  $IH1 = \langle \bigwedge ts1. \llbracket \bigwedge t'. t' \neq t \implies tbisim (ws\ t' = None)\ t' (ts1\ t')\ m1\ (ts2\ t')\ m2; ts1\ t = \lfloor xln \rfloor;$ 
       $r2.cond-action-oks\ (ls, (ts2, m2), ws, is)\ t\ cts \rrbracket$ 
       $\implies \tau mred1^{**}\ (ls, (ts1, m1), ws, is)\ (activate-cond-actions1\ (ls, (ts1, m1), ws, is)\ (ls,$ 
       $(ts2, m2), ws, is)\ cts) \rangle$ 
    note  $IH2 = \langle \bigwedge t'. ts1. \llbracket t' \neq t; \bigwedge t'. t' \neq t \implies tbisim (ws\ t' = None)\ t' (ts1\ t')\ m1\ (ts2\ t')\ m2; ts1$ 
       $t = \lfloor xln \rfloor;$ 
       $r2.cond-action-oks\ (ls, (ts2, m2), ws, is)\ t\ cts \rrbracket$ 
       $\implies tbisim (ws\ t' = None)\ t' (thr\ (activate-cond-actions1\ (ls, (ts1, m1), ws, is)\ (ls, (ts2,$ 
       $m2), ws, is)\ cts)\ t')\ m1\ (ts2\ t')\ m2 \rangle$ 
    note  $IH3 = \langle \bigwedge ts1. \llbracket \bigwedge t'. t' \neq t \implies tbisim (ws\ t' = None)\ t' (ts1\ t')\ m1\ (ts2\ t')\ m2; ts1\ t = \lfloor xln \rfloor;$ 
       $r2.cond-action-oks\ (ls, (ts2, m2), ws, is)\ t\ cts \rrbracket$ 
       $\implies r1.cond-action-oks\ (activate-cond-actions1\ (ls, (ts1, m1), ws, is)\ (ls, (ts2, m2), ws,$ 
       $is)\ cts)\ t\ cts \rangle$ 
    note  $IH4 = \langle \bigwedge ts1. \llbracket \bigwedge t'. t' \neq t \implies tbisim (ws\ t' = None)\ t' (ts1\ t')\ m1\ (ts2\ t')\ m2; ts1\ t = \lfloor xln \rfloor;$ 
       $r2.cond-action-oks\ (ls, (ts2, m2), ws, is)\ t\ cts \rrbracket$ 
       $\implies thr\ (activate-cond-actions1\ (ls, (ts1, m1), ws, is)\ (ls, (ts2, m2), ws, is)\ cts)\ t = \lfloor xln \rfloor \rangle$ 
    { fix ts1
      assume  $tbisim: \bigwedge t'. t' \neq t \implies tbisim (ws\ t' = None)\ t' (ts1\ t')\ m1\ (ts2\ t')\ m2$ 
      and  $ts1t: ts1\ t = \lfloor xln \rfloor$ 
      and  $ct: r2.cond-action-oks\ (ls, (ts2, m2), ws, is)\ t\ (ct \# cts)$ 
      from ct have 1:  $r2.cond-action-ok\ (ls, (ts2, m2), ws, is)\ t\ ct$ 
      and 2:  $r2.cond-action-oks\ (ls, (ts2, m2), ws, is)\ t\ cts$  by auto
      let  $?s1' = activate-cond-action1\ (ls, (ts1, m1), ws, is)\ (ls, (ts2, m2), ws, is)\ ct$ 
      from cond-actions-ok-bisim-ex- $\tau$ 1-inv[OF tbisim, OF - ts1t ts2t 1]
      have  $tbisim': \bigwedge t'. t' \neq t \implies tbisim (ws\ t' = None)\ t' (thr\ ?s1'\ t')\ m1\ (ts2\ t')\ m2$ 
      and  $red: \tau mRed1\ (ls, (ts1, m1), ws, is)\ ?s1'$  and  $ct': r1.cond-action-ok\ ?s1'\ t\ ct$ 
      and  $ts1't: thr\ ?s1'\ t = \lfloor xln \rfloor$  by blast+
      let  $?s1'' = activate-cond-actions1\ ?s1'\ (ls, (ts2, m2), ws, is)\ cts$ 
      have  $locks\ ?s1' = ls\ shr\ ?s1' = m1\ wset\ ?s1' = ws\ interrupts\ ?s1' = is$  by simp-all
      hence  $s1': (ls, (thr\ ?s1', m1), ws, is) = ?s1'$  by(cases  $?s1'$ ) auto
      from IH1[OF tbisim', OF - ts1't 2]  $s1'$  have  $red': \tau mRed1\ ?s1'\ ?s1''$  by simp
      with red show  $\tau mRed1\ (ls, (ts1, m1), ws, is)\ (activate-cond-actions1\ (ls, (ts1, m1), ws, is)\ (ls,$ 
       $(ts2, m2), ws, is)\ (ct \# cts))$ 
      by auto
      { fix t'
        assume  $t't: t' \neq t$ 
        from IH2[OF  $t't\ tbisim'$ , OF - ts1't 2]  $s1'$ 
        show  $tbisim (ws\ t' = None)\ t' (thr\ (activate-cond-actions1\ (ls, (ts1, m1), ws, is)\ (ls, (ts2, m2),$ 
         $ws, is)\ (ct \# cts))\ t')\ m1\ (ts2\ t')\ m2$ 
        by auto }
      from  $red'\ ct'$  have  $r1.cond-action-ok\ ?s1''\ t\ ct$  by(rule cond-actions-ok- $\tau$ mRed1-inv)
      with IH3[OF tbisim', OF - ts1't 2]  $s1'$ 
      show  $r1.cond-action-oks\ (activate-cond-actions1\ (ls, (ts1, m1), ws, is)\ (ls, (ts2, m2), ws, is)\ (ct$ 
       $\# cts))\ t\ (ct \# cts)$ 
      by auto
      from  $ts1't\ IH4$ [OF tbisim', OF - ts1't 2]  $s1'$ 
      show  $thr\ (activate-cond-actions1\ (ls, (ts1, m1), ws, is)\ (ls, (ts2, m2), ws, is)\ (ct \# cts))\ t = \lfloor xln \rfloor$ 
    } by auto }
    qed(auto)

```

**lemma** *cond-actions-ok-bisim-ex- $\tau$ 2-inv*:

**fixes** *ls ts1 m1 is ws ts2 m2 ct*

**defines**  $s2' \equiv \text{activate-cond-action2 } (ls, (ts1, m1), ws, is) (ls, (ts2, m2), ws, is) \text{ } ct$   
**assumes**  $mbisim: \bigwedge t'. t' \neq t \implies tbisim (ws \ t' = \text{None}) \ t' (ts1 \ t') \ m1 \ (ts2 \ t') \ m2$   
**and**  $ts1t: ts1 \ t = \text{Some } xln$   
**and**  $ts2t: ts2 \ t = \text{Some } xln'$   
**and**  $ct: r1.\text{cond-action-ok } (ls, (ts1, m1), ws, is) \ t \ ct$   
**shows**  $\tau mRed2 (ls, (ts2, m2), ws, is) \ s2'$   
**and**  $\bigwedge t'. t' \neq t \implies tbisim (ws \ t' = \text{None}) \ t' (ts1 \ t') \ m1 \ (thr \ s2' \ t') \ m2$   
**and**  $r2.\text{cond-action-ok } s2' \ t \ ct$   
**and**  $thr \ s2' \ t = \text{Some } xln'$   
**unfolding**  $s2'\text{-def}$   
**by**(blast intro:  $FWdelay\text{-bisimulation-final-base.cond-actions-ok-bisim-ex-}\tau 1\text{-inv}$ [ $OF \ FWdelay\text{-bisimulation-final-base-flip}$ ,  
**where**  $bisim\text{-wait} = \text{flip } bisim\text{-wait}$ ,  $unfolded \text{ flip-simps}$ ,  $OF \ mbisim - - ct$ ,  $OF - ts2t \ ts1t$ ])+

**lemma**  $\text{cond-actions-oks-bisim-ex-}\tau 2\text{-inv}$ :

**fixes**  $ls \ ts1 \ m1 \ ws \ is \ ts2 \ m2 \ cts$   
**defines**  $s2' \equiv \text{activate-cond-actions2 } (ls, (ts1, m1), ws, is) (ls, (ts2, m2), ws, is) \ cts$   
**assumes**  $tbisim: \bigwedge t'. t' \neq t \implies tbisim (ws \ t' = \text{None}) \ t' (ts1 \ t') \ m1 \ (ts2 \ t') \ m2$   
**and**  $ts1t: ts1 \ t = \text{Some } xln$   
**and**  $ts2t: ts2 \ t = \text{Some } xln'$   
**and**  $ct: r1.\text{cond-action-oks } (ls, (ts1, m1), ws, is) \ t \ cts$   
**shows**  $\tau mRed2 (ls, (ts2, m2), ws, is) \ s2'$   
**and**  $\bigwedge t'. t' \neq t \implies tbisim (ws \ t' = \text{None}) \ t' (ts1 \ t') \ m1 \ (thr \ s2' \ t') \ m2$   
**and**  $r2.\text{cond-action-oks } s2' \ t \ cts$   
**and**  $thr \ s2' \ t = \text{Some } xln'$

**unfolding**  $s2'\text{-def}$

**by**(blast intro:  $FWdelay\text{-bisimulation-final-base.cond-actions-oks-bisim-ex-}\tau 1\text{-inv}$ [ $OF \ FWdelay\text{-bisimulation-final-base-flip}$ ,  
**where**  $bisim\text{-wait} = \text{flip } bisim\text{-wait}$ ,  $unfolded \text{ flip-simps}$ ,  $OF \ tbisim - - ct$ ,  $OF - ts2t \ ts1t$ ])+

**lemma**  $mfinal1\text{-inv-simulation}$ :

**assumes**  $s1 \approx_m s2$   
**shows**  $\exists s2'. r2.mthr.\text{silent-moves } s2 \ s2' \wedge s1 \approx_m s2' \wedge r1.\text{final-threads } s1 \subseteq r2.\text{final-threads } s2'$   
 $\wedge \text{shr } s2' = \text{shr } s2$

**proof** –

**from**  $\langle s1 \approx_m s2 \rangle$  **have**  $\text{finite } (\text{dom } (thr \ s1))$  **by**(auto dest:  $mbisim\text{-finite1}$ )  
**moreover** **have**  $r1.\text{final-threads } s1 \subseteq \text{dom } (thr \ s1)$  **by**(auto simp add:  $r1.\text{final-thread-def}$ )  
**ultimately** **have**  $\text{finite } (r1.\text{final-threads } s1)$  **by**(blast intro:  $\text{finite-subset}$ )  
**thus**  $?thesis$  **using**  $\langle s1 \approx_m s2 \rangle$   
**proof**(induct  $A \equiv r1.\text{final-threads } s1$  arbitrary:  $s1 \ s2$  rule:  $\text{finite-induct}$ )  
**case**  $\text{empty}$   
**from**  $\langle \{\} = r1.\text{final-threads } s1 \rangle$  **symmetric** **have**  $\forall t. \neg r1.\text{final-thread } s1 \ t$  **by**(auto)  
**with**  $\langle s1 \approx_m s2 \rangle$  **show**  $?case$  **by** blast  
**next**  
**case**  $(\text{insert } t \ A)$   
**define**  $s1'$  **where**  $s1' = (\text{locks } s1, ((thr \ s1)(t := \text{None}), \text{shr } s1), \text{wset } s1, \text{interrupts } s1)$   
**define**  $s2'$  **where**  $s2' = (\text{locks } s2, ((thr \ s2)(t := \text{None}), \text{shr } s2), \text{wset } s2, \text{interrupts } s2)$   
**from**  $\langle t \notin A \rangle$   $\langle \text{insert } t \ A = r1.\text{final-threads } s1 \rangle$  **have**  $A = r1.\text{final-threads } s1'$   
**unfolding**  $s1'\text{-def}$  **by**(auto simp add:  $r1.\text{final-thread-def } r1.\text{final-threads-def}$ )  
**moreover** **from**  $\langle \text{insert } t \ A = r1.\text{final-threads } s1 \rangle$  **have**  $r1.\text{final-thread } s1 \ t$  **by** auto  
**hence**  $\text{wset } s1 \ t = \text{None}$  **by**(auto simp add:  $r1.\text{final-thread-def}$ )  
**with**  $\langle s1 \approx_m s2 \rangle$  **have**  $s1' \approx_m s2'$  **unfolding**  $s1'\text{-def } s2'\text{-def}$   
**by**(auto simp add:  $mbisim\text{-def intro: } tbisim\text{-NoneI intro!: } \text{wset-thread-okI dest: } \text{wset-thread-okD}$   
 $\text{split: if-split-asm}$ )  
**ultimately** **have**  $\exists s2''. r2.mthr.\text{silent-moves } s2' \ s2'' \wedge s1' \approx_m s2'' \wedge r1.\text{final-threads } s1' \subseteq$   
 $r2.\text{final-threads } s2'' \wedge \text{shr } s2'' = \text{shr } s2'$  **by**(rule  $\text{insert}$ )

then obtain  $s2''$  where  $reds: r2.mthr.silent-moves\ s2'\ s2''$   
 and  $s1' \approx_m s2''$  and  $fin: \bigwedge t. r1.final-thread\ s1'\ t \implies r2.final-thread\ s2''\ t$  and  $shr\ s2'' = shr\ s2'$  by *blast*  
 have  $thr\ s2'\ t = None$  unfolding  $s2'$ -def by *simp*  
 with  $\langle r2.mthr.silent-moves\ s2'\ s2'' \rangle$   
 have  $r2.mthr.silent-moves\ (locks\ s2', ((thr\ s2')(t \mapsto the\ (thr\ s2\ t)), shr\ s2'), wset\ s2', interrupts\ s2')$   
 $(locks\ s2'', ((thr\ s2'')(t \mapsto the\ (thr\ s2\ t)), shr\ s2''), wset\ s2'', interrupts\ s2'')$   
 by (rule  $r2.\tau mRedT-add-thread-inv$ )  
 also let  $?s2'' = (locks\ s2, ((thr\ s2'')(t \mapsto the\ (thr\ s2\ t)), shr\ s2), wset\ s2, interrupts\ s2)$   
 from  $\langle shr\ s2'' = shr\ s2' \rangle \langle s1' \approx_m s2'' \rangle \langle s1 \approx_m s2 \rangle$   
 have  $(locks\ s2'', ((thr\ s2'')(t \mapsto the\ (thr\ s2\ t)), shr\ s2''), wset\ s2'', interrupts\ s2'') = ?s2''$   
 unfolding  $s2'$ -def  $s1'$ -def by (simp add:  $mbisim-def$ )  
 also (back-subst) from  $\langle s1 \approx_m s2 \rangle$  have  $dom\ (thr\ s1) = dom\ (thr\ s2)$  by (rule  $mbisim-dom-eq$ )  
 with  $\langle r1.final-thread\ s1\ t \rangle$  have  $t \in dom\ (thr\ s2)$  by (auto simp add:  $r1.final-thread-def$ )  
 then obtain  $x2\ ln$  where  $tst2: thr\ s2\ t = [(x2, ln)]$  by *auto*  
 hence  $(locks\ s2', ((thr\ s2')(t \mapsto the\ (thr\ s2\ t)), shr\ s2'), wset\ s2', interrupts\ s2') = s2$   
 unfolding  $s2'$ -def by (cases  $s2$ ) (auto intro!: *ext*)  
 also from  $\langle s1 \approx_m s2 \rangle\ tst2$  obtain  $x1$   
 where  $tst1: thr\ s1\ t = [(x1, ln)]$   
 and  $bisim: t \vdash (x1, shr\ s1) \approx (x2, shr\ s2)$  by (auto dest:  $mbisim-thrD2$ )  
 from  $\langle shr\ s2'' = shr\ s2' \rangle$  have  $shr\ ?s2'' = shr\ s2$  by (simp add:  $s2'$ -def)  
 from  $\langle r1.final-thread\ s1\ t \rangle\ tst1$   
 have  $final: final1\ x1\ ln = no-wait-locks\ wset\ s1\ t = None$  by (auto simp add:  $r1.final-thread-def$ )  
 with  $final1-simulation[OF\ bisim]\ \langle shr\ ?s2'' = shr\ s2 \rangle$  obtain  $x2'\ m2'$   
 where  $red: r2.silent-moves\ t\ (x2, shr\ ?s2'')\ (x2', m2')$   
 and  $bisim': t \vdash (x1, shr\ s1) \approx (x2', m2')$  and  $final2\ x2'$  by *auto*  
 from  $\langle wset\ s1\ t = None \rangle \langle s1 \approx_m s2 \rangle$  have  $wset\ s2\ t = None$  by (simp add:  $mbisim-def$ )  
 with  $bisim\ r2.silent-moves-into-RedT-\tau-inv[OF\ red]\ tst2\ \langle ln = no-wait-locks \rangle$   
 have  $r2.mthr.silent-moves\ ?s2''\ (redT-upd-\varepsilon\ ?s2''\ t\ x2'\ m2')$  unfolding  $s2'$ -def by *auto*  
 also (rtranclp-trans)  
 from  $bisim\ r2.red-rtrancl-\tau-heapD-inv[OF\ red]$  have  $m2' = shr\ s2$  by *auto*  
 hence  $s1 \approx_m (redT-upd-\varepsilon\ ?s2''\ t\ x2'\ m2')$   
 using  $\langle s1' \approx_m s2'' \rangle \langle s1 \approx_m s2 \rangle\ tst1\ tst2\ \langle shr\ ?s2'' = shr\ s2 \rangle\ bisim'\ \langle shr\ s2'' = shr\ s2' \rangle \langle wset\ s2\ t = None \rangle$   
 unfolding  $s1'$ -def  $s2'$ -def by (auto simp add:  $mbisim-def\ redT-updLns-def\ split: if-split-asm\ intro: tbsim-SomeI$ )  
 moreover {  
 fix  $t'$   
 assume  $r1.final-thread\ s1\ t'$   
 with  $fin[of\ t']\ \langle final2\ x2' \rangle\ tst2\ \langle ln = no-wait-locks \rangle \langle wset\ s2\ t = None \rangle \langle s1' \approx_m s2'' \rangle \langle s1 \approx_m s2 \rangle$   
 have  $r2.final-thread\ (redT-upd-\varepsilon\ ?s2''\ t\ x2'\ m2')\ t'$  unfolding  $s1'$ -def  
 by (fastforce  $split: if-split-asm\ simp\ add: r2.final-thread-def\ r1.final-thread-def\ redT-updLns-def\ finfun-Diag-const2\ o-def\ mbisim-def$ )  
 }  
 moreover have  $shr\ (redT-upd-\varepsilon\ ?s2''\ t\ x2'\ m2') = shr\ s2$  using  $\langle m2' = shr\ s2 \rangle$  by *simp*  
 ultimately show  $?case$  by *blast*  
 qed  
 qed

lemma  $mfinal2-inv-simulation$ :

$s1 \approx_m s2 \implies \exists s1'. r1.mthr.silent-moves\ s1\ s1' \wedge s1' \approx_m s2 \wedge r2.final-threads\ s2 \subseteq r1.final-threads\ s1' \wedge shr\ s1' = shr\ s1$

**using** *FWdelay-bisimulation-final-base.mfinal1-inv-simulation*[*OF FWdelay-bisimulation-final-base-flip*,  
**where** *bisim-wait=flip bisim-wait*]  
**by**(*unfold flip-simps*)

**lemma** *mfinal1-simulation*:

**assumes**  $s1 \approx_m s2$  **and**  $r1.mfinal\ s1$

**shows**  $\exists s2'. r2.mthr.silent-moves\ s2\ s2' \wedge s1 \approx_m s2' \wedge r2.mfinal\ s2' \wedge shr\ s2' = shr\ s2$

**proof** –

**from** *mfinal1-inv-simulation*[*OF*  $\langle s1 \approx_m s2 \rangle$ ]

**obtain**  $s2'$  **where**  $1: r2.mthr.silent-moves\ s2\ s2'\ s1 \approx_m s2'\ shr\ s2' = shr\ s2$

**and**  $fin: \bigwedge t. r1.final-thread\ s1\ t \implies r2.final-thread\ s2'\ t$  **by** *blast*

**have**  $r2.mfinal\ s2'$

**proof**(*rule r2.mfinalI*)

**fix**  $t\ x2\ ln$

**assume**  $thr\ s2'\ t = \lfloor (x2, ln) \rfloor$

**with**  $\langle s1 \approx_m s2' \rangle$  **obtain**  $x1$  **where**  $thr\ s1\ t = \lfloor (x1, ln) \rfloor\ t \vdash (x1, shr\ s1) \approx (x2, shr\ s2')$

**by**(*auto dest: mbisim-thrD2*)

**from**  $\langle thr\ s1\ t = \lfloor (x1, ln) \rfloor \rangle\ \langle r1.mfinal\ s1 \rangle$  **have**  $r1.final-thread\ s1\ t$

**by**(*auto elim!: r1.mfinalE simp add: r1.final-thread-def*)

**hence**  $r2.final-thread\ s2'\ t$  **by**(*rule fin*)

**thus**  $final2\ x2 \wedge ln = no-wait-locks \wedge wset\ s2'\ t = None$

**using**  $\langle thr\ s2'\ t = \lfloor (x2, ln) \rfloor \rangle$  **by**(*auto simp add: r2.final-thread-def*)

**qed**

**with**  $1$  **show** *?thesis* **by** *blast*

**qed**

**lemma** *mfinal2-simulation*:

$\llbracket s1 \approx_m s2; r2.mfinal\ s2 \rrbracket$

$\implies \exists s1'. r1.mthr.silent-moves\ s1\ s1' \wedge s1' \approx_m s2 \wedge r1.mfinal\ s1' \wedge shr\ s1' = shr\ s1$

**using** *FWdelay-bisimulation-final-base.mfinal1-simulation*[*OF FWdelay-bisimulation-final-base-flip*, **where**  
*bisim-wait = flip bisim-wait*]

**by**(*unfold flip-simps*)

**end**

**locale** *FWdelay-bisimulation-obs* =

*FWdelay-bisimulation-final-base* - - - - -  $\tau move1\ \tau move2$

**for**  $\tau move1 :: ('l, 't, 'x1, 'm1, 'w, 'o) \tau moves$

**and**  $\tau move2 :: ('l, 't, 'x2, 'm2, 'w, 'o) \tau moves +$

**assumes** *delay-bisimulation-obs-locale*: *delay-bisimulation-obs* ( $r1\ t$ ) ( $r2\ t$ ) (*bisim*  $t$ ) (*ta-bisim* *bisim*)

$\tau move1\ \tau move2$

**and** *bisim-inv-red-other*:

$\llbracket t' \vdash (x, m1) \approx (xx, m2); t \vdash (x1, m1) \approx (x2, m2);$

$r1.silent-moves\ t\ (x1, m1)\ (x1', m1);$

$t \vdash (x1', m1) -1-ta1 \rightarrow (x1'', m1'); \neg \tau move1\ (x1', m1)\ ta1\ (x1'', m1');$

$r2.silent-moves\ t\ (x2, m2)\ (x2', m2);$

$t \vdash (x2', m2) -2-ta2 \rightarrow (x2'', m2'); \neg \tau move2\ (x2', m2)\ ta2\ (x2'', m2');$

$t \vdash (x1'', m1') \approx (x2'', m2'); ta-bisim\ bisim\ ta1\ ta2 \rrbracket$

$\implies t' \vdash (x, m1') \approx (xx, m2')$

**and** *bisim-waitI*:

$\llbracket t \vdash (x1, m1) \approx (x2, m2); r1.silent-moves\ t\ (x1, m1)\ (x1', m1);$

$t \vdash (x1', m1) -1-ta1 \rightarrow (x1'', m1'); \neg \tau move1\ (x1', m1)\ ta1\ (x1'', m1');$

$r2.silent-moves\ t\ (x2, m2)\ (x2', m2);$

$t \vdash (x2', m2) -2-ta2 \rightarrow (x2'', m2'); \neg \tau move2\ (x2', m2)\ ta2\ (x2'', m2');$

$t \vdash (x1'', m1') \approx (x2'', m2'); ta\text{-bisim bisim } ta1 \ ta2;$   
 $Suspend \ w \in \text{set } \{\{ta1\}\}_w; Suspend \ w \in \text{set } \{\{ta2\}\}_w \parallel$   
 $\implies x1'' \approx_w x2''$   
**and simulation-Wakeup1:**  
 $\parallel t \vdash (x1, m1) \approx (x2, m2); x1 \approx_w x2; t \vdash (x1, m1) -1-ta1 \rightarrow (x1', m1'); Notified \in \text{set } \{\{ta1\}\}_w$   
 $\vee WokenUp \in \text{set } \{\{ta1\}\}_w \parallel$   
 $\implies \exists ta2 \ x2' \ m2'. t \vdash (x2, m2) -2-ta2 \rightarrow (x2', m2') \wedge t \vdash (x1', m1') \approx (x2', m2') \wedge ta\text{-bisim}$   
 $bisim \ ta1 \ ta2$   
**and simulation-Wakeup2:**  
 $\parallel t \vdash (x1, m1) \approx (x2, m2); x1 \approx_w x2; t \vdash (x2, m2) -2-ta2 \rightarrow (x2', m2'); Notified \in \text{set } \{\{ta2\}\}_w$   
 $\vee WokenUp \in \text{set } \{\{ta2\}\}_w \parallel$   
 $\implies \exists ta1 \ x1' \ m1'. t \vdash (x1, m1) -1-ta1 \rightarrow (x1', m1') \wedge t \vdash (x1', m1') \approx (x2', m2') \wedge ta\text{-bisim}$   
 $bisim \ ta1 \ ta2$   
**and ex-final1-conv-ex-final2:**  
 $(\exists x1. \text{final1 } x1) \longleftrightarrow (\exists x2. \text{final2 } x2)$

**sublocale FWdelay-bisimulation-obs <**  
 $\text{delay-bisimulation-obs } r1 \ t \ r2 \ t \text{ bisim } t \ ta\text{-bisim bisim } \tau\text{move1 } \tau\text{move2}$  **for**  $t$   
**by**(rule delay-bisimulation-obs-locale)

**context FWdelay-bisimulation-obs begin**

**lemma FWdelay-bisimulation-obs-flip:**

$FW\text{delay-bisimulation-obs final2 } r2 \text{ final1 } r1 \ (\lambda t. \text{flip } (\text{bisim } t)) \ (\text{flip bisim-wait}) \ \tau\text{move2 } \tau\text{move1}$   
**apply**(rule FWdelay-bisimulation-obs.intro)  
**apply**(rule FWdelay-bisimulation-final-base-flip)  
**apply**(rule FWdelay-bisimulation-obs-axioms.intro)  
**apply**(unfold flip-simps)  
**apply**(rule delay-bisimulation-obs-axioms)  
**apply**(erule (9) bisim-inv-red-other)  
**apply**(erule (10) bisim-waitI)  
**apply**(erule (3) simulation-Wakeup2)  
**apply**(erule (3) simulation-Wakeup1)  
**apply**(rule ex-final1-conv-ex-final2[symmetric])  
**done**

**end**

**lemma FWdelay-bisimulation-obs-flip-simps [flip-simps]:**

$FW\text{delay-bisimulation-obs final2 } r2 \text{ final1 } r1 \ (\lambda t. \text{flip } (\text{bisim } t)) \ (\text{flip bisim-wait}) \ \tau\text{move2 } \tau\text{move1} =$   
 $FW\text{delay-bisimulation-obs final1 } r1 \text{ final2 } r2 \text{ bisim bisim-wait } \tau\text{move1 } \tau\text{move2}$   
**by**(auto dest: FWdelay-bisimulation-obs.FWdelay-bisimulation-obs-flip simp only: flip-flip)

**context FWdelay-bisimulation-obs begin**

**lemma mbisim-redT-upd:**

**fixes**  $s1 \ t \ ta1 \ x1' \ m1' \ s2 \ ta2 \ x2' \ m2' \ ln$   
**assumes**  $s1': \text{redT-upd } s1 \ t \ ta1 \ x1' \ m1' \ s1'$   
**and**  $s2': \text{redT-upd } s2 \ t \ ta2 \ x2' \ m2' \ s2'$   
**and** [simp]:  $wset \ s1 = wset \ s2 \ locks \ s1 = locks \ s2$   
**and**  $wset: wset \ s1' = wset \ s2'$   
**and**  $interrupts: interrupts \ s1' = interrupts \ s2'$   
**and**  $fin1: \text{finite } (dom \ (thr \ s1))$



```

and wsts: wset-thread-ok (wset s1) (thr s1)
and tst: thr s1 t = [(x1, ln)]
and tst': thr s2 t = [(x2, ln)]
and aoel: r1.actions-ok s1 t ta1
and aoel2: r2.actions-ok s2 t ta2
and tasim: ta-bisim bisim ta1 ta2
and bisim': t ⊢ (x1', m1') ≈ (x2', m2')
and bisimw: wset s1' t = None ∨ x1' ≈w x2'
and τred1: r1.silent-moves t (x1'', shr s1) (x1, shr s1)
and red1: t ⊢ (x1, shr s1) -1-ta1 → (x1', m1')
and τred2: r2.silent-moves t (x2'', shr s2) (x2, shr s2)
and red2: t ⊢ (x2, shr s2) -2-ta2 → (x2', m2')
and bisim: t ⊢ (x1'', shr s1) ≈ (x2'', shr s2)
and τ1: ¬ τmove1 (x1, shr s1) ta1 (x1', m1')
and τ2: ¬ τmove2 (x2, shr s2) ta2 (x2', m2')
and tbisim: ∧t'. t ≠ t' ⇒ tbisim (wset s1 t' = None) t' (thr s1 t') (shr s1) (thr s2 t') (shr s2)
shows s1' ≈m s2'
proof(rule mbisimI)
  from fin1 s1' show finite (dom (thr s1'))
  by(auto simp add: redT-updTs-finite-dom-inv)
next
  from tasim s1' s2' show locks s1' = locks s2'
  by(auto simp add: redT-updLs-def o-def ta-bisim-def)
next
  from wset show wset s1' = wset s2'.
next
  from interrupts show interrupts s1' = interrupts s2'.
next
  from wsts s1' s2' wset show wset-thread-ok (wset s1') (thr s1')
  by(fastforce intro!: wset-thread-okI split: if-split-asm dest: redT-updTs-None wset-thread-okD redT-updWs-None-implies-None)
next
  fix T
  assume thr s1' T = None
  moreover with tst s1' have [simp]: t ≠ T by auto
  from tbisim[OF this] have (thr s1 T = None) = (thr s2 T = None)
  by(auto simp add: tbisim-def)
  hence (redT-updTs (thr s1) {ta1}_t T = None) = (redT-updTs (thr s2) {ta2}_t T = None)
  using tasim by -(rule redT-updTs-nta-bisim-inv, simp-all add: ta-bisim-def)
  ultimately show thr s2' T = None using s2' s1' by(auto split: if-split-asm)
next
  fix T X1 LN
  assume tsT: thr s1' T = [(X1, LN)]
  show ∃ x2. thr s2' T = [(x2, LN)] ∧ T ⊢ (X1, shr s1') ≈ (x2, shr s2') ∧ (wset s2' T = None ∨
X1 ≈w x2)
  proof(cases thr s1 T)
    case None
    with tst have t ≠ T by auto
    with tbisim[OF this] None have tsT': thr s2 T = None by(simp add: tbisim-def)
    from None ⟨t ≠ T⟩ tsT aoel s1' obtain M1
      where ntset: NewThread T X1 M1 ∈ set {ta1}_t and [simp]: LN = no-wait-locks
      by(auto dest!: redT-updTs-new-thread)
    from ntset obtain tas1 tas1' where {ta1}_t = tas1 @ NewThread T X1 M1 # tas1'
      by(auto simp add: in-set-conv-decomp)
    with tasim obtain tas2 X2 M2 tas2' where {ta2}_t = tas2 @ NewThread T X2 M2 # tas2'

```

$\text{length } \text{tas2} = \text{length } \text{tas2} \text{ length } \text{tas1}' = \text{length } \text{tas2}'$  **and**  $\text{Bisim}: T \vdash (X1, M1) \approx (X2, M2)$   
**by**(*auto simp add: list-all2-append1 list-all2-Cons1 ta-bisim-def*)  
**hence**  $\text{ntset}': \text{NewThread } T \ X2 \ M2 \in \text{set } \{\text{ta2}\}_t$  **by** *auto*  
**with**  $\text{tsT}' \langle t \neq T \rangle \text{ aoe2 } s2' \text{ have } \text{thr } s2' \ T = \lfloor (X2, \text{no-wait-locks}) \rfloor$   
**by**(*auto intro: redT-updTs-new-thread-ts*)  
**moreover from**  $\text{ntset}' \text{ red2}$  **have**  $m2' = M2$  **by**(*auto dest: r2.new-thread-memory*)  
**moreover from**  $\text{ntset} \text{ red1}$  **have**  $m1' = M1$   
**by**(*auto dest: r1.new-thread-memory*)  
**moreover from**  $\text{wsts } \text{None}$  **have**  $\text{wset } s1 \ T = \text{None}$  **by**(*rule wset-thread-okD*)  
**ultimately show**  $?thesis$  **using**  $\text{Bisim} \langle t \neq T \rangle \ s1' \ s2'$   
**by**(*auto simp add: redT-updWs-None-implies-None*)  
**next**  
**case** (*Some a*)  
**show**  $?thesis$   
**proof**(*cases t = T*)  
**case** *True*  
**with**  $\text{tst } \text{tsT} \ s1'$  **have**  $[\text{simp}]: X1 = x1' \ LN = \text{redT-updLns } (\text{locks } s1) \ t \ \text{ln } \{\text{ta1}\}_l$  **by**(*auto*)  
**show**  $?thesis$  **using** *True bisim' bisimw tasim tst tst' s1' s2' wset*  
**by**(*auto simp add: redT-updLns-def ta-bisim-def*)  
**next**  
**case** *False*  
**with** *Some aoe1 tsT s1'* **have**  $\text{thr } s1 \ T = \lfloor (X1, LN) \rfloor$  **by**(*auto dest: redT-updTs-Some*)  
**with**  $\text{tbisim}[OF \text{False}]$  **obtain**  $X2$   
**where**  $\text{tsT}': \text{thr } s2 \ T = \lfloor (X2, LN) \rfloor$  **and**  $\text{Bisim}: T \vdash (X1, \text{shr } s1) \approx (X2, \text{shr } s2)$   
**and**  $\text{bisimw}: \text{wset } s1 \ T = \text{None} \vee X1 \approx_w X2$  **by**(*auto simp add: tbisim-def*)  
**with**  $\text{aoe2 } \text{False } s2'$  **have**  $\text{tsT}': \text{thr } s2' \ T = \lfloor (X2, LN) \rfloor$  **by**(*auto simp add: redT-updTs-Some*)  
**moreover from**  $\text{Bisim } \text{bisim } \tau \text{red1 } \text{red1 } \tau 1 \ \tau \text{red2 } \text{red2 } \tau 2 \ \text{bisim}' \ \text{tasim}$   
**have**  $T \vdash (X1, m1') \approx (X2, m2')$  **by**(*rule bisim-inv-red-other*)  
**ultimately show**  $?thesis$  **using** *False bisimw s1' s2'*  
**by**(*auto simp add: redT-updWs-None-implies-None*)  
**qed**  
**qed**  
**qed**

**theorem** *mbisim-simulation1*:  
**assumes**  $\text{mbisim}: \text{mbisim } s1 \ s2$  **and**  $\neg \text{m}\tau\text{move1 } s1 \ \text{tl1 } s1' \ r1.\text{redT } s1 \ \text{tl1 } s1'$   
**shows**  $\exists s2' \ s2'' \ \text{tl2}. \ r2.\text{mthr}.\text{silent-moves } s2 \ s2' \wedge r2.\text{redT } s2' \ \text{tl2 } s2'' \wedge$   
 $\neg \text{m}\tau\text{move2 } s2' \ \text{tl2 } s2'' \wedge \text{mbisim } s1' \ s2'' \wedge \text{mta-bisim } \text{tl1 } \text{tl2}$

**proof** –  
**from** *assms* **obtain**  $t \ \text{ta1}$  **where**  $\text{tl1} \ [\text{simp}]: \text{tl1} = (t, \text{ta1})$  **and**  $\text{redT}: s1 \ -1 - t \triangleright \text{ta1} \rightarrow s1'$   
**and**  $\text{m}\tau: \neg \text{m}\tau\text{move1 } s1 \ (t, \text{ta1}) \ s1'$  **by**(*cases tl1*) *fastforce*  
**obtain**  $\text{ls1 } \text{ts1 } \text{m1 } \text{ws1 } \text{is1}$  **where**  $[\text{simp}]: s1 = (\text{ls1}, (\text{ts1}, \text{m1}), \text{ws1}, \text{is1})$  **by**(*cases s1*) *fastforce*  
**obtain**  $\text{ls1}' \ \text{ts1}' \ \text{m1}' \ \text{ws1}' \ \text{is1}'$  **where**  $[\text{simp}]: s1' = (\text{ls1}', (\text{ts1}', \text{m1}'), \text{ws1}', \text{is1}')$  **by**(*cases s1'*)  
*fastforce*  
**obtain**  $\text{ls2 } \text{ts2 } \text{m2 } \text{ws2 } \text{is2}$  **where**  $[\text{simp}]: s2 = (\text{ls2}, (\text{ts2}, \text{m2}), \text{ws2}, \text{is2})$  **by**(*cases s2*) *fastforce*  
**from**  $\text{mbisim}$  **have**  $[\text{simp}]: \text{ls2} = \text{ls1} \ \text{ws2} = \text{ws1} \ \text{is2} = \text{is1}$  *finite (dom ts1)* **by**(*auto simp add: mbisim-def*)  
**from**  $\text{redT}$  **show**  $?thesis$   
**proof** *cases*  
**case** (*redT-normal x1 x1' M1'*)  
**hence**  $\text{red}: t \vdash (x1, m1) \ -1 - \text{ta1} \rightarrow (x1', M1')$   
**and**  $\text{tst}: \text{ts1 } t = \lfloor (x1, \text{no-wait-locks}) \rfloor$   
**and**  $\text{aoe}: r1.\text{actions-ok } s1 \ t \ \text{ta1}$   
**and**  $s1': \text{redT-upd } s1 \ t \ \text{ta1 } x1' \ M1' \ s1'$  **by** *auto*

from *mbisim* *tst* obtain *x2* where *tst'*: *ts2* *t* =  $\lfloor (x2, \text{no-wait-locks}) \rfloor$   
 and *bisim*:  $t \vdash (x1, m1) \approx (x2, m2)$  by (auto *dest*: *mbisim-thrD1*)  
 from *mτ* have  $\tau: \neg \tau \text{move1 } (x1, m1) \text{ ta1 } (x1', M1')$   
 proof (rule *contrapos-nn*)  
 assume  $\tau: \tau \text{move1 } (x1, m1) \text{ ta1 } (x1', M1')$   
 moreover hence [simp]: *ta1* =  $\varepsilon$  by (rule *r1.silent-tl*)  
 moreover have [simp]: *M1'* = *m1* by (rule *r1.τmove-heap[OF red τ, symmetric]*)  
 ultimately show *mτmove1* *s1* (*t*, *ta1*) *s1'* using *s1' tst s1'*  
 by (auto *simp add*: *redT-updLs-def o-def intro*: *r1.mτmove.intros elim*: *rtranc3p-cases*)  
 qed  
 show ?thesis  
 proof (cases *ws1 t*)  
 case *None*  
 note *wst* = *this*  
 from *simulation1* [*OF bisim red τ*] obtain *x2'* *M2'* *x2''* *M2''* *ta2*  
 where *red21*: *r2.silent-moves* *t* (*x2*, *m2*) (*x2'*, *M2'*)  
 and *red22*:  $t \vdash (x2', M2') -2-ta2 \rightarrow (x2'', M2'')$  and  $\tau2: \neg \tau \text{move2 } (x2', M2') \text{ ta2 } (x2'', M2'')$   
 and *bisim'*:  $t \vdash (x1', M1') \approx (x2'', M2'')$   
 and *tasim*: *ta-bisim* *bisim* *ta1* *ta2* by *auto*  
 let *?s2'* = *redT-upd-ε* *s2* *t* *x2'* *M2'*  
 let *?S2'* = *activate-cond-actions2* *s1* *?s2'*  $\lfloor \text{ta2} \rfloor_c$   
 let *?s2''* = (*redT-updLs* (*locks* *?S2'*) *t*  $\lfloor \text{ta2} \rfloor_l$ , ((*redT-updTs* (*thr* *?S2'*)  $\lfloor \text{ta2} \rfloor_t$ )(*t*  $\mapsto$  (*x2''*, *redT-updLns* (*locks* *?S2'*) *t* (*snd* (*the* (*thr* *?S2'* *t*)))  $\lfloor \text{ta2} \rfloor_l$ )), *M2''*), *wset* *s1'*, *interrupts* *s1'*)  
 from *red21* *tst'* *wst* *bisim* have  $\tau \text{Red2}$  *s2* *?s2'*  
 by (rule *r2.silent-moves-into-RedT-τ-inv, auto*)  
 moreover from *red21* *bisim* have [simp]: *M2'* = *m2* by (auto *dest*: *r2.red-rtranc1-τ-heapD-inv*)  
 from *tasim* have [simp]:  $\lfloor \text{ta1} \rfloor_l = \lfloor \text{ta2} \rfloor_l$   $\lfloor \text{ta1} \rfloor_w = \lfloor \text{ta2} \rfloor_w$   $\lfloor \text{ta1} \rfloor_c = \lfloor \text{ta2} \rfloor_c$   $\lfloor \text{ta1} \rfloor_i = \lfloor \text{ta2} \rfloor_i$   
 and *nta*: *list-all2* (*nta-bisim* *bisim*)  $\lfloor \text{ta1} \rfloor_t \lfloor \text{ta2} \rfloor_t$  by (auto *simp add*: *ta-bisim-def*)  
 from *mbisim* have *tbisim*:  $\bigwedge t. \text{tbisim } (ws1 \ t = \text{None}) \ t \ (ts1 \ t) \ m1 \ (ts2 \ t) \ m2$  by (*simp add*: *mbisim-def*)  
 hence *tbisim'*:  $\bigwedge t'. t' \neq t \implies \text{tbisim } (ws1 \ t' = \text{None}) \ t' \ (ts1 \ t') \ m1 \ (\text{thr } ?s2' \ t') \ m2$  by (auto)  
 from *aoe* have *cao1*: *r1.cond-action-oks* (*ls1*, (*ts1*, *m1*), *ws1*, *is1*) *t*  $\lfloor \text{ta2} \rfloor_c$  by *auto*  
 from *tst'* have *thr* *?s2'* *t* =  $\lfloor (x2', \text{no-wait-locks}) \rfloor$  by (auto *simp add*: *redT-updLns-def o-def finfun-Diag-const2*)  
 from *cond-actions-oks-bisim-ex-τ2-inv* [*OF tbisim'*, *OF - tst this cao1*]  
 have *red21'*:  $\tau \text{Red2 } ?s2' \ ?S2' \text{ and } \text{tbisim}'': \bigwedge t'. t' \neq t \implies \text{tbisim } (ws1 \ t' = \text{None}) \ t' \ (ts1 \ t') \ m1 \ (\text{thr } ?S2' \ t') \ m2$   
 and *cao2*: *r2.cond-action-oks* *?S2'* *t*  $\lfloor \text{ta2} \rfloor_c$  and *tst''*: *thr* *?S2'* *t* =  $\lfloor (x2', \text{no-wait-locks}) \rfloor$   
 by (auto *simp del*: *fun-upd-apply*)  
 note *red21'* also (*rtranc1p-trans*)  
 from *tbisim''* *tst''* *tst* have  $\forall t'. ts1 \ t' = \text{None} \iff \text{thr } ?S2' \ t' = \text{None}$  by (*force simp add*: *tbisim-def*)  
 from *aoe thread-oks-bisim-inv* [*OF this nta*] have *thread-oks* (*thr* *?S2'*)  $\lfloor \text{ta2} \rfloor_t$  by *simp*  
 with *cao2* *aoe* have *aoe'*: *r2.actions-ok* *?S2'* *t* *ta2* by *auto*  
 with *red22* *tst''* *s1'* have *?S2'*  $-2-t \triangleright ta2 \rightarrow ?s2''$   
 by (rule *r2.redT.redT-normal, auto*)  
 moreover  
 from  $\tau2$  have  $\neg m\tau \text{move2 } ?S2' \ (t, \text{ta2}) \ ?s2''$   
 proof (rule *contrapos-nn*)  
 assume  $m\tau: m\tau \text{move2 } ?S2' \ (t, \text{ta2}) \ ?s2''$   
 thus  $\tau \text{move2 } (x2', M2') \text{ ta2 } (x2'', M2'')$  using *tst''* *tst'*  
 by cases *auto*

```

qed
moreover
{
  note s1'
  moreover have redT-upd ?S2' t ta2 x2'' M2'' ?s2'' using s1' by auto
  moreover have wset s1 = wset ?S2' locks s1 = locks ?S2' by simp-all
  moreover have wset s1' = wset ?s2'' by simp
  moreover have interrupts s1' = interrupts ?s2'' by simp
  moreover have finite (dom (thr s1)) by simp
  moreover from mbisim have wset-thread-ok (wset s1) (thr s1) by (simp add: mbisim-def)
  moreover from tst have thr s1 t = [(x1, no-wait-locks)] by simp
  moreover note tst'' aoe aoe' tasim bisim'
  moreover have wset s1' t = None  $\vee$  x1'  $\approx_w$  x2''
  proof (cases wset s1' t)
    case None thus ?thesis ..
  next
    case (Some w)
    with wst s1' obtain w' where Suspend1: Suspend w'  $\in$  set {ta1}_w
    by (auto dest: redT-updWs-None-SomeD)
    with tasim have Suspend2: Suspend w'  $\in$  set {ta2}_w by (simp add: ta-bisim-def)
    from bisim-waitI[OF bisim rtranclp.rtrancl-refl red  $\tau$  - - bisim' tasim Suspend1 this, of x2']
red21 red22  $\tau$ 2
    have x1'  $\approx_w$  x2'' by auto
    thus ?thesis ..
  qed
  moreover note rtranclp.rtrancl-refl
  moreover from red have t  $\vdash$  (x1, shr s1) -1-ta1  $\rightarrow$  (x1', M1') by simp
  moreover from red21 have r2.silent-moves t (x2, shr ?S2') (x2', shr ?S2') by simp
  moreover from red22 have t  $\vdash$  (x2', shr ?S2') -2-ta2  $\rightarrow$  (x2'', M2'') by simp
  moreover from bisim have t  $\vdash$  (x1, shr s1)  $\approx$  (x2, shr ?S2') by simp
  moreover from  $\tau$  have  $\neg \tau$ move1 (x1, shr s1) ta1 (x1', M1') by simp
  moreover from  $\tau$ 2 have  $\neg \tau$ move2 (x2', shr ?S2') ta2 (x2'', M2'') by simp
  moreover from tbisim''
  have  $\bigwedge t'. t \neq t' \implies$  tbisim (wset s1 t' = None) t' (thr s1 t') (shr s1) (thr ?S2' t') (shr ?S2')
  by simp
  ultimately have mbisim s1' ?s2'' by (rule mbisim-redT-upd)
}
ultimately show ?thesis using tasim unfolding tl1 s1' by fastforce
next
case (Some w)
with mbisim tst tst' have x1  $\approx_w$  x2
by (auto dest: mbisim-thrD1)
from aoe Some have wakeup: Notified  $\in$  set {ta1}_w  $\vee$  WokenUp  $\in$  set {ta1}_w
by (auto simp add: wset-actions-ok-def split: if-split-asm)
from simulation-Wakeup1[OF bisim (x1  $\approx_w$  x2) red this]
obtain ta2 x2' m2' where red2: t  $\vdash$  (x2, m2) -2-ta2  $\rightarrow$  (x2', m2')
and bisim': t  $\vdash$  (x1', M1')  $\approx$  (x2', m2')
and tasim: ta1  $\sim_m$  ta2 by auto

let ?S2' = activate-cond-actions2 s1 s2 {ta2}_c

let ?s2' = (redT-updLs (locks ?S2') t {ta2}_t, ((redT-updTs (thr ?S2') {ta2}_t)(t  $\mapsto$  (x2',
redT-updLns (locks ?S2') t (snd (the (thr ?S2' t))) {ta2}_t), m2'), wset s1', interrupts s1'))

```

**from** *tasim* **have** [simp]:  $\llbracket ta1 \rrbracket_l = \llbracket ta2 \rrbracket_l \llbracket ta1 \rrbracket_w = \llbracket ta2 \rrbracket_w \llbracket ta1 \rrbracket_c = \llbracket ta2 \rrbracket_c \llbracket ta1 \rrbracket_i = \llbracket ta2 \rrbracket_i$   
**and** *nta*: *list-all2* (*nta-bisim bisim*)  $\llbracket ta1 \rrbracket_t \llbracket ta2 \rrbracket_t$  **by** (*auto simp add: ta-bisim-def*)  
**from** *mbisim* **have** *tbsim*:  $\bigwedge t. tbsim (ws1\ t = None)\ t\ (ts1\ t)\ m1\ (ts2\ t)\ m2$  **by** (*simp add: mbisim-def*)  
**hence** *tbsim'*:  $\bigwedge t'. t' \neq t \implies tbsim (ws1\ t' = None)\ t'\ (ts1\ t')\ m1\ (ts2\ t')\ m2$  **by** (*auto*)  
**from** *aoe* **have** *cao1*: *r1.cond-action-oks* (*ls1*, (*ts1*, *m1*), *ws1*, *is1*) *t*  $\llbracket ta2 \rrbracket_c$  **by** *auto*  
**from** *tst'* **have** *thr s2 t* =  $\llbracket (x2, no-wait-locks) \rrbracket$   
**by** (*auto simp add: redT-updLns-def o-def finfun-Diag-const2*)  
**from** *cond-actions-oks-bisim-ex- $\tau$ 2-inv*[*OF tbsim'*, *OF - tst this cao1*]  
**have** *red21'*:  $\tau mRed2\ s2\ ?S2'$  **and** *tbsim''*:  $\bigwedge t'. t' \neq t \implies tbsim (ws1\ t' = None)\ t'\ (ts1\ t')\ m1\ (ts2\ t')\ m2$   
**and** *cao2*: *r2.cond-action-oks*  $?S2'\ t\ \llbracket ta2 \rrbracket_c$  **and** *tst''*: *thr ?S2' t* =  $\llbracket (x2, no-wait-locks) \rrbracket$   
**by** (*auto simp del: fun-upd-apply*)  
**note** *red21' moreover*  
**from** *tbsim'' tst'' tst* **have**  $\forall t'. ts1\ t' = None \longleftrightarrow thr\ ?S2'\ t' = None$  **by** (*force simp add: tbsim-def*)  
**from** *aoe thread-oks-bisim-inv*[*OF this nta*] **have** *thread-oks* (*thr ?S2'*)  $\llbracket ta2 \rrbracket_t$  **by** *simp*  
**with** *cao2 aoe* **have** *aoe'*: *r2.actions-ok*  $?S2'\ t\ ta2$  **by** *auto*  
**with** *red2 tst'' s1' tasim* **have**  $?S2' - 2 - t \triangleright ta2 \rightarrow ?s2'$   
**by**  $-(rule\ r2.redT-normal, auto\ simp\ add: ta-bisim-def)$   
**moreover from** *wakeup tasim*  
**have**  $\tau 2: \neg \tau move2\ (x2, m2)\ ta2\ (x2', m2')$  **by** (*auto dest: r2.silent-tl*)  
**hence**  $\neg m\tau move2\ ?S2'\ (t, ta2)\ ?s2'$   
**proof**(*rule contrapos-nn*)  
**assume** *m $\tau$* :  $m\tau move2\ ?S2'\ (t, ta2)\ ?s2'$   
**thus**  $\tau move2\ (x2, m2)\ ta2\ (x2', m2')$  **using** *tst'' tst'*  
**by** *cases auto*  
**qed**  
**moreover** {  
**note** *s1'*  
**moreover have** *redT-upd*  $?S2'\ t\ ta2\ x2'\ m2'\ ?s2'$  **using** *s1' tasim* **by** (*auto simp add: ta-bisim-def*)  
**moreover have** *wset s1* = *wset ?S2' locks s1* = *locks ?S2'* **by** *simp-all*  
**moreover have** *wset s1'* = *wset ?s2'* **by** *simp*  
**moreover have** *interrupts s1'* = *interrupts ?s2'* **by** *simp*  
**moreover have** *finite* (*dom* (*thr s1*)) **by** *simp*  
**moreover from** *mbisim* **have** *wset-thread-ok* (*wset s1*) (*thr s1*) **by** (*rule mbisim-wset-thread-ok1*)  
**moreover from** *tst* **have** *thr s1 t* =  $\llbracket (x1, no-wait-locks) \rrbracket$  **by** *simp*  
**moreover from** *tst''* **have** *thr ?S2' t* =  $\llbracket (x2, no-wait-locks) \rrbracket$  **by** *simp*  
**moreover note** *aoe aoe' tasim bisim'*  
**moreover have** *wset s1' t* = *None*  $\vee\ x1' \approx_w x2'$   
**proof**(*cases wset s1' t*)  
**case** *None* **thus** *?thesis* ..  
**next**  
**case** (*Some w'*)  
**with** *redT-updWs-WokenUp-SuspendD*[*OF - wakeup, of t wset s1 wset s1' w'*] *s1'*  
**obtain** *w'* **where** *Suspend1*: *Suspend* *w' ∈ set*  $\llbracket ta1 \rrbracket_w$  **by** (*auto*)  
**with** *tasim* **have** *Suspend2*: *Suspend* *w' ∈ set*  $\llbracket ta2 \rrbracket_w$  **by** (*simp add: ta-bisim-def*)  
**with** *bisim rtranclp.rtrancl-refl red  $\tau$  rtranclp.rtrancl-refl red2  $\tau 2$  bisim' tasim Suspend1*  
**have**  $x1' \approx_w x2'$  **by** (*rule bisim-waitI*)  
**thus** *?thesis* ..  
**qed**  
**moreover note** *rtranclp.rtrancl-refl*

```

moreover from red have  $t \vdash (x1, \text{shr } s1) -1 - ta1 \rightarrow (x1', M1')$  by simp
moreover note rtrancpl.rtrancpl-refl
moreover from red2 have  $t \vdash (x2, \text{shr } ?S2') -2 - ta2 \rightarrow (x2', m2')$  by simp
moreover from bisim have  $t \vdash (x1, \text{shr } s1) \approx (x2, \text{shr } ?S2')$  by simp
moreover from  $\tau$  have  $\neg \tau \text{move1 } (x1, \text{shr } s1) \text{ ta1 } (x1', M1')$  by simp
moreover from  $\tau 2$  have  $\neg \tau \text{move2 } (x2, \text{shr } ?S2') \text{ ta2 } (x2', m2')$  by simp
moreover from tbisim'' have  $\bigwedge t'. t \neq t' \implies \text{tbisim } (\text{wset } s1 \ t' = \text{None}) \ t' \ (\text{thr } s1 \ t') \ (\text{shr } s1) \ (\text{thr } ?S2' \ t') \ (\text{shr } ?S2')$  by simp
ultimately have  $s1' \approx_m ?s2'$  by (rule mbisim-redT-upd) }
moreover from tasim have  $tl1 \sim_T (t, ta2)$  by simp
ultimately show ?thesis unfolding  $s1'$  by blast
qed
next
case (redT-acquire  $x1 \ n \ ln$ )
hence [simp]:  $ta1 = (K\$ \ [], [], [], [], \text{convert-RA } ln)$ 
and tst:  $\text{thr } s1 \ t = [(x1, ln)]$  and wst:  $\neg \text{waiting } (\text{wset } s1 \ t)$ 
and maa: may-acquire-all (locks  $s1$ )  $t \ ln$  and ln:  $0 < ln \ \$ \ n$ 
and  $s1'$ :  $s1' = (\text{acquire-all } ls1 \ t \ ln, (\text{ts1}(t \mapsto (x1, \text{no-wait-locks})), m1), ws1, is1)$  by auto
from tst mbisim obtain  $x2$  where tst':  $ts2 \ t = [(x2, ln)]$ 
and bisim:  $t \vdash (x1, m1) \approx (x2, m2)$  by (auto dest: mbisim-thrD1)
let  $?s2' = (\text{acquire-all } ls1 \ t \ ln, (\text{ts2}(t \mapsto (x2, \text{no-wait-locks})), m2), ws1, is1)$ 
from tst' wst maa ln have  $s2 -2 - t \triangleright (K\$ \ [], [], [], [], \text{convert-RA } ln) \rightarrow ?s2'$ 
by (rule r2.redT.redT-acquire, auto)
moreover from tst' ln have  $\neg \tau \text{move2 } s2 \ (t, (K\$ \ [], [], [], [], \text{convert-RA } ln)) \ ?s2'$ 
by (auto simp add: acquire-all-def fun-eq-iff elim!: r2.mmove.cases)
moreover have mbisim  $s1' \ ?s2'$ 
proof (rule mbisimI)
from  $s1'$  show locks  $s1' = \text{locks } ?s2'$  by auto
next
from  $s1'$  show wset  $s1' = \text{wset } ?s2'$  by auto
next
from  $s1'$  show interrupts  $s1' = \text{interrupts } ?s2'$  by auto
next
fix  $t'$  assume  $\text{thr } s1' \ t' = \text{None}$ 
with  $s1'$  have  $\text{thr } s1 \ t' = \text{None}$  by (auto split: if-split-asm)
with mbisim-thrNone-eq[OF mbisim] have  $ts2 \ t' = \text{None}$  by simp
with tst' show  $\text{thr } ?s2' \ t' = \text{None}$  by auto
next
fix  $t' \ X1 \ LN$ 
assume ts't:  $\text{thr } s1' \ t' = [(X1, LN)]$ 
show  $\exists x2. \text{thr } ?s2' \ t' = [(x2, LN)] \wedge t' \vdash (X1, \text{shr } s1') \approx (x2, \text{shr } ?s2') \wedge (\text{wset } ?s2' \ t' = \text{None} \vee X1 \approx_w x2)$ 
proof (cases  $t' = t$ )
case True
with  $s1' \text{ tst } ts't$  have [simp]:  $X1 = x1 \ LN = \text{no-wait-locks}$  by simp-all
with mbisim-thrD1[OF mbisim tst] bisim tst tst' True  $s1' \text{ wst}$  show ?thesis by (auto)
next
case False
with ts't  $s1' \text{ have}$   $ts1 \ t' = [(X1, LN)]$  by auto
with mbisim obtain  $X2$  where  $ts2 \ t' = [(X2, LN)] \ t' \vdash (X1, m1) \approx (X2, m2) \ \text{wset } ?s2' \ t' = \text{None} \vee X1 \approx_w X2$ 
by (auto dest: mbisim-thrD1)
with False  $s1'$  show ?thesis by auto
qed

```

```

next
  from  $s1'$  show finite (dom (thr  $s1'$ )) by auto
next
  from mbisim-wset-thread-ok1[OF mbisim]
  show wset-thread-ok (wset  $s1'$ ) (thr  $s1'$ ) using  $s1'$  by(auto intro: wset-thread-ok-upd)
qed
moreover have  $(t, K\$ \square, \square, \square, \square, \square, \text{convert-RA } ln) \sim T (t, K\$ \square, \square, \square, \square, \square, \text{convert-RA } ln)$ 
  by(simp add: ta-bisim-def)
ultimately show ?thesis by fastforce
qed
qed

theorem mbisim-simulation2:
   $\llbracket \text{mbisim } s1 \ s2; r2.\text{redT } s2 \ tl2 \ s2'; \neg m\tau\text{move2 } s2 \ tl2 \ s2' \rrbracket$ 
 $\implies \exists s1' \ s1'' \ tl1. r1.\text{mthr}.\text{silent-moves } s1 \ s1' \wedge r1.\text{redT } s1' \ tl1 \ s1'' \wedge \neg m\tau\text{move1 } s1' \ tl1 \ s1'' \wedge$ 
 $\text{mbisim } s1'' \ s2' \wedge \text{mta-bisim } tl1 \ tl2$ 
using FWdelay-bisimulation-obs.mbisim-simulation1[OF FWdelay-bisimulation-obs-flip]
unfolding flip-simps .

end

locale FWdelay-bisimulation-diverge =
  FWdelay-bisimulation-obs - - - - -  $\tau\text{move1 } \tau\text{move2}$ 
  for  $\tau\text{move1} :: ('l, 't, 'x1, 'm1, 'w, 'o) \tau\text{moves}$ 
  and  $\tau\text{move2} :: ('l, 't, 'x2, 'm2, 'w, 'o) \tau\text{moves} +$ 
  assumes delay-bisimulation-diverge-locale: delay-bisimulation-diverge  $(r1 \ t) (r2 \ t) (\text{bisim } t) (\text{ta-bisim } t)$ 
   $\tau\text{move1 } \tau\text{move2}$ 

sublocale FWdelay-bisimulation-diverge <
  delay-bisimulation-diverge  $r1 \ t \ r2 \ t \ \text{bisim } t \ \text{ta-bisim } t \ \tau\text{move1 } \tau\text{move2}$  for  $t$ 
by(rule delay-bisimulation-diverge-locale)

context FWdelay-bisimulation-diverge begin

lemma FWdelay-bisimulation-diverge-flip:
  FWdelay-bisimulation-diverge final2  $r2$  final1  $r1$   $(\lambda t. \text{flip } (\text{bisim } t)) (\text{flip } \text{bisim-wait}) \tau\text{move2 } \tau\text{move1}$ 
apply(rule FWdelay-bisimulation-diverge.intro)
apply(rule FWdelay-bisimulation-obs-flip)
apply(rule FWdelay-bisimulation-diverge-axioms.intro)
apply(unfold flip-simps)
apply(rule delay-bisimulation-diverge-axioms)
done

end

lemma FWdelay-bisimulation-diverge-flip-simps [flip-simps]:
  FWdelay-bisimulation-diverge final2  $r2$  final1  $r1$   $(\lambda t. \text{flip } (\text{bisim } t)) (\text{flip } \text{bisim-wait}) \tau\text{move2 } \tau\text{move1}$ 
=
  FWdelay-bisimulation-diverge final1  $r1$  final2  $r2$  bisim bisim-wait  $\tau\text{move1 } \tau\text{move2}$ 
by(auto dest: FWdelay-bisimulation-diverge.FWdelay-bisimulation-diverge-flip simp only: flip-flip)

context FWdelay-bisimulation-diverge begin

lemma bisim-inv1:

```

```

assumes bisim:  $t \vdash s1 \approx s2$ 
and red:  $t \vdash s1 \rightarrow \tau \rightarrow s1'$ 
obtains  $s2'$  where  $t \vdash s1' \approx s2'$ 
proof(atomize-elim)
  show  $\exists s2'. t \vdash s1' \approx s2'$ 
  proof(cases  $\tau \text{move1 } s1 \text{ ta1 } s1'$ )
    case True
      with red have r1.silent-move  $t \ s1 \ s1'$  by auto
      from simulation-silent1[OF bisim this]
      show ?thesis by auto
    next
      case False
      from simulation1[OF bisim red False] show ?thesis by auto
  qed
qed

lemma bisim-inv2:
  assumes  $t \vdash s1 \approx s2 \ t \vdash s2 \rightarrow \tau \rightarrow s2'$ 
  obtains  $s1'$  where  $t \vdash s1' \approx s2'$ 
using assms FWdelay-bisimulation-diverge.bisim-inv1[OF FWdelay-bisimulation-diverge-flip]
unfolding flip-simps by blast

lemma bisim-inv: bisim-inv
by(blast intro!: bisim-invI elim: bisim-inv1 bisim-inv2)

lemma bisim-inv- $\tau$ s1:
  assumes  $t \vdash s1 \approx s2$  and r1.silent-moves  $t \ s1 \ s1'$ 
  obtains  $s2'$  where  $t \vdash s1' \approx s2'$ 
using assms by(rule bisim-inv- $\tau$ s1-inv[OF bisim-inv])

lemma bisim-inv- $\tau$ s2:
  assumes  $t \vdash s1 \approx s2$  and r2.silent-moves  $t \ s2 \ s2'$ 
  obtains  $s1'$  where  $t \vdash s1' \approx s2'$ 
using assms by(rule bisim-inv- $\tau$ s2-inv[OF bisim-inv])

lemma red1-rtranc1- $\tau$ -into-RedT- $\tau$ :
  assumes r1.silent-moves  $t \ (x1, \text{shr } s1) \ (x1', m1')$   $t \vdash (x1, \text{shr } s1) \approx (x2, m2)$ 
  and thr  $s1 \ t = \lfloor (x1, \text{no-wait-locks}) \rfloor$  wset  $s1 \ t = \text{None}$ 
  shows  $\tau m \text{Red1 } s1 \ (\text{redT-upd-}\epsilon \ s1 \ t \ x1' \ m1')$ 
using assms by(blast intro: r1.silent-moves-into-RedT- $\tau$ -inv)

lemma red2-rtranc1- $\tau$ -into-RedT- $\tau$ :
  assumes r2.silent-moves  $t \ (x2, \text{shr } s2) \ (x2', m2')$ 
  and  $t \vdash (x1, m1) \approx (x2, \text{shr } s2)$  thr  $s2 \ t = \lfloor (x2, \text{no-wait-locks}) \rfloor$  wset  $s2 \ t = \text{None}$ 
  shows  $\tau m \text{Red2 } s2 \ (\text{redT-upd-}\epsilon \ s2 \ t \ x2' \ m2')$ 
using assms by(blast intro: r2.silent-moves-into-RedT- $\tau$ -inv)

lemma red1-rtranc1- $\tau$ -heapD:
   $\llbracket r1.silent-moves \ t \ s1 \ s1'; t \vdash s1 \approx s2 \rrbracket \implies \text{snd } s1' = \text{snd } s1$ 
by(blast intro: r1.red-rtranc1- $\tau$ -heapD-inv)

lemma red2-rtranc1- $\tau$ -heapD:
   $\llbracket r2.silent-moves \ t \ s2 \ s2'; t \vdash s1 \approx s2 \rrbracket \implies \text{snd } s2' = \text{snd } s2$ 
by(blast intro: r2.red-rtranc1- $\tau$ -heapD-inv)

```



**lemma** *mbisim-simulation-silent1*:

**assumes**  $m\tau': r1.mthr.silent-move\ s1\ s1'$  **and** *mbisim*:  $s1 \approx_m s2$

**shows**  $\exists s2'. r2.mthr.silent-moves\ s2\ s2' \wedge s1' \approx_m s2'$

**proof** –

**from**  $m\tau'$  **obtain** *tl1* **where**  $m\tau: m\tau move1\ s1\ tl1\ s1'\ r1.redT\ s1\ tl1\ s1'$  **by** *auto*

**obtain** *ls1 ts1 m1 ws1 is1* **where** [*simp*]:  $s1 = (ls1, (ts1, m1), ws1, is1)$  **by** (*cases s1*) *fastforce*

**obtain** *ls1' ts1' m1' ws1' is1'* **where** [*simp*]:  $s1' = (ls1', (ts1', m1'), ws1', is1')$  **by** (*cases s1'*)

*fastforce*

**obtain** *ls2 ts2 m2 ws2 is2* **where** [*simp*]:  $s2 = (ls2, (ts2, m2), ws2, is2)$  **by** (*cases s2*) *fastforce*

**from**  $m\tau$  **obtain** *t* **where**  $tl1 = (t, \varepsilon)$  **by** (*auto elim!*:  $r1.m\tau move.cases\ dest: r1.silent-tl$ )

**with**  $m\tau$  **have**  $m\tau: m\tau move1\ s1\ (t, \varepsilon)\ s1'$  **and**  $redT1: s1 \rightarrow -1 - t \triangleright \varepsilon \rightarrow s1'$  **by** *simp-all*

**from**  $m\tau$  **obtain**  $x\ x'\ ln'$  **where**  $tst: ts1\ t = \lfloor (x, no-wait-locks) \rfloor$

**and**  $ts't: ts1'\ t = \lfloor (x', ln') \rfloor$  **and**  $\tau: \tau move1\ (x, m1)\ \varepsilon\ (x', m1')$

**by** (*fastforce elim: r1.m\tau move.cases*)

**from** *mbisim* **have** [*simp*]:  $ls2 = ls1\ ws2 = ws1\ is2 = is1$  *finite* (*dom ts1*) **by** (*auto simp add: mbisim-def*)

**from**  $redT1$  **show** *?thesis*

**proof** *cases*

**case** (*redT-normal x1 x1' M'*)

**with**  $tst\ ts't$  **have** [*simp*]:  $x = x1\ x' = x1'$

**and**  $red: t \vdash (x1, m1) \rightarrow -1 - \varepsilon \rightarrow (x1', M')$

**and**  $tst: thr\ s1\ t = \lfloor (x1, no-wait-locks) \rfloor$

**and**  $wst: wset\ s1\ t = None$

**and**  $s1': redT-upd\ s1\ t\ \varepsilon\ x1'\ M'\ s1'$  **by** (*auto*)

**from**  $s1'\ tst$  **have** [*simp*]:  $ls1' = ls1\ ws1' = ws1\ is1' = is1\ M' = m1'\ ts1' = ts1(t \mapsto (x1', no-wait-locks))$

**by** (*auto simp add: redT-updLs-def redT-updLns-def o-def redT-updWs-def elim!: rtrancl3p-cases*)

**from** *mbisim*  $tst$  **obtain**  $x2$  **where**  $tst': ts2\ t = \lfloor (x2, no-wait-locks) \rfloor$

**and**  $bisim: t \vdash (x1, m1) \approx (x2, m2)$  **by** (*auto dest: mbisim-thrD1*)

**from**  $r1.\tau move-heap[OF\ red]\ \tau$  **have** [*simp*]:  $m1 = M'$  **by** *simp*

**from**  $red\ \tau$  **have**  $r1.silent-move\ t\ (x1, m1)\ (x1', M')$  **by** *auto*

**from** *simulation-silent1* [*OF bisim this*]

**obtain**  $x2'\ m2'$  **where**  $red: r2.silent-moves\ t\ (x2, m2)\ (x2', m2')$

**and**  $bisim': t \vdash (x1', m1) \approx (x2', m2')$  **by** *auto*

**from**  $red\ bisim$  **have** [*simp*]:  $m2' = m2$

**by** (*auto dest: red2-rtrancl-\tau-heapD*)

**let**  $?s2' = redT-upd-\varepsilon\ s2\ t\ x2'\ m2'$

**from**  $red\ tst'\ wst\ bisim$  **have**  $\tau mRed2\ s2\ ?s2'$

**by**  $-(rule\ red2-rtrancl-\tau-into-RedT-\tau,\ auto)$

**moreover** **have** *mbisim*  $s1'\ ?s2'$

**proof** (*rule mbisimI*)

**show**  $locks\ s1' = locks\ ?s2'\ wset\ s1' = wset\ ?s2'\ interrupts\ s1' = interrupts\ ?s2'$  **by** *auto*

**next**

**fix**  $t'$

**assume**  $thr\ s1'\ t' = None$

**hence**  $ts1\ t' = None$  **by** (*auto split: if-split-asm*)

**with** *mbisim-thrNone-eq* [*OF mbisim*] **have**  $ts2\ t' = None$  **by** *simp*

**with**  $tst'$  **show**  $thr\ ?s2'\ t' = None$  **by** *auto*

**next**

**fix**  $t'\ X1\ LN$

**assume**  $ts't': thr\ s1'\ t' = \lfloor (X1, LN) \rfloor$

**show**  $\exists x2. thr\ ?s2'\ t' = \lfloor (x2, LN) \rfloor \wedge t' \vdash (X1, shr\ s1') \approx (x2, shr\ ?s2') \wedge (wset\ ?s2'\ t' = None \vee X1 \approx_w x2)$

```

proof(cases  $t' = t$ )
  case True
    note this[simp]
    with  $s1' \text{ } \text{tst} \text{ } ts't'$  have [simp]:  $X1 = x1' \text{ } LN = \text{no-wait-locks}$ 
      by(simp-all)(auto simp add: redT-updLns-def o-def finfun-Diag-const2)
    with  $bisim' \text{ } \text{tst}' \text{ } \text{wst}$  show ?thesis by(auto simp add: redT-updLns-def o-def finfun-Diag-const2)
  next
    case False
    with  $ts't'$  have  $ts1 \text{ } t' = \lfloor (X1, LN) \rfloor$  by auto
    with mbisim obtain  $X2$  where  $ts2 \text{ } t' = \lfloor (X2, LN) \rfloor$   $t' \vdash (X1, m1) \approx (X2, m2)$   $ws1 \text{ } t' =$ 
      None  $\vee X1 \approx_w X2$ 
    by(auto dest: mbisim-thrD1)
    with False show ?thesis by auto
  qed
next
  show finite (dom (thr  $s1'$ )) by simp
next
  from mbisim-wset-thread-ok1[OF mbisim]
  show wset-thread-ok (wset  $s1'$ ) (thr  $s1'$ ) by(auto intro: wset-thread-ok-upd)
qed
ultimately show ?thesis by(auto)
next
  case redT-acquire
  with tst have False by auto
  thus ?thesis ..
qed
qed

```

**lemma** *mbisim-simulation-silent2*:

$\llbracket \text{mbisim } s1 \text{ } s2; r2.mthr.silent-move } s2 \text{ } s2' \rrbracket$   
 $\implies \exists s1'. r1.mthr.silent-moves } s1 \text{ } s1' \wedge \text{mbisim } s1' \text{ } s2'$

**using** *FWdelay-bisimulation-diverge.mbisim-simulation-silent1*[*OF FWdelay-bisimulation-diverge-flip*]  
**unfolding** *flip-simps* .

**lemma** *mbisim-simulation1'*:

**assumes** *mbisim*: *mbisim*  $s1 \text{ } s2$  **and**  $\neg m\tau move1 \text{ } s1 \text{ } tl1 \text{ } s1' \text{ } r1.redT \text{ } s1 \text{ } tl1 \text{ } s1'$   
**shows**  $\exists s2' \text{ } s2'' \text{ } tl2. r2.mthr.silent-moves } s2 \text{ } s2' \wedge r2.redT \text{ } s2' \text{ } tl2 \text{ } s2'' \wedge$   
 $\neg m\tau move2 \text{ } s2' \text{ } tl2 \text{ } s2'' \wedge \text{mbisim } s1' \text{ } s2'' \wedge mta-bisim \text{ } tl1 \text{ } tl2$

**using** *mbisim-simulation1* *assms* .

**lemma** *mbisim-simulation2'*:

$\llbracket \text{mbisim } s1 \text{ } s2; r2.redT \text{ } s2 \text{ } tl2 \text{ } s2'; \neg m\tau move2 \text{ } s2 \text{ } tl2 \text{ } s2' \rrbracket$   
 $\implies \exists s1' \text{ } s1'' \text{ } tl1. r1.mthr.silent-moves } s1 \text{ } s1' \wedge r1.redT \text{ } s1' \text{ } tl1 \text{ } s1'' \wedge \neg m\tau move1 \text{ } s1' \text{ } tl1 \text{ } s1'' \wedge$   
 $\text{mbisim } s1'' \text{ } s2' \wedge mta-bisim \text{ } tl1 \text{ } tl2$

**using** *FWdelay-bisimulation-diverge.mbisim-simulation1'*[*OF FWdelay-bisimulation-diverge-flip*]  
**unfolding** *flip-simps* .

**lemma** *m $\tau$ diverge-simulation1*:

**assumes**  $s1 \approx_m s2$   
**and**  $r1.mthr.\tau \text{diverge } s1$   
**shows**  $r2.mthr.\tau \text{diverge } s2$

**proof** –

**from**  $\langle s1 \approx_m s2 \rangle$  **have** *finite* (*dom* (*thr*  $s1$ ))  
**by**(*rule mbisim-finite1*)+

**from**  $r1.\tau\text{diverge-}\tau\text{mredTD}[OF \langle r1.mthr.\tau\text{diverge } s1 \rangle \text{ this}]$   
**obtain**  $t \ x$  **where**  $\text{thr } s1 \ t = \lfloor (x, \text{no-wait-locks}) \rfloor \ \text{wset } s1 \ t = \text{None}$   $r1.\tau\text{diverge } t \ (x, \text{shr } s1)$  **by** *blast*  
**from**  $\langle s1 \approx_m s2 \rangle \langle \text{thr } s1 \ t = \lfloor (x, \text{no-wait-locks}) \rfloor \rangle$  **obtain**  $x'$   
**where**  $\text{thr } s2 \ t = \lfloor (x', \text{no-wait-locks}) \rfloor \ t \vdash (x, \text{shr } s1) \approx (x', \text{shr } s2)$   
**by**(*auto dest: mbisim-thrD1*)  
**from**  $\langle s1 \approx_m s2 \rangle \langle \text{wset } s1 \ t = \text{None} \rangle$  **have**  $\text{wset } s2 \ t = \text{None}$  **by**(*simp add: mbisim-def*)  
**from**  $\langle t \vdash (x, \text{shr } s1) \approx (x', \text{shr } s2) \rangle \langle r1.\tau\text{diverge } t \ (x, \text{shr } s1) \rangle$   
**have**  $r2.\tau\text{diverge } t \ (x', \text{shr } s2)$  **by**(*simp add:  $\tau\text{diverge-bisim-inv}$* )  
**thus** *?thesis* **using**  $\langle \text{thr } s2 \ t = \lfloor (x', \text{no-wait-locks}) \rfloor \rangle \langle \text{wset } s2 \ t = \text{None} \rangle$   
**by**(*rule  $r2.\tau\text{diverge-into-}\tau\text{mredT}$* )  
**qed**

**lemma**  $\tau\text{diverge-mbisim-inv}$ :

$s1 \approx_m s2 \implies r1.mthr.\tau\text{diverge } s1 \longleftrightarrow r2.mthr.\tau\text{diverge } s2$

**apply**(*rule iffI*)

**apply**(*erule (1)  $m\tau\text{diverge-simulation1}$* )

**by**(*rule  $FW\text{delay-bisimulation-diverge.m}\tau\text{diverge-simulation1}[OF \text{FWdelay-bisimulation-diverge-flip, unfolded flip-simps}]$* )

**lemma**  $\text{mbisim-delay-bisimulation}$ :

$\text{delay-bisimulation-diverge } r1.\text{redT } r2.\text{redT } \text{mbisim } \text{mta-bisim } m\tau\text{move1 } m\tau\text{move2}$

**apply**(*unfold-locales*)

**apply**(*rule  $\text{mbisim-simulation1 mbisim-simulation2 mbisim-simulation-silent1 mbisim-simulation-silent2}$*   
 $\tau\text{diverge-mbisim-inv}$ |*assumption*)**+**

**done**

**theorem**  $\text{mdelay-bisimulation-final-base}$ :

$\text{delay-bisimulation-final-base } r1.\text{redT } r2.\text{redT } \text{mbisim } m\tau\text{move1 } m\tau\text{move2 } r1.m\text{final } r2.m\text{final}$

**apply**(*unfold-locales*)

**apply**(*blast dest:  $m\text{final1-simulation } m\text{final2-simulation}$* )**+**

**done**

**end**

**sublocale**  $FW\text{delay-bisimulation-diverge} < mthr$ :  $\text{delay-bisimulation-diverge } r1.\text{redT } r2.\text{redT } \text{mbisim}$   
 $\text{mta-bisim } m\tau\text{move1 } m\tau\text{move2}$

**by**(*rule  $\text{mbisim-delay-bisimulation}$* )

**sublocale**  $FW\text{delay-bisimulation-diverge} <$

$mthr$ :  $\text{delay-bisimulation-final-base } r1.\text{redT } r2.\text{redT } \text{mbisim } \text{mta-bisim } m\tau\text{move1 } m\tau\text{move2 } r1.m\text{final}$   
 $r2.m\text{final}$

**by**(*rule  $\text{mdelay-bisimulation-final-base}$* )

**context**  $FW\text{delay-bisimulation-diverge}$  **begin**

**lemma**  $\text{mthr-delay-bisimulation-diverge-final}$ :

$\text{delay-bisimulation-diverge-final } r1.\text{redT } r2.\text{redT } \text{mbisim } \text{mta-bisim } m\tau\text{move1 } m\tau\text{move2 } r1.m\text{final}$   
 $r2.m\text{final}$

**by**(*unfold-locales*)

**end**

**sublocale**  $FW\text{delay-bisimulation-diverge} <$

$mthr$ :  $\text{delay-bisimulation-diverge-final } r1.\text{redT } r2.\text{redT } \text{mbisim } \text{mta-bisim } m\tau\text{move1 } m\tau\text{move2 } r1.m\text{final}$

*r2.mfinal*  
**by**(rule *mthr-delay-bisimulation-diverge-final*)

### 1.18.3 Strong bisimulation as corollary

**locale** *FWbisimulation* = *FWbisimulation-base* - - - *r2 convert-RA bisim*  $\lambda x1\ x2$ . *True* +  
*r1: multithreaded final1 r1 convert-RA* +  
*r2: multithreaded final2 r2 convert-RA*  
**for** *r2* :: ('l,'t,'x2,'m2,'w,'o) *semantics* ( $- \vdash - \dashv\dashrightarrow - [50,0,0,50] 80$ )  
**and** *convert-RA* :: 'l *released-locks*  $\Rightarrow$  'o *list*  
**and** *bisim* :: 't  $\Rightarrow$  ('x1  $\times$  'm1, 'x2  $\times$  'm2) *bisim* ( $- \vdash - / \approx - [50, 50, 50] 60$ ) +  
**assumes** *bisimulation-locale: bisimulation* (*r1 t*) (*r2 t*) (*bisim t*) (*ta-bisim bisim*)  
**and** *bisim-final*:  $t \vdash (x1, m1) \approx (x2, m2) \Rightarrow final1\ x1 \longleftrightarrow final2\ x2$   
**and** *bisim-inv-red-other*:  
 $\llbracket t' \vdash (x, m1) \approx (xx, m2); t \vdash (x1, m1) \approx (x2, m2);$   
 $t \vdash (x1, m1) -1-ta1 \rightarrow (x1', m1'); t \vdash (x2, m2) -2-ta2 \rightarrow (x2', m2');$   
 $t \vdash (x1', m1') \approx (x2', m2'); ta-bisim\ bisim\ ta1\ ta2 \rrbracket$   
 $\Rightarrow t' \vdash (x, m1') \approx (xx, m2')$   
**and** *ex-final1-conv-ex-final2*:  
 $(\exists x1. final1\ x1) \longleftrightarrow (\exists x2. final2\ x2)$

**sublocale** *FWbisimulation* < *bisim?*: *bisimulation* *r1 t r2 t bisim t ta-bisim bisim* **for** *t*  
**by**(rule *bisimulation-locale*)

**sublocale** *FWbisimulation* < *bisim-diverge?*:

*FWdelay-bisimulation-diverge final1 r1 final2 r2 convert-RA bisim*  $\lambda x1\ x2$ . *True*  $\lambda s\ ta\ s'$ . *False*  $\lambda s\ ta\ s'$ . *False*

**proof** -

**interpret** *biw*: *bisimulation-into-delay* *r1 t r2 t bisim t ta-bisim bisim*  $\lambda s\ ta\ s'$ . *False*  $\lambda s\ ta\ s'$ . *False*  
**for** *t*

**by**(*unfold-locale*s) *simp*

**show** *FWdelay-bisimulation-diverge final1 r1 final2 r2 bisim* ( $\lambda x1\ x2$ . *True*) ( $\lambda s\ ta\ s'$ . *False*) ( $\lambda s\ ta\ s'$ . *False*)

**proof**(*unfold-locale*s)

**fix** *t' x m1 xx m2 x1 x2 t x1' ta1 x1'' m1' x2' ta2 x2'' m2'*

**assume** *bisim*:  $t' \vdash (x, m1) \approx (xx, m2)$  **and** *bisim12*:  $t \vdash (x1, m1) \approx (x2, m2)$

**and**  $\tau1$ :  $\tau trsys.silent-moves\ (r1\ t)\ (\lambda s\ ta\ s'.\ False)\ (x1, m1)\ (x1', m1)$

**and** *red1*:  $t \vdash (x1', m1) -1-ta1 \rightarrow (x1'', m1')$

**and**  $\tau2$ :  $\tau trsys.silent-moves\ (r2\ t)\ (\lambda s\ ta\ s'.\ False)\ (x2, m2)\ (x2', m2)$

**and** *red2*:  $t \vdash (x2', m2) -2-ta2 \rightarrow (x2'', m2')$

**and** *bisim12'*:  $t \vdash (x1'', m1') \approx (x2'', m2')$  **and** *tasim*:  $ta1 \sim m\ ta2$

**from**  $\tau1\ \tau2$  **have** [*simp*]:  $x1' = x1\ x2' = x2$  **by**(*simp-all add: rtrancpl-False*  $\tau moves-False$ )

**from** *bisim12* *bisim-inv-red-other*[*OF bisim - red1 red2 bisim12' tasim*]

**show**  $t' \vdash (x, m1') \approx (xx, m2')$  **by** *simp*

**next**

**fix** *t x1 m1 x2 m2 ta1 x1' m1'*

**assume**  $t \vdash (x1, m1) \approx (x2, m2)$   $t \vdash (x1, m1) -1-ta1 \rightarrow (x1', m1')$

**from** *simulation1*[*OF this*]

**show**  $\exists ta2\ x2'\ m2'. t \vdash (x2, m2) -2-ta2 \rightarrow (x2', m2') \wedge t \vdash (x1', m1') \approx (x2', m2') \wedge ta1 \sim m\ ta2$

**by** *auto*

**next**

**fix** *t x1 m1 x2 m2 ta2 x2' m2'*

**assume**  $t \vdash (x1, m1) \approx (x2, m2)$   $t \vdash (x2, m2) -2-ta2 \rightarrow (x2', m2')$

```

    from simulation2[OF this]
    show  $\exists ta1\ x1'\ m1'.\ t \vdash (x1, m1) -1 -ta1 \rightarrow (x1', m1') \wedge t \vdash (x1', m1') \approx (x2', m2') \wedge ta1 \sim_m$ 
    ta2
    by auto
  next
    show  $(\exists x1.\ final1\ x1) \longleftrightarrow (\exists x2.\ final2\ x2)$  by (rule ex-final1-conv-ex-final2)
  qed(fastforce simp add: bisim-final)+
qed

```

**context** FWbisimulation **begin**

```

lemma FWbisimulation-flip: FWbisimulation final2 r2 final1 r1 ( $\lambda t.\ flip\ (bisim\ t)$ )
apply (rule FWbisimulation.intro)
  apply (rule r2.multithreaded-axioms)
  apply (rule r1.multithreaded-axioms)
apply (rule FWbisimulation-axioms.intro)
  apply (unfold flip-simps)
  apply (rule bisimulation-axioms)
  apply (erule bisim-final[symmetric])
  apply (erule (5) bisim-inv-red-other)
apply (rule ex-final1-conv-ex-final2[symmetric])
done

```

**end**

**lemma** FWbisimulation-flip-simps [flip-simps]:

$FWbisimulation\ final2\ r2\ final1\ r1\ (\lambda t.\ flip\ (bisim\ t)) = FWbisimulation\ final1\ r1\ final2\ r2\ bisim$   
 by (auto dest: FWbisimulation.FWbisimulation-flip simp only: flip-flip)

**context** FWbisimulation **begin**

The notation for mbisim is lost because *bisim-wait* is instantiated to  $\lambda x1\ x2.\ True$ . This reintroduces the syntax, but it does not work for output mode. This would require a new abbreviation.

**notation** mbisim ( $- \approx_m - [50, 50] 60$ )

**theorem** mbisim-bisimulation:

$bisimulation\ r1.redT\ r2.redT\ mbisim\ mta-bisim$

**proof**

```

  fix s1 s2 tta1 s1'
  assume mbisim:  $s1 \approx_m s2$  and r1.redT s1 tta1 s1'
  from mthr.simulation1[OF this]
  show  $\exists s2'\ tta2.\ r2.redT\ s2\ tta2\ s2' \wedge s1' \approx_m s2' \wedge tta1 \sim_T tta2$ 
    by (auto simp add:  $\tau moves-False\ m\tau move-False$ )
  next
    fix s2 s1 tta2 s2'
    assume s1  $\approx_m s2$  and r2.redT s2 tta2 s2'
    from mthr.simulation2[OF this]
    show  $\exists s1'\ tta1.\ r1.redT\ s1\ tta1\ s1' \wedge s1' \approx_m s2' \wedge tta1 \sim_T tta2$ 
      by (auto simp add:  $\tau moves-False\ m\tau move-False$ )
  qed

```

**lemma** mbisim-wset-eq:

$s1 \approx_m s2 \implies wset\ s1 = wset\ s2$

**by**(*simp add: mbisim-def*)

**lemma** *mbisim-mfinal*:

$s1 \approx_m s2 \implies r1.mfinal\ s1 \longleftrightarrow r2.mfinal\ s2$

**apply**(*auto intro!: r2.mfinalI r1.mfinalI dest: mbisim-thrD2 mbisim-thrD1 bisim-final elim: r1.mfinalE r2.mfinalE*)

**apply**(*frule (1) mbisim-thrD2, drule mbisim-wset-eq, auto elim: r1.mfinalE*)

**apply**(*frule (1) mbisim-thrD1, drule mbisim-wset-eq, auto elim: r2.mfinalE*)

**done**

**end**

**sublocale** *FWbisimulation* < *mthr: bisimulation* *r1.redT r2.redT mbisim mta-bisim*

**by**(*rule mbisim-bisimulation*)

**sublocale** *FWbisimulation* < *mthr: bisimulation-final* *r1.redT r2.redT mbisim mta-bisim r1.mfinal r2.mfinal*

**by**(*unfold-locales*)(*rule mbisim-mfinal*)

**end**

## 1.19 Preservation of deadlock across bisimulations

**theory** *FWBisimDeadlock*

**imports**

*FWBisimulation*

*FWDeadlock*

**begin**

**context** *FWdelay-bisimulation-obs* **begin**

**lemma** *actions-ok1-ex-actions-ok2*:

**assumes** *r1.actions-ok s1 t ta1*

**and** *ta1 ~<sub>m</sub> ta2*

**obtains** *s2* **where** *r2.actions-ok s2 t ta2*

**proof** –

**let** *?s2* = (*locks s1, (λt. map-option (λ(x1, ln). (SOME x2. if final1 x1 then final2 x2 else ¬ final2 x2, ln)) (thr s1 t), undefined), wset s1, interrupts s1*)

**from** *⟨ta1 ~<sub>m</sub> ta2⟩* **have**  $\llbracket ta1 \rrbracket_c = \llbracket ta2 \rrbracket_c$  **by**(*simp add: ta-bisim-def*)

**with** *⟨r1.actions-ok s1 t ta1⟩* **have** *cao1: r1.cond-action-oks s1 t ⟦ta2⟧<sub>c</sub>* **by** *auto*

**have** *r2.cond-action-oks ?s2 t ⟦ta2⟧<sub>c</sub>* **unfolding** *r2.cond-action-oks-conv-set*

**proof**

**fix** *ct*

**assume** *ct ∈ set ⟦ta2⟧<sub>c</sub>*

**with** *cao1* **have** *r1.cond-action-ok s1 t ct*

**unfolding** *r1.cond-action-oks-conv-set* **by** *auto*

**thus** *r2.cond-action-ok ?s2 t ct* **using** *ex-final1-conv-ex-final2*

**by**(*cases ct*)(*fastforce intro: someI-ex[where P=final2]*)**+**

**qed**

**hence** *r2.actions-ok ?s2 t ta2*

**using** *assms* **by**(*auto simp add: ta-bisim-def split del: if-split elim: rev-iffD1[OF - thread-oks-bisim-inv]*)

**thus** *thesis* **by**(*rule that*)

**qed**

**lemma** *actions-ok2-ex-actions-ok1*:  
**assumes**  $r2.actions\text{-}ok\ s2\ t\ ta2$   
**and**  $ta1 \sim_m ta2$   
**obtains**  $s1$  **where**  $r1.actions\text{-}ok\ s1\ t\ ta1$   
**using** *FWdelay-bisimulation-obs.actions-ok1-ex-actions-ok2* [*OF FWdelay-bisimulation-obs-flip*] *assms*  
**unfolding** *flip-simps* .

**lemma** *ex-actions-ok1-conv-ex-actions-ok2*:  
 $ta1 \sim_m ta2 \implies (\exists s1. r1.actions\text{-}ok\ s1\ t\ ta1) \longleftrightarrow (\exists s2. r2.actions\text{-}ok\ s2\ t\ ta2)$   
**by**(*metis actions-ok1-ex-actions-ok2 actions-ok2-ex-actions-ok1*)

**end**

**context** *FWdelay-bisimulation-diverge* **begin**

**lemma** *no- $\tau$ Move1- $\tau$ s-to-no- $\tau$ Move2*:  
**fixes** *no- $\tau$ moves1 no- $\tau$ moves2*  
**defines**  $no\text{-}\tau moves1 \equiv \lambda s1\ t. wset\ s1\ t = None \wedge (\exists x. thr\ s1\ t = \lfloor (x, no\text{-}wait\text{-}locks) \rfloor) \wedge (\forall x' m'. \neg r1.silent\text{-}move\ t\ (x, shr\ s1)\ (x', m'))$   
**defines**  $no\text{-}\tau moves2 \equiv \lambda s2\ t. wset\ s2\ t = None \wedge (\exists x. thr\ s2\ t = \lfloor (x, no\text{-}wait\text{-}locks) \rfloor) \wedge (\forall x' m'. \neg r2.silent\text{-}move\ t\ (x, shr\ s2)\ (x', m'))$   
**assumes** *mbisim*:  $s1 \approx_m (ls2, (ts2, m2), ws2, is2)$

**shows**  $\exists ts2'. r2.mthr.silent\text{-}moves\ (ls2, (ts2, m2), ws2, is2)\ (ls2, (ts2', m2), ws2, is2) \wedge$   
 $(\forall t. no\text{-}\tau moves1\ s1\ t \longrightarrow no\text{-}\tau moves2\ (ls2, (ts2', m2), ws2, is2)\ t) \wedge s1 \approx_m (ls2, (ts2', m2), ws2, is2)$

**proof** –

**from** *mbisim* **have**  $finite\ (dom\ (thr\ s1))$  **by**(*simp add: mbisim-def*)  
**hence**  $finite\ \{t. no\text{-}\tau moves1\ s1\ t\}$  **unfolding** *no- $\tau$ moves1-def*  
**by**–(*rule finite-subset, auto*)  
**thus** *?thesis* **using**  $\langle s1 \approx_m (ls2, (ts2, m2), ws2, is2) \rangle$   
**proof**(*induct A*  $\equiv \{t. no\text{-}\tau moves1\ s1\ t\}$  *arbitrary: s1 ts2 rule: finite-induct*)  
**case** *empty*  
**from**  $\langle \{\} = \{t. no\text{-}\tau moves1\ s1\ t\} \rangle$  [*symmetric*] **have**  $no\text{-}\tau moves1\ s1 = (\lambda t. False)$   
**by**(*auto intro: ext*)  
**thus** *?case* **using**  $\langle s1 \approx_m (ls2, (ts2, m2), ws2, is2) \rangle$  **by** *auto*

**next**

**case** (*insert t A*)  
**note**  $mbisim = \langle s1 \approx_m (ls2, (ts2, m2), ws2, is2) \rangle$   
**from**  $\langle insert\ t\ A = \{t. no\text{-}\tau moves1\ s1\ t\} \rangle$   
**have**  $no\text{-}\tau moves1\ s1\ t$  **by** *auto*  
**then obtain**  $x1$  **where**  $ts1t: thr\ s1\ t = \lfloor (x1, no\text{-}wait\text{-}locks) \rfloor$   
**and**  $ws1t: wset\ s1\ t = None$   
**and**  $\tau 1: \bigwedge x1m1'. \neg r1.silent\text{-}move\ t\ (x1, shr\ s1)\ x1m1'$   
**by**(*auto simp add: no- $\tau$ moves1-def*)

**from**  $ts1t\ mbisim$  **obtain**  $x2$  **where**  $ts2t: ts2\ t = \lfloor (x2, no\text{-}wait\text{-}locks) \rfloor$   
**and**  $t \vdash (x1, shr\ s1) \approx (x2, m2)$  **by**(*auto dest: mbisim-thrD1*)  
**from**  $mbisim\ ws1t$  **have**  $ws2\ t = None$  **by**(*simp add: mbisim-def*)

**let**  $?s1 = (locks\ s1, ((thr\ s1)(t := None), shr\ s1), wset\ s1, interrupts\ s1)$   
**let**  $?s2 = (ls2, (ts2(t := None), m2), ws2, is2)$   
**from**  $\langle insert\ t\ A = \{t. no\text{-}\tau moves1\ s1\ t\} \rangle$   $\langle t \notin A \rangle$

```

have A: A = {t. no-τmoves1 ?s1 t} by(auto simp add: no-τmoves1-def)
have ?s1 ≈m ?s2
proof(rule mbisimI)
  from mbisim
  show finite (dom (thr ?s1)) locks ?s1 = locks ?s2 wset ?s1 = wset ?s2 interrupts ?s1 = interrupts
    ?s2
    by(simp-all add: mbisim-def)
next
  from mbisim-wset-thread-ok1[OF mbisim] ws1t show wset-thread-ok (wset ?s1) (thr ?s1)
    by(auto intro!: wset-thread-okI dest: wset-thread-okD split: if-split-asm)
next
  fix t'
  assume thr ?s1 t' = None
  with mbisim-thrNone-eq[OF mbisim, of t']
  show thr ?s2 t' = None by auto
next
  fix t' x1 ln
  assume thr ?s1 t' = [(x1, ln)]
  hence thr s1 t' = [(x1, ln)] t' ≠ t by(auto split: if-split-asm)
  with mbisim-thrD1[OF mbisim ⟨thr s1 t' = [(x1, ln)]⟩] mbisim
  show ∃ x2. thr ?s2 t' = [(x2, ln)] ∧ t' ⊢ (x1, shr ?s1) ≈ (x2, shr ?s2) ∧ (wset ?s2 t' = None
    ∨ x1 ≈w x2)
    by(auto simp add: mbisim-def)
qed
with A have ∃ ts2'. r2.mthr.silent-moves ?s2 (ls2, (ts2', m2), ws2, is2) ∧ (∀ t. no-τmoves1 ?s1 t
  → no-τmoves2 (ls2, (ts2', m2), ws2, is2) t) ∧ ?s1 ≈m (ls2, (ts2', m2), ws2, is2) by(rule insert)
then obtain ts2' where r2.mthr.silent-moves ?s2 (ls2, (ts2', m2), ws2, is2)
  and no-τ: ∧t. no-τmoves1 ?s1 t ⇒ no-τmoves2 (ls2, (ts2', m2), ws2, is2) t
  and ?s1 ≈m (ls2, (ts2', m2), ws2, is2) by auto
let ?s2' = (ls2, (ts2'(t ↦ (x2, no-wait-locks)), m2), ws2, is2)
from ts2t have ts2(t ↦ (x2, no-wait-locks)) = ts2 by(auto intro: ext)
with r2.τmRedT-add-thread-inv[OF ⟨r2.mthr.silent-moves ?s2 (ls2, (ts2', m2), ws2, is2)⟩, of t
  (x2, no-wait-locks)]
have r2.mthr.silent-moves (ls2, (ts2, m2), ws2, is2) ?s2' by simp
from no-τmove1-τs-to-no-τmove2[OF ⟨t ⊢ (x1, shr s1) ≈ (x2, m2)⟩ τ1]
obtain x2' m2' where r2.silent-moves t (x2, m2) (x2', m2')
  and ∧x2'' m2''. ¬ r2.silent-move t (x2', m2') (x2'', m2'')
  and t ⊢ (x1, shr s1) ≈ (x2', m2') by auto
let ?s2'' = (ls2, (ts2'(t ↦ (x2', no-wait-locks)), m2'), ws2, is2)
from red2-rtrancl-τ-heapD[OF ⟨r2.silent-moves t (x2, m2) (x2', m2')⟩ ⟨t ⊢ (x1, shr s1) ≈ (x2,
  m2)⟩]
have m2' = m2 by simp
with ⟨r2.silent-moves t (x2, m2) (x2', m2')⟩ have r2.silent-moves t (x2, shr ?s2') (x2', m2) by
  simp
hence r2.mthr.silent-moves ?s2' (redT-upd-ε ?s2' t x2' m2)
  by(rule red2-rtrancl-τ-into-RedT-τ)(auto simp add: ⟨ws2 t = None⟩ intro: ⟨t ⊢ (x1, shr s1) ≈
    (x2, m2)⟩)
also have redT-upd-ε ?s2' t x2' m2 = ?s2'' using ⟨m2' = m2⟩
  by(auto simp add: fun-eq-iff redT-updLns-def finfun-Diag-const2 o-def)
finally (back-subst) have r2.mthr.silent-moves (ls2, (ts2, m2), ws2, is2) ?s2''
  using ⟨r2.mthr.silent-moves (ls2, (ts2, m2), ws2, is2) ?s2'⟩ by-(rule rtranclp-trans)
moreover {
  fix t'
  assume no-τ1: no-τmoves1 s1 t'

```



```

have no- $\tau$ moves2 ?s2'' t'
proof(cases t' = t)
  case True thus ?thesis
    using  $\langle ws2\ t = None \rangle \langle \bigwedge x2''\ m2''. \neg r2.silent-move\ t\ (x2',\ m2')\ (x2'',\ m2'') \rangle$  by(simp add:
no- $\tau$ moves2-def)
  next
    case False
    with no- $\tau$ 1 have no- $\tau$ moves1 ?s1 t' by(simp add: no- $\tau$ moves1-def)
    hence no- $\tau$ moves2 (ls2, (ts2', m2), ws2, is2) t'
      by(rule  $\langle no- $\tau$ moves1\ ?s1\ t' \implies no- $\tau$ moves2\ (ls2,\ (ts2',\ m2),\ ws2,\ is2)\ t' \rangle$ )
    with False  $\langle m2' = m2 \rangle$  show ?thesis by(simp add: no- $\tau$ moves2-def)
  qed }
moreover have s1  $\approx_m$  ?s2''
proof(rule mbisimI)
  from mbisim
  show finite (dom (thr s1)) locks s1 = locks ?s2'' wset s1 = wset ?s2'' interrupts s1 = interrupts
?s2''
    by(simp-all add: mbisim-def)
  next
    from mbisim show wset-thread-ok (wset s1) (thr s1) by(rule mbisim-wset-thread-ok1)
  next
    fix t'
    assume thr s1 t' = None
    hence thr ?s1 t' = None t'  $\neq$  t using ts1t by auto
    with mbisim-thrNone-eq[OF  $\langle ?s1 \approx_m (ls2, (ts2', m2), ws2, is2) \rangle$ , of t']
    show thr ?s2'' t' = None by simp
  next
    fix t' x1' ln'
    assume thr s1 t' =  $\lfloor (x1', ln') \rfloor$ 
    show  $\exists x2. thr\ ?s2''\ t' = \lfloor (x2, ln') \rfloor \wedge t' \vdash (x1', shr\ s1) \approx (x2, shr\ ?s2'') \wedge (wset\ ?s2''\ t' =$ 
None  $\vee x1' \approx_w x2)$ 
    proof(cases t = t')
      case True
      with  $\langle thr\ s1\ t' = \lfloor (x1', ln') \rfloor \rangle$  ts1t  $\langle t \vdash (x1, shr\ s1) \approx (x2', m2') \rangle \langle m2' = m2 \rangle \langle ws2\ t = None \rangle$ 
      show ?thesis by auto
    next
      case False
      with mbisim-thrD1[OF  $\langle ?s1 \approx_m (ls2, (ts2', m2), ws2, is2) \rangle$ , of t' x1' ln']  $\langle thr\ s1\ t' = \lfloor (x1',$ 
ln')  $\rangle \langle m2' = m2 \rangle$  mbisim
      show ?thesis by(auto simp add: mbisim-def)
    qed
  qed
  ultimately show ?case unfolding  $\langle m2' = m2 \rangle$  by blast
qed
qed

```

lemma no- $\tau$ Move2- $\tau$ s-to-no- $\tau$ Move1:

```

fixes no- $\tau$ moves1 no- $\tau$ moves2
defines no- $\tau$ moves1  $\equiv \lambda s1\ t. wset\ s1\ t = None \wedge (\exists x. thr\ s1\ t = \lfloor (x, no-wait-locks) \rfloor) \wedge (\forall x'\ m'. \neg$ 
 $r1.silent-move\ t\ (x, shr\ s1)\ (x', m'))$ 
defines no- $\tau$ moves2  $\equiv \lambda s2\ t. wset\ s2\ t = None \wedge (\exists x. thr\ s2\ t = \lfloor (x, no-wait-locks) \rfloor) \wedge (\forall x'\ m'. \neg$ 
 $r2.silent-move\ t\ (x, shr\ s2)\ (x', m'))$ 
assumes (ls1, (ts1, m1), ws1, is1)  $\approx_m$  s2

```

**shows**  $\exists ts1'. r1.mthr.silent-moves (ls1, (ts1, m1), ws1, is1) (ls1, (ts1', m1), ws1, is1) \wedge$   
 $(\forall t. no-\tau moves2 s2 t \longrightarrow no-\tau moves1 (ls1, (ts1', m1), ws1, is1) t) \wedge (ls1, (ts1', m1),$   
 $ws1, is1) \approx_m s2$   
**using** *assms FWdelay-bisimulation-diverge.no- $\tau$ Move1- $\tau$ s-to-no- $\tau$ Move2[OF FWdelay-bisimulation-diverge-flip]*  
**unfolding** *flip-simps* **by** *blast*

**lemma** *deadlock-mbisim-not-final-thread-pres:*

**assumes** *dead*:  $t \in r1.deadlocked s1 \vee r1.deadlock s1$

**and** *nfin*:  $r1.not-final-thread s1 t$

**and** *fin*:  $r1.final-thread s1 t \implies r2.final-thread s2 t$

**and** *mbisim*:  $s1 \approx_m s2$

**shows**  $r2.not-final-thread s2 t$

**proof** –

**from** *nfin* **obtain**  $x1\ ln$  **where**  $thr\ s1\ t = \lfloor (x1, ln) \rfloor$  **by** *cases auto*

**with** *mbisim* **obtain**  $x2$  **where**  $thr\ s2\ t = \lfloor (x2, ln) \rfloor$   $t \vdash (x1, shr\ s1) \approx (x2, shr\ s2)$   $wset\ s1\ t =$   
 $None \vee x1 \approx_w x2$

**by**(*auto dest: mbisim-thrD1*)

**show**  $r2.not-final-thread s2 t$

**proof**(*cases wset s1 t = None  $\wedge$  ln = no-wait-locks*)

**case** *False*

**with**  $\langle r1.not-final-thread s1 t \rangle \langle thr\ s1\ t = \lfloor (x1, ln) \rfloor \rangle \langle thr\ s2\ t = \lfloor (x2, ln) \rfloor \rangle$  *mbisim*

**show** *?thesis* **by** *cases(auto simp add: mbisim-def r2.not-final-thread-iff)*

**next**

**case** *True*

**with**  $\langle r1.not-final-thread s1 t \rangle \langle thr\ s1\ t = \lfloor (x1, ln) \rfloor \rangle$  **have**  $\neg final1\ x1$  **by**(*cases*) *auto*

**have**  $\neg final2\ x2$

**proof**

**assume** *final2 x2*

**with** *final2-simulation[OF  $\langle t \vdash (x1, shr\ s1) \approx (x2, shr\ s2) \rangle$ ]*

**obtain**  $x1'\ m1'$  **where**  $r1.silent-moves\ t\ (x1, shr\ s1)\ (x1', m1')\ t \vdash (x1', m1') \approx (x2, shr\ s2)$

*final1 x1'* **by** *auto*

**from**  $\langle r1.silent-moves\ t\ (x1, shr\ s1)\ (x1', m1') \rangle$  **have**  $x1' = x1$

**proof**(*cases rule: converse-rtrancplE2[consumes 1, case-names refl step]*)

**case** (*step x1'' m1''*)

**from**  $\langle r1.silent-move\ t\ (x1, shr\ s1)\ (x1'', m1'') \rangle$

**have**  $t \vdash (x1, shr\ s1) -1-\varepsilon \rightarrow (x1'', m1'')$  **by**(*auto dest: r1.silent-tl*)

**hence**  $r1.redT\ s1\ (t, \varepsilon)\ (redT-upd-\varepsilon\ s1\ t\ x1''\ m1'')$

**using**  $\langle thr\ s1\ t = \lfloor (x1, ln) \rfloor \rangle$  *True*

**by**  $-(erule\ r1.redT-normal, auto\ simp\ add: redT-updLns-def\ finfun-Diag-const2\ o-def\ redT-updWs-def)$

**hence** *False* **using** *dead* **by**(*auto intro: r1.deadlock-no-red r1.red-no-deadlock*)

**thus** *?thesis ..*

**qed** *simp*

**with**  $\langle \neg final1\ x1 \rangle \langle final1\ x1' \rangle$  **show** *False* **by** *simp*

**qed**

**thus** *?thesis* **using**  $\langle thr\ s2\ t = \lfloor (x2, ln) \rfloor \rangle$  **by**(*auto simp add: r2.not-final-thread-iff*)

**qed**

**qed**

**lemma** *deadlocked1-imp- $\tau$ s-deadlocked2:*

**assumes** *mbisim*:  $s1 \approx_m s2$

**and** *dead*:  $t \in r1.deadlocked s1$

**shows**  $\exists s2'. r2.mthr.silent-moves\ s2\ s2' \wedge t \in r2.deadlocked\ s2' \wedge s1 \approx_m s2'$

**proof** –

**from** *mfinal1-inv-simulation*[*OF mbisim*]  
**obtain** *ls2 ts2 m2 ws2 is2* **where** *red1*: *r2.mthr.silent-moves s2 (ls2, (ts2, m2), ws2, is2)*  
**and** *s1*  $\approx_m$  (*ls2, (ts2, m2), ws2, is2*) **and** *m2* = *shr s2*  
**and** *fin*:  $\bigwedge t. r1.final-thread\ s1\ t \implies r2.final-thread\ (ls2, (ts2, m2), ws2, is2)\ t$  **by** *fastforce*  
**from** *no- $\tau$ Move1- $\tau$ s-to-no- $\tau$ Move2*[*OF*  $\langle s1 \approx_m (ls2, (ts2, m2), ws2, is2) \rangle$ ]  
**obtain** *ts2'* **where** *red2*: *r2.mthr.silent-moves (ls2, (ts2, m2), ws2, is2) (ls2, (ts2', m2), ws2, is2)*  
**and** *no- $\tau$* :  $\bigwedge t\ x1\ x2\ x2'\ m2'. \llbracket wset\ s1\ t = None; thr\ s1\ t = \lfloor (x1, no-wait-locks) \rfloor; ts2'\ t = \lfloor (x2,$   
*no-wait-locks)* $\rfloor$ ;  

$$\bigwedge x'\ m'. r1.silent-move\ t\ (x1, shr\ s1)\ (x', m') \implies False \rrbracket$$
  

$$\implies \neg r2.silent-move\ t\ (x2, m2)\ (x2', m2')$$
  
**and** *mbisim*: *s1*  $\approx_m$  (*ls2, (ts2', m2), ws2, is2*) **by** *fastforce*  
**from** *mbisim* **have** *mbisim-egs*: *ls2* = *locks s1* *ws2* = *wset s1 is2* = *interrupts s1*  
**by**(*simp-all add: mbisim-def*)  
**let** *?s2* = (*ls2, (ts2', m2), ws2, is2*)  
**from** *red2* **have** *fin'*:  $\bigwedge t. r1.final-thread\ s1\ t \implies r2.final-thread\ ?s2\ t$   
**by**(*rule r2. $\tau$ mRedT-preserves-final-thread*)(*rule fin*)  
**from** *dead*  
**have** *t*  $\in$  *r2.deadlocked ?s2*  
**proof**(*coinduct*)  
**case** (*deadlocked t*)  
**thus** *?case*  
**proof**(*cases rule: r1.deadlocked-elim*s)  
**case** (*lock x1*)  
**hence** *csmw*:  $\bigwedge LT. r1.can-sync\ t\ x1\ (shr\ s1)\ LT \implies$   
 $\exists lt \in LT. r1.must-wait\ s1\ t\ lt\ (r1.deadlocked\ s1 \cup r1.final-threads\ s1)$   
**by** *blast*  
**from**  $\langle thr\ s1\ t = \lfloor (x1, no-wait-locks) \rfloor \rangle$  *mbisim* **obtain** *x2*  
**where** *ts2'*  $t = \lfloor (x2, no-wait-locks) \rfloor$  **and** *bisim*:  $t \vdash (x1, shr\ s1) \approx (x2, m2)$   
**by**(*auto dest: mbisim-thrD1*)  
**note**  $\langle ts2'\ t = \lfloor (x2, no-wait-locks) \rfloor \rangle$  **moreover**  
**{ from**  $\langle r1.must-sync\ t\ x1\ (shr\ s1) \rangle$  **obtain** *ta1 x1' m1'*  
**where** *r1*:  $t \vdash (x1, shr\ s1) -1-ta1 \rightarrow (x1', m1')$   
**and** *s1'*:  $\exists s1'. r1.actions-ok\ s1'\ t\ ta1$  **by**(*fastforce elim: r1.must-syncE*)  
**have**  $\neg \tau move1\ (x1, shr\ s1)\ ta1\ (x1', m1')$  (**is**  $\neg ?\tau$ )  
**proof**  
**assume** *? $\tau$*   
**hence** *ta1* =  $\varepsilon$  **by**(*rule r1.silent-tl*)  
**with** *r1* **have** *r1.can-sync t x1 (shr s1)* {}  
**by**(*auto intro!: r1.can-syncI simp add: collect-locks-def collect-interrupts-def*)  
**from** *csmw*[*OF this*] **show** *False* **by** *blast*  
**qed**  
**from** *simulation1*[*OF bisim r1 this*]  
**obtain** *x2' m2' x2'' m2'' ta2* **where** *r2*: *r2.silent-moves t (x2, m2) (x2', m2')*  
**and** *r2'*:  $t \vdash (x2', m2') -2-ta2 \rightarrow (x2'', m2'')$   
**and**  $\tau2$ :  $\neg \tau move2\ (x2', m2')\ ta2\ (x2'', m2'')$   
**and** *bisim'*:  $t \vdash (x1', m1') \approx (x2'', m2'')$  **and** *tasim*: *ta1*  $\sim_m$  *ta2* **by** *auto*  
**from** *r2*  
**have**  $\exists ta2\ x2'\ m2'\ s2'. t \vdash (x2, m2) -2-ta2 \rightarrow (x2', m2') \wedge r2.actions-ok\ s2'\ t\ ta2$   
**proof**(*cases rule: converse-rtranclpE2*[*consumes 1, case-names base step*])  
**case** *base*  
**from** *r2'*[*folded base*] *s1'*[*unfolded ex-actions-ok1-conv-ex-actions-ok2*[*OF tasim*]]  
**show** *?thesis* **by** *blast*  
**next**  
**case** (*step x2''' m2'''*)

hence  $t \vdash (x2, m2) -2-\varepsilon \rightarrow (x2''', m2''')$  **by**(*auto dest: r2.silent-tl*)  
 moreover **have**  $r2.actions-ok$  (*undefined*, (*undefined*, *undefined*), *Map.empty*, *undefined*)  $t \varepsilon$   
**by** *auto*  
 ultimately **show** *?thesis* **by**-(*rule exI conjI|assumption*)+  
 qed  
 hence  $r2.must-sync\ t\ x2\ m2$  **unfolding**  $r2.must-sync-def2$  . }  
**moreover**  
 { **fix**  $LT$   
**assume**  $r2.can-sync\ t\ x2\ m2\ LT$   
**then obtain**  $ta2\ x2'\ m2'$  **where**  $r2: t \vdash (x2, m2) -2-ta2 \rightarrow (x2', m2')$   
**and**  $LT: LT = collect-locks\ \{\!\{ta2\}\!\}_l <+> collect-cond-actions\ \{\!\{ta2\}\!\}_c <+> collect-interrupts\ \{\!\{ta2\}\!\}_i$   
**by**(*auto elim: r2.can-syncE*)  
**from**  $\langle wset\ s1\ t = None \rangle \langle thr\ s1\ t = \lfloor (x1, no-wait-locks) \rfloor \rangle \langle ts2'\ t = \lfloor (x2, no-wait-locks) \rfloor \rangle$   
**have**  $\neg r2.silent-move\ t\ (x2, m2)\ (x2', m2')$   
**proof**(*rule no- $\tau$* )  
**fix**  $x1'\ m1'$   
**assume**  $r1.silent-move\ t\ (x1, shr\ s1)\ (x1', m1')$   
**hence**  $t \vdash (x1, shr\ s1) -1-\varepsilon \rightarrow (x1', m1')$  **by**(*auto dest: r1.silent-tl*)  
**hence**  $r1.can-sync\ t\ x1\ (shr\ s1)\ \{\}$   
**by**(*auto intro: r1.can-syncI simp add: collect-locks-def collect-interrupts-def*)  
**with** *csmw[OF this]* **show** *False* **by** *blast*  
 qed  
**with**  $r2$  **have**  $\neg \tau move2\ (x2, m2)\ ta2\ (x2', m2')$  **by** *auto*  
**from** *simulation2[OF bisim r2 this]* **obtain**  $x1'\ m1'\ x1''\ m1''\ ta1$   
**where**  $\tau r1: r1.silent-moves\ t\ (x1, shr\ s1)\ (x1', m1')$   
**and**  $r1: t \vdash (x1', m1') -1-ta1 \rightarrow (x1'', m1'')$   
**and**  $\neg \tau move1\ (x1', m1')\ ta1\ (x1'', m1'')$   
**and**  $bisim': t \vdash (x1'', m1'') \approx (x2', m2')$   
**and**  $tlsim: ta1 \sim_m ta2$  **by** *auto*  
**from**  $\tau r1$  **obtain** [*simp*]:  $x1' = x1\ m1' = shr\ s1$   
**proof**(*cases rule: converse-rtranclpE2[consumes 1, case-names refl step]*)  
**case** (*step X M*)  
**from**  $\langle r1.silent-move\ t\ (x1, shr\ s1)\ (X, M) \rangle$   
**have**  $t \vdash (x1, shr\ s1) -1-\varepsilon \rightarrow (X, M)$  **by**(*auto dest: r1.silent-tl*)  
**hence**  $r1.can-sync\ t\ x1\ (shr\ s1)\ \{\}$   
**by**(*auto intro: r1.can-syncI simp add: collect-locks-def collect-interrupts-def*)  
**with** *csmw[OF this]* **have** *False* **by** *blast*  
**thus** *?thesis ..*  
 qed *blast*  
**from**  $tlsim\ LT$  **have**  $LT = collect-locks\ \{\!\{ta1\}\!\}_l <+> collect-cond-actions\ \{\!\{ta1\}\!\}_c <+> collect-interrupts\ \{\!\{ta1\}\!\}_i$   
**by**(*auto simp add: ta-bisim-def*)  
**with**  $r1$  **have**  $r1.can-sync\ t\ x1\ (shr\ s1)\ LT$  **by**(*auto intro: r1.can-syncI*)  
**from** *csmw[OF this]* **obtain**  $lt$   
**where**  $lt: lt \in LT$  **and**  $mw: r1.must-wait\ s1\ t\ lt\ (r1.deadlocked\ s1 \cup r1.final-threads\ s1)$  **by** *blast*  
**have**  $subset: r1.deadlocked\ s1 \cup r1.final-threads\ s1 \subseteq r1.deadlocked\ s1 \cup r2.deadlocked\ s2 \cup r2.final-threads\ s2$   
**by**(*auto dest: fin'*)  
**from**  $mw$  **have**  $r2.must-wait\ s2\ t\ lt\ (r1.deadlocked\ s1 \cup r2.deadlocked\ s2 \cup r2.final-threads\ s2)$   
**proof**(*cases rule: r1.must-wait-elim*)  
**case** *lock* **thus** *?thesis* **by**(*auto simp add: mbisim-egs dest!: fin'*)

```

next
  case (join t')
  from ⟨r1.not-final-thread s1 t'⟩ obtain x1 ln
    where thr s1 t' = [(x1, ln)] by cases auto
    with mbisim obtain x2 where ts2' t' = [(x2, ln)] t' ⊢ (x1, shr s1) ≈ (x2, m2) by(auto
dest: mbisim-thrD1)
  show ?thesis
  proof(cases wset s1 t' = None ∧ ln = no-wait-locks)
    case False
    with ⟨r1.not-final-thread s1 t'⟩ ⟨thr s1 t' = [(x1, ln)]⟩ ⟨ts2' t' = [(x2, ln)]⟩ ⟨lt = Inr (Inl
t')⟩ join
    show ?thesis by(auto simp add: mbisim-eqs r2.not-final-thread-iff r1.final-thread-def)
  next
  case True
  with ⟨r1.not-final-thread s1 t'⟩ ⟨thr s1 t' = [(x1, ln)]⟩ have ¬ final1 x1 by(cases) auto
    with join ⟨thr s1 t' = [(x1, ln)]⟩ have t' ∈ r1.deadlocked s1 by(auto simp add:
r1.final-thread-def)
  have ¬ final2 x2
  proof
    assume final2 x2
    with final2-simulation[OF ⟨t' ⊢ (x1, shr s1) ≈ (x2, m2)⟩]
    obtain x1' m1' where r1.silent-moves t' (x1, shr s1) (x1', m1')
      and t' ⊢ (x1', m1') ≈ (x2, m2) final1 x1' by auto
    from ⟨r1.silent-moves t' (x1, shr s1) (x1', m1')⟩ have x1' = x1
    proof(cases rule: converse-rtrancpE2[consumes 1, case-names refl step])
      case (step x1'' m1'')
      from ⟨r1.silent-move t' (x1, shr s1) (x1'', m1'')⟩
      have t' ⊢ (x1, shr s1) -1-ε→ (x1'', m1'') by(auto dest: r1.silent-tl)
      hence r1.redT s1 (t', ε) (redT-upd-ε s1 t' x1'' m1'')
      using ⟨thr s1 t' = [(x1, ln)]⟩ True
      by -(erule r1.redT-normal, auto simp add: redT-updLns-def redT-updWs-def fin-
fun-Diag-const2 o-def)
    hence False using ⟨t' ∈ r1.deadlocked s1⟩ by(rule r1.red-no-deadlock)
    thus ?thesis ..
  qed simp
  with ⟨¬ final1 x1⟩ ⟨final1 x1'⟩ show False by simp
  qed
  thus ?thesis using ⟨ts2' t' = [(x2, ln)]⟩ join
    by(auto simp add: r2.not-final-thread-iff r1.final-thread-def)
  qed
next
  case (interrupt t')
  have r2.all-final-except ?s2 (r1.deadlocked s1 ∪ r2.deadlocked ?s2 ∪ r2.final-threads ?s2)
  proof(rule r2.all-final-exceptI)
    fix t''
    assume r2.not-final-thread ?s2 t''
    then obtain x2 ln where thr ?s2 t'' = [(x2, ln)]
      and fin: ¬ final2 x2 ∨ ln ≠ no-wait-locks ∨ wset ?s2 t'' ≠ None
      by(auto simp add: r2.not-final-thread-iff)
    from ⟨thr ?s2 t'' = [(x2, ln)]⟩ mbisim
    obtain x1 where ts1t'': thr s1 t'' = [(x1, ln)]
      and bisim'': t'' ⊢ (x1, shr s1) ≈ (x2, shr ?s2)
      by(auto dest: mbisim-thrD2)
    have r1.not-final-thread s1 t''

```

```

proof(cases wset ?s2 t'' = None ∧ ln = no-wait-locks)
  case True
  with fin have ¬ final2 x2 by simp
  hence ¬ final1 x1
  proof(rule contrapos-nn)
    assume final1 x1
    with final1-simulation[OF bisim'']
    obtain x2' m2' where τs2: r2.silent-moves t'' (x2, shr ?s2) (x2', m2')
      and bisim''': t'' ⊢ (x1, shr s1) ≈ (x2', m2')
      and final2 x2' by auto
    from τs2 have x2' = x2
    proof(cases rule: converse-rtrancpE2[consumes 1, case-names refl step])
      case refl thus ?thesis by simp
    next
      case (step x2'' m2'')
      from True have wset s1 t'' = None thr s1 t'' = [(x1, no-wait-locks)] ts2' t'' = [(x2,
no-wait-locks)]
        using ts1t'' ⟨thr ?s2 t'' = [(x2, ln)]⟩ mbisim by(simp-all add: mbisim-def)
      hence no-τ2: ¬ r2.silent-move t'' (x2, m2) (x2'', m2'')
      proof(rule no-τ)
        fix x1' m1'
        assume r1.silent-move t'' (x1, shr s1) (x1', m1')
        with ⟨final1 x1⟩ show False by(auto dest: r1.final-no-red)
      qed
      with ⟨r2.silent-move t'' (x2, shr ?s2) (x2'', m2'')⟩ have False by simp
      thus ?thesis ..
    qed
    with ⟨final2 x2'⟩ show final2 x2 by simp
  qed
  with ts1t'' show ?thesis ..
next
  case False
  with ts1t'' mbisim show ?thesis by(auto simp add: r1.not-final-thread-iff mbisim-def)
qed
with ⟨r1.all-final-except s1 (r1.deadlocked s1 ∪ r1.final-threads s1)⟩
have t'' ∈ r1.deadlocked s1 ∪ r1.final-threads s1 by(rule r1.all-final-exceptD)
thus t'' ∈ r1.deadlocked s1 ∪ r2.deadlocked ?s2 ∪ r2.final-threads ?s2
  by(auto dest: fin' simp add: mbisim-eqs)
qed
thus ?thesis using interrupt mbisim by(auto simp add: mbisim-def)
qed
hence ∃ lt ∈ LT. r2.must-wait ?s2 t lt (r1.deadlocked s1 ∪ r2.deadlocked ?s2 ∪ r2.final-threads
?s2)
  using ⟨lt ∈ LT⟩ by blast }
moreover from mbisim ⟨wset s1 t = None⟩ have wset ?s2 t = None by(simp add: mbisim-def)
ultimately have ?Lock by simp
thus ?thesis ..
next
  case (wait x1 ln)
  from mbisim ⟨thr s1 t = [(x1, ln)]⟩
  obtain x2 where ts2' t = [(x2, ln)] by(auto dest: mbisim-thrD1)
  moreover
  have r2.all-final-except ?s2 (r1.deadlocked s1)
  proof(rule r2.all-final-exceptI)

```

```

fix t
assume r2.not-final-thread ?s2 t
then obtain x2 ln where ts2' t = [(x2, ln)] by(auto simp add: r2.not-final-thread-iff)
with mbisim obtain x1 where thr s1 t = [(x1, ln)] t ⊢ (x1, shr s1) ≈ (x2, m2) by(auto dest:
mbisim-thrD2)
hence r1.not-final-thread s1 t using ⟨r2.not-final-thread ?s2 t⟩ ⟨ts2' t = [(x2, ln)]⟩ mbisim
fin'[of t]
by(cases wset s1 t)(auto simp add: r1.not-final-thread-iff r2.not-final-thread-iff mbisim-def
r1.final-thread-def r2.final-thread-def)
with ⟨r1.all-final-except s1 (r1.deadlocked s1)⟩
show t ∈ r1.deadlocked s1 by(rule r1.all-final-exceptD)
qed
hence r2.all-final-except ?s2 (r1.deadlocked s1 ∪ r2.deadlocked ?s2)
by(rule r2.all-final-except-mono') blast
moreover
from ⟨waiting (wset s1 t)⟩ mbisim
have waiting (wset ?s2 t) by(simp add: mbisim-def)
ultimately have ?Wait by simp
thus ?thesis by blast
next
case (acquire x1 ln l t')
from mbisim ⟨thr s1 t = [(x1, ln)]⟩
obtain x2 where ts2' t = [(x2, ln)] by(auto dest: mbisim-thrD1)
moreover
from ⟨t' ∈ r1.deadlocked s1 ∨ r1.final-thread s1 t'⟩
have (t' ∈ r1.deadlocked s1 ∨ t' ∈ r2.deadlocked ?s2) ∨ r2.final-thread ?s2 t' by(blast dest: fin')
moreover
from mbisim ⟨has-lock (locks s1 $ l) t'⟩
have has-lock (locks ?s2 $ l) t' by(simp add: mbisim-def)
ultimately have ?Acquire
using ⟨0 < ln $ l⟩ ⟨t ≠ t'⟩ ⟨¬ waiting (wset s1 t)⟩ mbisim
by(auto simp add: mbisim-def)
thus ?thesis by blast
qed
qed
with red1 red2 mbisim show ?thesis by(blast intro: rtranclp-trans)
qed

lemma deadlocked2-imp-τs-deadlocked1:
  ⟦ s1 ≈m s2; t ∈ r2.deadlocked s2 ⟧
  ⟹ ∃ s1'. r1.mthr.silent-moves s1 s1' ∧ t ∈ r1.deadlocked s1' ∧ s1' ≈m s2
using FWdelay-bisimulation-diverge.deadlocked1-imp-τs-deadlocked2[OF FWdelay-bisimulation-diverge-flip]
unfolding flip-simps .

lemma deadlock1-imp-τs-deadlock2:
  assumes mbisim: s1 ≈m s2
  and dead: r1.deadlock s1
  shows ∃ s2'. r2.mthr.silent-moves s2 s2' ∧ r2.deadlock s2' ∧ s1 ≈m s2'
proof(cases ∃ t. r1.not-final-thread s1 t)
case True
then obtain t where nfin: r1.not-final-thread s1 t ..
from mfinal1-inv-simulation[OF mbisim]
obtain ls2 ts2 m2 ws2 is2 where red1: r2.mthr.silent-moves s2 (ls2, (ts2, m2), ws2, is2)
and s1 ≈m (ls2, (ts2, m2), ws2, is2) and m2 = shr s2

```

**and**  $\text{fin}: \bigwedge t. r1.\text{final-thread } s1 \ t \implies r2.\text{final-thread } (ls2, (ts2, m2), ws2, is2) \ t$  **by** *fastforce*  
**from**  $\text{no-}\tau\text{Move1-}\tau\text{s-to-no-}\tau\text{Move2}[OF \langle s1 \approx m (ls2, (ts2, m2), ws2, is2) \rangle]$   
**obtain**  $ts2'$  **where**  $\text{red2}: r2.\text{mthr.silent-moves } (ls2, (ts2, m2), ws2, is2) (ls2, (ts2', m2), ws2, is2)$   
**and**  $\text{no-}\tau: \bigwedge t \ x1 \ x2 \ x2' \ m2'. \llbracket \text{wset } s1 \ t = \text{None}; \text{thr } s1 \ t = \llbracket (x1, \text{no-wait-locks}) \rrbracket; ts2' \ t = \llbracket (x2, \text{no-wait-locks}) \rrbracket;$   
 $\bigwedge x' \ m'. r1.\text{silent-move } t \ (x1, \text{shr } s1) \ (x', m') \implies \text{False} \rrbracket$   
 $\implies \neg r2.\text{silent-move } t \ (x2, m2) \ (x2', m2')$   
**and**  $\text{mbisim}: s1 \approx m (ls2, (ts2', m2), ws2, is2)$  **by** *fastforce*  
**from**  $\text{mbisim}$  **have**  $\text{mbisim-egs}: ls2 = \text{locks } s1 \ ws2 = \text{wset } s1 \ is2 = \text{interrupts } s1$   
**by** (*simp-all add: mbisim-def*)  
**let**  $?s2 = (ls2, (ts2', m2), ws2, is2)$   
**from**  $\text{red2}$  **have**  $\text{fin}': \bigwedge t. r1.\text{final-thread } s1 \ t \implies r2.\text{final-thread } ?s2 \ t$   
**by** (*rule r2.τmRedT-preserves-final-thread*)(*rule fin*)  
**have**  $r2.\text{deadlock } ?s2$   
**proof** (*rule r2.deadlockI, goal-cases*)  
**case** ( $1 \ t \ x2$ )  
**note**  $ts2t = \langle \text{thr } ?s2 \ t = \llbracket (x2, \text{no-wait-locks}) \rrbracket \rangle$   
**with**  $\text{mbisim}$  **obtain**  $x1$  **where**  $ts1t: \text{thr } s1 \ t = \llbracket (x1, \text{no-wait-locks}) \rrbracket$   
**and**  $\text{bisim}: t \vdash (x1, \text{shr } s1) \approx (x2, m2)$  **by** (*auto dest: mbisim-thrD2*)  
**from**  $\langle \text{wset } ?s2 \ t = \text{None} \rangle$   $\text{mbisim}$  **have**  $ws1t: \text{wset } s1 \ t = \text{None}$  **by** (*simp add: mbisim-def*)  
**have**  $\neg \text{final1 } x1$   
**proof**  
**assume**  $\text{final1 } x1$   
**with**  $ts1t \ ws1t$  **have**  $r1.\text{final-thread } s1 \ t$  **by** (*simp add: r1.final-thread-def*)  
**hence**  $r2.\text{final-thread } ?s2 \ t$  **by** (*rule fin'*)  
**with**  $\langle \neg \text{final2 } x2 \rangle \ ts2t \ \langle \text{wset } ?s2 \ t = \text{None} \rangle$  **show** *False* **by** (*simp add: r2.final-thread-def*)  
**qed**  
**from**  $r1.\text{deadlockD1}[OF \text{ dead } ts1t \text{ this } \langle \text{wset } s1 \ t = \text{None} \rangle]$   
**have**  $\text{ms}: r1.\text{must-sync } t \ x1 \ (\text{shr } s1)$   
**and**  $\text{csmw}: \bigwedge LT. r1.\text{can-sync } t \ x1 \ (\text{shr } s1) \ LT \implies \exists lt \in LT. r1.\text{must-wait } s1 \ t \ lt \ (\text{dom } (\text{thr } s1))$   
**by** *blast+*  
**{**  
**from**  $\langle r1.\text{must-sync } t \ x1 \ (\text{shr } s1) \rangle$  **obtain**  $ta1 \ x1' \ m1'$   
**where**  $r1: t \vdash (x1, \text{shr } s1) -1-ta1 \rightarrow (x1', m1')$   
**and**  $s1': \exists s1'. r1.\text{actions-ok } s1' \ t \ ta1$  **by** (*fastforce elim: r1.must-syncE*)  
**have**  $\neg \tau\text{move1 } (x1, \text{shr } s1) \ ta1 \ (x1', m1') \ (\text{is } \neg ?\tau)$   
**proof**  
**assume**  $? \tau$   
**hence**  $ta1 = \varepsilon$  **by** (*rule r1.silent-tl*)  
**with**  $r1$  **have**  $r1.\text{can-sync } t \ x1 \ (\text{shr } s1) \ \{\}$   
**by** (*auto intro!: r1.can-syncI simp add: collect-locks-def collect-interrupts-def*)  
**from**  $\text{csmw}[OF \text{ this}]$  **show** *False* **by** *blast*  
**qed**  
**from**  $\text{simulation1}[OF \text{ bisim } r1 \text{ this}]$   
**obtain**  $x2' \ m2' \ x2'' \ m2'' \ ta2$  **where**  $r2: r2.\text{silent-moves } t \ (x2, m2) \ (x2', m2')$   
**and**  $r2': t \vdash (x2', m2') -2-ta2 \rightarrow (x2'', m2'')$   
**and**  $\text{bisim}': t \vdash (x1', m1') \approx (x2'', m2'')$  **and**  $\text{tasim}: ta1 \sim m \ ta2$  **by** *auto*  
**from**  $r2$   
**have**  $\exists ta2 \ x2' \ m2' \ s2'. t \vdash (x2, m2) -2-ta2 \rightarrow (x2', m2') \wedge r2.\text{actions-ok } s2' \ t \ ta2$   
**proof** (*cases rule: converse-rtranclpE2[consumes 1, case-names base step]*)  
**case** *base*  
**from**  $r2'[folded \text{ base}] \ s1'[\text{unfolded ex-actions-ok1-conv-ex-actions-ok2}[OF \text{ tasim}]]$   
**show**  $?thesis$  **by** *blast*  
**next**



```

    case (step  $x2'''$   $m2'''$ )
    hence  $t \vdash (x2, m2) -2-\varepsilon \rightarrow (x2''', m2''')$  by (auto dest:  $r2.silent-tl$ )
    moreover have  $r2.actions-ok$  (undefined, (undefined, undefined), Map.empty, undefined)  $t \varepsilon$ 
by auto
    ultimately show ?thesis by -(rule exI conjI|assumption)+
    qed
    hence  $r2.must-sync$   $t$   $x2$   $m2$  unfolding  $r2.must-sync-def2$  . }
moreover
{ fix  $LT$ 
  assume  $r2.can-sync$   $t$   $x2$   $m2$   $LT$ 
  then obtain  $ta2$   $x2'$   $m2'$  where  $r2: t \vdash (x2, m2) -2-ta2 \rightarrow (x2', m2')$ 
    and  $LT: LT = collect-locks \{ta2\}_l <+> collect-cond-actions \{ta2\}_c <+> collect-interrupts$ 
 $\{ta2\}_i$ 
    by (auto elim:  $r2.can-syncE$ )
  from  $ts2t$  have  $ts2' t = [(x2, no-wait-locks)]$  by simp
  with  $ws1t$   $ts1t$  have  $\neg r2.silent-move$   $t$   $(x2, m2)$   $(x2', m2')$ 
  proof (rule no- $\tau$ )
    fix  $x1'$   $m1'$ 
    assume  $r1.silent-move$   $t$   $(x1, shr s1)$   $(x1', m1')$ 
    hence  $t \vdash (x1, shr s1) -1-\varepsilon \rightarrow (x1', m1')$  by (auto dest:  $r1.silent-tl$ )
    hence  $r1.can-sync$   $t$   $x1$  (shr  $s1$ ) {}
    by (auto intro:  $r1.can-syncI$  simp add: collect-locks-def collect-interrupts-def)
    with  $csmw[OF this]$  show False by blast
  qed
  with  $r2$  have  $\neg \tau move2$   $(x2, m2)$   $ta2$   $(x2', m2')$  by auto
  from  $simulation2[OF bisim r2 this]$  obtain  $x1'$   $m1'$   $x1''$   $m1''$   $ta1$ 
    where  $\tau r1: r1.silent-moves$   $t$   $(x1, shr s1)$   $(x1', m1')$ 
    and  $r1: t \vdash (x1', m1') -1-ta1 \rightarrow (x1'', m1'')$ 
    and  $\neg \tau1: \neg \tau move1$   $(x1', m1')$   $ta1$   $(x1'', m1'')$ 
    and  $bisim': t \vdash (x1'', m1'') \approx (x2', m2')$ 
    and  $tlsim: ta1 \sim_m ta2$  by auto
  from  $\tau r1$  obtain [simp]:  $x1' = x1$   $m1' = shr s1$ 
  proof (cases rule: converse-rtrancpE2[consumes 1, case-names refl step])
    case (step  $X M$ )
    from  $\langle r1.silent-move$   $t$   $(x1, shr s1)$   $(X, M) \rangle$ 
    have  $t \vdash (x1, shr s1) -1-\varepsilon \rightarrow (X, M)$  by (auto dest:  $r1.silent-tl$ )
    hence  $r1.can-sync$   $t$   $x1$  (shr  $s1$ ) {}
    by (auto intro:  $r1.can-syncI$  simp add: collect-locks-def collect-interrupts-def)
    with  $csmw[OF this]$  have False by blast
    thus ?thesis ..
  qed blast
  from  $tlsim$   $LT$  have  $LT = collect-locks \{ta1\}_l <+> collect-cond-actions \{ta1\}_c <+> collect-interrupts \{ta1\}_i$ 
    by (auto simp add: ta-bisim-def)
  with  $r1$  have  $r1.can-sync$   $t$   $x1$  (shr  $s1$ )  $LT$  by (auto intro:  $r1.can-syncI$ )
  from  $csmw[OF this]$  obtain  $lt$ 
    where  $lt: lt \in LT$   $r1.must-wait$   $s1$   $t$   $lt$  (dom (thr  $s1$ )) by blast
  from  $\langle r1.must-wait$   $s1$   $t$   $lt$  (dom (thr  $s1$ ))  $\rangle$  have  $r2.must-wait$  ? $s2$   $t$   $lt$  (dom (thr ? $s2$ ))
  proof (cases rule:  $r1.must-wait-elim$ s)
    case (lock  $l$ )
    with  $mbisim-dom-eq[OF mbisim]$  show ?thesis by (auto simp add: mbisim-egs)
  next
    case (join  $t'$ )
    from  $dead$   $deadlock-mbisim-not-final-thread-pres[OF - \langle r1.not-final-thread$   $s1$   $t' \rangle$   $fin'$   $mbisim]$ 

```

```

    have  $r2.not\text{-}final\text{-}thread\ ?s2\ t'$  by auto
    thus  $?thesis$  using join mbisim-dom-eq[OF mbisim] by auto
next
  case (interrupt  $t'$ )
  have  $r2.all\text{-}final\text{-}except\ ?s2\ (dom\ (thr\ ?s2))$  by (auto intro!:  $r2.all\text{-}final\text{-}exceptI$ )
  with interrupt show  $?thesis$  by (auto simp add: mbisim-eqs)
qed
with  $lt$  have  $\exists lt \in LT. r2.must\text{-}wait\ ?s2\ t\ lt\ (dom\ (thr\ ?s2))$  by blast }
ultimately show  $?case$  by fastforce
next
  case (2  $t\ x2\ ln\ l$ )
  note dead moreover
  from mbisim  $\langle thr\ ?s2\ t = [(x2, ln)] \rangle$ 
  obtain  $x1$  where  $thr\ s1\ t = [(x1, ln)]$  by (auto dest: mbisim-thrD2)
  moreover note  $\langle 0 < ln\ \$\ l \rangle$ 
  moreover from  $\langle \neg waiting\ (wset\ ?s2\ t) \rangle$  mbisim
  have  $\neg waiting\ (wset\ s1\ t)$  by (simp add: mbisim-def)
  ultimately obtain  $l'\ t'$  where  $0 < ln\ \$\ l'\ t' \neq t'$   $thr\ s1\ t' \neq None$  has-lock (locks  $s1\ \$\ l'$ )  $t'$ 
    by (rule  $r1.deadlockD2$ )
  thus  $?case$  using mbisim-thrNone-eq[OF mbisim, of  $t'$ ] mbisim by (auto simp add: mbisim-def)
next
  case (3  $t\ x2\ w$ )
  from mbisim-thrD2[OF mbisim this]
  obtain  $x1$  where  $thr\ s1\ t = [(x1, no\text{-}wait\text{-}locks)]$  by auto
  with dead have  $wset\ s1\ t \neq [PostWS\ w]$  by (rule  $r1.deadlockD3[rule\text{-}format]$ )
  with mbisim show  $?case$  by (simp add: mbisim-def)
qed
with  $red1\ red2\ mbisim$  show  $?thesis$  by (blast intro: rtrancpl-trans)
next
  case False
  hence  $r1.mfinal\ s1$  by (auto intro:  $r1.mfinalI$  simp add:  $r1.not\text{-}final\text{-}thread\text{-}iff$ )
  from  $mfinal1\text{-}simulation$ [OF mbisim this]
  obtain  $s2'$  where  $\tau mRed2\ s2\ s2'\ s1 \approx m\ s2'\ r2.mfinal\ s2'\ shr\ s2' = shr\ s2$  by blast
  thus  $?thesis$  by (blast intro:  $r2.mfinal\text{-}deadlock$ )
qed

lemma deadlock2-imp- $\tau s$ -deadlock1:
   $\llbracket s1 \approx m\ s2; r2.deadlock\ s2 \rrbracket$ 
   $\implies \exists s1'. r1.mthr.silent\text{-}moves\ s1\ s1' \wedge r1.deadlock\ s1' \wedge s1' \approx m\ s2$ 
using FWdelay-bisimulation-diverge.deadlock1-imp- $\tau s$ -deadlock2[OF FWdelay-bisimulation-diverge-flip]
unfolding flip-simps .

lemma deadlocked'1-imp- $\tau s$ -deadlocked'2:
   $\llbracket s1 \approx m\ s2; r1.deadlocked'\ s1 \rrbracket$ 
   $\implies \exists s2'. r2.mthr.silent\text{-}moves\ s2\ s2' \wedge r2.deadlocked'\ s2' \wedge s1 \approx m\ s2'$ 
unfolding  $r1.deadlock\text{-}eq\text{-}deadlocked'$ [symmetric]  $r2.deadlock\text{-}eq\text{-}deadlocked'$ [symmetric]
by (rule deadlock1-imp- $\tau s$ -deadlock2)

lemma deadlocked'2-imp- $\tau s$ -deadlocked'1:
   $\llbracket s1 \approx m\ s2; r2.deadlocked'\ s2 \rrbracket \implies \exists s1'. r1.mthr.silent\text{-}moves\ s1\ s1' \wedge r1.deadlocked'\ s1' \wedge s1' \approx m\ s2$ 
unfolding  $r1.deadlock\text{-}eq\text{-}deadlocked'$ [symmetric]  $r2.deadlock\text{-}eq\text{-}deadlocked'$ [symmetric]
by (rule deadlock2-imp- $\tau s$ -deadlock1)

```

end

context *FWbisimulation* begin

lemma *mbisim-final-thread-preserve1*:

assumes *mbisim*:  $s1 \approx_m s2$  and *fin*:  $r1.\text{final-thread } s1 \ t$

shows  $r2.\text{final-thread } s2 \ t$

proof –

from *fin* obtain  $x1$  where  $ts1t$ :  $\text{thr } s1 \ t = \lfloor (x1, \text{no-wait-locks}) \rfloor$

and *fin1*:  $\text{final1 } x1$  and *ws1t*:  $\text{wset } s1 \ t = \text{None}$

by(*auto elim*:  $r1.\text{final-threadE}$ )

from *mbisim*  $ts1t$  obtain  $x2$

where  $ts2t$ :  $\text{thr } s2 \ t = \lfloor (x2, \text{no-wait-locks}) \rfloor$

and *bisim*:  $t \vdash (x1, \text{shr } s1) \approx (x2, \text{shr } s2)$  by(*auto dest*:  $\text{mbisim-thrD1}$ )

note  $ts2t$  moreover from *fin1* *bisim* have  $\text{final2 } x2$  by(*auto dest*:  $\text{bisim-final}$ )

moreover from *mbisim* *ws1t* have  $\text{wset } s2 \ t = \text{None}$  by(*simp add*:  $\text{mbisim-def}$ )

ultimately show *?thesis* by(*rule*  $r2.\text{final-threadI}$ )

qed

lemma *mbisim-final-thread-preserve2*:

$\llbracket s1 \approx_m s2; r2.\text{final-thread } s2 \ t \rrbracket \implies r1.\text{final-thread } s1 \ t$

using *FWbisimulation.mbisim-final-thread-preserve1*[*OF FWbisimulation-flip*]

unfolding *flip-simps* .

lemma *mbisim-final-thread-inv*:

$s1 \approx_m s2 \implies r1.\text{final-thread } s1 \ t \longleftrightarrow r2.\text{final-thread } s2 \ t$

by(*blast intro*: *mbisim-final-thread-preserve1* *mbisim-final-thread-preserve2*)

lemma *mbisim-not-final-thread-inv*:

assumes *bisim*:  $\text{mbisim } s1 \ s2$

shows  $r1.\text{not-final-thread } s1 = r2.\text{not-final-thread } s2$

proof(*rule ext*)

fix  $t$

show  $r1.\text{not-final-thread } s1 \ t = r2.\text{not-final-thread } s2 \ t$

proof(*cases thr*  $s1 \ t$ )

case *None*

with *mbisim-thrNone-eq*[*OF bisim*, *of t*] have  $\text{thr } s2 \ t = \text{None}$  by *simp*

with *None* show *?thesis*

by(*auto elim!*:  $r2.\text{not-final-thread.cases } r1.\text{not-final-thread.cases}$   
*intro*:  $r2.\text{not-final-thread.intros } r1.\text{not-final-thread.intros}$ )

next

case (*Some a*)

then obtain  $x1 \ ln$  where  $tst1$ :  $\text{thr } s1 \ t = \lfloor (x1, \ln) \rfloor$  by(*cases a*) *auto*

from *mbisim-thrD1*[*OF bisim tst1*] obtain  $x2$

where  $tst2$ :  $\text{thr } s2 \ t = \lfloor (x2, \ln) \rfloor$  and *bisimt*:  $t \vdash (x1, \text{shr } s1) \approx (x2, \text{shr } s2)$  by *blast*

from *bisim* have  $\text{wset } s2 = \text{wset } s1$  by(*simp add*:  $\text{mbisim-def}$ )

with  $tst2 \ tst1 \ \text{bisim-final}$ [*OF bisimt*] show *?thesis*

by(*simp add*:  $r1.\text{not-final-thread-conv } r2.\text{not-final-thread-conv}$ )(*rule*  $\text{mbisim-final-thread-inv}$ [*OF*

*bisim*])

qed

qed

lemma *mbisim-deadlocked-preserve1*:

assumes *mbisim*:  $s1 \approx_m s2$  and *dead*:  $t \in r1.\text{deadlocked } s1$

shows  $t \in r2.deadlocked\ s2$   
**proof** –  
**from**  $deadlocked1-imp-\tau s-deadlocked2[OF\ mbisim\ dead]$   
**obtain**  $s2'$  **where**  $r2.mthr.silent-moves\ s2\ s2'$   
**and**  $t \in r2.deadlocked\ s2'$  **by**  $blast$   
**from**  $\langle r2.mthr.silent-moves\ s2\ s2' \rangle$  **have**  $s2' = s2$   
**by**( $rule\ converse-rtrancpE$ )( $auto\ elim: r2.m\tau move.cases$ )  
**with**  $\langle t \in r2.deadlocked\ s2' \rangle$  **show**  $?thesis$  **by**  $simp$   
**qed**

**lemma**  $mbisim-deadlocked-preserve2$ :  
 $\llbracket s1 \approx m\ s2; t \in r2.deadlocked\ s2 \rrbracket \implies t \in r1.deadlocked\ s1$   
**using**  $FWbisimulation.mbisim-deadlocked-preserve1[OF\ FWbisimulation-flip]$   
**unfolding**  $flip-simps$  .

**lemma**  $mbisim-deadlocked-inv$ :  
 $s1 \approx m\ s2 \implies r1.deadlocked\ s1 = r2.deadlocked\ s2$   
**by**( $blast\ intro!: mbisim-deadlocked-preserve1\ mbisim-deadlocked-preserve2$ )

**lemma**  $mbisim-deadlocked'-inv$ :  
 $s1 \approx m\ s2 \implies r1.deadlocked'\ s1 \longleftrightarrow r2.deadlocked'\ s2$   
**unfolding**  $r1.deadlocked'-def\ r2.deadlocked'-def$   
**by**( $simp\ add: mbisim-not-final-thread-inv\ mbisim-deadlocked-inv$ )

**lemma**  $mbisim-deadlock-inv$ :  
 $s1 \approx m\ s2 \implies r1.deadlock\ s1 = r2.deadlock\ s2$   
**unfolding**  $r1.deadlock-eq-deadlocked'\ r2.deadlock-eq-deadlocked'$   
**by**( $rule\ mbisim-deadlocked'-inv$ )

**end**

**context**  $FWbisimulation$  **begin**

**lemma**  $bisim-can-sync-preserve1$ :  
**assumes**  $bisim: t \vdash (x1, m1) \approx (x2, m2)$  **and**  $cs: t \vdash \langle x1, m1 \rangle\ LT\ \wr 1$   
**shows**  $t \vdash \langle x2, m2 \rangle\ LT\ \wr 2$   
**proof** –  
**from**  $cs$  **obtain**  $ta1\ x1'\ m1'$  **where**  $red1: t \vdash (x1, m1) -1-ta1 \rightarrow (x1', m1')$   
**and**  $LT: LT = collect-locks\ \{\!\{ta1}\!\}_l <+> collect-cond-actions\ \{\!\{ta1}\!\}_c <+> collect-interrupts\ \{\!\{ta1}\!\}_i$   
**by**( $rule\ r1.can-syncE$ )  
**from**  $bisimulation.simulation1[OF\ bisimulation-axioms,\ OF\ bisim\ red1]$  **obtain**  $x2'\ ta2\ m2'$   
**where**  $red2: t \vdash (x2, m2) -2-ta2 \rightarrow (x2', m2')$   
**and**  $tasim: ta1 \sim m\ ta2$  **by**  $fastforce$   
**from**  $tasim\ LT$  **have**  $LT = collect-locks\ \{\!\{ta2}\!\}_l <+> collect-cond-actions\ \{\!\{ta2}\!\}_c <+> collect-interrupts\ \{\!\{ta2}\!\}_i$   
**by**( $auto\ simp\ add: ta-bisim-def$ )  
**with**  $red2$  **show**  $?thesis$  **by**( $rule\ r2.can-syncI$ )  
**qed**

**lemma**  $bisim-can-sync-preserve2$ :  
 $\llbracket t \vdash (x1, m1) \approx (x2, m2); t \vdash \langle x2, m2 \rangle\ LT\ \wr 2 \rrbracket \implies t \vdash \langle x1, m1 \rangle\ LT\ \wr 1$   
**using**  $FWbisimulation.bisim-can-sync-preserve1[OF\ FWbisimulation-flip]$

unfolding *flip-simps* .

**lemma** *bisim-can-sync-inv*:

$t \vdash (x1, m1) \approx (x2, m2) \implies t \vdash \langle x1, m1 \rangle LT \wr 1 \longleftrightarrow t \vdash \langle x2, m2 \rangle LT \wr 2$   
**by**(*blast intro: bisim-can-sync-preserve1 bisim-can-sync-preserve2*)

**lemma** *bisim-must-sync-preserve1*:

**assumes** *bisim*:  $t \vdash (x1, m1) \approx (x2, m2)$  **and** *ms*:  $t \vdash \langle x1, m1 \rangle \wr 1$   
**shows**  $t \vdash \langle x2, m2 \rangle \wr 2$

**proof** –

**from** *ms* **obtain** *ta1 x1' m1'* **where** *red1*:  $t \vdash (x1, m1) -1-ta1 \rightarrow (x1', m1')$   
**and** *s1'*:  $\exists s1'. r1.actions-ok\ s1'\ t\ ta1$  **by**(*fastforce elim: r1.must-syncE*)  
**from** *bisimulation.simulation1*[*OF bisimulation-axioms, OF bisim red1*] **obtain** *x2' ta2 m2'*  
**where** *red2*:  $t \vdash (x2, m2) -2-ta2 \rightarrow (x2', m2')$   
**and** *tasim*:  $ta1 \sim_m ta2$  **by** *fastforce*  
**from** *ex-actions-ok1-conv-ex-actions-ok2*[*OF tasim, of t*] *s1' red2*  
**show** *?thesis* **unfolding** *r2.must-sync-def2* **by** *blast*

**qed**

**lemma** *bisim-must-sync-preserve2*:

$\llbracket t \vdash (x1, m1) \approx (x2, m2); t \vdash \langle x2, m2 \rangle \wr 2 \rrbracket \implies t \vdash \langle x1, m1 \rangle \wr 1$   
**using** *FWbisimulation.bisim-must-sync-preserve1*[*OF FWbisimulation-flip*]  
**unfolding** *flip-simps* .

**lemma** *bisim-must-sync-inv*:

$t \vdash (x1, m1) \approx (x2, m2) \implies t \vdash \langle x1, m1 \rangle \wr 1 \longleftrightarrow t \vdash \langle x2, m2 \rangle \wr 2$   
**by**(*blast intro: bisim-must-sync-preserve1 bisim-must-sync-preserve2*)

**end**

**end**

## 1.20 Semantic properties of lifted predicates

**theory** *FWLiftingSem*

**imports**

*FWSemantics*

*FWLifting*

**begin**

**context** *multithreaded-base* **begin**

**lemma** *redT-preserves-ts-inv-ok*:

$\llbracket s -t>ta \rightarrow s'; ts-inv-ok\ (thr\ s)\ I \rrbracket$   
 $\implies ts-inv-ok\ (thr\ s')\ (upd-invs\ I\ P\ \{\{ta\}_t\})$   
**by**(*erule redT.cases*)(*fastforce intro: ts-inv-ok-upd-invs ts-inv-ok-upd-ts redT-updTs-Some*)**+**

**lemma** *RedT-preserves-ts-inv-ok*:

$\llbracket s -\triangleright ttas \rightarrow^* s'; ts-inv-ok\ (thr\ s)\ I \rrbracket$   
 $\implies ts-inv-ok\ (thr\ s')\ (upd-invs\ I\ Q\ (concat\ (map\ (thr-a \circ snd)\ ttas)))$   
**by**(*induct rule: RedT-induct*)(*auto intro: redT-preserves-ts-inv-ok*)

**lemma** *redT-upd-inv-ext*:

**fixes**  $I :: 't \multimap 'i$   
**shows**  $\llbracket s \multimap ta \rightarrow s'; ts\text{-inv-ok} (thr\ s) I \rrbracket \implies I \subseteq_m upd\text{-invs} I P \llbracket ta \rrbracket_t$   
**by**(*erule redT.cases, auto intro: ts-inv-ok-inv-ext-upd-invs*)

**lemma** *RedT-upd-inv-ext:*

**fixes**  $I :: 't \multimap 'i$   
**shows**  $\llbracket s \multimap ttas \rightarrow^* s'; ts\text{-inv-ok} (thr\ s) I \rrbracket$   
 $\implies I \subseteq_m upd\text{-invs} I P (\text{concat} (\text{map} (thr\text{-a} \circ \text{snd}) ttas))$   
**proof**(*induct rule: RedT-induct*)  
**case** *refl* **thus** ?*case* **by** *simp*  
**next**  
**case** (*step S TTAS S' T TA S''*)  
**hence**  $ts\text{-inv-ok} (thr\ S') (upd\text{-invs} I P (\text{concat} (\text{map} (thr\text{-a} \circ \text{snd}) TTAS)))$   
**by**  $\neg(\text{rule RedT-preserves-ts-inv-ok})$   
**hence**  $upd\text{-invs} I P (\text{concat} (\text{map} (thr\text{-a} \circ \text{snd}) TTAS)) \subseteq_m upd\text{-invs} (upd\text{-invs} I P (\text{concat} (\text{map} (thr\text{-a} \circ \text{snd}) TTAS))) P \llbracket TA \rrbracket_t$   
**using** *step* **by**  $\neg(\text{rule redT-upd-inv-ext})$   
**with** *step* **show** ?*case* **by**(*auto elim!: map-le-trans simp add: comp-def*)  
**qed**

**end**

**locale** *lifting-inv = multithreaded final r convert-RA*

**for** *final*  $:: 'x \Rightarrow \text{bool}$   
**and**  $r :: ('l, 't, 'x, 'm, 'w, 'o) \text{ semantics } (- \vdash - \multimap - [50, 0, 0, 50] 80)$   
**and** *convert-RA*  $:: 'l \text{ released-locks} \Rightarrow 'o \text{ list}$   
 $+$   
**fixes**  $P :: 'i \Rightarrow 't \Rightarrow 'x \Rightarrow 'm \Rightarrow \text{bool}$   
**assumes** *invariant-red*:  $\llbracket t \vdash \langle x, m \rangle \multimap ta \rightarrow \langle x', m' \rangle; P\ i\ t\ x\ m \rrbracket \implies P\ i\ t\ x'\ m'$   
**and** *invariant-NewThread*:  $\llbracket t \vdash \langle x, m \rangle \multimap ta \rightarrow \langle x', m' \rangle; P\ i\ t\ x\ m; \text{NewThread}\ t''\ x''\ m' \in \text{set } \llbracket ta \rrbracket_t \rrbracket$   
 $\implies \exists i''. P\ i''\ t''\ x''\ m'$   
**and** *invariant-other*:  $\llbracket t \vdash \langle x, m \rangle \multimap ta \rightarrow \langle x', m' \rangle; P\ i\ t\ x\ m; P\ i''\ t''\ x''\ m \rrbracket \implies P\ i''\ t''\ x''\ m'$   
**begin**

**lemma** *redT-updTs-invariant:*

**fixes**  $ln$   
**assumes** *tsiP*:  $ts\text{-inv} P\ I\ ts\ m$   
**and** *red*:  $t \vdash \langle x, m \rangle \multimap ta \rightarrow \langle x', m' \rangle$   
**and** *tao*:  $\text{thread-oks}\ ts\ \llbracket ta \rrbracket_t$   
**and** *tst*:  $ts\ t = \lfloor (x, ln) \rfloor$   
**shows**  $ts\text{-inv} P (upd\text{-invs} I P \llbracket ta \rrbracket_t) ((\text{redT-updTs}\ ts\ \llbracket ta \rrbracket_t)(t \mapsto (x', ln'))) m'$   
**proof**(*rule ts-invI*)  
**fix**  $T\ X\ LN$   
**assume**  $XLN: ((\text{redT-updTs}\ ts\ \llbracket ta \rrbracket_t)(t \mapsto (x', ln'))) T = \lfloor (X, LN) \rfloor$   
**from** *tsiP*  $\langle ts\ t = \lfloor (x, ln) \rfloor \rangle$  **obtain**  $i$  **where**  $I\ t = \lfloor i \rfloor\ P\ i\ t\ x\ m$   
**by**(*auto dest: ts-invD*)  
**show**  $\exists i. upd\text{-invs} I P \llbracket ta \rrbracket_t T = \lfloor i \rfloor \wedge P\ i\ T\ X\ m'$   
**proof**(*cases T = t*)  
**case** *True*  
**from**  $\langle P\ i\ t\ x\ m \rangle$  **have**  $P\ i\ t\ x'\ m'$  **by**(*rule invariant-red*)  
**moreover** **from**  $\langle I\ t = \lfloor i \rfloor \rangle \langle ts\ t = \lfloor (x, ln) \rfloor \rangle$  *tao*  
**have**  $upd\text{-invs} I P \llbracket ta \rrbracket_t t = \lfloor i \rfloor$   
**by**(*simp add: upd-invs-Some*)

ultimately show *?thesis* using *True XLN* by *simp*

next

case *False*

show *?thesis*

proof(*cases ts T*)

case *None*

with *XLN tao False* have  $\exists m'. \text{NewThread } T \ X \ m' \in \text{set } \llbracket ta \rrbracket_t$

by(*auto dest: redT-updTs-new-thread*)

with *red* have *nt: NewThread T X m' ∈ set ⟦ta⟧<sub>t</sub>* by(*auto dest: new-thread-memory*)

with *red ⟨P i t x m⟩* have  $\exists i''. P \ i'' \ T \ X \ m'$  by(*rule invariant-NewThread*)

hence *P (SOME i. P i T X m') T X m'* by(*rule someI-ex*)

with *nt tao* show *?thesis* by(*auto intro: SOME-new-thread-upd-invs*)

next

case (*Some a*)

obtain *X' LN'* where [*simp*]: *a = (X', LN')* by(*cases a*)

with *⟨ts T = ⌊a⌋⟩* have *esT: ts T = ⌊(X', LN')⌋* by *simp*

hence *redT-updTs ts ⟦ta⟧<sub>t</sub> T = ⌊(X', LN')⌋*

using *⟨thread-oks ts ⟦ta⟧<sub>t</sub>⟩* by(*auto intro: redT-updTs-Some*)

moreover from *esT tsiP* obtain *i'* where *I T = ⌊i'⌋ P i' T X' m*

by(*auto dest: ts-invD*)

from *red ⟨P i t x m⟩ ⟨P i' T X' m⟩*

have *P i' T X' m'* by(*rule invariant-other*)

moreover from *⟨I T = ⌊i'⌋⟩ esT tao* have *upd-invs I P ⟦ta⟧<sub>t</sub> T = ⌊i'⌋*

by(*simp add: upd-invs-Some*)

ultimately show *?thesis* using *XLN False* by *simp*

qed

qed

qed

theorem *redT-invariant*:

assumes *redT: s -t>ta→ s'*

and *esinvP: ts-inv P I (thr s) (shr s)*

shows *ts-inv P (upd-invs I P ⟦ta⟧<sub>t</sub>) (thr s') (shr s')*

using *redT*

proof(*cases rule: redT-elim*s)

case *acquire* thus *?thesis* using *esinvP*

by(*auto intro!: ts-invI split: if-split-asm dest: ts-invD*)

next

case (*normal x x' m'*)

with *esinvP*

have *ts-inv P (upd-invs I P ⟦ta⟧<sub>t</sub>) ((redT-updTs (thr s) ⟦ta⟧<sub>t</sub>)(t ↦ (x', redT-updLns (locks s) t no-wait-locks ⟦ta⟧<sub>t</sub>))) m'*

by(*auto intro: redT-updTs-invariant*)

thus *?thesis* using *normal* by *simp*

qed

theorem *RedT-invariant*:

assumes *RedT: s -▷ttas→\* s'*

and *esinvQ: ts-inv P I (thr s) (shr s)*

shows *ts-inv P (upd-invs I P (concat (map (thr-a ∘ snd) ttas))) (thr s') (shr s')*

using *RedT esinvQ*

proof(*induct rule: RedT-induct*)

case *refl* thus *?case* by(*simp (no-asm)*)

next

```

case (step  $S$   $TTAS$   $S'$   $T$   $TA$   $S''$ )
note  $IH = \langle ts\text{-}inv\ P\ I\ (thr\ S)\ (shr\ S) \implies ts\text{-}inv\ P\ (upd\text{-}invs\ I\ P\ (concat\ (map\ (thr\text{-}a\ \circ\ snd)\ TTAS)))$ 
 $(thr\ S')\ (shr\ S') \rangle$ 
with  $\langle ts\text{-}inv\ P\ I\ (thr\ S)\ (shr\ S) \rangle$ 
have  $ts\text{-}inv\ P\ (upd\text{-}invs\ I\ P\ (concat\ (map\ (thr\text{-}a\ \circ\ snd)\ TTAS)))\ (thr\ S')\ (shr\ S')$  by blast
with  $\langle S' - T \triangleright TA \rightarrow S'' \rangle$ 
have  $ts\text{-}inv\ P\ (upd\text{-}invs\ (upd\text{-}invs\ I\ P\ (concat\ (map\ (thr\text{-}a\ \circ\ snd)\ TTAS)))\ P\ \{\!\{TA\}\!\}_t)\ (thr\ S'')\ (shr\ S'')$ 
by(rule redT-invariant)
thus ?case by(simp add: comp-def)
qed

```

```

lemma invariant3p-ts-inv: invariant3p redT {s.  $\exists I. ts\text{-}inv\ P\ I\ (thr\ s)\ (shr\ s)$ }
by(auto intro!: invariant3pI dest: redT-invariant)

```

**end**

```

locale lifting-wf = multithreaded final r convert-RA
for final :: 'x  $\Rightarrow$  bool
and r :: ('l, 't, 'x, 'm, 'w, 'o) semantics (-  $\vdash$  -  $\dashrightarrow$  - [50,0,0,50] 80)
and convert-RA :: 'l released-locks  $\Rightarrow$  'o list
+
fixes P :: 't  $\Rightarrow$  'x  $\Rightarrow$  'm  $\Rightarrow$  bool
assumes preserves-red:  $\llbracket t \vdash \langle x, m \rangle - ta \rightarrow \langle x', m' \rangle; P\ t\ x\ m \rrbracket \implies P\ t\ x'\ m'$ 
and preserves-NewThread:  $\llbracket t \vdash \langle x, m \rangle - ta \rightarrow \langle x', m' \rangle; P\ t\ x\ m; NewThread\ t''\ x''\ m' \in set\ \{\!\{ta\}\!\}_t \rrbracket$ 
 $\implies P\ t''\ x''\ m'$ 
and preserves-other:  $\llbracket t \vdash \langle x, m \rangle - ta \rightarrow \langle x', m' \rangle; P\ t\ x\ m; P\ t''\ x''\ m \rrbracket \implies P\ t''\ x''\ m'$ 
begin

```

```

lemma lifting-inv: lifting-inv final r ( $\lambda\text{-} :: unit. P$ )
by(unfold-locales)(blast intro: preserves-red preserves-NewThread preserves-other)+

```

```

lemma redT-updT-s-preserves:

```

```

fixes ln
assumes esokQ: ts-ok  $P\ ts\ m$ 
and red:  $t \vdash \langle x, m \rangle - ta \rightarrow \langle x', m' \rangle$ 
and  $ts\ t = \lfloor (x, ln) \rfloor$ 
and thread-oks  $ts\ \{\!\{ta\}\!\}_t$ 
shows ts-ok  $P\ ((redT\text{-}updTs\ ts\ \{\!\{ta\}\!\}_t)(t \mapsto (x', ln')))\ m'$ 
proof -
interpret lifting-inv final r convert-RA  $\lambda\text{-} :: unit. P$  by(rule lifting-inv)
from esokQ obtain  $I :: 't \rightarrow unit$  where  $ts\text{-}inv\ (\lambda\text{-}. P)\ I\ ts\ m$  by(rule ts-ok-into-ts-inv-const)
hence  $ts\text{-}inv\ (\lambda\text{-}. P)\ (upd\text{-}invs\ I\ (\lambda\text{-}. P)\ \{\!\{ta\}\!\}_t)\ ((redT\text{-}updTs\ ts\ \{\!\{ta\}\!\}_t)(t \mapsto (x', ln')))\ m'$ 
using red  $\langle thread\text{-}oks\ ts\ \{\!\{ta\}\!\}_t \rangle\ \langle ts\ t = \lfloor (x, ln) \rfloor \rangle$  by(rule redT-updT-s-invariant)
thus ?thesis by(rule ts-inv-const-into-ts-ok)
qed

```

```

theorem redT-preserves:

```

```

assumes redT:  $s - t \triangleright ta \rightarrow s'$ 
and esokQ: ts-ok  $P\ (thr\ s)\ (shr\ s)$ 
shows ts-ok  $P\ (thr\ s')\ (shr\ s')$ 
proof -
interpret lifting-inv final r convert-RA  $\lambda\text{-} :: unit. P$  by(rule lifting-inv)
from esokQ obtain  $I :: 't \rightarrow unit$  where  $ts\text{-}inv\ (\lambda\text{-}. P)\ I\ (thr\ s)\ (shr\ s)$  by(rule ts-ok-into-ts-inv-const)

```



**with** *redT* **have** *ts-inv* ( $\lambda\cdot. P$ ) (*upd-invs* *I* ( $\lambda\cdot. P$ )  $\llbracket ta \rrbracket_t$ ) (*thr* *s'*) (*shr* *s'*) **by**(*rule redT-invariant*)  
**thus** *?thesis* **by**(*rule ts-inv-const-into-ts-ok*)  
**qed**

**theorem** *RedT-preserves*:

$\llbracket s \rightarrow^* ts \rightarrow^* s'; ts\text{-ok } P \text{ (thr } s) \text{ (shr } s) \rrbracket \implies ts\text{-ok } P \text{ (thr } s') \text{ (shr } s')$   
**by**(*erule (1) RedT-lift-preserveD*)(*fastforce elim: redT-preserves*)

**lemma** *invariant3p-ts-ok: invariant3p redT*  $\{s. ts\text{-ok } P \text{ (thr } s) \text{ (shr } s)\}$   
**by**(*auto intro!: invariant3pI intro: redT-preserves*)

**end**

**lemma** *lifting-wf-Const* [*intro!*]:

**assumes** *multithreaded final r*

**shows** *lifting-wf final r* ( $\lambda t x m. k$ )

**proof** –

**interpret** *multithreaded final r* **using** *assms* .

**show** *?thesis* **by** *unfold-locales blast+*

**qed**

**end**

## 1.21 Synthetic first and last actions for each thread

**theory** *FWInitFinLift*

**imports**

*FWLTS*

*FWLiftingSem*

**begin**

**datatype** *status* =

*PreStart*

| *Running*

| *Finished*

**abbreviation** *convert-TA-initial* ::  $(l, t, x, m, w, o)$  *thread-action*  $\Rightarrow (l, t, status \times x, m, w, o)$  *thread-action*  
**where** *convert-TA-initial* == *convert-extTA* (*Pair PreStart*)

**lemma** *convert-obs-initial-convert-TA-initial*:

*convert-obs-initial* (*convert-TA-initial ta*) = *convert-TA-initial* (*convert-obs-initial ta*)

**by**(*simp add: convert-obs-initial-def*)

**lemma** *convert-TA-initial-inject* [*simp*]:

*convert-TA-initial ta* = *convert-TA-initial ta'*  $\longleftrightarrow ta = ta'$

**by**(*cases ta*)(*cases ta', auto*)

**context** *final-thread* **begin**

**primrec** *init-fin-final* :: *status*  $\times x \Rightarrow bool$

**where** *init-fin-final* (*status, x*)  $\longleftrightarrow status = Finished \wedge final x$

**end**

**context** *multithreaded-base* **begin**

**inductive** *init-fin* :: ('l,'t,status × 'x,'m,'w,'o action) semantics (- ⊢ - →<sub>i</sub> - [50,0,0,51] 51)  
**where**

*NormalAction*:

$t \vdash \langle x, m \rangle -ta \rightarrow \langle x', m' \rangle$

$\implies t \vdash ((Running, x), m) -convert-TA-initial (convert-obs-initial ta) \rightarrow_i ((Running, x'), m')$

| *InitialThreadAction*:

$t \vdash ((PreStart, x), m) -\llbracket InitialThreadAction \rrbracket \rightarrow_i ((Running, x), m)$

| *ThreadFinishAction*:

$final\ x \implies t \vdash ((Running, x), m) -\llbracket ThreadFinishAction \rrbracket \rightarrow_i ((Finished, x), m)$

**end**

**declare** *split-paired-Ex* [*simp del*]

**inductive-simps** (**in** *multithreaded-base*) *init-fin-simps* [*simp*]:

$t \vdash ((Finished, x), m) -ta \rightarrow_i xm'$

$t \vdash ((PreStart, x), m) -ta \rightarrow_i xm'$

$t \vdash ((Running, x), m) -ta \rightarrow_i xm'$

$t \vdash xm -ta \rightarrow_i ((Finished, x'), m')$

$t \vdash xm -ta \rightarrow_i ((Running, x'), m')$

$t \vdash xm -ta \rightarrow_i ((PreStart, x'), m')$

**declare** *split-paired-Ex* [*simp*]

**context** *multithreaded* **begin**

**lemma** *multithreaded-init-fin*: *multithreaded init-fin-final init-fin*

**by**(*unfold-locales*)(*fastforce simp add: init-fin.simps convert-obs-initial-def ta-upd-simps dest: new-thread-memory*)

**end**

**locale** *if-multithreaded-base* = *multithreaded-base* +

**constrains** *final* :: 'x ⇒ bool

**and** *r* :: ('l,'t,'x,'m,'w,'o) semantics

**and** *convert-RA* :: 'l released-locks ⇒ 'o list

**sublocale** *if-multithreaded-base* < *if: multithreaded-base*

*init-fin-final*

*init-fin*

*map NormalAction* ○ *convert-RA*

.

**locale** *if-multithreaded* = *if-multithreaded-base* + *multithreaded* +

**constrains** *final* :: 'x ⇒ bool

**and** *r* :: ('l,'t,'x,'m,'w,'o) semantics

**and** *convert-RA* :: 'l released-locks ⇒ 'o list

**sublocale** *if-multithreaded* < *if: multithreaded*

*init-fin-final*

*init-fin*  
*map NormalAction*  $\circ$  *convert-RA*  
**by**(*rule multithreaded-init-fin*)

**context**  $\tau$ *multithreaded* **begin**

**inductive** *init-fin- $\tau$ move* :: ('l,'t,status  $\times$  'x,'m,'w,'o action)  $\tau$ *moves*

**where**

$\tau$ *move* (*x*, *m*) *ta* (*x'*, *m'*)  
 $\implies$  *init-fin- $\tau$ move* ((*Running*, *x*), *m*) (*convert-TA-initial* (*convert-obs-initial ta*)) ((*Running*, *x'*),  
*m'*)

**lemma** *init-fin- $\tau$ move-simps* [*simp*]:

*init-fin- $\tau$ move* ((*PreStart*, *x*), *m*) *ta* *x'm' = False*  
*init-fin- $\tau$ move* *xm ta* ((*PreStart*, *x'*), *m'*) = *False*  
*init-fin- $\tau$ move* ((*Running*, *x*), *m*) *ta* ((*s*, *x'*), *m'*)  $\longleftrightarrow$   
 $(\exists ta'. ta = \text{convert-TA-initial} (\text{convert-obs-initial } ta') \wedge s = \text{Running} \wedge \tau\text{move } (x, m) \text{ ta' } (x', m'))$   
*init-fin- $\tau$ move* ((*s*, *x*), *m*) *ta* ((*Running*, *x'*), *m'*)  $\longleftrightarrow$   
 $s = \text{Running} \wedge (\exists ta'. ta = \text{convert-TA-initial} (\text{convert-obs-initial } ta') \wedge \tau\text{move } (x, m) \text{ ta' } (x', m'))$   
*init-fin- $\tau$ move* ((*Finished*, *x*), *m*) *ta* *x'm' = False*  
*init-fin- $\tau$ move* *xm ta* ((*Finished*, *x'*), *m'*) = *False*  
**by**(*simp-all add: init-fin- $\tau$ move.simps*)

**lemma** *init-fin-silent-move-RunningI*:

**assumes** *silent-move* *t* (*x*, *m*) (*x'*, *m'*)  
**shows**  $\tau\text{trsys.silent-move}$  (*init-fin t*) *init-fin- $\tau$ move* ((*Running*, *x*), *m*) ((*Running*, *x'*), *m'*)  
**using** *assms* **by**(*cases*)(*auto intro:  $\tau$ trsys.silent-move.intros init-fin.NormalAction*)

**lemma** *init-fin-silent-moves-RunningI*:

**assumes** *silent-moves* *t* (*x*, *m*) (*x'*, *m'*)  
**shows**  $\tau\text{trsys.silent-moves}$  (*init-fin t*) *init-fin- $\tau$ move* ((*Running*, *x*), *m*) ((*Running*, *x'*), *m'*)  
**using** *assms*  
**by**(*induct rule: rtranclp-induct2*)(*auto elim: rtranclp.rtrancl-into-rtrancl intro: init-fin-silent-move-RunningI*)

**lemma** *init-fin-silent-moveD*:

**assumes**  $\tau\text{trsys.silent-move}$  (*init-fin t*) *init-fin- $\tau$ move* ((*s*, *x*), *m*) ((*s'*, *x'*), *m'*)  
**shows** *silent-move* *t* (*x*, *m*) (*x'*, *m'*)  $\wedge s = s' \wedge s' = \text{Running}$   
**using** *assms* **by**(*auto elim!:  $\tau$ trsys.silent-move.cases init-fin.cases*)

**lemma** *init-fin-silent-movesD*:

**assumes**  $\tau\text{trsys.silent-moves}$  (*init-fin t*) *init-fin- $\tau$ move* ((*s*, *x*), *m*) ((*s'*, *x'*), *m'*)  
**shows** *silent-moves* *t* (*x*, *m*) (*x'*, *m'*)  $\wedge s = s'$   
**using** *assms*  
**by**(*induct* ((*s*, *x*), *m*) ((*s'*, *x'*), *m'*) *arbitrary: s' x' m'*)  
(*auto 7 2 simp only: dest!: init-fin-silent-moveD intro: rtranclp.rtrancl-into-rtrancl*)

**lemma** *init-fin- $\tau$ divergeD*:

**assumes**  $\tau\text{trsys.}\tau\text{diverge}$  (*init-fin t*) *init-fin- $\tau$ move* ((*status*, *x*), *m*)  
**shows**  $\tau\text{diverge}$  *t* (*x*, *m*)  $\wedge \text{status} = \text{Running}$

**proof**

**from** *assms* **show** *status = Running*

**by**(*cases rule:  $\tau\text{trsys.}\tau\text{diverge.cases[consumes 1]}$* )(*auto dest: init-fin-silent-moveD*)

**moreover** **define** *xm* **where** *xm* = (*x*, *m*)

**ultimately** **have**  $\exists x m. xm = (x, m) \wedge \tau\text{trsys.}\tau\text{diverge}$  (*init-fin t*) *init-fin- $\tau$ move* ((*Running*, *x*), *m*)

```

    using assms by blast
  thus  $\tau$ diverge  $t \ x m$ 
proof (coinduct)
  case ( $\tau$ diverge  $x m$ )
  then obtain  $x \ m$ 
    where  $\text{diverge} : \tau \text{trsys}.\tau \text{diverge} (\text{init-fin } t) \text{ init-fin-}\tau \text{move} ((\text{Running}, x), m)$ 
    and  $x m : x m = (x, m)$  by blast
  thus ?case
    by (cases rule:  $\tau \text{trsys}.\tau \text{diverge}.\text{cases}[ \text{consumes } 1 ]$ ) (auto dest!:  $\text{init-fin-silent-moveD}$ )
qed
qed

```

```

lemma init-fin- $\tau$ diverge-RunningI:
  assumes  $\tau$ diverge  $t \ (x, m)$ 
  shows  $\tau \text{trsys}.\tau \text{diverge} (\text{init-fin } t) \text{ init-fin-}\tau \text{move} ((\text{Running}, x), m)$ 
proof -
  define  $sxm$  where  $sxm = ((\text{Running}, x), m)$ 
  with assms have  $\exists x \ m. \tau \text{diverge } t \ (x, m) \wedge sxm = ((\text{Running}, x), m)$  by blast
  thus  $\tau \text{trsys}.\tau \text{diverge} (\text{init-fin } t) \text{ init-fin-}\tau \text{move } sxm$ 
proof (coinduct rule:  $\tau \text{trsys}.\tau \text{diverge}.\text{coinduct}[ \text{consumes } 1, \text{case-names } \tau \text{diverge} ]$ )
  case ( $\tau$ diverge  $sxm$ )
  then obtain  $x \ m$  where  $\tau \text{diverge } t \ (x, m)$  and  $sxm = ((\text{Running}, x), m)$  by blast
  thus ?case by (cases) (auto intro:  $\text{init-fin-silent-move-RunningI}$ )
qed
qed

```

```

lemma init-fin- $\tau$ diverge-conv:
   $\tau \text{trsys}.\tau \text{diverge} (\text{init-fin } t) \text{ init-fin-}\tau \text{move} ((\text{status}, x), m) \longleftrightarrow$ 
   $\tau \text{diverge } t \ (x, m) \wedge \text{status} = \text{Running}$ 
by (blast intro:  $\text{init-fin-}\tau \text{diverge-RunningI}$  dest:  $\text{init-fin-}\tau \text{divergeD}$ )

end

```

```

lemma init-fin- $\tau$ moves-False:
   $\tau \text{multithreaded}.\text{init-fin-}\tau \text{move} (\lambda - -. \text{False}) = (\lambda - -. \text{False})$ 
by (simp add:  $\text{fun-eq-iff } \tau \text{multithreaded}.\text{init-fin-}\tau \text{move}.\text{simps}$ )

```

```

locale if- $\tau$ multithreaded = if-multithreaded-base +  $\tau \text{multithreaded}$  +
  constrains  $\text{final} :: 'x \Rightarrow \text{bool}$ 
  and  $r :: ('l, 't, 'x, 'm, 'w, 'o)$  semantics
  and  $\text{convert-RA} :: 'l \text{ released-locks} \Rightarrow 'o \text{ list}$ 
  and  $\tau \text{move} :: ('l, 't, 'x, 'm, 'w, 'o) \tau \text{moves}$ 

```

```

sublocale if- $\tau$ multithreaded < if:  $\tau \text{multithreaded}$ 
  init-fin-final
  init-fin
  map  $\text{NormalAction} \circ \text{convert-RA}$ 
  init-fin- $\tau \text{move}$ 
.

```

```

locale if- $\tau$ multithreaded-wf = if-multithreaded-base +  $\tau \text{multithreaded-wf}$  +
  constrains  $\text{final} :: 'x \Rightarrow \text{bool}$ 
  and  $r :: ('l, 't, 'x, 'm, 'w, 'o)$  semantics
  and  $\text{convert-RA} :: 'l \text{ released-locks} \Rightarrow 'o \text{ list}$ 

```

```

and  $\tau move :: ('l, 't, 'x, 'm, 'w, 'o) \tau moves$ 

sublocale  $if\text{-}\tau multithreaded\text{-}wf < if\text{-}multithreaded$ 
by  $unfold\text{-}locales$ 

sublocale  $if\text{-}\tau multithreaded\text{-}wf < if\text{-}\tau multithreaded$  .

context  $\tau multithreaded\text{-}wf$  begin

lemma  $\tau multithreaded\text{-}wf\text{-}init\text{-}fin$ :
   $\tau multithreaded\text{-}wf\ init\text{-}fin\text{-}final\ init\text{-}fin\ init\text{-}fin\text{-}\tau move$ 
proof –
  interpret  $if$ :  $multithreaded\ init\text{-}fin\text{-}final\ init\text{-}fin\ map\ NormalAction \circ convert\text{-}RA$ 
    by( $rule\ multithreaded\text{-}init\text{-}fin$ )
  show  $?thesis$ 
  proof( $unfold\text{-}locales$ )
    fix  $t\ x\ m\ ta\ x'\ m'$ 
    assume  $init\text{-}fin\text{-}\tau move\ (x, m)\ ta\ (x', m')\ t \vdash (x, m) \text{-}ta \rightarrow i\ (x', m')$ 
    thus  $m = m'$  by( $cases$ )( $auto\ dest: \tau move\text{-}heap$ )
  next
    fix  $s\ ta\ s'$ 
    assume  $init\text{-}fin\text{-}\tau move\ s\ ta\ s'$ 
    thus  $ta = \varepsilon$  by( $cases$ )( $auto\ dest: silent\text{-}tl$ )
  qed
qed

end

sublocale  $if\text{-}\tau multithreaded\text{-}wf < if$ :  $\tau multithreaded\text{-}wf$ 
   $init\text{-}fin\text{-}final$ 
   $init\text{-}fin$ 
   $map\ NormalAction \circ convert\text{-}RA$ 
   $init\text{-}fin\text{-}\tau move$ 
by( $rule\ \tau multithreaded\text{-}wf\text{-}init\text{-}fin$ )

primrec  $init\text{-}fin\text{-}lift\text{-}inv :: ('i \Rightarrow 't \Rightarrow 'x \Rightarrow 'm \Rightarrow bool) \Rightarrow 'i \Rightarrow 't \Rightarrow status \times 'x \Rightarrow 'm \Rightarrow bool$ 
where  $init\text{-}fin\text{-}lift\text{-}inv\ P\ I\ t\ (s, x) = P\ I\ t\ x$ 

context  $lifting\text{-}inv$  begin

lemma  $lifting\text{-}inv\text{-}init\text{-}fin\text{-}lift\text{-}inv$ :
   $lifting\text{-}inv\ init\text{-}fin\text{-}final\ init\text{-}fin\ (init\text{-}fin\text{-}lift\text{-}inv\ P)$ 
proof –
  interpret  $if$ :  $multithreaded\ init\text{-}fin\text{-}final\ init\text{-}fin\ map\ NormalAction \circ convert\text{-}RA$ 
    by( $rule\ multithreaded\text{-}init\text{-}fin$ )
  show  $?thesis$ 
    by( $unfold\text{-}locales$ )( $fastforce\ elim!$ :  $init\text{-}fin.cases\ dest: invariant\text{-}red\ invariant\text{-}NewThread\ invariant\text{-}other$ ) $+$ 
  qed

end

locale  $if\text{-}lifting\text{-}inv =$ 

```

```

    if-multithreaded +
    lifting-inv +
    constrains final :: 'x ⇒ bool
    and r :: ('l,'t,'x,'m,'w,'o) semantics
    and convert-RA :: 'l released-locks ⇒ 'o list
    and P :: 'i ⇒ 't ⇒ 'x ⇒ 'm ⇒ bool

```

```

sublocale if-lifting-inv < if: lifting-inv
  init-fin-final
  init-fin
  map NormalAction ◦ convert-RA
  init-fin-lift-inv P
by(rule lifting-inv-init-fin-lift-inv)

```

```

primrec init-fin-lift :: ('t ⇒ 'x ⇒ 'm ⇒ bool) ⇒ 't ⇒ status × 'x ⇒ 'm ⇒ bool
where init-fin-lift P t (s, x) = P t x

```

```

context lifting-wf begin

```

```

lemma lifting-wf-init-fin-lift:
  lifting-wf init-fin-final init-fin (init-fin-lift P)

```

```

proof –

```

```

  interpret if: multithreaded init-fin-final init-fin map NormalAction ◦ convert-RA
  by(rule multithreaded-init-fin)

```

```

  show ?thesis

```

```

  by(unfold-locales)(fastforce elim!: init-fin.cases dest: dest: preserves-red preserves-other preserves-NewThread)+
qed

```

```

end

```

```

locale if-lifting-wf =
  if-multithreaded +
  lifting-wf +
  constrains final :: 'x ⇒ bool
  and r :: ('l,'t,'x,'m,'w,'o) semantics
  and convert-RA :: 'l released-locks ⇒ 'o list
  and P :: 't ⇒ 'x ⇒ 'm ⇒ bool

```

```

sublocale if-lifting-wf < if: lifting-wf
  init-fin-final
  init-fin
  map NormalAction ◦ convert-RA
  init-fin-lift P
by(rule lifting-wf-init-fin-lift)

```

```

lemma (in if-lifting-wf) if-lifting-inv:
  if-lifting-inv final r (λ-::unit. P)

```

```

proof –

```

```

  interpret lifting-inv final r convert-RA λ-::unit. P by(rule lifting-inv)

```

```

  show ?thesis by unfold-locales

```

```

qed

```

```

locale τlifting-inv = τmultithreaded-wf +
  lifting-inv +

```

```

constrains final :: 'x ⇒ bool
and r :: ('l,'t,'x,'m,'w,'o) semantics
and convert-RA :: 'l released-locks ⇒ 'o list
and τmove :: ('l,'t,'x,'m,'w,'o) τmoves
and P :: 'i ⇒ 't ⇒ 'x ⇒ 'm ⇒ bool
begin

lemma redT-silent-move-invariant:
   $\llbracket \tau mredT\ s\ s';\ ts\text{-inv}\ P\ Is\ (thr\ s)\ (shr\ s) \rrbracket \implies ts\text{-inv}\ P\ Is\ (thr\ s')\ (shr\ s')$ 
by(auto dest!: redT-invariant mτmove-silentD)

lemma redT-silent-moves-invariant:
   $\llbracket mthr.\text{silent-moves}\ s\ s';\ ts\text{-inv}\ P\ Is\ (thr\ s)\ (shr\ s) \rrbracket \implies ts\text{-inv}\ P\ Is\ (thr\ s')\ (shr\ s')$ 
by(induct rule: rtranclp-induct)(auto dest: redT-silent-move-invariant)

lemma redT-τrtrancl3p-invariant:
   $\llbracket mthr.\tau rtrancl3p\ s\ ttas\ s';\ ts\text{-inv}\ P\ Is\ (thr\ s)\ (shr\ s) \rrbracket$ 
   $\implies ts\text{-inv}\ P\ (upd\text{-invs}\ Is\ P\ (concat\ (map\ (thr\text{-}a\ \circ\ snd)\ ttas)))\ (thr\ s')\ (shr\ s')$ 
proof(induct arbitrary: Is rule: mthr.τrtrancl3p.induct)
  case τrtrancl3p-refl thus ?case by simp
next
  case (τrtrancl3p-step s s' tls s'' tl)
  thus ?case by(cases tl)(force dest: redT-invariant)
next
  case (τrtrancl3p-τstep s s' tls s'' tl)
  thus ?case by(cases tl)(force dest: redT-invariant mτmove-silentD)
qed

end

locale τlifting-wf = τmultithreaded +
  lifting-wf +
  constrains final :: 'x ⇒ bool
  and r :: ('l,'t,'x,'m,'w,'o) semantics
  and convert-RA :: 'l released-locks ⇒ 'o list
  and τmove :: ('l,'t,'x,'m,'w,'o) τmoves
  and P :: 't ⇒ 'x ⇒ 'm ⇒ bool
begin

lemma redT-silent-move-preserves:
   $\llbracket \tau mredT\ s\ s';\ ts\text{-ok}\ P\ (thr\ s)\ (shr\ s) \rrbracket \implies ts\text{-ok}\ P\ (thr\ s')\ (shr\ s')$ 
by(auto dest: redT-preserves)

lemma redT-silent-moves-preserves:
   $\llbracket mthr.\text{silent-moves}\ s\ s';\ ts\text{-ok}\ P\ (thr\ s)\ (shr\ s) \rrbracket \implies ts\text{-ok}\ P\ (thr\ s')\ (shr\ s')$ 
by(induct rule: rtranclp.induct)(auto dest: redT-silent-move-preserves)

lemma redT-τrtrancl3p-preserves:
   $\llbracket mthr.\tau rtrancl3p\ s\ ttas\ s';\ ts\text{-ok}\ P\ (thr\ s)\ (shr\ s) \rrbracket \implies ts\text{-ok}\ P\ (thr\ s')\ (shr\ s')$ 
by(induct rule: mthr.τrtrancl3p.induct)(auto dest: redT-silent-moves-preserves redT-preserves)

end

definition init-fn-lift-state :: status ⇒ ('l,'t,'x,'m,'w) state ⇒ ('l,'t,status × 'x,'m,'w) state

```

**where**  $\text{init-fin-lift-state } s \sigma = (\text{locks } \sigma, (\lambda t. \text{map-option } (\lambda(x, \text{ln}). ((s, x), \text{ln})) (\text{thr } \sigma t), \text{shr } \sigma), \text{wset } \sigma, \text{interrupts } \sigma)$

**definition**  $\text{init-fin-descend-thr} :: ('l, 't, 'status \times 'x) \text{ thread-info} \Rightarrow ('l, 't, 'x) \text{ thread-info}$   
**where**  $\text{init-fin-descend-thr } ts = \text{map-option } (\lambda((s, x), \text{ln}). (x, \text{ln})) \circ ts$

**definition**  $\text{init-fin-descend-state} :: ('l, 't, 'status \times 'x, 'm, 'w) \text{ state} \Rightarrow ('l, 't, 'x, 'm, 'w) \text{ state}$   
**where**  $\text{init-fin-descend-state } \sigma = (\text{locks } \sigma, (\text{init-fin-descend-thr } (\text{thr } \sigma), \text{shr } \sigma), \text{wset } \sigma, \text{interrupts } \sigma)$

**lemma**  $\text{ts-ok-init-fin-lift-init-fin-lift-state [simp]}$ :  
 $\text{ts-ok } (\text{init-fin-lift } P) (\text{thr } (\text{init-fin-lift-state } s \sigma)) (\text{shr } (\text{init-fin-lift-state } s \sigma)) \longleftrightarrow \text{ts-ok } P (\text{thr } \sigma) (\text{shr } \sigma)$   
**by**( $\text{auto simp add: init-fin-lift-state-def intro!: ts-okI dest: ts-okD}$ )

**lemma**  $\text{ts-inv-init-fin-lift-inv-init-fin-lift-state [simp]}$ :  
 $\text{ts-inv } (\text{init-fin-lift-inv } P) I (\text{thr } (\text{init-fin-lift-state } s \sigma)) (\text{shr } (\text{init-fin-lift-state } s \sigma)) \longleftrightarrow \text{ts-inv } P I (\text{thr } \sigma) (\text{shr } \sigma)$   
**by**( $\text{auto simp add: init-fin-lift-state-def intro!: ts-invI dest: ts-invD}$ )

**lemma**  $\text{init-fin-lift-state-conv-simps}$ :  
**shows**  $\text{shr-init-fin-lift-state: shr } (\text{init-fin-lift-state } s \sigma) = \text{shr } \sigma$   
**and**  $\text{locks-init-fin-lift-state: locks } (\text{init-fin-lift-state } s \sigma) = \text{locks } \sigma$   
**and**  $\text{wset-init-fin-lift-state: wset } (\text{init-fin-lift-state } s \sigma) = \text{wset } \sigma$   
**and**  $\text{interrupts-init-fin-lift-state: interrupts } (\text{init-fin-lift-state } s \sigma) = \text{interrupts } \sigma$   
**and**  $\text{thr-init-fin-lift-state: thr } (\text{init-fin-lift-state } s \sigma) t = \text{map-option } (\lambda(x, \text{ln}). ((s, x), \text{ln})) (\text{thr } \sigma t)$   
**by**( $\text{simp-all add: init-fin-lift-state-def}$ )

**lemma**  $\text{thr-init-fin-lift-state'}$ :  
 $\text{thr } (\text{init-fin-lift-state } s \sigma) = \text{map-option } (\lambda(x, \text{ln}). ((s, x), \text{ln})) \circ \text{thr } \sigma$   
**by**( $\text{simp add: fun-eq-iff thr-init-fin-lift-state}$ )

**lemma**  $\text{init-fin-descend-thr-Some-conv [simp]}$ :  
 $\bigwedge \text{ln. ts } t = \lfloor ((\text{status}, x), \text{ln}) \rfloor \implies \text{init-fin-descend-thr } ts \ t = \lfloor (x, \text{ln}) \rfloor$   
**by**( $\text{simp add: init-fin-descend-thr-def}$ )

**lemma**  $\text{init-fin-descend-thr-None-conv [simp]}$ :  
 $\text{ts } t = \text{None} \implies \text{init-fin-descend-thr } ts \ t = \text{None}$   
**by**( $\text{simp add: init-fin-descend-thr-def}$ )

**lemma**  $\text{init-fin-descend-thr-eq-None [simp]}$ :  
 $\text{init-fin-descend-thr } ts \ t = \text{None} \longleftrightarrow \text{ts } t = \text{None}$   
**by**( $\text{simp add: init-fin-descend-thr-def}$ )

**lemma**  $\text{init-fin-descend-state-simps [simp]}$ :  
 $\text{init-fin-descend-state } (ls, (ts, m), ws, is) = (ls, (\text{init-fin-descend-thr } ts, m), ws, is)$   
 $\text{locks } (\text{init-fin-descend-state } s) = \text{locks } s$   
 $\text{thr } (\text{init-fin-descend-state } s) = \text{init-fin-descend-thr } (\text{thr } s)$   
 $\text{shr } (\text{init-fin-descend-state } s) = \text{shr } s$   
 $\text{wset } (\text{init-fin-descend-state } s) = \text{wset } s$   
 $\text{interrupts } (\text{init-fin-descend-state } s) = \text{interrupts } s$   
**by**( $\text{simp-all add: init-fin-descend-state-def}$ )

**lemma**  $\text{init-fin-descend-thr-update [simp]}$ :



$\text{init-fin-descend-thr } (ts(t := v)) = (\text{init-fin-descend-thr } ts)(t := \text{map-option } (\lambda((\text{status}, x), \text{ln}). (x, \text{ln}))) v)$

**by**(simp add: init-fin-descend-thr-def fun-eq-iff)

**lemma** ts-ok-init-fin-descend-state:

$ts\text{-ok } P \ (\text{init-fin-descend-thr } ts) = ts\text{-ok } (\text{init-fin-lift } P) \ ts$

**by**(rule ext)(auto 4 3 intro!: ts-okI dest: ts-okD simp add: init-fin-descend-thr-def)

**lemma** free-thread-id-init-fin-descend-thr [simp]:

$\text{free-thread-id } (\text{init-fin-descend-thr } ts) = \text{free-thread-id } ts$

**by**(simp add: free-thread-id.simps fun-eq-iff)

**lemma** redT-updT'-init-fin-descend-thr-eq-None [simp]:

$\text{redT-updT}' (\text{init-fin-descend-thr } ts) \text{ nt } t = \text{None} \longleftrightarrow \text{redT-updT}' ts \text{ nt } t = \text{None}$

**by**(cases nt) simp-all

**lemma** thread-ok-init-fin-descend-thr [simp]:

$\text{thread-ok } (\text{init-fin-descend-thr } ts) \text{ nta} = \text{thread-ok } ts \text{ nta}$

**by**(cases nta) simp-all

**lemma** threads-ok-init-fin-descend-thr [simp]:

$\text{thread-oks } (\text{init-fin-descend-thr } ts) \text{ ntas} = \text{thread-oks } ts \text{ ntas}$

**by**(induct ntas arbitrary: ts)(auto elim!: thread-oks-ts-change[THEN iffD1, rotated 1])

**lemma** init-fin-descend-thr-redT-updT [simp]:

$\text{init-fin-descend-thr } (\text{redT-updT } ts \ (\text{convert-new-thread-action } (\text{Pair status}) \text{ nt})) = \text{redT-updT } (\text{init-fin-descend-thr } ts) \text{ nt}$

**by**(cases nt) simp-all

**lemma** init-fin-descend-thr-redT-updT<sub>s</sub> [simp]:

$\text{init-fin-descend-thr } (\text{redT-updT}_s ts \ (\text{map } (\text{convert-new-thread-action } (\text{Pair status})) \text{ nts})) = \text{redT-updT}_s (\text{init-fin-descend-thr } ts) \text{ nts}$

**by**(induct nts arbitrary: ts) simp-all

**context** final-thread **begin**

**lemma** cond-action-ok-init-fin-descend-stateI [simp]:

$\text{final-thread.cond-action-ok init-fin-final } s \text{ t } ct \implies \text{cond-action-ok } (\text{init-fin-descend-state } s) \text{ t } ct$

**by**(cases ct)(auto simp add: final-thread.cond-action-ok.simps init-fin-descend-thr-def)

**lemma** cond-action-oks-init-fin-descend-stateI [simp]:

$\text{final-thread.cond-action-oks init-fin-final } s \text{ t } cts \implies \text{cond-action-oks } (\text{init-fin-descend-state } s) \text{ t } cts$

**by**(induct cts)(simp-all add: final-thread.cond-action-oks.simps cond-action-ok-init-fin-descend-stateI)

**end**

**definition** lift-start-obs :: 't  $\Rightarrow$  'o list  $\Rightarrow$  ('t  $\times$  'o action) list

**where** lift-start-obs t obs = (t, InitialThreadAction) # map ( $\lambda ob. (t, \text{NormalAction } ob)$ ) obs

**lemma** length-lift-start-obs [simp]: length (lift-start-obs t obs) = Suc (length obs)

**by**(simp add: lift-start-obs-def)

**lemma** set-lift-start-obs [simp]:

```

    set (lift-start-obs t obs) =
      insert (t, InitialThreadAction) ((Pair t o NormalAction) ' set obs)
  by(auto simp add: lift-start-obs-def o-def)

```

**lemma** *distinct-lift-start-obs* [simp]: *distinct (lift-start-obs t obs) = distinct obs*  
 by(auto simp add: lift-start-obs-def distinct-map intro: inj-onI)

```

end
theory FWBisimLift imports
  FWInitFinLift
  FWBisimulation
begin

```

**context** *FWbisimulation-base* **begin**

**inductive** *init-fin-bisim* :: '*t*  $\Rightarrow$  ((*status*  $\times$  '*x1*)  $\times$  '*m1*, (*status*  $\times$  '*x2*)  $\times$  '*m2*) bisim  
 (-  $\vdash$  -  $\approx_i$  - [50,50,50] 60)

**for** *t* :: '*t*

**where**

```

  PreStart: t  $\vdash$  (x1, m1)  $\approx$  (x2, m2)  $\Rightarrow$  t  $\vdash$  ((PreStart, x1), m1)  $\approx_i$  ((PreStart, x2), m2)
| Running: t  $\vdash$  (x1, m1)  $\approx$  (x2, m2)  $\Rightarrow$  t  $\vdash$  ((Running, x1), m1)  $\approx_i$  ((Running, x2), m2)
| Finished:
  [| t  $\vdash$  (x1, m1)  $\approx$  (x2, m2); final1 x1; final2 x2 |]
   $\Rightarrow$  t  $\vdash$  ((Finished, x1), m1)  $\approx_i$  ((Finished, x2), m2)

```

**definition** *init-fin-bisim-wait* :: (*status*  $\times$  '*x1*, *status*  $\times$  '*x2*) bisim (-  $\approx_{iw}$  - [50,50] 60)

**where**

*init-fin-bisim-wait* = ( $\lambda$ (*status1*, *x1*) (*status2*, *x2*). *status1* = Running  $\wedge$  *status2* = Running  $\wedge$  *x1*  $\approx_w$  *x2*)

**inductive-simps** *init-fin-bisim-simps* [simp]:

```

  t  $\vdash$  ((PreStart, x1), m1)  $\approx_i$  ((s2, x2), m2)
  t  $\vdash$  ((Running, x1), m1)  $\approx_i$  ((s2, x2), m2)
  t  $\vdash$  ((Finished, x1), m1)  $\approx_i$  ((s2, x2), m2)
  t  $\vdash$  ((s1, x1), m1)  $\approx_i$  ((PreStart, x2), m2)
  t  $\vdash$  ((s1, x1), m1)  $\approx_i$  ((Running, x2), m2)
  t  $\vdash$  ((s1, x1), m1)  $\approx_i$  ((Finished, x2), m2)

```

**lemma** *init-fin-bisim-iff*:

```

  t  $\vdash$  ((s1, x1), m1)  $\approx_i$  ((s2, x2), m2)  $\longleftrightarrow$ 
    s1 = s2  $\wedge$  t  $\vdash$  (x1, m1)  $\approx$  (x2, m2)  $\wedge$  (s2 = Finished  $\longrightarrow$  final1 x1  $\wedge$  final2 x2)

```

**by**(cases *s1*) *auto*

**lemma** *nta-bisim-init-fin-bisim* [simp]:

```

  nta-bisim init-fin-bisim (convert-new-thread-action (Pair PreStart) nt1)
    (convert-new-thread-action (Pair PreStart) nt2) =
    nta-bisim bisim nt1 nt2

```

**by**(cases *nt1*) *simp-all*

**lemma** *ta-bisim-init-fin-bisim-convert* [simp]:

*ta-bisim init-fin-bisim (convert-TA-initial (convert-obs-initial ta1)) (convert-TA-initial (convert-obs-initial ta2))*  $\longleftrightarrow$  *ta1*  $\sim_m$  *ta2*

**by**(auto simp add: ta-bisim-def list-all2-map1 list-all2-map2)

**lemma** *ta-bisim-init-fin-bisim-InitialThreadAction* [simp]:

*ta-bisim init-fin-bisim* {InitialThreadAction} {InitialThreadAction}  
**by**(simp add: ta-bisim-def)

**lemma** *ta-bisim-init-fin-bisim-ThreadFinishAction* [simp]:

*ta-bisim init-fin-bisim* {ThreadFinishAction} {ThreadFinishAction}  
**by**(simp add: ta-bisim-def)

**lemma** *init-fin-bisim-wait-simps* [simp]:

$(\text{status1}, x1) \approx_{iw} (\text{status2}, x2) \longleftrightarrow \text{status1} = \text{Running} \wedge \text{status2} = \text{Running} \wedge x1 \approx_w x2$   
**by**(simp add: init-fin-bisim-wait-def)

**lemma** *init-fin-lift-state-mbisimI*:

$s \approx_m s' \implies$   
*FWbisimulation-base.mbisim init-fin-bisim init-fin-bisim-wait* (*init-fin-lift-state Running s*) (*init-fin-lift-state Running s'*)  
**apply**(rule *FWbisimulation-base.mbisimI*)  
**apply**(simp add: thr-init-fin-list-state' o-def dom-map-option mbisim-finite1)  
**apply**(simp add: locks-init-fin-lift-state mbisim-def)  
**apply**(simp add: wset-init-fin-lift-state mbisim-def)  
**apply**(simp add: interrupts-init-fin-lift-state mbisim-def)  
**apply**(clarsimp simp add: wset-init-fin-lift-state mbisim-def thr-init-fin-list-state' o-def wset-thread-ok-conv-dom dom-map-option del: subsetI)  
**apply**(drule-tac  $t=t$  in mbisim-thrNone-eq)  
**apply**(simp add: thr-init-fin-list-state)  
**apply**(clarsimp simp add: thr-init-fin-list-state shr-init-fin-lift-state wset-init-fin-lift-state init-fin-bisim-iff)  
**apply**(frule (1) mbisim-thrD1)  
**apply**(simp add: mbisim-def)  
**done**

**end**

**context** *FWdelay-bisimulation-base* **begin**

**lemma** *init-fin-delay-bisimulation-final-base*:

*delay-bisimulation-final-base* (*r1.init-fin t*) (*r2.init-fin t*) (*init-fin-bisim t*)  
 $r1.\text{init-fin-}\tau\text{move } r2.\text{init-fin-}\tau\text{move } (\lambda(x1, m). r1.\text{init-fin-final } x1) (\lambda(x2, m). r2.\text{init-fin-final } x2)$   
**by**(unfold-locales)(auto 4 3)

**end**

**lemma** *init-fin-bisim-flip* [flip-simps]:

*FWbisimulation-base.init-fin-bisim final2 final1* ( $\lambda t. \text{flip } (\text{bisim } t)$ ) =  
 $(\lambda t. \text{flip } (\text{FWbisimulation-base.init-fin-bisim final1 final2 bisim } t))$   
**by**(auto simp only: *FWbisimulation-base.init-fin-bisim-iff flip-simps fun-eq-iff split-paired-Ex*)

**lemma** *init-fin-bisim-wait-flip* [flip-simps]:

*FWbisimulation-base.init-fin-bisim-wait* (*flip bisim-wait*) =  
 $\text{flip } (\text{FWbisimulation-base.init-fin-bisim-wait bisim-wait})$   
**by**(auto simp add: fun-eq-iff *FWbisimulation-base.init-fin-bisim-wait-simps flip-simps*)

**context** *FWdelay-bisimulation-lift-aux* **begin**

**lemma** *init-fin-FWdelay-bisimulation-lift-aux*:

*FWdelay-bisimulation-lift-aux*  $r1.init-fin-final\ r1.init-fin\ r2.init-fin-final\ r2.init-fin\ r1.init-fin-\tau move$   
 $r2.init-fin-\tau move$   
**by**(*intro FWdelay-bisimulation-lift-aux.intro*  $r1.\tau multithreaded-wf-init-fin\ r2.\tau multithreaded-wf-init-fin$ )

**lemma** *init-fin-FWdelay-bisimulation-final-base*:

*FWdelay-bisimulation-final-base*  
 $r1.init-fin-final\ r1.init-fin\ r2.init-fin-final\ r2.init-fin$   
 $init-fin-bisim\ r1.init-fin-\tau move\ r2.init-fin-\tau move$

**by**(*intro FWdelay-bisimulation-final-base.intro init-fin-FWdelay-bisimulation-lift-aux FWdelay-bisimulation-final-b*  
*init-fin-delay-bisimulation-final-base*)

**end**

**context** *FWdelay-bisimulation-obs* **begin**

**lemma** *init-fin-simulation1*:

**assumes** *bisim*:  $t \vdash s1 \approx_i s2$   
**and** *red1*:  $r1.init-fin\ t\ s1\ tl1\ s1'$   
**and**  $\tau 1$ :  $\neg r1.init-fin-\tau move\ s1\ tl1\ s1'$   
**shows**  $\exists s2'\ s2''\ tl2. (\tau trsys.silent-move\ (r2.init-fin\ t)\ r2.init-fin-\tau move)^{**}\ s2\ s2' \wedge$   
 $r2.init-fin\ t\ s2'\ tl2\ s2'' \wedge \neg r2.init-fin-\tau move\ s2'\ tl2\ s2'' \wedge$   
 $t \vdash s1' \approx_i s2'' \wedge ta-bisim\ init-fin-bisim\ tl1\ tl2$

**proof** –

**from** *bisim* **obtain** *status*  $x1\ m1\ x2\ m2$   
**where**  $s1$ :  $s1 = ((status, x1), m1)$   
**and**  $s2$ :  $s2 = ((status, x2), m2)$   
**and** *bisim*:  $t \vdash (x1, m1) \approx (x2, m2)$   
**and** *finished*:  $status = Finished \implies final1\ x1 \wedge final2\ x2$   
**by**(*cases*  $s1$ )(*cases*  $s2$ , *fastforce simp add: init-fin-bisim-iff*)

**from** *red1* **show** *?thesis* **unfolding**  $s1$

**proof**(*cases*)

**case** (*NormalAction*  $ta1\ x1'\ m1'$ )

**with**  $\tau 1\ s1$  **have**  $\neg \tau move1\ (x1, m1)\ ta1\ (x1', m1')$  **by**(*simp*)

**from** *simulation1*[*OF* *bisim*  $\langle t \vdash (x1, m1) -1-ta1 \rightarrow (x1', m1') \rangle$  *this*]

**obtain**  $x2'\ m2'\ x2''\ m2''\ ta2$

**where** *red2*:  $r2.silent-moves\ t\ (x2, m2)\ (x2', m2')$

**and** *red2'*:  $t \vdash (x2', m2') -2-ta2 \rightarrow (x2'', m2'')$

**and**  $\tau 2$ :  $\neg \tau move2\ (x2', m2')\ ta2\ (x2'', m2'')$

**and** *bisim'*:  $t \vdash (x1', m1') \approx (x2'', m2'')$

**and** *tasim*:  $ta1 \sim_m ta2$  **by** *auto*

**let**  $?s2' = ((Running, x2'), m2')$

**let**  $?s2'' = ((Running, x2''), m2'')$

**let**  $?ta2 = (convert-TA-initial\ (convert-obs-initial\ ta2))$

**from** *red2* **have**  $\tau trsys.silent-moves\ (r2.init-fin\ t)\ r2.init-fin-\tau move\ s2\ ?s2'$

**unfolding**  $s2\ \langle status = Running \rangle$  **by**(*rule*  $r2.init-fin-silent-moves-RunningI$ )

**moreover from** *red2'* **have**  $r2.init-fin\ t\ ?s2'\ ?ta2\ ?s2''$  **by**(*rule*  $r2.init-fin.NormalAction$ )

**moreover from**  $\tau 2$  **have**  $\neg r2.init-fin-\tau move\ ?s2'\ ?ta2\ ?s2''$  **by** *simp*

**moreover from** *bisim'* **have**  $t \vdash s1' \approx_i ?s2''$  **using**  $\langle s1' = ((Running, x1'), m1') \rangle$  **by** *simp*

**moreover from** *tasim*  $\langle tl1 = convert-TA-initial\ (convert-obs-initial\ ta1) \rangle$

**have** *ta-bisim* *init-fin-bisim*  $tl1\ ?ta2$  **by** *simp*

**ultimately show** *?thesis* **by** *blast*

**next**

**case** *InitialThreadAction*

**with**  $s1\ s2\ bisim$  **show** *?thesis* **by**(*auto simp del: split-paired-Ex*)

**next**  
**case** *ThreadFinishAction*  
**from** *final1-simulation*[*OF bisim*]  $\langle \text{final1 } x1 \rangle$   
**obtain**  $x2' \ m2'$  **where** *red2*: *r2.silent-moves* *t* ( $x2, m2$ ) ( $x2', m2'$ )  
**and** *bisim'*:  $t \vdash (x1, m1) \approx (x2', m2')$   
**and** *fin2*: *final2*  $x2'$  **by** *auto*  
**let**  $?s2' = ((\text{Running}, x2'), m2')$   
**let**  $?s2'' = ((\text{Finished}, x2'), m2')$   
**from** *red2* **have** *trsys.silent-moves* (*r2.init-fin* *t*) *r2.init-fin- $\tau$ move* *s2*  $?s2'$   
**unfolding** *s2*  $\langle \text{status} = \text{Running} \rangle$  **by** (*rule* *r2.init-fin-silent-moves-RunningI*)  
**moreover from** *fin2* **have** *r2.init-fin* *t*  $?s2'$   $\{\{\text{ThreadFinishAction}\}\}$   $?s2''$  ..  
**moreover have**  $\neg$  *r2.init-fin- $\tau$ move*  $?s2'$   $\{\{\text{ThreadFinishAction}\}\}$   $?s2''$  **by** *simp*  
**moreover have**  $t \vdash s1' \approx_i ?s2''$   
**using**  $\langle s1' = ((\text{Finished}, x1), m1) \rangle$  *fin2*  $\langle \text{final1 } x1 \rangle$  *bisim'* **by** *simp*  
**ultimately show** *?thesis* **unfolding**  $\langle \text{tl1} = \{\{\text{ThreadFinishAction}\}\} \rangle$   
**by** (*blast intro: ta-bisim-init-fin-bisim-ThreadFinishAction*)  
**qed**  
**qed**

**lemma** *init-fin-simulation2*:  
 $\llbracket t \vdash s1 \approx_i s2; r2.\text{init-fin } t \ s2 \ \text{tl2} \ s2'; \neg r2.\text{init-fin-}\tau\text{move } s2 \ \text{tl2} \ s2' \rrbracket$   
 $\implies \exists s1' \ s1'' \ \text{tl1}. (\text{trsys.silent-move } (r1.\text{init-fin } t) \ r1.\text{init-fin-}\tau\text{move})^{**} \ s1 \ s1' \wedge$   
 $r1.\text{init-fin } t \ s1' \ \text{tl1} \ s1'' \wedge \neg r1.\text{init-fin-}\tau\text{move } s1' \ \text{tl1} \ s1'' \wedge$   
 $t \vdash s1'' \approx_i s2' \wedge \text{ta-bisim init-fin-bisim } \text{tl1} \ \text{tl2}$   
**using** *FWdelay-bisimulation-obs.init-fin-simulation1*[*OF FWdelay-bisimulation-obs-flip*]  
**unfolding** *flip-simps* .

**lemma** *init-fin-simulation-Wakeup1*:  
**assumes** *bisim*:  $t \vdash (sx1, m1) \approx_i (sx2, m2)$   
**and** *wait*:  $sx1 \approx_{iw} sx2$   
**and** *red1*: *r1.init-fin* *t* ( $sx1, m1$ ) *ta1* ( $sx1', m1'$ )  
**and** *wakeup*: *Notified*  $\in \text{set } \{\{ta1\}\}_w \vee \text{WokenUp} \in \text{set } \{\{ta1\}\}_w$   
**shows**  $\exists ta2 \ sx2' \ m2'. r2.\text{init-fin } t \ (sx2, m2) \ ta2 \ (sx2', m2') \wedge t \vdash (sx1', m1') \approx_i (sx2', m2') \wedge$   
 $\text{ta-bisim init-fin-bisim } ta1 \ ta2$

**proof** –  
**from** *bisim wait* **obtain** *status*  $x1 \ x2$   
**where**  $sx1: sx1 = (\text{status}, x1)$   
**and**  $sx2: sx2 = (\text{status}, x2)$   
**and** *Bisim*:  $t \vdash (x1, m1) \approx (x2, m2)$   
**and** *Wait*:  $x1 \approx_w x2$  **by** *cases auto*  
**from** *red1 wakeup* *sx1* **obtain**  $x1' \ ta1'$   
**where**  $sx1': sx1' = (\text{Running}, x1')$   
**and** *status*: *status* = *Running*  
**and** *Red1*:  $t \vdash (x1, m1) -1-ta1' \rightarrow (x1', m1')$   
**and** *ta1*: *ta1* = *convert-TA-initial* (*convert-obs-initial* *ta1'*)  
**and** *Wakeup*: *Notified*  $\in \text{set } \{\{ta1'\}\}_w \vee \text{WokenUp} \in \text{set } \{\{ta1'\}\}_w$   
**by** *cases auto*  
**from** *simulation-Wakeup1*[*OF Bisim Wait Red1 Wakeup*] **obtain**  $ta2' \ x2' \ m2'$   
**where** *red2*:  $t \vdash (x2, m2) -2-ta2' \rightarrow (x2', m2')$   
**and** *bisim'*:  $t \vdash (x1', m1') \approx (x2', m2')$   
**and** *tasim*:  $ta1' \sim_m ta2'$  **by** *blast*  
**let**  $?sx2' = (\text{Running}, x2')$   
**let**  $?ta2 = \text{convert-TA-initial } (\text{convert-obs-initial } ta2')$   
**from** *red2* **have** *r2.init-fin* *t* ( $sx2, m2$ )  $?ta2 \ (?sx2', m2')$  **unfolding** *sx2 status* ..

moreover from *bisim' sx1'* have  $t \vdash (sx1', m1') \approx_i (?sx2', m2')$  by *simp*  
 moreover from *tasim ta1* have *ta-bisim init-fin-bisim ta1 ?ta2* by *simp*  
 ultimately show *?thesis* by *blast*  
**qed**

**lemma** *init-fin-simulation-Wakeup2*:

$\llbracket t \vdash (sx1, m1) \approx_i (sx2, m2); sx1 \approx_{iw} sx2; r2.\text{init-fin } t (sx2, m2) \text{ ta2 } (sx2', m2');$   
 $\text{Notified} \in \text{set } \llbracket \text{ta2} \rrbracket_w \vee \text{WokenUp} \in \text{set } \llbracket \text{ta2} \rrbracket_w \rrbracket$   
 $\implies \exists ta1 \text{ sx1' m1'}. r1.\text{init-fin } t (sx1, m1) \text{ ta1 } (sx1', m1') \wedge t \vdash (sx1', m1') \approx_i (sx2', m2') \wedge$   
 $\text{ta-bisim init-fin-bisim ta1 ta2}$

**using** *FWdelay-bisimulation-obs.init-fin-simulation-Wakeup1*[*OF FWdelay-bisimulation-obs-flip*]  
**unfolding** *flip-simps* .

**lemma** *init-fin-delay-bisimulation-obs*:

*delay-bisimulation-obs* (*r1.init-fin t*) (*r2.init-fin t*) (*init-fin-bisim t*) (*ta-bisim init-fin-bisim*)  
*r1.init-fin- $\tau$ move* *r2.init-fin- $\tau$ move*

**by**(*unfold-locales*)(*erule* (2) *init-fin-simulation1 init-fin-simulation2*)+

**lemma** *init-fin-FWdelay-bisimulation-obs*:

*FWdelay-bisimulation-obs* *r1.init-fin-final* *r1.init-fin* *r2.init-fin-final* *r2.init-fin* *init-fin-bisim* *init-fin-bisim-wait*  
*r1.init-fin- $\tau$ move* *r2.init-fin- $\tau$ move*

**proof**(*intro FWdelay-bisimulation-obs.intro init-fin-FWdelay-bisimulation-final-base FWdelay-bisimulation-obs-ax*  
*init-fin-delay-bisimulation-obs*)

**fix** *t' sx m1 sxx m2 t sx1 sx2 sx1' ta1 sx1'' m1' sx2' ta2 sx2'' m2'*  
**assume** *bisim*:  $t' \vdash (sx, m1) \approx_i (sxx, m2)$   
**and** *bisim1*:  $t \vdash (sx1, m1) \approx_i (sx2, m2)$   
**and** *red1*:  *$\tau$ trsys.silent-moves* (*r1.init-fin t*) *r1.init-fin- $\tau$ move* (*sx1, m1*) (*sx1', m1*)  
**and** *red1'*: *r1.init-fin t* (*sx1', m1*) *ta1* (*sx1'', m1'*)  
**and**  *$\tau1$* :  $\neg r1.\text{init-fin-}\tau\text{move } (sx1', m1) \text{ ta1 } (sx1'', m1')$   
**and** *red2*:  *$\tau$ trsys.silent-moves* (*r2.init-fin t*) *r2.init-fin- $\tau$ move* (*sx2, m2*) (*sx2', m2*)  
**and** *red2'*: *r2.init-fin t* (*sx2', m2*) *ta2* (*sx2'', m2'*)  
**and**  *$\tau2$* :  $\neg r2.\text{init-fin-}\tau\text{move } (sx2', m2) \text{ ta2 } (sx2'', m2')$   
**and** *bisim1'*:  $t \vdash (sx1'', m1') \approx_i (sx2'', m2')$   
**and** *tasim*: *ta-bisim init-fin-bisim ta1 ta2*

**from** *bisim* **obtain** *status x xx*

**where** *sx:sx* = (*status, x*)  
**and** *sxx:sxx* = (*status, xx*)  
**and** *Bisim*:  $t' \vdash (x, m1) \approx (xx, m2)$   
**and** *Finish*: *status* = *Finished*  $\implies$  *final1 x*  $\wedge$  *final2 xx*  
**by**(*cases sx*)(*cases sxx, auto simp add: init-fin-bisim-iff*)

**from** *bisim1* **obtain** *status1 x1 x2*

**where** *sx1*: *sx1* = (*status1, x1*)  
**and** *sx2*: *sx2* = (*status1, x2*)  
**and** *Bisim1*:  $t \vdash (x1, m1) \approx (x2, m2)$   
**by**(*cases sx1*)(*cases sx2, auto simp add: init-fin-bisim-iff*)

**from** *bisim1'* **obtain** *status1' x1'' x2''*

**where** *sx1''*: *sx1''* = (*status1', x1''*)  
**and** *sx2''*: *sx2''* = (*status1', x2''*)  
**and** *Bisim1'*:  $t \vdash (x1'', m1') \approx (x2'', m2')$   
**by**(*cases sx1''*)(*cases sx2'', auto simp add: init-fin-bisim-iff*)

**from** *red1 sx1* **obtain** *x1'* **where** *sx1'*: *sx1'* = (*status1, x1'*)

**and** *Red1*: *r1.silent-moves t* (*x1, m1*) (*x1', m1*)  
**by**(*cases sx1'*)(*auto dest: r1.init-fin-silent-movesD*)

**from** *red2 sx2* **obtain** *x2'* **where** *sx2'*: *sx2'* = (*status1, x2'*)

```

    and Red2: r2.silent-moves t (x2, m2) (x2', m2)
    by(cases sx2')(auto dest: r2.init-fin-silent-movesD)
show t' ⊢ (sx, m1') ≈i (sxx, m2')
proof(cases status1 = Running ∧ status1' = Running)
  case True
  with red1' sx1' sx1'' obtain ta1'
    where Red1': t ⊢ (x1', m1) -1-ta1' → (x1'', m1')
    and ta1: ta1 = convert-TA-initial (convert-obs-initial ta1')
    by cases auto
  from red2' sx2' sx2'' True obtain ta2'
    where Red2': t ⊢ (x2', m2) -2-ta2' → (x2'', m2')
    and ta2: ta2 = convert-TA-initial (convert-obs-initial ta2')
    by cases auto
  from τ1 sx1' sx1'' ta1 True have τ1': ¬ τmove1 (x1', m1) ta1' (x1'', m1') by simp
  from τ2 sx2' sx2'' ta2 True have τ2': ¬ τmove2 (x2', m2) ta2' (x2'', m2') by simp
  from tasim ta1 ta2 have ta1' ~m ta2' by simp
  with Bisim Bisim1 Red1 Red1' τ1' Red2 Red2' τ2' Bisim1'
  have t' ⊢ (x, m1') ≈ (xx, m2') by(rule bisim-inv-red-other)
  with True Finish show ?thesis unfolding sx sxx by(simp add: init-fin-bisim-iff)
next
  case False
  with red1' sx1' sx1'' have m1' = m1 by cases auto
  moreover from red2' sx2' sx2'' False have m2' = m2 by cases auto
  ultimately show ?thesis using bisim by simp
qed
next
fix t sx1 m1 sx2 m2 sx1' ta1 sx1'' m1' sx2' ta2 sx2'' m2' w
assume bisim: t ⊢ (sx1, m1) ≈i (sx2, m2)
  and red1: τtrsys.silent-moves (r1.init-fin t) r1.init-fin-τmove (sx1, m1) (sx1', m1)
  and red1': r1.init-fin t (sx1', m1) ta1 (sx1'', m1')
  and τ1: ¬ r1.init-fin-τmove (sx1', m1) ta1 (sx1'', m1')
  and red2: τtrsys.silent-moves (r2.init-fin t) r2.init-fin-τmove (sx2, m2) (sx2', m2)
  and red2': r2.init-fin t (sx2', m2) ta2 (sx2'', m2')
  and τ2: ¬ r2.init-fin-τmove (sx2', m2) ta2 (sx2'', m2')
  and bisim': t ⊢ (sx1'', m1') ≈i (sx2'', m2')
  and tasim: ta-bisim init-fin-bisim ta1 ta2
  and suspend1: Suspend w ∈ set {ta1}w
  and suspend2: Suspend w ∈ set {ta2}w
from bisim obtain status x1 x2
  where sx1: sx1 = (status, x1)
  and sx2: sx2 = (status, x2)
  and Bisim: t ⊢ (x1, m1) ≈ (x2, m2)
  by(cases sx1)(cases sx2, auto simp add: init-fin-bisim-iff)
from bisim' obtain status' x1'' x2''
  where sx1'': sx1'' = (status', x1'')
  and sx2'': sx2'' = (status', x2'')
  and Bisim': t ⊢ (x1'', m1') ≈ (x2'', m2')
  by(cases sx1'')(cases sx2'', auto simp add: init-fin-bisim-iff)
from red1 sx1 obtain x1' where sx1': sx1' = (status, x1')
  and Red1: r1.silent-moves t (x1, m1) (x1', m1)
  by(cases sx1')(auto dest: r1.init-fin-silent-movesD)
from red2 sx2 obtain x2' where sx2': sx2' = (status, x2')
  and Red2: r2.silent-moves t (x2, m2) (x2', m2)
  by(cases sx2')(auto dest: r2.init-fin-silent-movesD)

```

```

from  $red1' \text{ } sx1' \text{ } sx1'' \text{ } suspend1$  obtain  $ta1'$ 
  where  $Red1': t \vdash (x1', m1) -1-ta1' \rightarrow (x1'', m1')$ 
  and  $ta1: ta1 = \text{convert-TA-initial} (\text{convert-obs-initial } ta1')$ 
  and  $Suspend1: Suspend \ w \in \text{set } \{ta1'\}_w$ 
  and  $status: status = \text{Running } status' = \text{Running}$  by cases auto
from  $red2' \text{ } sx2' \text{ } sx2'' \text{ } suspend2$  obtain  $ta2'$ 
  where  $Red2': t \vdash (x2', m2) -2-ta2' \rightarrow (x2'', m2')$ 
  and  $ta2: ta2 = \text{convert-TA-initial} (\text{convert-obs-initial } ta2')$ 
  and  $Suspend2: Suspend \ w \in \text{set } \{ta2'\}_w$  by cases auto
from  $\tau1 \text{ } sx1' \text{ } sx1'' \text{ } ta1 \text{ } status$  have  $\tau1': \neg \tau \text{move1} \ (x1', m1) \ ta1' \ (x1'', m1')$  by simp
from  $\tau2 \text{ } sx2' \text{ } sx2'' \text{ } ta2 \text{ } status$  have  $\tau2': \neg \tau \text{move2} \ (x2', m2) \ ta2' \ (x2'', m2')$  by simp
from  $tasim \ ta1 \ ta2$  have  $ta1' \sim_m ta2'$  by simp
with  $Bisim \ Red1 \ Red1' \ \tau1' \ Red2 \ Red2' \ \tau2' \ Bisim'$  have  $x1'' \approx_w x2''$ 
  using  $Suspend1 \ Suspend2$  by(rule bisim-waitI)
thus  $sx1'' \approx_{iw} sx2''$  using  $sx1'' \text{ } sx2'' \text{ } status$  by simp
next
  fix  $t \text{ } sx1 \text{ } m1 \text{ } sx2 \text{ } m2 \text{ } ta1 \text{ } sx1' \text{ } m1'$ 
  assume  $t \vdash (sx1, m1) \approx_i (sx2, m2)$  and  $sx1 \approx_{iw} sx2$ 
    and  $r1.\text{init-fin} \ t \ (sx1, m1) \ ta1 \ (sx1', m1')$ 
    and  $Notified \in \text{set } \{ta1'\}_w \vee WokenUp \in \text{set } \{ta1'\}_w$ 
  thus  $\exists \ ta2 \text{ } sx2' \text{ } m2'. \ r2.\text{init-fin} \ t \ (sx2, m2) \ ta2 \ (sx2', m2') \wedge t \vdash (sx1', m1') \approx_i (sx2', m2') \wedge$ 
     $ta\text{-bisim} \ \text{init-fin-bisim} \ ta1 \ ta2$ 
    by(rule init-fin-simulation-Wakeup1)
  next
    fix  $t \text{ } sx1 \text{ } m1 \text{ } sx2 \text{ } m2 \text{ } ta2 \text{ } sx2' \text{ } m2'$ 
    assume  $t \vdash (sx1, m1) \approx_i (sx2, m2)$  and  $sx1 \approx_{iw} sx2$ 
      and  $r2.\text{init-fin} \ t \ (sx2, m2) \ ta2 \ (sx2', m2')$ 
      and  $Notified \in \text{set } \{ta2'\}_w \vee WokenUp \in \text{set } \{ta2'\}_w$ 
    thus  $\exists \ ta1 \text{ } sx1' \text{ } m1'. \ r1.\text{init-fin} \ t \ (sx1, m1) \ ta1 \ (sx1', m1') \wedge t \vdash (sx1', m1') \approx_i (sx2', m2') \wedge$ 
       $ta\text{-bisim} \ \text{init-fin-bisim} \ ta1 \ ta2$ 
      by(rule init-fin-simulation-Wakeup2)
    next
      show  $(\exists \ sx1. \ r1.\text{init-fin-final} \ sx1) = (\exists \ sx2. \ r2.\text{init-fin-final} \ sx2)$ 
      using  $ex\text{-final1-conv-ex-final2}$  by(auto)
  qed

end

context FWdelay-bisimulation-diverge begin

lemma init-fin-simulation-silent1:
   $\llbracket t \vdash \text{sxm1} \approx_i \text{sxm2}; \tau \text{trsys}.\text{silent-move} \ (r1.\text{init-fin} \ t) \ r1.\text{init-fin-}\tau \text{move} \ \text{sxm1} \ \text{sxm1}' \rrbracket$ 
   $\implies \exists \ \text{sxm2}'. \ \tau \text{trsys}.\text{silent-moves} \ (r2.\text{init-fin} \ t) \ r2.\text{init-fin-}\tau \text{move} \ \text{sxm2} \ \text{sxm2}' \wedge t \vdash \text{sxm1}' \approx_i \text{sxm2}'$ 
by(cases sxm1')(auto 4 4 elim!: init-fin-bisim.cases dest!: r1.init-fin-silent-moveD dest: simulation-silent1
intro!: r2.init-fin-silent-moves-RunningI)

lemma init-fin-simulation-silent2:
   $\llbracket t \vdash \text{sxm1} \approx_i \text{sxm2}; \tau \text{trsys}.\text{silent-move} \ (r2.\text{init-fin} \ t) \ r2.\text{init-fin-}\tau \text{move} \ \text{sxm2} \ \text{sxm2}' \rrbracket$ 
   $\implies \exists \ \text{sxm1}'. \ \tau \text{trsys}.\text{silent-moves} \ (r1.\text{init-fin} \ t) \ r1.\text{init-fin-}\tau \text{move} \ \text{sxm1} \ \text{sxm1}' \wedge t \vdash \text{sxm1}' \approx_i \text{sxm2}'$ 
using FWdelay-bisimulation-diverge.init-fin-simulation-silent1 [OF FWdelay-bisimulation-diverge-flip]
unfolding flip-simps .

lemma init-fin-τ-diverge-bisim-inv:
   $t \vdash \text{sxm1} \approx_i \text{sxm2}$ 

```



$\implies \tau \text{trsys}.\tau \text{diverge } (r1.\text{init-fin } t) \ r1.\text{init-fin-}\tau \text{move } sxm1 =$   
 $\tau \text{trsys}.\tau \text{diverge } (r2.\text{init-fin } t) \ r2.\text{init-fin-}\tau \text{move } sxm2$   
**by**(cases  $sxm1$ )(cases  $sxm2$ , auto simp add:  $r1.\text{init-fin-}\tau \text{diverge-conv}$   $r2.\text{init-fin-}\tau \text{diverge-conv}$   $\text{init-fin-bisim-iff}$   
 $\tau \text{diverge-bisim-inv}$ )

**lemma** *init-fin-delay-bisimulation-diverge*:

$\text{delay-bisimulation-diverge } (r1.\text{init-fin } t) \ (r2.\text{init-fin } t) \ (\text{init-fin-bisim } t) \ (\text{ta-bisim } \text{init-fin-bisim})$   
 $r1.\text{init-fin-}\tau \text{move } r2.\text{init-fin-}\tau \text{move}$

**by**(blast intro:  $\text{delay-bisimulation-diverge.intro}$   $\text{init-fin-delay-bisimulation-obs}$   $\text{delay-bisimulation-diverge-axioms.intro}$   
 $\text{init-fin-simulation-silent1}$   $\text{init-fin-simulation-silent2}$   $\text{init-fin-}\tau \text{diverge-bisim-inv}$  del:  $\text{iffI}$ ) $+$

**lemma** *init-fin-FWdelay-bisimulation-diverge*:

$\text{FWdelay-bisimulation-diverge } r1.\text{init-fin-final } r1.\text{init-fin } r2.\text{init-fin-final } r2.\text{init-fin } \text{init-fin-bisim } \text{init-fin-bisim-wait}$   
 $r1.\text{init-fin-}\tau \text{move } r2.\text{init-fin-}\tau \text{move}$

**by**(intro  $\text{FWdelay-bisimulation-diverge.intro}$   $\text{init-fin-FWdelay-bisimulation-obs}$   $\text{FWdelay-bisimulation-diverge-axioms.intro}$   
 $\text{init-fin-delay-bisimulation-diverge}$ )

**end**

**context** *FWbisimulation* **begin**

**lemma** *init-fin-simulation1*:

**assumes**  $t \vdash s1 \approx_i s2$  **and**  $r1.\text{init-fin } t \ s1 \ \text{tl1} \ s1'$

**shows**  $\exists s2' \ \text{tl2}. \ r2.\text{init-fin } t \ s2 \ \text{tl2} \ s2' \wedge t \vdash s1' \approx_i s2' \wedge \text{ta-bisim } \text{init-fin-bisim} \ \text{tl1} \ \text{tl2}$

**using**  $\text{init-fin-simulation1}[OF \ \text{assms}]$  **by**(auto simp add:  $\tau \text{moves-False}$   $\text{init-fin-}\tau \text{moves-False}$ )

**lemma** *init-fin-simulation2*:

$\llbracket t \vdash s1 \approx_i s2; \ r2.\text{init-fin } t \ s2 \ \text{tl2} \ s2' \rrbracket$

$\implies \exists s1' \ \text{tl1}. \ r1.\text{init-fin } t \ s1 \ \text{tl1} \ s1' \wedge t \vdash s1' \approx_i s2' \wedge \text{ta-bisim } \text{init-fin-bisim} \ \text{tl1} \ \text{tl2}$

**using**  $\text{FWbisimulation.init-fin-simulation1}[OF \ \text{FWbisimulation-flip}]$

**unfolding**  $\text{flip-simps}$  .

**lemma** *init-fin-bisimulation*:

$\text{bisimulation } (r1.\text{init-fin } t) \ (r2.\text{init-fin } t) \ (\text{init-fin-bisim } t) \ (\text{ta-bisim } \text{init-fin-bisim})$

**by**( $\text{unfold-locales}$ )(erule (1)  $\text{init-fin-simulation1}$   $\text{init-fin-simulation2}$ ) $+$

**lemma** *init-fin-FWbisimulation*:

$\text{FWbisimulation } r1.\text{init-fin-final } r1.\text{init-fin } r2.\text{init-fin-final } r2.\text{init-fin } \text{init-fin-bisim}$

**proof**(intro  $\text{FWbisimulation.intro}$   $r1.\text{multithreaded-init-fin}$   $r2.\text{multithreaded-init-fin}$   $\text{FWbisimulation-axioms.intro}$   
 $\text{init-fin-bisimulation}$ )

**fix**  $t \ sx1 \ m1 \ sx2 \ m2$

**assume**  $t \vdash (sx1, m1) \approx_i (sx2, m2)$

**thus**  $r1.\text{init-fin-final } sx1 = r2.\text{init-fin-final } sx2$

**by** cases simp-all

**next**

**fix**  $t' \ sx \ m1 \ sxx \ m2 \ t \ sx1 \ sx2 \ ta1 \ sx1' \ m1' \ ta2 \ sx2' \ m2'$

**assume**  $t' \vdash (sx, m1) \approx_i (sxx, m2)$   $t \vdash (sx1, m1) \approx_i (sx2, m2)$

**and**  $r1.\text{init-fin } t \ (sx1, m1) \ ta1 \ (sx1', m1')$

**and**  $r2.\text{init-fin } t \ (sx2, m2) \ ta2 \ (sx2', m2')$

**and**  $t \vdash (sx1', m1') \approx_i (sx2', m2')$

**and**  $\text{ta-bisim } \text{init-fin-bisim} \ ta1 \ ta2$

**from**  $\text{FWdelay-bisimulation-obs.bisim-inv-red-other}$

$[OF \ \text{init-fin-FWdelay-bisimulation-obs}, \ OF \ \text{this}(1-2) - \text{this}(3) - - \text{this}(4) - \text{this}(5-6)]$

**show**  $t' \vdash (sx, m1') \approx_i (sxx, m2')$  **by**(simp add:  $\text{init-fin-}\tau \text{moves-False}$ )

```
next
  show  $(\exists sx1. r1.init-fin-final\ sx1) = (\exists sx2. r2.init-fin-final\ sx2)$ 
    using ex-final1-conv-ex-final2 by(auto)
qed
end
end
```

## Chapter 2

# Data Flow Analysis Framework

### 2.1 Semilattices

```
theory Semilat
imports Main HOL-Library.While-Combinator
begin

type-synonym 'a ord    = 'a  $\Rightarrow$  'a  $\Rightarrow$  bool
type-synonym 'a binop  = 'a  $\Rightarrow$  'a  $\Rightarrow$  'a
type-synonym 'a sl     = 'a set  $\times$  'a ord  $\times$  'a binop

definition lesub :: 'a  $\Rightarrow$  'a ord  $\Rightarrow$  'a  $\Rightarrow$  bool
  where lesub x r y  $\longleftrightarrow$  r x y

definition lesssub :: 'a  $\Rightarrow$  'a ord  $\Rightarrow$  'a  $\Rightarrow$  bool
  where lesssub x r y  $\longleftrightarrow$  lesub x r y  $\wedge$  x  $\neq$  y

definition plussub :: 'a  $\Rightarrow$  ('a  $\Rightarrow$  'b  $\Rightarrow$  'c)  $\Rightarrow$  'b  $\Rightarrow$  'c
  where plussub x f y = f x y

notation (ASCII)
  lesub ((- /<='-- -) [50, 1000, 51] 50) and
  lesssub ((- /<'-- -) [50, 1000, 51] 50) and
  plussub ((- /+'-- -) [65, 1000, 66] 65)

notation
  lesub ((- / $\sqsubseteq$ - -) [50, 0, 51] 50) and
  lesssub ((- / $\sqsubset$ - -) [50, 0, 51] 50) and
  plussub ((- / $\sqcup$ - -) [65, 0, 66] 65)

abbreviation (input)
  lesub1 :: 'a  $\Rightarrow$  'a ord  $\Rightarrow$  'a  $\Rightarrow$  bool ((- / $\sqsubseteq$ - -) [50, 1000, 51] 50)
  where x  $\sqsubseteq_r$  y == x  $\sqsubseteq_r$  y

abbreviation (input)
  lesssub1 :: 'a  $\Rightarrow$  'a ord  $\Rightarrow$  'a  $\Rightarrow$  bool ((- / $\sqsubset$ - -) [50, 1000, 51] 50)
  where x  $\sqsubset_r$  y == x  $\sqsubset_r$  y

abbreviation (input)
```

*plussub1* :: 'a  $\Rightarrow$  ('a  $\Rightarrow$  'b  $\Rightarrow$  'c)  $\Rightarrow$  'b  $\Rightarrow$  'c ((- / $\sqsubseteq_r$  -) [65, 1000, 66] 65)  
**where**  $x \sqcup_f y == x \sqcup_f y$

**definition** *ord* :: ('a  $\times$  'a) *set*  $\Rightarrow$  'a *ord*

**where**

$ord\ r = (\lambda x\ y. (x, y) \in r)$

**definition** *order* :: 'a *ord*  $\Rightarrow$  *bool*

**where**

$order\ r \longleftrightarrow (\forall x. x \sqsubseteq_r x) \wedge (\forall x\ y. x \sqsubseteq_r y \wedge y \sqsubseteq_r x \longrightarrow x=y) \wedge (\forall x\ y\ z. x \sqsubseteq_r y \wedge y \sqsubseteq_r z \longrightarrow x \sqsubseteq_r z)$

**definition** *top* :: 'a *ord*  $\Rightarrow$  'a  $\Rightarrow$  *bool*

**where**

$top\ r\ T \longleftrightarrow (\forall x. x \sqsubseteq_r T)$

**definition** *acc* :: 'a *set*  $\Rightarrow$  'a *ord*  $\Rightarrow$  *bool*

**where**

$acc\ A\ r \longleftrightarrow wf\ \{(y, x). x \in A \wedge y \in A \wedge x \sqsubset_r y\}$

**definition** *closed* :: 'a *set*  $\Rightarrow$  'a *binop*  $\Rightarrow$  *bool*

**where**

$closed\ A\ f \longleftrightarrow (\forall x \in A. \forall y \in A. x \sqcup_f y \in A)$

**definition** *semilat* :: 'a *sl*  $\Rightarrow$  *bool*

**where**

$semilat = (\lambda(A, r, f). order\ r \wedge closed\ A\ f \wedge$   
 $(\forall x \in A. \forall y \in A. x \sqsubseteq_r x \sqcup_f y) \wedge$   
 $(\forall x \in A. \forall y \in A. y \sqsubseteq_r x \sqcup_f y) \wedge$   
 $(\forall x \in A. \forall y \in A. \forall z \in A. x \sqsubseteq_r z \wedge y \sqsubseteq_r z \longrightarrow x \sqcup_f y \sqsubseteq_r z))$

**definition** *is-ub* :: ('a  $\times$  'a) *set*  $\Rightarrow$  'a  $\Rightarrow$  'a  $\Rightarrow$  'a  $\Rightarrow$  *bool*

**where**

$is-ub\ r\ x\ y\ u \longleftrightarrow (x, u) \in r \wedge (y, u) \in r$

**definition** *is-lub* :: ('a  $\times$  'a) *set*  $\Rightarrow$  'a  $\Rightarrow$  'a  $\Rightarrow$  'a  $\Rightarrow$  *bool*

**where**

$is-lub\ r\ x\ y\ u \longleftrightarrow is-ub\ r\ x\ y\ u \wedge (\forall z. is-ub\ r\ x\ y\ z \longrightarrow (u, z) \in r)$

**definition** *some-lub* :: ('a  $\times$  'a) *set*  $\Rightarrow$  'a  $\Rightarrow$  'a  $\Rightarrow$  'a

**where**

$some-lub\ r\ x\ y = (SOME\ z. is-lub\ r\ x\ y\ z)$

**locale** *Semilat* =

**fixes**  $A :: 'a\ set$

**fixes**  $r :: 'a\ ord$

**fixes**  $f :: 'a\ binop$

**assumes** *semilat*: *semilat* (A, r, f)

**lemma** *order-refl* [*simp*, *intro*]:  $order\ r \Longrightarrow x \sqsubseteq_r x$

**lemma** *order-antisym*:  $\llbracket order\ r; x \sqsubseteq_r y; y \sqsubseteq_r x \rrbracket \Longrightarrow x = y$

**lemma** *order-trans*:  $\llbracket order\ r; x \sqsubseteq_r y; y \sqsubseteq_r z \rrbracket \Longrightarrow x \sqsubseteq_r z$

**lemma** *order-less-irrefl* [*intro*, *simp*]:  $\text{order } r \implies \neg x \sqsubseteq_r x$

**lemma** *order-less-trans*:  $\llbracket \text{order } r; x \sqsubseteq_r y; y \sqsubseteq_r z \rrbracket \implies x \sqsubseteq_r z$

**lemma** *topD* [*simp*, *intro*]:  $\text{top } r \ T \implies x \sqsubseteq_r T$

**lemma** *top-le-conv* [*simp*]:  $\llbracket \text{order } r; \text{top } r \ T \rrbracket \implies (T \sqsubseteq_r x) = (x = T)$

**lemma** *semilat-Def*:

$\text{semilat}(A, r, f) \iff \text{order } r \wedge \text{closed } A \ f \wedge$   
 $(\forall x \in A. \forall y \in A. x \sqsubseteq_r x \sqcup_f y) \wedge$   
 $(\forall x \in A. \forall y \in A. y \sqsubseteq_r x \sqcup_f y) \wedge$   
 $(\forall x \in A. \forall y \in A. \forall z \in A. x \sqsubseteq_r z \wedge y \sqsubseteq_r z \longrightarrow x \sqcup_f y \sqsubseteq_r z)$

**lemma** (*in Semilat*) *orderI* [*simp*, *intro*]:  $\text{order } r$

**lemma** (*in Semilat*) *closedI* [*simp*, *intro*]:  $\text{closed } A \ f$

**lemma** *closedD*:  $\llbracket \text{closed } A \ f; x \in A; y \in A \rrbracket \implies x \sqcup_f y \in A$

**lemma** *closed-UNIV* [*simp*]:  $\text{closed } UNIV \ f$

**lemma** (*in Semilat*) *closed-f* [*simp*, *intro*]:  $\llbracket x \in A; y \in A \rrbracket \implies x \sqcup_f y \in A$

**lemma** (*in Semilat*) *refl-r* [*intro*, *simp*]:  $x \sqsubseteq_r x$  **by** *simp*

**lemma** (*in Semilat*) *antisym-r* [*intro?*]:  $\llbracket x \sqsubseteq_r y; y \sqsubseteq_r x \rrbracket \implies x = y$

**lemma** (*in Semilat*) *trans-r* [*trans*, *intro?*]:  $\llbracket x \sqsubseteq_r y; y \sqsubseteq_r z \rrbracket \implies x \sqsubseteq_r z$

**lemma** (*in Semilat*) *ub1* [*simp*, *intro?*]:  $\llbracket x \in A; y \in A \rrbracket \implies x \sqsubseteq_r x \sqcup_f y$

**lemma** (*in Semilat*) *ub2* [*simp*, *intro?*]:  $\llbracket x \in A; y \in A \rrbracket \implies y \sqsubseteq_r x \sqcup_f y$

**lemma** (*in Semilat*) *lub* [*simp*, *intro?*]:

$\llbracket x \sqsubseteq_r z; y \sqsubseteq_r z; x \in A; y \in A; z \in A \rrbracket \implies x \sqcup_f y \sqsubseteq_r z$

**lemma** (*in Semilat*) *plus-le-conv* [*simp*]:

$\llbracket x \in A; y \in A; z \in A \rrbracket \implies (x \sqcup_f y \sqsubseteq_r z) = (x \sqsubseteq_r z \wedge y \sqsubseteq_r z)$

**lemma** (*in Semilat*) *le-iff-plus-unchanged*:

**assumes**  $x \in A$  **and**  $y \in A$

**shows**  $x \sqsubseteq_r y \iff x \sqcup_f y = y$  (**is**  $?P \iff ?Q$ )

**lemma** (*in Semilat*) *le-iff-plus-unchanged2*:

**assumes**  $x \in A$  **and**  $y \in A$

**shows**  $x \sqsubseteq_r y \iff y \sqcup_f x = y$  (**is**  $?P \iff ?Q$ )

**lemma** (*in Semilat*) *plus-assoc* [*simp*]:

**assumes**  $a: a \in A$  **and**  $b: b \in A$  **and**  $c: c \in A$

**shows**  $a \sqcup_f (b \sqcup_f c) = a \sqcup_f b \sqcup_f c$

**lemma** (*in Semilat*) *plus-com-lemma*:

$\llbracket a \in A; b \in A \rrbracket \implies a \sqcup_f b \sqsubseteq_r b \sqcup_f a$

**lemma** (*in Semilat*) *plus-commutative*:

$\llbracket a \in A; b \in A \rrbracket \implies a \sqcup_f b = b \sqcup_f a$

**lemma** *is-lubD*:

$$is-lub\ r\ x\ y\ u \implies is-ub\ r\ x\ y\ u \wedge (\forall z. is-ub\ r\ x\ y\ z \longrightarrow (u, z) \in r)$$

**lemma** *is-ubI*:

$$\llbracket (x, u) \in r; (y, u) \in r \rrbracket \implies is-ub\ r\ x\ y\ u$$

**lemma** *is-ubD*:

$$is-ub\ r\ x\ y\ u \implies (x, u) \in r \wedge (y, u) \in r$$

**lemma** *is-lub-bigger1* [iff]:

$$is-lub\ (r^{\widehat{*}})\ x\ y\ y = ((x, y) \in r^{\widehat{*}})$$

**lemma** *is-lub-bigger2* [iff]:

$$is-lub\ (r^{\widehat{*}})\ x\ y\ x = ((y, x) \in r^{\widehat{*}})$$

**lemma** *extend-lub*:

**assumes** *single-valued*  $r$

**and** *is-lub*  $(r^*)\ x\ y\ u$

**and**  $(x', x) \in r$

**shows**  $\exists v. is-lub\ (r^*)\ x'\ y\ v$

**lemma** *single-valued-has-lubs*:

**assumes** *single-valued*  $r$

**and** *in-r*:  $(x, u) \in r^* (y, u) \in r^*$

**shows**  $\exists z. is-lub\ (r^*)\ x\ y\ z$

**lemma** *some-lub-conv*:

$$\llbracket acyclic\ r; is-lub\ (r^{\widehat{*}})\ x\ y\ u \rrbracket \implies some-lub\ (r^{\widehat{*}})\ x\ y = u$$

**lemma** *is-lub-some-lub*:

$$\llbracket single-valued\ r; acyclic\ r; (x, u) \in r^{\widehat{*}}; (y, u) \in r^{\widehat{*}} \rrbracket \\ \implies is-lub\ (r^{\widehat{*}})\ x\ y\ (some-lub\ (r^{\widehat{*}})\ x\ y)$$

### 2.1.1 An executable lub-finder

**definition** *exec-lub* ::  $('a * 'a)\ set \Rightarrow ('a \Rightarrow 'a) \Rightarrow 'a\ binop$

**where**

$$exec-lub\ r\ f\ x\ y = while\ (\lambda z. (x, z) \notin r^*)\ f\ y$$

**lemma** *exec-lub-refl*: *exec-lub*  $r\ f\ T\ T = T$

**by** (*simp add: exec-lub-def while-unfold*)

**lemma** *acyclic-single-valued-finite*:

$$\llbracket acyclic\ r; single-valued\ r; (x, y) \in r^* \rrbracket \\ \implies finite\ (r \cap \{a. (x, a) \in r^*\} \times \{b. (b, y) \in r^*\})$$

**lemma** *exec-lub-conv*:

$$\llbracket acyclic\ r; \forall x\ y. (x, y) \in r \longrightarrow f\ x = y; is-lub\ (r^*)\ x\ y\ u \rrbracket \implies \\ exec-lub\ r\ f\ x\ y = u$$

**lemma** *is-lub-exec-lub*:

$$\llbracket single-valued\ r; acyclic\ r; (x, u) : r^{\widehat{*}}; (y, u) : r^{\widehat{*}}; \forall x\ y. (x, y) \in r \longrightarrow f\ x = y \rrbracket \\ \implies is-lub\ (r^{\widehat{*}})\ x\ y\ (exec-lub\ r\ f\ x\ y)$$

**end**

## 2.2 The Error Type

```

theory Err
imports Semilat
begin

datatype 'a err = Err | OK 'a

type-synonym 'a ebinop = 'a  $\Rightarrow$  'a  $\Rightarrow$  'a err
type-synonym 'a esl = 'a set  $\times$  'a ord  $\times$  'a ebinop

primrec ok-val :: 'a err  $\Rightarrow$  'a
where
  ok-val (OK x) = x

definition lift :: ('a  $\Rightarrow$  'b err)  $\Rightarrow$  ('a err  $\Rightarrow$  'b err)
where
  lift f e = (case e of Err  $\Rightarrow$  Err | OK x  $\Rightarrow$  f x)

definition lift2 :: ('a  $\Rightarrow$  'b  $\Rightarrow$  'c err)  $\Rightarrow$  'a err  $\Rightarrow$  'b err  $\Rightarrow$  'c err
where
  lift2 f e1 e2 =
    (case e1 of Err  $\Rightarrow$  Err | OK x  $\Rightarrow$  (case e2 of Err  $\Rightarrow$  Err | OK y  $\Rightarrow$  f x y))

definition le :: 'a ord  $\Rightarrow$  'a err ord
where
  le r e1 e2 =
    (case e2 of Err  $\Rightarrow$  True | OK y  $\Rightarrow$  (case e1 of Err  $\Rightarrow$  False | OK x  $\Rightarrow$  x  $\sqsubseteq_r$  y))

definition sup :: ('a  $\Rightarrow$  'b  $\Rightarrow$  'c)  $\Rightarrow$  ('a err  $\Rightarrow$  'b err  $\Rightarrow$  'c err)
where
  sup f = lift2 ( $\lambda$ x y. OK (x  $\sqcup_f$  y))

definition err :: 'a set  $\Rightarrow$  'a err set
where
  err A = insert Err {OK x | x. x  $\in$  A}

definition esl :: 'a sl  $\Rightarrow$  'a esl
where
  esl = ( $\lambda$ (A,r,f). (A, r,  $\lambda$ x y. OK(f x y)))

definition sl :: 'a esl  $\Rightarrow$  'a err sl
where
  sl = ( $\lambda$ (A,r,f). (err A, le r, lift2 f))

abbreviation
  err-semilat :: 'a esl  $\Rightarrow$  bool where
  err-semilat L == semilat(sl L)

primrec strict :: ('a  $\Rightarrow$  'b err)  $\Rightarrow$  ('a err  $\Rightarrow$  'b err)
where
  strict f Err = Err
  | strict f (OK x) = f x

```

**lemma** *err-def'*:

$err\ A = insert\ Err\ \{x. \exists y \in A. x = OK\ y\}$

**lemma** *strict-Some* [simp]:

$(strict\ f\ x = OK\ y) = (\exists z. x = OK\ z \wedge f\ z = OK\ y)$

**lemma** *not-Err-eq*:  $(x \neq Err) = (\exists a. x = OK\ a)$

**lemma** *not-OK-eq*:  $(\forall y. x \neq OK\ y) = (x = Err)$

**lemma** *unfold-lesub-err*:  $e1 \sqsubseteq_{le\ r} e2 = le\ r\ e1\ e2$

**lemma** *le-err-refl*:  $\forall x. x \sqsubseteq_r x \implies e \sqsubseteq_{le\ r} e$

**lemma** *le-err-trans* [rule-format]:

$order\ r \implies e1 \sqsubseteq_{le\ r} e2 \longrightarrow e2 \sqsubseteq_{le\ r} e3 \longrightarrow e1 \sqsubseteq_{le\ r} e3$

**lemma** *le-err-antisym* [rule-format]:

$order\ r \implies e1 \sqsubseteq_{le\ r} e2 \longrightarrow e2 \sqsubseteq_{le\ r} e1 \longrightarrow e1 = e2$

**lemma** *OK-le-err-OK*:  $(OK\ x \sqsubseteq_{le\ r} OK\ y) = (x \sqsubseteq_r y)$

**lemma** *order-le-err* [iff]:  $order(le\ r) = order\ r$

**lemma** *le-Err* [iff]:  $e \sqsubseteq_{le\ r} Err$

**lemma** *Err-le-conv* [iff]:  $Err \sqsubseteq_{le\ r} e = (e = Err)$

**lemma** *le-OK-conv* [iff]:  $e \sqsubseteq_{le\ r} OK\ x = (\exists y. e = OK\ y \wedge y \sqsubseteq_r x)$

**lemma** *OK-le-conv*:  $OK\ x \sqsubseteq_{le\ r} e = (e = Err \vee (\exists y. e = OK\ y \wedge x \sqsubseteq_r y))$

**lemma** *top-Err* [iff]:  $top\ (le\ r)\ Err$

**lemma** *OK-less-conv* [rule-format, iff]:

$OK\ x \sqsubseteq_{le\ r} e = (e = Err \vee (\exists y. e = OK\ y \wedge x \sqsubseteq_r y))$

**lemma** *not-Err-less* [rule-format, iff]:  $\neg(Err \sqsubseteq_{le\ r} x)$

**lemma** *semilat-errI* [intro]: **assumes** *Semilat* *A* *r* *f*

**shows** *semilat*(*err* *A*, *le* *r*, *lift2*( $\lambda x\ y. OK(f\ x\ y)$ ))

**lemma** *err-semilat-eslI-aux*:

**assumes** *Semilat* *A* *r* *f* **shows** *err-semilat*(*esl*(*A*, *r*, *f*))

**lemma** *err-semilat-eslI* [intro, simp]:

$semilat\ L \implies err-semilat\ (esl\ L)$

**lemma** *acc-err* [simp, intro!]:  $acc\ A\ r \implies acc\ (err\ A)\ (le\ r)$

**lemma** *Err-in-err* [iff]:  $Err : err\ A$

**lemma** *Ok-in-err* [iff]:  $(OK\ x \in err\ A) = (x \in A)$

### 2.2.1 lift

**lemma** *lift-in-errI*:

$\llbracket e \in err\ S; \forall x \in S. e = OK\ x \longrightarrow f\ x \in err\ S \rrbracket \implies lift\ f\ e \in err\ S$

**lemma** *Err-lift2* [simp]:  $Err \sqcup_{lift2\ f} x = Err$

**lemma** *lift2-Err* [simp]:  $x \sqcup_{lift2\ f} Err = Err$

**lemma** *OK-lift2-OK* [simp]:  $OK\ x \sqcup_{lift2\ f} OK\ y = x \sqcup_f y$

### 2.2.2 sup

**lemma** *Err-sup-Err* [simp]:  $Err \sqcup_{sup\ f} x = Err$

**lemma** *Err-sup-Err2* [simp]:  $x \sqcup_{sup\ f} Err = Err$

**lemma** *Err-sup-OK* [simp]:  $OK\ x \sqcup_{sup\ f} OK\ y = OK\ (x \sqcup_f y)$

**lemma** *Err-sup-eq-OK-conv* [iff]:

$(sup\ f\ ex\ ey = OK\ z) = (\exists x\ y. ex = OK\ x \wedge ey = OK\ y \wedge f\ x\ y = z)$

**lemma** *Err-sup-eq-Err* [iff]:  $(sup\ f\ ex\ ey = Err) = (ex = Err \vee ey = Err)$

### 2.2.3 semilat (err A) (le r) f

**lemma** *semilat-le-err-Err-plus* [simp]:

$\llbracket x \in err\ A; semilat(err\ A, le\ r, f) \rrbracket \implies Err \sqcup_f x = Err$

**lemma** *semilat-le-err-plus-Err* [simp]:



$\llbracket x \in \text{err } A; \text{semilat}(\text{err } A, \text{le } r, f) \rrbracket \implies x \sqcup_f \text{Err} = \text{Err}$   
**lemma** *semilat-le-err-OK1*:  
 $\llbracket x \in A; y \in A; \text{semilat}(\text{err } A, \text{le } r, f); \text{OK } x \sqcup_f \text{OK } y = \text{OK } z \rrbracket$   
 $\implies x \sqsubseteq_r z$   
**lemma** *semilat-le-err-OK2*:  
 $\llbracket x \in A; y \in A; \text{semilat}(\text{err } A, \text{le } r, f); \text{OK } x \sqcup_f \text{OK } y = \text{OK } z \rrbracket$   
 $\implies y \sqsubseteq_r z$   
**lemma** *eq-order-le*:  
 $\llbracket x=y; \text{order } r \rrbracket \implies x \sqsubseteq_r y$   
**lemma** *OK-plus-OK-eq-Err-conv* [simp]:  
**assumes**  $x \in A \quad y \in A \quad \text{semilat}(\text{err } A, \text{le } r, f_e)$   
**shows**  $(\text{OK } x \sqcup_{f_e} \text{OK } y = \text{Err}) = (\neg(\exists z \in A. x \sqsubseteq_r z \wedge y \sqsubseteq_r z))$

## 2.2.4 semilat (err(Union AS))

**lemma** *all-bex-swap-lemma* [iff]:  
 $(\forall x. (\exists y \in A. x = f y) \longrightarrow P x) = (\forall y \in A. P(f y))$   
**lemma** *closed-err-Union-lift2I*:  
 $\llbracket \forall A \in AS. \text{closed}(\text{err } A) (\text{lift2 } f); AS \neq \{\};$   
 $\forall A \in AS. \forall B \in AS. A \neq B \longrightarrow (\forall a \in A. \forall b \in B. a \sqcup_f b = \text{Err}) \rrbracket$   
 $\implies \text{closed}(\text{err}(\text{Union } AS)) (\text{lift2 } f)$

If  $AS = \{\}$  the thm collapses to  $\text{order } r \wedge \text{closed } \{\text{Err}\} f \wedge \text{Err} \sqcup_f \text{Err} = \text{Err}$  which may not hold

**lemma** *err-semilat-UnionI*:  
 $\llbracket \forall A \in AS. \text{err-semilat}(A, r, f); AS \neq \{\};$   
 $\forall A \in AS. \forall B \in AS. A \neq B \longrightarrow (\forall a \in A. \forall b \in B. \neg a \sqsubseteq_r b \wedge a \sqcup_f b = \text{Err}) \rrbracket$   
 $\implies \text{err-semilat}(\text{Union } AS, r, f)$   
**end**

## 2.3 More about Options

**theory** *Opt*  
**imports**  
*Err*  
**begin**

**definition** *le* :: 'a ord  $\Rightarrow$  'a option ord  
**where**  
 $\text{le } r \ o_1 \ o_2 =$   
 $(\text{case } o_2 \text{ of } \text{None} \Rightarrow o_1 = \text{None} \mid \text{Some } y \Rightarrow (\text{case } o_1 \text{ of } \text{None} \Rightarrow \text{True} \mid \text{Some } x \Rightarrow x \sqsubseteq_r y))$

**definition** *opt* :: 'a set  $\Rightarrow$  'a option set  
**where**  
 $\text{opt } A = \text{insert } \text{None } \{\text{Some } y \mid y. y \in A\}$

**definition** *sup* :: 'a ebinop  $\Rightarrow$  'a option ebinop  
**where**  
 $\text{sup } f \ o_1 \ o_2 =$   
 $(\text{case } o_1 \text{ of } \text{None} \Rightarrow \text{OK } o_2$   
 $\mid \text{Some } x \Rightarrow (\text{case } o_2 \text{ of } \text{None} \Rightarrow \text{OK } o_1$   
 $\mid \text{Some } y \Rightarrow (\text{case } f \ x \ y \text{ of } \text{Err} \Rightarrow \text{Err} \mid \text{OK } z \Rightarrow \text{OK } (\text{Some } z))))$

**definition** *esl* :: 'a esl  $\Rightarrow$  'a option esl

where

$$esl = (\lambda(A, r, f). (opt\ A, le\ r, sup\ f))$$

**lemma** *unfold-le-opt*:

$$o_1 \sqsubseteq_{le\ r} o_2 = \\ (case\ o_2\ of\ None \Rightarrow o_1 = None \mid \\ \quad Some\ y \Rightarrow (case\ o_1\ of\ None \Rightarrow True \mid Some\ x \Rightarrow x \sqsubseteq_r y))$$

**lemma** *le-opt-refl*:  $order\ r \Longrightarrow x \sqsubseteq_{le\ r} x$

## 2.4 Products as Semilattices

**theory** *Product*

**imports** *Err*

**begin**

**definition** *le* ::  $'a\ ord \Rightarrow 'b\ ord \Rightarrow ('a \times 'b)\ ord$

where

$$le\ r_A\ r_B = (\lambda(a_1, b_1)\ (a_2, b_2). a_1 \sqsubseteq_{r_A} a_2 \wedge b_1 \sqsubseteq_{r_B} b_2)$$

**definition** *sup* ::  $'a\ ebinop \Rightarrow 'b\ ebinop \Rightarrow ('a \times 'b)\ ebinop$

where

$$sup\ f\ g = (\lambda(a_1, b_1)(a_2, b_2). Err.sup\ Pair\ (a_1 \sqcup_f a_2)\ (b_1 \sqcup_g b_2))$$

**definition** *esl* ::  $'a\ esl \Rightarrow 'b\ esl \Rightarrow ('a \times 'b)\ esl$

where

$$esl = (\lambda(A, r_A, f_A)\ (B, r_B, f_B). (A \times B, le\ r_A\ r_B, sup\ f_A\ f_B))$$

**abbreviation**

$$lesubprod :: 'a \times 'b \Rightarrow ('a \Rightarrow 'a \Rightarrow bool) \Rightarrow ('b \Rightarrow 'b \Rightarrow bool) \Rightarrow 'a \times 'b \Rightarrow bool \\ ((- / \sqsubseteq'(-, -) -) [50, 0, 0, 51] 50) \textbf{ where} \\ p \sqsubseteq(rA, rB)\ q == p \sqsubseteq_{Product.le\ rA\ rB}\ q$$

**lemma** *unfold-lesub-prod*:  $x \sqsubseteq(r_A, r_B)\ y = le\ r_A\ r_B\ x\ y$

**lemma** *le-prod-Pair-conv* [iiff]:  $((a_1, b_1) \sqsubseteq(r_A, r_B)\ (a_2, b_2)) = (a_1 \sqsubseteq_{r_A} a_2 \ \&\ b_1 \sqsubseteq_{r_B} b_2)$

**lemma** *less-prod-Pair-conv*:

$$((a_1, b_1) \sqsubset_{Product.le\ r_A\ r_B}\ (a_2, b_2)) = \\ (a_1 \sqsubset_{r_A} a_2 \ \&\ b_1 \sqsubset_{r_B} b_2 \mid a_1 \sqsubseteq_{r_A} a_2 \ \&\ b_1 \sqsubset_{r_B} b_2)$$

**lemma** *order-le-prod* [iiff]:  $order(Product.le\ r_A\ r_B) = (order\ r_A \ \&\ order\ r_B)$

**lemma** *acc-le-prodI* [intro!]:

$$\llbracket acc\ A\ r_A; acc\ B\ r_B \rrbracket \Longrightarrow acc\ (A \times B)\ (Product.le\ r_A\ r_B)$$

**lemma** *closed-lift2-sup*:

$$\llbracket closed\ (err\ A)\ (lift2\ f); closed\ (err\ B)\ (lift2\ g) \rrbracket \Longrightarrow \\ closed\ (err(A \times B))\ (lift2(sup\ f\ g))$$

**lemma** *unfold-plussub-lift2*:  $e_1 \sqcup_{lift2\ f} e_2 = lift2\ f\ e_1\ e_2$

**lemma** *plus-eq-Err-conv* [simp]:

$$\textbf{assumes}\ x \in A\ y \in A\ semilat(err\ A, Err.le\ r, lift2\ f) \\ \textbf{shows}\ (x \sqcup_f y = Err) = (\neg(\exists z \in A. x \sqsubseteq_r z \wedge y \sqsubseteq_r z))$$

**lemma** *err-semilat-Product-esl*:

$$\bigwedge L_1\ L_2. \llbracket err-semilat\ L_1; err-semilat\ L_2 \rrbracket \Longrightarrow err-semilat(Product.esl\ L_1\ L_2)$$

end

## 2.5 Fixed Length Lists

**theory** *Listn*  
**imports** *Err*  
**begin**

**definition** *list* :: *nat*  $\Rightarrow$  '*a* *set*  $\Rightarrow$  '*a* *list* *set*  
**where**  
*list* *n* *A* = {*xs*. *size xs* = *n*  $\wedge$  *set xs*  $\subseteq$  *A*}

**definition** *le* :: '*a* *ord*  $\Rightarrow$  ('*a* *list*)*ord*  
**where**  
*le* *r* = *list-all2* ( $\lambda x y. x \sqsubseteq_r y$ )

**abbreviation**  
*lesublist* :: '*a* *list*  $\Rightarrow$  '*a* *ord*  $\Rightarrow$  '*a* *list*  $\Rightarrow$  *bool* ((- / [ $\sqsubseteq$ -] -) [50, 0, 51] 50) **where**  
*x* [ $\sqsubseteq_r$ ] *y* == *x* <=-(*Listn.le* *r*) *y*

**abbreviation**  
*lesssublist* :: '*a* *list*  $\Rightarrow$  '*a* *ord*  $\Rightarrow$  '*a* *list*  $\Rightarrow$  *bool* ((- / [ $\sqsubseteq$ -] -) [50, 0, 51] 50) **where**  
*x* [ $\sqsubseteq_r$ ] *y* == *x* <-(*Listn.le* *r*) *y*

**abbreviation**  
*plussublist* :: '*a* *list*  $\Rightarrow$  ('*a*  $\Rightarrow$  '*b*  $\Rightarrow$  '*c*)  $\Rightarrow$  '*b* *list*  $\Rightarrow$  '*c* *list*  
 ((- / [ $\sqcup$ -] -) [65, 0, 66] 65) **where**  
*x* [ $\sqcup_f$ ] *y* == *x*  $\sqcup_{map2 f}$  *y*

**primrec** *coalesce* :: '*a* *err* *list*  $\Rightarrow$  '*a* *list* *err*  
**where**  
*coalesce* [] = *OK* []  
| *coalesce* (*ex* # *exs*) = *Err.sup* (#) *ex* (*coalesce* *exs*)

**definition** *sl* :: *nat*  $\Rightarrow$  '*a* *sl*  $\Rightarrow$  '*a* *list* *sl*  
**where**  
*sl* *n* = ( $\lambda(A,r,f). (list\ n\ A, le\ r, map2\ f)$ )

**definition** *sup* :: ('*a*  $\Rightarrow$  '*b*  $\Rightarrow$  '*c* *err*)  $\Rightarrow$  '*a* *list*  $\Rightarrow$  '*b* *list*  $\Rightarrow$  '*c* *list* *err*  
**where**  
*sup* *f* = ( $\lambda xs\ ys. if\ size\ xs = size\ ys\ then\ coalesce(xs\ [\sqcup_f]\ ys)\ else\ Err$ )

**definition** *upto-esl* :: *nat*  $\Rightarrow$  '*a* *esl*  $\Rightarrow$  '*a* *list* *esl*  
**where**  
*upto-esl* *m* = ( $\lambda(A,r,f). (Union\ \{list\ n\ A\ |\ n.\ n \leq m\}, le\ r, sup\ f)$ )

**lemmas** [*simp*] = *set-update-subsetI*

**lemma** *unfold-lesub-list*: *xs* [ $\sqsubseteq_r$ ] *ys* = *Listn.le* *r* *xs* *ys*

**lemma** *Nil-le-conv* [*iff*]: ([] [ $\sqsubseteq_r$ ] *ys*) = (*ys* = [])

**lemma** *Cons-notle-Nil* [*iff*]:  $\neg x \# xs\ [\sqsubseteq_r]\ []$

**lemma** *Cons-le-Cons* [iff]:  $x\#xs \sqsubseteq_r y\#ys = (x \sqsubseteq_r y \wedge xs \sqsubseteq_r ys)$

**lemma** *Cons-less-Conss* [simp]:

$order\ r \implies x\#xs \sqsubseteq_r y\#ys = (x \sqsubseteq_r y \wedge xs \sqsubseteq_r ys \vee x = y \wedge xs \sqsubseteq_r ys)$

**lemma** *list-update-le-cong*:

$\llbracket i < size\ xs; xs \sqsubseteq_r ys; x \sqsubseteq_r y \rrbracket \implies xs[i:=x] \sqsubseteq_r ys[i:=y]$

**lemma** *le-listD*:  $\llbracket xs \sqsubseteq_r ys; p < size\ xs \rrbracket \implies xs!p \sqsubseteq_r ys!p$

**lemma** *le-list-refl*:  $\forall x. x \sqsubseteq_r x \implies xs \sqsubseteq_r xs$

**lemma** *le-list-trans*:  $\llbracket order\ r; xs \sqsubseteq_r ys; ys \sqsubseteq_r zs \rrbracket \implies xs \sqsubseteq_r zs$

**lemma** *le-list-antisym*:  $\llbracket order\ r; xs \sqsubseteq_r ys; ys \sqsubseteq_r xs \rrbracket \implies xs = ys$

**lemma** *order-listI* [simp, intro!]:  $order\ r \implies order(Listn.le\ r)$

**lemma** *lesub-list-impl-same-size* [simp]:  $xs \sqsubseteq_r ys \implies size\ ys = size\ xs$

**lemma** *lesssub-lengthD*:  $xs \sqsubseteq_r ys \implies size\ ys = size\ xs$

**lemma** *le-list-appendI*:  $a \sqsubseteq_r b \implies c \sqsubseteq_r d \implies a@c \sqsubseteq_r b@d$

**lemma** *le-listI*:

**assumes**  $length\ a = length\ b$

**assumes**  $\bigwedge n. n < length\ a \implies a!n \sqsubseteq_r b!n$

**shows**  $a \sqsubseteq_r b$

**lemma** *listI*:  $\llbracket size\ xs = n; set\ xs \subseteq A \rrbracket \implies xs \in list\ n\ A$

**lemma** *listE-length* [simp]:  $xs \in list\ n\ A \implies size\ xs = n$

**lemma** *less-lengthI*:  $\llbracket xs \in list\ n\ A; p < n \rrbracket \implies p < size\ xs$

**lemma** *listE-set* [simp]:  $xs \in list\ n\ A \implies set\ xs \subseteq A$

**lemma** *list-0* [simp]:  $list\ 0\ A = \{\}\}$

**lemma** *in-list-Suc-iff*:

$(xs \in list\ (Suc\ n)\ A) = (\exists y \in A. \exists ys \in list\ n\ A. xs = y\#ys)$

**lemma** *Cons-in-list-Suc* [iff]:

$(x\#xs \in list\ (Suc\ n)\ A) = (x \in A \wedge xs \in list\ n\ A)$

**lemma** *list-not-empty*:

$\exists a. a \in A \implies \exists xs. xs \in list\ n\ A$

**lemma** *nth-in* [rule-format, simp]:

$\forall i\ n. size\ xs = n \longrightarrow set\ xs \subseteq A \longrightarrow i < n \longrightarrow (xs!i) \in A$

**lemma** *listE-nth-in*:  $\llbracket xs \in list\ n\ A; i < n \rrbracket \implies xs!i \in A$

**lemma** *listn-Cons-Suc* [elim!]:

$l\#xs \in list\ n\ A \implies (\bigwedge n'. n = Suc\ n' \implies l \in A \implies xs \in list\ n'\ A \implies P) \implies P$

**lemma** *listn-appendE* [elim!]:

$a@c \in list\ n\ A \implies (\bigwedge n1\ n2. n=n1+n2 \implies a \in list\ n1\ A \implies b \in list\ n2\ A \implies P) \implies P$

**lemma** *listt-update-in-list* [simp, intro!]:

$\llbracket xs \in list\ n\ A; x \in A \rrbracket \implies xs[i := x] \in list\ n\ A$

**lemma** *list-appendI* [intro?]:

$\llbracket a \in list\ n\ A; b \in list\ m\ A \rrbracket \implies a @ b \in list\ (n+m)\ A$

**lemma** *list-map* [simp]:  $(map\ f\ xs \in list\ (size\ xs)\ A) = (f\ ' set\ xs \subseteq A)$

**lemma** *list-replicateI* [intro]:  $x \in A \implies replicate\ n\ x \in list\ n\ A$

**lemma** *plus-list-Nil* [simp]:  $\llbracket \sqcup_f \rrbracket xs = []$

**lemma** *plus-list-Cons* [simp]:

$(x\#xs) \sqcup_f ys = (case\ ys\ of\ [] \Rightarrow [] \mid y\#ys \Rightarrow (x \sqcup_f y)\#(xs \sqcup_f ys))$

**lemma** *length-plus-list* [rule-format, simp]:

$\forall ys. size(xs \sqcup_f ys) = min(size\ xs)\ (size\ ys)$

**lemma** *nth-plus-list* [rule-format, simp]:

$\forall xs\ ys\ i. size\ xs = n \longrightarrow size\ ys = n \longrightarrow i < n \longrightarrow (xs \sqcup_f ys)!i = (xs!i) \sqcup_f (ys!i)$

**lemma** (in *Semilat*) *plus-list-ub1* [rule-format]:

$\llbracket \text{set } xs \subseteq A; \text{set } ys \subseteq A; \text{size } xs = \text{size } ys \rrbracket$   
 $\implies xs \sqsubseteq_r xs \sqcup_f ys$   
**lemma** (in *Semilat*) *plus-list-ub2*:  
 $\llbracket \text{set } xs \subseteq A; \text{set } ys \subseteq A; \text{size } xs = \text{size } ys \rrbracket \implies ys \sqsubseteq_r xs \sqcup_f ys$   
**lemma** (in *Semilat*) *plus-list-lub* [rule-format]:  
**shows**  $\forall xs\ ys\ zs. \text{set } xs \subseteq A \longrightarrow \text{set } ys \subseteq A \longrightarrow \text{set } zs \subseteq A$   
 $\longrightarrow \text{size } xs = n \wedge \text{size } ys = n \longrightarrow$   
 $xs \sqsubseteq_r zs \wedge ys \sqsubseteq_r zs \longrightarrow xs \sqcup_f ys \sqsubseteq_r zs$   
**lemma** (in *Semilat*) *list-update-incr* [rule-format]:  
 $x \in A \implies \text{set } xs \subseteq A \longrightarrow$   
 $(\forall i. i < \text{size } xs \longrightarrow xs \sqsubseteq_r xs[i := x \sqcup_f xs!i])$   
**lemma** *acc-le-listI'* [intro!]:  
 $\llbracket \text{order } r; \text{acc } A\ r \rrbracket \implies \text{acc } (\bigcup n. \text{list } n\ A) (\text{Listn.le } r)$

## 2.6 Typing and Dataflow Analysis Framework

**theory** *Typing-Framework*

**imports**

*Semilattices*

**begin**

The relationship between dataflow analysis and a welltyped-instruction predicate.

**type-synonym**

*'s step-type* = *nat*  $\Rightarrow$  *'s*  $\Rightarrow$  (*nat*  $\times$  *'s*) *list*

**definition** *stable* :: *'s ord*  $\Rightarrow$  *'s step-type*  $\Rightarrow$  *'s list*  $\Rightarrow$  *nat*  $\Rightarrow$  *bool*

**where**

*stable* *r step*  $\tau s\ p \longleftrightarrow (\forall (q, \tau) \in \text{set } (\text{step } p\ (\tau s!p)). \tau \sqsubseteq_r \tau s!q)$

**definition** *stables* :: *'s ord*  $\Rightarrow$  *'s step-type*  $\Rightarrow$  *'s list*  $\Rightarrow$  *bool*

**where**

*stables* *r step*  $\tau s \longleftrightarrow (\forall p < \text{size } \tau s. \text{stable } r\ \text{step } \tau s\ p)$

**definition** *wt-step* :: *'s ord*  $\Rightarrow$  *'s*  $\Rightarrow$  *'s step-type*  $\Rightarrow$  *'s list*  $\Rightarrow$  *bool*

**where**

*wt-step* *r T step*  $\tau s \longleftrightarrow (\forall p < \text{size } \tau s. \tau s!p \neq T \wedge \text{stable } r\ \text{step } \tau s\ p)$

**definition** *is-bcv* :: *'s ord*  $\Rightarrow$  *'s*  $\Rightarrow$  *'s step-type*  $\Rightarrow$  *nat*  $\Rightarrow$  *'s set*  $\Rightarrow$  (*'s list*  $\Rightarrow$  *'s list*)  $\Rightarrow$  *bool*

**where**

*is-bcv* *r T step* *n A* *bcv*  $\longleftrightarrow (\forall \tau s_0 \in \text{list } n\ A.$

$(\forall p < n. (\text{bcv } \tau s_0)!p \neq T) = (\exists \tau s \in \text{list } n\ A. \tau s_0 \sqsubseteq_r \tau s \wedge \text{wt-step } r\ T\ \text{step } \tau s))$

**end**

## 2.7 More on Semilattices

**theory** *SemilatAlg*

**imports** *Typing-Framework*

**begin**

**definition** *lesubstep-type* :: (*nat*  $\times$  *'s*) *set*  $\Rightarrow$  *'s ord*  $\Rightarrow$  (*nat*  $\times$  *'s*) *set*  $\Rightarrow$  *bool*

$((- / \{\sqsubseteq_r\} -) [50, 0, 51] 50)$

**where**  $A \{\sqsubseteq_r\} B \equiv \forall (p, \tau) \in A. \exists \tau'. (p, \tau') \in B \wedge \tau \sqsubseteq_r \tau'$

**notation** (*ASCII*)

*lesubstep-type*  $((- / \{<='--\} -) [50, 0, 51] 50)$

**primrec** *pluslssub*  $:: 'a \text{ list} \Rightarrow ('a \Rightarrow 'a \Rightarrow 'a) \Rightarrow 'a \Rightarrow 'a \ ((- / \sqcup -) [65, 0, 66] 65)$

**where**

*pluslssub*  $\square f y = y$

$| \text{ pluslssub } (x \# xs) f y = \text{ pluslssub } xs f (x \sqcup_f y)$

**definition** *bounded*  $:: 's \text{ step-type} \Rightarrow \text{nat} \Rightarrow \text{bool}$

**where**

*bounded step*  $n \longleftrightarrow (\forall p < n. \forall \tau. \forall (q, \tau') \in \text{set } (\text{step } p \ \tau). \ q < n)$

**definition** *pres-type*  $:: 's \text{ step-type} \Rightarrow \text{nat} \Rightarrow 's \text{ set} \Rightarrow \text{bool}$

**where**

*pres-type step*  $n \ A \longleftrightarrow (\forall \tau \in A. \forall p < n. \forall (q, \tau') \in \text{set } (\text{step } p \ \tau). \ \tau' \in A)$

**definition** *mono*  $:: 's \text{ ord} \Rightarrow 's \text{ step-type} \Rightarrow \text{nat} \Rightarrow 's \text{ set} \Rightarrow \text{bool}$

**where**

*mono r step*  $n \ A \longleftrightarrow$

$(\forall \tau \ p \ \tau'. \ \tau \in A \wedge p < n \wedge \tau \sqsubseteq_r \tau' \longrightarrow \text{set } (\text{step } p \ \tau) \ \{\sqsubseteq_r\} \ \text{set } (\text{step } p \ \tau'))$

**lemma** *[iff]*:  $\{\} \ \{\sqsubseteq_r\} \ B$

**lemma** *[iff]*:  $(A \ \{\sqsubseteq_r\} \ \{\}) = (A = \{\})$

**lemma** *lesubstep-union*:

$\llbracket A_1 \ \{\sqsubseteq_r\} \ B_1; A_2 \ \{\sqsubseteq_r\} \ B_2 \rrbracket \Longrightarrow A_1 \cup A_2 \ \{\sqsubseteq_r\} \ B_1 \cup B_2$

**lemma** *pres-typeD*:

$\llbracket \text{pres-type step } n \ A; s \in A; p < n; (q, s') \in \text{set } (\text{step } p \ s) \rrbracket \Longrightarrow s' \in A$

**lemma** *monoD*:

$\llbracket \text{mono } r \text{ step } n \ A; p < n; s \in A; s \sqsubseteq_r t \rrbracket \Longrightarrow \text{set } (\text{step } p \ s) \ \{\sqsubseteq_r\} \ \text{set } (\text{step } p \ t)$

**lemma** *boundedD*:

$\llbracket \text{bounded step } n; p < n; (q, t) \in \text{set } (\text{step } p \ xs) \rrbracket \Longrightarrow q < n$

**lemma** *lesubstep-type-refl* [*simp*, *intro*]:

$(\bigwedge x. x \sqsubseteq_r x) \Longrightarrow A \ \{\sqsubseteq_r\} \ A$

**lemma** *lesub-step-typeD*:

$A \ \{\sqsubseteq_r\} \ B \Longrightarrow (x, y) \in A \Longrightarrow \exists y'. (x, y') \in B \wedge y \sqsubseteq_r y'$

**lemma** *list-update-le-listI* [*rule-format*]:

$\text{set } xs \subseteq A \longrightarrow \text{set } ys \subseteq A \longrightarrow xs \ [\sqsubseteq_r] \ ys \longrightarrow p < \text{size } xs \longrightarrow$

$x \sqsubseteq_r ys!p \longrightarrow \text{semilat}(A, r, f) \longrightarrow x \in A \longrightarrow$

$xs[p := x \sqcup_f xs!p] \ [\sqsubseteq_r] \ ys$

**lemma** *plusplus-closed*: **assumes** *Semilat* *A r f* **shows**

$\bigwedge y. \llbracket \text{set } x \subseteq A; y \in A \rrbracket \Longrightarrow x \sqcup_f y \in A$

**lemma** (**in** *Semilat*) *pp-ub2*:

$\bigwedge y. \llbracket \text{set } x \subseteq A; y \in A \rrbracket \Longrightarrow y \sqsubseteq_r x \sqcup_f y$

**lemma** (**in** *Semilat*) *pp-ub1*:

**shows**  $\bigwedge y. \llbracket \text{set } ls \subseteq A; y \in A; x \in \text{set } ls \rrbracket \Longrightarrow x \sqsubseteq_r ls \sqcup_f y$

**lemma** (**in** *Semilat*) *pp-lub*:

**assumes** *z*:  $z \in A$

**shows**

$$\bigwedge y. y \in A \implies \text{set } xs \subseteq A \implies \forall x \in \text{set } xs. x \sqsubseteq_r z \implies y \sqsubseteq_r z \implies xs \sqcup_f y \sqsubseteq_r z$$

**lemma** *ub1'*: **assumes** *Semilat A r f*  
**shows**  $\llbracket \forall (p,s) \in \text{set } S. s \in A; y \in A; (a,b) \in \text{set } S \rrbracket$   
 $\implies b \sqsubseteq_r \text{map snd } [(p', t') \leftarrow S. p' = a] \sqcup_f y$

**lemma** *plusplus-empty*:  
 $\forall s'. (q, s') \in \text{set } S \longrightarrow s' \sqcup_f ss ! q = ss ! q \implies$   
 $(\text{map snd } [(p', t') \leftarrow S. p' = q] \sqcup_f ss ! q) = ss ! q$

**end**

## 2.8 Lifting the Typing Framework to err, app, and eff

**theory** *Typing-Framework-err*

**imports**

*Typing-Framework*

*SemilatAlg*

**begin**

**definition** *wt-err-step* ::  $'s \text{ ord} \Rightarrow 's \text{ err step-type} \Rightarrow 's \text{ err list} \Rightarrow \text{bool}$

**where**

$$\text{wt-err-step } r \text{ step } \tau s \longleftrightarrow \text{wt-step } (\text{Err.le } r) \text{ Err step } \tau s$$

**definition** *wt-app-eff* ::  $'s \text{ ord} \Rightarrow (\text{nat} \Rightarrow 's \Rightarrow \text{bool}) \Rightarrow 's \text{ step-type} \Rightarrow 's \text{ list} \Rightarrow \text{bool}$

**where**

$$\text{wt-app-eff } r \text{ app step } \tau s \longleftrightarrow$$

$$(\forall p < \text{size } \tau s. \text{app } p (\tau s ! p) \wedge (\forall (q, \tau) \in \text{set } (\text{step } p (\tau s ! p)). \tau \leq\text{-}r \tau s ! q))$$

**definition** *map-snd* ::  $('b \Rightarrow 'c) \Rightarrow ('a \times 'b) \text{ list} \Rightarrow ('a \times 'c) \text{ list}$

**where**

$$\text{map-snd } f = \text{map } (\lambda(x,y). (x, f y))$$

**definition** *error* ::  $\text{nat} \Rightarrow (\text{nat} \times 'a \text{ err}) \text{ list}$

**where**

$$\text{error } n = \text{map } (\lambda x. (x, \text{Err})) [0..<n]$$

**definition** *err-step* ::  $\text{nat} \Rightarrow (\text{nat} \Rightarrow 's \Rightarrow \text{bool}) \Rightarrow 's \text{ step-type} \Rightarrow 's \text{ err step-type}$

**where**

$$\text{err-step } n \text{ app step } p t =$$

$$(\text{case } t \text{ of}$$

$$\quad \text{Err} \Rightarrow \text{error } n$$

$$| \text{OK } \tau \Rightarrow \text{if app } p \tau \text{ then map-snd OK (step } p \tau) \text{ else error } n)$$

**definition** *app-mono* ::  $'s \text{ ord} \Rightarrow (\text{nat} \Rightarrow 's \Rightarrow \text{bool}) \Rightarrow \text{nat} \Rightarrow 's \text{ set} \Rightarrow \text{bool}$

**where**

$$\text{app-mono } r \text{ app } n A \longleftrightarrow$$

$$(\forall s p t. s \in A \wedge p < n \wedge s \sqsubseteq_r t \longrightarrow \text{app } p t \longrightarrow \text{app } p s)$$

**lemmas** *err-step-defs* = *err-step-def map-snd-def error-def*

**lemma** *bounded-err-stepD*:

$\llbracket \text{bounded } (\text{err-step } n \text{ app step}) \ n;$   
 $p < n; \text{ app } p \ a; (q, b) \in \text{set } (\text{step } p \ a) \rrbracket \implies q < n$

**lemma** *in-map-sndD*:  $(a, b) \in \text{set } (\text{map-snd } f \ xs) \implies \exists b'. (a, b') \in \text{set } xs$

**lemma** *bounded-err-stepI*:

$\forall p. p < n \longrightarrow (\forall s. \text{ ap } p \ s \longrightarrow (\forall (q, s') \in \text{set } (\text{step } p \ s). q < n))$   
 $\implies \text{bounded } (\text{err-step } n \text{ app step}) \ n$

**lemma** *bounded-lift*:

$\text{bounded step } n \implies \text{bounded } (\text{err-step } n \text{ app step}) \ n$

**lemma** *le-list-map-OK* [*simp*]:

$\bigwedge b. (\text{map } OK \ a \ [\sqsubseteq_{Err.le} \ r] \ \text{map } OK \ b) = (a \ [\sqsubseteq_r] \ b)$

**lemma** *map-snd-lessI*:

$\text{set } xs \ \{\sqsubseteq_r\} \ \text{set } ys \implies \text{set } (\text{map-snd } OK \ xs) \ \{\sqsubseteq_{Err.le} \ r\} \ \text{set } (\text{map-snd } OK \ ys)$

**lemma** *mono-lift*:

$\llbracket \text{order } r; \text{ app-mono } r \text{ app } n \ A; \text{ bounded } (\text{err-step } n \text{ app step}) \ n;$   
 $\forall s \ p \ t. s \in A \wedge p < n \wedge s \sqsubseteq_r t \longrightarrow \text{app } p \ t \longrightarrow \text{set } (\text{step } p \ s) \ \{\sqsubseteq_r\} \ \text{set } (\text{step } p \ t) \rrbracket$   
 $\implies \text{mono } (Err.le \ r) \ (\text{err-step } n \text{ app step}) \ n \ (\text{err } A)$

**lemma** *in-errorD*:  $(x, y) \in \text{set } (\text{error } n) \implies y = Err$

**lemma** *pres-type-lift*:

$\forall s \in A. \forall p. p < n \longrightarrow \text{app } p \ s \longrightarrow (\forall (q, s') \in \text{set } (\text{step } p \ s). s' \in A)$   
 $\implies \text{pres-type } (\text{err-step } n \text{ app step}) \ n \ (\text{err } A)$

**lemma** *wt-err-imp-wt-app-eff*:

**assumes** *wt*:  $\text{wt-err-step } r \ (\text{err-step } (\text{size } ts) \text{ app step}) \ ts$   
**assumes** *b*:  $\text{bounded } (\text{err-step } (\text{size } ts) \text{ app step}) \ (\text{size } ts)$   
**shows**  $\text{wt-app-eff } r \text{ app step } (\text{map ok-val } ts)$

**lemma** *wt-app-eff-imp-wt-err*:

**assumes** *app-eff*:  $\text{wt-app-eff } r \text{ app step } ts$   
**assumes** *bounded*:  $\text{bounded } (\text{err-step } (\text{size } ts) \text{ app step}) \ (\text{size } ts)$   
**shows**  $\text{wt-err-step } r \ (\text{err-step } (\text{size } ts) \text{ app step}) \ (\text{map } OK \ ts)$

**end**

## 2.9 Kildall's Algorithm

**theory** *Kildall*

**imports** *SemilatAlg ../Basic/Auxiliary*

**begin**

**locale** *Kildall-base* =

**fixes**  $s\text{-}\alpha :: 'w \Rightarrow \text{nat set}$   
**and**  $s\text{-empty} :: 'w$   
**and**  $s\text{-is-empty} :: 'w \Rightarrow \text{bool}$   
**and**  $s\text{-choose} :: 'w \Rightarrow \text{nat}$   
**and**  $s\text{-remove} :: \text{nat} \Rightarrow 'w \Rightarrow 'w$   
**and**  $s\text{-insert} :: \text{nat} \Rightarrow 'w \Rightarrow 'w$

**begin**



**primrec** *propa* :: 's binop  $\Rightarrow$  (nat  $\times$  's) list  $\Rightarrow$  's list  $\Rightarrow$  'w  $\Rightarrow$  's list \* 'w

**where**

*propa* *f* []  $\tau s$  *w* = ( $\tau s$ , *w*)  
| *propa* *f* (*q*'#*qs*)  $\tau s$  *w* = (let (*q*, $\tau$ ) = *q*';  
 $u = \tau \sqcup_f \tau s!q$ ;  
 $w' = (\text{if } u = \tau s!q \text{ then } w \text{ else } s\text{-insert } q \text{ } w)$   
in *propa* *f* *qs* ( $\tau s[q := u]$ ) *w'*)

**definition** *iter* :: 's binop  $\Rightarrow$  's step-type  $\Rightarrow$  's list  $\Rightarrow$  'w  $\Rightarrow$  's list  $\times$  'w

**where**

*iter* *f* *step*  $\tau s$  *w* =  
while ( $\lambda(\tau s, w). \neg s\text{-is-empty } w$ )  
( $\lambda(\tau s, w). \text{let } p = s\text{-choose } w \text{ in } \text{propa } f \text{ (step } p \text{ } (\tau s!p)) \tau s \text{ (s-remove } p \text{ } w)$ )  
( $\tau s, w$ )

**definition** *unstabes* :: 's ord  $\Rightarrow$  's step-type  $\Rightarrow$  's list  $\Rightarrow$  'w

**where**

*unstabes* *r* *step*  $\tau s$  = foldr *s-insert* (filter ( $\lambda p. \neg \text{stable } r \text{ step } \tau s \text{ } p$ ) [0..*size*  $\tau s$ ]) *s-empty*

**definition** *kildall* :: 's ord  $\Rightarrow$  's binop  $\Rightarrow$  's step-type  $\Rightarrow$  's list  $\Rightarrow$  's list

**where** *kildall* *r* *f* *step*  $\tau s \equiv \text{fst}(\text{iter } f \text{ step } \tau s \text{ (unstabes } r \text{ step } \tau s))$

**primrec** *t- $\alpha$*  :: 's list  $\times$  'w  $\Rightarrow$  's list  $\times$  nat set

**where** *t- $\alpha$*  ( $\tau s$ , *w*) = ( $\tau s$ , *s- $\alpha$*  *w*)

**end**

**primrec** *merges* :: 's binop  $\Rightarrow$  (nat  $\times$  's) list  $\Rightarrow$  's list  $\Rightarrow$  's list

**where**

*merges* *f* []  $\tau s$  =  $\tau s$   
| *merges* *f* (*p*'#*ps*)  $\tau s$  = (let (*p*, $\tau$ ) = *p*' in *merges* *f* *ps* ( $\tau s[p := \tau \sqcup_f \tau s!p]$ ))

**locale** *Kildall* =

*Kildall-base* +

**assumes** *empty-spec* [*simp*]: *s- $\alpha$*  *s-empty* = {}  
**and** *is-empty-spec* [*simp*]: *s-is-empty* *A*  $\longleftrightarrow$  *s- $\alpha$*  *A* = {}  
**and** *choose-spec*: *s- $\alpha$*  *A*  $\neq$  {}  $\implies$  *s-choose* *A*  $\in$  *s- $\alpha$*  *A*  
**and** *remove-spec* [*simp*]: *s- $\alpha$*  (*s-remove* *n* *A*) = *s- $\alpha$*  *A* - {*n*}  
**and** *insert-spec* [*simp*]: *s- $\alpha$*  (*s-insert* *n* *A*) = *insert* *n* (*s- $\alpha$*  *A*)

**begin**

**lemma** *s- $\alpha$ -foldr-s-insert*:

*s- $\alpha$*  (foldr *s-insert* *xs* *A*) = foldr *insert* *xs* (*s- $\alpha$*  *A*)

**by**(induct *xs* arbitrary: *A*) *simp-all*

**lemma** *unstabes-spec* [*simp*]: *s- $\alpha$*  (*unstabes* *r* *step*  $\tau s$ ) = {*p*. *p* < *size*  $\tau s$   $\wedge$   $\neg \text{stable } r \text{ step } \tau s \text{ } p$ }

**proof** –

**have** {*p*. *p* < *size*  $\tau s$   $\wedge$   $\neg \text{stable } r \text{ step } \tau s \text{ } p$ } = foldr *insert* (filter ( $\lambda p. \neg \text{stable } r \text{ step } \tau s \text{ } p$ ) [0..*size*  $\tau s$ ]) {}

**unfolding** *foldr-insert-conv-set* **by** *auto*

**thus** ?thesis **by**(*simp* add: *unstabes-def* *s- $\alpha$ -foldr-s-insert*)

**qed**

**end**

**lemmas** [simp] = *Let-def Semilat.le-iff-plus-unchanged* [OF *Semilat.intro, symmetric*]

**lemma** (in *Semilat*) *nth-merges*:

$\bigwedge ss. \llbracket p < \text{length } ss; ss \in \text{list } n \ A; \forall (p,t) \in \text{set } ps. p < n \wedge t \in A \rrbracket \implies$   
 $(\text{merges } f \ ps \ ss)!p = \text{map } \text{snd} \ [(p',t') \leftarrow ps. p'=p] \sqcup_f ss!p$   
 $(\text{is } \bigwedge ss. \llbracket -; -; ?\text{steptype } ps \rrbracket \implies ?P \ ss \ ps)$

**lemma** *length-merges* [simp]:

$\bigwedge ss. \text{size}(\text{merges } f \ ps \ ss) = \text{size } ss$

**lemma** (in *Semilat*) *merges-preserves-type-lemma*:

**shows**  $\forall xs. xs \in \text{list } n \ A \longrightarrow (\forall (p,x) \in \text{set } ps. p < n \wedge x \in A)$   
 $\longrightarrow \text{merges } f \ ps \ xs \in \text{list } n \ A$

**lemma** (in *Semilat*) *merges-preserves-type* [simp]:

$\llbracket xs \in \text{list } n \ A; \forall (p,x) \in \text{set } ps. p < n \wedge x \in A \rrbracket$   
 $\implies \text{merges } f \ ps \ xs \in \text{list } n \ A$

**by** (simp add: *merges-preserves-type-lemma*)

**lemma** (in *Semilat*) *merges-incr-lemma*:

$\forall xs. xs \in \text{list } n \ A \longrightarrow (\forall (p,x) \in \text{set } ps. p < \text{size } xs \wedge x \in A) \longrightarrow xs \sqsubseteq_r \text{merges } f \ ps \ xs$

**lemma** (in *Semilat*) *merges-incr*:

$\llbracket xs \in \text{list } n \ A; \forall (p,x) \in \text{set } ps. p < \text{size } xs \wedge x \in A \rrbracket$   
 $\implies xs \sqsubseteq_r \text{merges } f \ ps \ xs$

**by** (simp add: *merges-incr-lemma*)

**lemma** (in *Semilat*) *merges-same-conv* [rule-format]:

$(\forall xs. xs \in \text{list } n \ A \longrightarrow (\forall (p,x) \in \text{set } ps. p < \text{size } xs \wedge x \in A) \longrightarrow$   
 $(\text{merges } f \ ps \ xs = xs) = (\forall (p,x) \in \text{set } ps. x \sqsubseteq_r xs!p))$

**lemma** (in *Semilat*) *list-update-le-listI* [rule-format]:

$\text{set } xs \subseteq A \longrightarrow \text{set } ys \subseteq A \longrightarrow xs \sqsubseteq_r ys \longrightarrow p < \text{size } xs \longrightarrow$   
 $x \sqsubseteq_r ys!p \longrightarrow x \in A \longrightarrow xs[p := x \sqcup_f xs!p] \sqsubseteq_r ys$

**lemma** (in *Semilat*) *merges-pres-le-ub*:

**assumes**  $\text{set } ts \subseteq A \ \text{set } ss \subseteq A$

$\forall (p,t) \in \text{set } ps. t \sqsubseteq_r ts!p \wedge t \in A \wedge p < \text{size } ts \ \ ss \sqsubseteq_r ts$

**shows**  $\text{merges } f \ ps \ ss \sqsubseteq_r ts$

**context** *Kildall* **begin**

### 2.9.1 propa

**lemma** *decomp-propa*:

$\bigwedge ss \ w. (\forall (q,t) \in \text{set } qs. q < \text{size } ss) \implies$   
 $t \cdot \alpha \ (\text{propa } f \ qs \ ss \ w) =$   
 $(\text{merges } f \ qs \ ss, \{q. \exists t. (q,t) \in \text{set } qs \wedge t \sqcup_f ss!q \neq ss!q\} \cup s \cdot \alpha \ w)$

**apply** (*induct qs*)

**apply** *simp*

**apply** (*simp* (*no-asm*))

**apply** *clarify*

**apply** *simp*

```

apply (rule conjI)
  apply blast
apply (simp add: nth-list-update)
apply blast
done

```

**end**

**lemma** (in *Semilat*) *stable-pres-lemma*:  
**shows**  $\llbracket \text{pres-type step } n \ A; \text{ bounded step } n;$   
 $ss \in \text{list } n \ A; p \in w; \forall q \in w. q < n;$   
 $\forall q. q < n \longrightarrow q \notin w \longrightarrow \text{stable } r \text{ step } ss \ q; q < n;$   
 $\forall s'. (q, s') \in \text{set } (\text{step } p \ (ss!p)) \longrightarrow s' \sqcup_f ss!q = ss!q;$   
 $q \notin w \vee q = p \rrbracket$   
 $\implies \text{stable } r \text{ step } (\text{merges } f \ (\text{step } p \ (ss!p)) \ ss) \ q$

**lemma** (in *Semilat*) *merges-bounded-lemma*:  
 $\llbracket \text{mono } r \text{ step } n \ A; \text{ bounded step } n;$   
 $\forall (p', s') \in \text{set } (\text{step } p \ (ss!p)). s' \in A; ss \in \text{list } n \ A; ts \in \text{list } n \ A; p < n;$   
 $ss \sqsubseteq_r ts; \forall p. p < n \longrightarrow \text{stable } r \text{ step } ts \ p \rrbracket$   
 $\implies \text{merges } f \ (\text{step } p \ (ss!p)) \ ss \sqsubseteq_r ts$

**lemma** *termination-lemma*: **assumes** *Semilat* *A* *r* *f*  
**shows**  $\llbracket ss \in \text{list } n \ A; \forall (q, t) \in \text{set } qs. q < n \wedge t \in A; p \in w \rrbracket \implies$   
 $ss \sqsubseteq_r \text{merges } f \ qs \ ss \vee$   
 $\text{merges } f \ qs \ ss = ss \wedge \{q. \exists t. (q, t) \in \text{set } qs \wedge t \sqcup_f ss!q \neq ss!q\} \cup (w - \{p\}) \subset w$   
**context** *Kildall-base* **begin**

**definition** *s-finite-psubset* ::  $('w * 'w) \text{ set}$   
**where** *s-finite-psubset* ==  $\{(A, B). s\text{-}\alpha \ A < s\text{-}\alpha \ B \ \& \ \text{finite } (s\text{-}\alpha \ B)\}$

**lemma** *s-finite-psubset-inv-image*:  
 $s\text{-finite-psubset} = \text{inv-image finite-psubset } s\text{-}\alpha$   
**by** (auto simp add: *s-finite-psubset-def* *finite-psubset-def*)

**lemma** *wf-s-finite-psubset* [simp]: *wf s-finite-psubset*  
**unfolding** *s-finite-psubset-inv-image* **by** *simp*

**end**

**context** *Kildall* **begin**

## 2.9.2 *iter*

**lemma** *iter-properties*[*rule-format*]: **assumes** *Semilat* *A* *r* *f*  
**shows**  $\llbracket \text{acc } A \ r; \text{ pres-type step } n \ A; \text{ mono } r \text{ step } n \ A;$   
 $\text{bounded step } n; \forall p \in s\text{-}\alpha \ w0. p < n; ss0 \in \text{list } n \ A;$   
 $\forall p < n. p \notin s\text{-}\alpha \ w0 \longrightarrow \text{stable } r \text{ step } ss0 \ p \rrbracket \implies$   
 $t\text{-}\alpha \ (\text{iter } f \text{ step } ss0 \ w0) = (ss', w')$   
 $\longrightarrow$   
 $ss' \in \text{list } n \ A \wedge \text{stables } r \text{ step } ss' \wedge ss0 \sqsubseteq_r ss' \wedge$   
 $(\forall ts \in \text{list } n \ A. ss0 \sqsubseteq_r ts \wedge \text{stables } r \text{ step } ts \longrightarrow ss' \sqsubseteq_r ts)$   
**lemma** *kildall-properties*: **assumes** *Semilat* *A* *r* *f*  
**shows**  $\llbracket \text{acc } A \ r; \text{ pres-type step } n \ A; \text{ mono } r \text{ step } n \ A;$

```

    bounded step n; ss0 ∈ list n A ] ⇒ ⇒
    kildall r f step ss0 ∈ list n A ∧
    stables r step (kildall r f step ss0) ∧
    ss0 [⊆r] kildall r f step ss0 ∧
    (∀ ts ∈ list n A. ss0 [⊆r] ts ∧ stables r step ts ⇒ ⇒
      kildall r f step ss0 [⊆r] ts)
end

interpretation Kildall set [] λxs. xs = [] hd removeAll Cons
by(unfold-locales) auto

lemmas kildall-code [code] =
  kildall-def
  Kildall-base.propg.simps
  Kildall-base.iter-def
  Kildall-base.unstables-def
  Kildall-base.kildall-def

end

```

## 2.10 The Lightweight Bytecode Verifier

```

theory LBVSPEC
imports SemilatAlg Opt
begin

type-synonym
  's certificate = 's list

primrec merge :: 's certificate ⇒ 's binop ⇒ 's ord ⇒ 's ⇒ nat ⇒ (nat × 's) list ⇒ 's ⇒ 's
where
  merge cert f r T pc [] x = x
| merge cert f r T pc (s#ss) x = merge cert f r T pc ss (let (pc',s') = s in
  if pc'=pc+1 then s' ⊔f x
  else if s' ⊆r cert!pc' then x
  else T)

definition wtl-inst :: 's certificate ⇒ 's binop ⇒ 's ord ⇒ 's ⇒
  's step-type ⇒ nat ⇒ 's ⇒ 's
where
  wtl-inst cert f r T step pc s = merge cert f r T pc (step pc s) (cert!(pc+1))

definition wtl-cert :: 's certificate ⇒ 's binop ⇒ 's ord ⇒ 's ⇒ 's ⇒
  's step-type ⇒ nat ⇒ 's ⇒ 's
where
  wtl-cert cert f r T B step pc s =
    (if cert!pc = B then
      wtl-inst cert f r T step pc s
    else
      if s ⊆r cert!pc then wtl-inst cert f r T step pc (cert!pc) else T)

primrec wtl-inst-list :: 'a list ⇒ 's certificate ⇒ 's binop ⇒ 's ord ⇒ 's ⇒ 's ⇒
  's step-type ⇒ nat ⇒ 's ⇒ 's

```

**where**

$wtl-inst-list [] \quad cert\ f\ r\ T\ B\ step\ pc\ s = s$   
 $| wtl-inst-list\ (i\#is)\ cert\ f\ r\ T\ B\ step\ pc\ s =$   
 $(let\ s' = wtl-cert\ cert\ f\ r\ T\ B\ step\ pc\ s\ in$   
 $if\ s' = T \vee s = T\ then\ T\ else\ wtl-inst-list\ is\ cert\ f\ r\ T\ B\ step\ (pc+1)\ s')$

**definition**  $cert-ok :: 's\ certificate \Rightarrow nat \Rightarrow 's \Rightarrow 's \Rightarrow 's\ set \Rightarrow bool$

**where**

$cert-ok\ cert\ n\ T\ B\ A \longleftrightarrow (\forall i < n. cert!i \in A \wedge cert!i \neq T) \wedge (cert!n = B)$

**definition**  $bottom :: 'a\ ord \Rightarrow 'a \Rightarrow bool$

**where**

$bottom\ r\ B \longleftrightarrow (\forall x. B \sqsubseteq_r x)$

**locale**  $lbv = Semilat +$

**fixes**  $T :: 'a\ (\top)$

**fixes**  $B :: 'a\ (\perp)$

**fixes**  $step :: 'a\ step-type$

**assumes**  $top: top\ r\ \top$

**assumes**  $T-A: \top \in A$

**assumes**  $bot: bottom\ r\ \perp$

**assumes**  $B-A: \perp \in A$

**fixes**  $merge :: 'a\ certificate \Rightarrow nat \Rightarrow (nat \times 'a)\ list \Rightarrow 'a \Rightarrow 'a$

**defines**  $mrg-def: merge\ cert \equiv LBVSPEC.merge\ cert\ f\ r\ \top$

**fixes**  $wti :: 'a\ certificate \Rightarrow nat \Rightarrow 'a \Rightarrow 'a$

**defines**  $wti-def: wti\ cert \equiv wtl-inst\ cert\ f\ r\ \top\ step$

**fixes**  $wtc :: 'a\ certificate \Rightarrow nat \Rightarrow 'a \Rightarrow 'a$

**defines**  $wtc-def: wtc\ cert \equiv wtl-cert\ cert\ f\ r\ \top\ \perp\ step$

**fixes**  $wtl :: 'b\ list \Rightarrow 'a\ certificate \Rightarrow nat \Rightarrow 'a \Rightarrow 'a$

**defines**  $wtl-def: wtl\ ins\ cert \equiv wtl-inst-list\ ins\ cert\ f\ r\ \top\ \perp\ step$

**lemma** **(in**  $lbv$ )  $wti$ :

$wti\ c\ pc\ s = merge\ c\ pc\ (step\ pc\ s)\ (c!(pc+1))$

**lemma** **(in**  $lbv$ )  $wtc$ :

$wtc\ c\ pc\ s = (if\ c!pc = \perp\ then\ wti\ c\ pc\ s\ else\ if\ s \sqsubseteq_r\ c!pc\ then\ wti\ c\ pc\ (c!pc)\ else\ \top)$

**lemma**  $cert-okD1$   $[intro?]$ :

$cert-ok\ c\ n\ T\ B\ A \Longrightarrow pc < n \Longrightarrow c!pc \in A$

**lemma**  $cert-okD2$   $[intro?]$ :

$cert-ok\ c\ n\ T\ B\ A \Longrightarrow c!n = B$

**lemma**  $cert-okD3$   $[intro?]$ :

$cert-ok\ c\ n\ T\ B\ A \Longrightarrow B \in A \Longrightarrow pc < n \Longrightarrow c!Suc\ pc \in A$

**lemma**  $cert-okD4$   $[intro?]$ :

$cert-ok\ c\ n\ T\ B\ A \Longrightarrow pc < n \Longrightarrow c!pc \neq T$

**declare** *Let-def* [*simp*]

### 2.10.1 more semilattice lemmas

**lemma** (in *lbv*) *sup-top* [*simp*, *elim*]:

**assumes**  $x: x \in A$   
**shows**  $x \sqcup_f \top = \top$

**lemma** (in *lbv*) *plusplussup-top* [*simp*, *elim*]:

**set**  $xs \subseteq A \implies xs \sqcup_f \top = \top$   
**by** (*induct xs*) *auto*

**lemma** (in *Semilat*) *pp-ub1'*:

**assumes**  $S: \text{snd'set } S \subseteq A$   
**assumes**  $y: y \in A$  **and**  $ab: (a, b) \in \text{set } S$   
**shows**  $b \sqsubseteq_r \text{map snd } [(p', t') \leftarrow S \cdot p' = a] \sqcup_f y$

**lemma** (in *lbv*) *bottom-le* [*simp*, *intro!*]:  $\perp \sqsubseteq_r x$

**by** (*insert bot*) (*simp add: bottom-def*)

**lemma** (in *lbv*) *le-bottom* [*simp*]:  $x \sqsubseteq_r \perp = (x = \perp)$

**by** (*blast intro: antisym-r*)

### 2.10.2 merge

**lemma** (in *lbv*) *merge-Nil* [*simp*]:

*merge c pc [] x = x* **by** (*simp add: mrg-def*)

**lemma** (in *lbv*) *merge-Cons* [*simp*]:

*merge c pc (l#ls) x = merge c pc ls (if fst l=pc+1 then snd l +-f x*  
*else if snd l  $\sqsubseteq_r$  c!fst l then x*  
*else  $\top$ )*  
**by** (*simp add: mrg-def split-beta*)

**lemma** (in *lbv*) *merge-Err* [*simp*]:

*snd'set ss  $\subseteq A \implies \text{merge c pc ss } \top = \top$*   
**by** (*induct ss*) *auto*

**lemma** (in *lbv*) *merge-not-top*:

$\bigwedge x. \text{snd'set } ss \subseteq A \implies \text{merge c pc ss } x \neq \top \implies$   
 $\forall (pc', s') \in \text{set } ss. (pc' \neq pc+1 \longrightarrow s' \sqsubseteq_r c!pc')$   
 $(\text{is } \bigwedge x. ?\text{set } ss \implies ?\text{merge } ss x \implies ?P ss)$

**lemma** (in *lbv*) *merge-def*:

**shows**  
 $\bigwedge x. x \in A \implies \text{snd'set } ss \subseteq A \implies$   
 $\text{merge c pc ss } x =$   
 $(\text{if } \forall (pc', s') \in \text{set } ss. pc' \neq pc+1 \longrightarrow s' \sqsubseteq_r c!pc' \text{ then}$   
 $\text{map snd } [(p', t') \leftarrow ss. p' = pc+1] \sqcup_f x$   
 $\text{else } \top)$   
 $(\text{is } \bigwedge x. - \implies - \implies ?\text{merge } ss x = ?\text{if } ss x \text{ is } \bigwedge x. - \implies - \implies ?P ss x)$

**lemma** (in *lbv*) *merge-not-top-s*:

**assumes**  $x: x \in A$  **and**  $ss: \text{snd'set } ss \subseteq A$   
**assumes**  $m: \text{merge c pc ss } x \neq \top$

**shows**  $\text{merge } c \text{ pc } ss \ x = (\text{map } \text{snd } [(p', t') \leftarrow ss. p' = pc + 1] \sqcup_f x)$

### 2.10.3 wtl-inst-list

**lemmas**  $[iff] = \text{not-Err-eq}$

**lemma** (**in**  $lbv$ )  $\text{wtl-Nil } [simp]$ :  $\text{wtl } [] \ c \ pc \ s = s$   
**by** ( $\text{simp add: wtl-def}$ )

**lemma** (**in**  $lbv$ )  $\text{wtl-Cons } [simp]$ :  
 $\text{wtl } (i\#is) \ c \ pc \ s =$   
 $(\text{let } s' = \text{wtc } c \ pc \ s \text{ in if } s' = \top \vee s = \top \text{ then } \top \text{ else } \text{wtl is } c \ (pc+1) \ s')$   
**by** ( $\text{simp add: wtl-def wtc-def}$ )

**lemma** (**in**  $lbv$ )  $\text{wtl-Cons-not-top}$ :  
 $\text{wtl } (i\#is) \ c \ pc \ s \neq \top =$   
 $(\text{wtc } c \ pc \ s \neq \top \wedge s \neq \top \wedge \text{wtl is } c \ (pc+1) \ (\text{wtc } c \ pc \ s) \neq \top)$   
**by** ( $\text{auto simp del: split-paired-Ex}$ )

**lemma** (**in**  $lbv$ )  $\text{wtl-top } [simp]$ :  $\text{wtl } ls \ c \ pc \ \top = \top$   
**by** ( $\text{cases } ls$ )  $\text{auto}$

**lemma** (**in**  $lbv$ )  $\text{wtl-not-top}$ :  
 $\text{wtl } ls \ c \ pc \ s \neq \top \implies s \neq \top$   
**by** ( $\text{cases } s = \top$ )  $\text{auto}$

**lemma** (**in**  $lbv$ )  $\text{wtl-append } [simp]$ :  
 $\bigwedge pc \ s. \text{wtl } (a@b) \ c \ pc \ s = \text{wtl } b \ c \ (pc + \text{length } a) \ (\text{wtl } a \ c \ pc \ s)$   
**by** ( $\text{induct } a$ )  $\text{auto}$

**lemma** (**in**  $lbv$ )  $\text{wtl-take}$ :  
 $\text{wtl is } c \ pc \ s \neq \top \implies \text{wtl } (\text{take } pc' \ is) \ c \ pc \ s \neq \top$   
 $(\text{is } ?\text{wtl is } \neq - \implies -)$

**lemma**  $\text{take-Suc}$ :  
 $\forall n. n < \text{length } l \implies \text{take } (\text{Suc } n) \ l = (\text{take } n \ l) @ [!n] \ (\text{is } ?P \ l)$

**lemma** (**in**  $lbv$ )  $\text{wtl-Suc}$ :  
**assumes**  $\text{suc: } pc + 1 < \text{length } is$   
**assumes**  $\text{wtl: } \text{wtl } (\text{take } pc \ is) \ c \ 0 \ s \neq \top$   
**shows**  $\text{wtl } (\text{take } (pc+1) \ is) \ c \ 0 \ s = \text{wtc } c \ pc \ (\text{wtl } (\text{take } pc \ is) \ c \ 0 \ s)$

**lemma** (**in**  $lbv$ )  $\text{wtl-all}$ :  
**assumes**  $\text{all: } \text{wtl is } c \ 0 \ s \neq \top \ (\text{is } ?\text{wtl is } \neq -)$   
**assumes**  $\text{pc: } pc < \text{length } is$   
**shows**  $\text{wtc } c \ pc \ (\text{wtl } (\text{take } pc \ is) \ c \ 0 \ s) \neq \top$

### 2.10.4 preserves-type

**lemma** (**in**  $lbv$ )  $\text{merge-pres}$ :  
**assumes**  $s0: \text{snd'set } ss \subseteq A \text{ and } x: x \in A$   
**shows**  $\text{merge } c \ pc \ ss \ x \in A$

**lemma**  $\text{pres-typeD2}$ :  
 $\text{pres-type step } n \ A \implies s \in A \implies p < n \implies \text{snd'set } (\text{step } p \ s) \subseteq A$   
**by**  $\text{auto (drule pres-typeD)}$

**lemma** (**in**  $lbv$ )  $\text{wti-pres } [intro?]$ :

```

assumes pres: pres-type step n A
assumes cert:  $c!(pc+1) \in A$ 
assumes s-pc:  $s \in A \text{ } pc < n$ 
shows wti c pc s  $s \in A$ 
lemma (in lbv) wtc-pres:
  assumes pres-type step n A
  assumes  $c!pc \in A$  and  $c!(pc+1) \in A$ 
  assumes  $s \in A$  and  $pc < n$ 
  shows wtc c pc s  $s \in A$ 
lemma (in lbv) wtl-pres:
  assumes pres: pres-type step (length is) A
  assumes cert: cert-ok c (length is)  $\top \perp A$ 
  assumes s:  $s \in A$ 
  assumes all: wtl is c 0 s  $\neq \top$ 
  shows  $pc < \text{length is} \implies \text{wtl (take pc is) c 0 s} \in A$ 
  (is  $?len \text{ pc} \implies ?wtl \text{ pc} \in A$ )
end

```

## 2.11 Correctness of the LBV

```

theory LBVCorrect
imports LBVSpec Typing-Framework
begin

locale lbvs = lbv +
  fixes  $s_0 :: 'a$ 
  fixes  $c :: 'a \text{ list}$ 
  fixes  $ins :: 'b \text{ list}$ 
  fixes  $\tau s :: 'a \text{ list}$ 
  defines phi-def:
     $\tau s \equiv \text{map } (\lambda pc. \text{if } c!pc = \perp \text{ then wtl (take pc ins) c 0 } s_0 \text{ else } c!pc)$ 
     $[0..<\text{size ins}]$ 

  assumes bounded: bounded step (size ins)
  assumes cert: cert-ok c (size ins)  $\top \perp A$ 
  assumes pres: pres-type step (size ins) A

lemma (in lbvs) phi-None [intro?]:
   $\llbracket pc < \text{size ins}; c!pc = \perp \rrbracket \implies \tau s!pc = \text{wtl (take pc ins) c 0 } s_0$ 
lemma (in lbvs) phi-Some [intro?]:
   $\llbracket pc < \text{size ins}; c!pc \neq \perp \rrbracket \implies \tau s!pc = c!pc$ 
lemma (in lbvs) phi-len [simp]:  $\text{size } \tau s = \text{size ins}$ 
lemma (in lbvs) wtl-suc-pc:
  assumes all: wtl ins c 0  $s_0 \neq \top$ 
  assumes pc:  $pc+1 < \text{size ins}$ 
  shows  $\text{wtl (take (pc+1) ins) c 0 } s_0 \sqsubseteq_r \tau s!(pc+1)$ 
lemma (in lbvs) wtl-stable:
  assumes wtl: wtl ins c 0  $s_0 \neq \top$ 
  assumes  $s_0$ :  $s_0 \in A$  and pc:  $pc < \text{size ins}$ 
  shows stable r step  $\tau s$  pc
lemma (in lbvs) phi-not-top:
  assumes wtl: wtl ins c 0  $s_0 \neq \top$  and pc:  $pc < \text{size ins}$ 
  shows  $\tau s!pc \neq \top$ 

```



```

lemma (in lvs) phi-in-A:
  assumes wtl: wtl ins c 0 s0 ≠ ⊤ and s0: s0 ∈ A
  shows τs ∈ list (size ins) A
lemma (in lvs) phi0:
  assumes wtl: wtl ins c 0 s0 ≠ ⊤ and 0: 0 < size ins
  shows s0 ⊆r τs!0

theorem (in lvs) wtl-sound:
  assumes wtl: wtl ins c 0 s0 ≠ ⊤ and s0: s0 ∈ A
  shows ∃τs. wt-step r ⊤ step τs

theorem (in lvs) wtl-sound-strong:
  assumes wtl: wtl ins c 0 s0 ≠ ⊤
  assumes s0: s0 ∈ A and ins: 0 < size ins
  shows ∃τs ∈ list (size ins) A. wt-step r ⊤ step τs ∧ s0 ⊆r τs!0
end

```

## 2.12 Completeness of the LBV

```

theory LBVComplete
imports LBVSpec Typing-Framework
begin

```

```

definition is-target :: 's step-type ⇒ 's list ⇒ nat ⇒ bool where
  is-target step τs pc' ⟷ (∃pc s'. pc' ≠ pc+1 ∧ pc < size τs ∧ (pc',s') ∈ set (step pc (τs!pc)))

```

```

definition make-cert :: 's step-type ⇒ 's list ⇒ 's ⇒ 's certificate where
  make-cert step τs B = map (λpc. if is-target step τs pc then τs!pc else B) [0..

```

```

lemma [code]:
  is-target step τs pc' =
    list-ex (λpc. pc' ≠ pc+1 ∧ List.member (map fst (step pc (τs!pc))) pc') [0..

```

```

  assumes mono: mono r step (size τs) A
  assumes pres: pres-type step (size τs) A
  assumes τs: ∀pc < size τs. τs!pc ∈ A ∧ τs!pc ≠ ⊤
  assumes bounded: bounded step (size τs)

```

```

  assumes B-neq-T: ⊥ ≠ ⊤

```

```

lemma (in lbvc) cert: cert-ok c (size τs) ⊤ ⊥ A
lemmas [simp del] = split-paired-Ex

```

```

lemma (in lbvc) cert-target [intro?]:
  ⟦ (pc',s') ∈ set (step pc (τs!pc));
    pc' ≠ pc+1; pc < size τs; pc' < size τs ⟧
  ⟹ c!pc' = τs!pc'

```

```

lemma (in lbvc) cert-approx [intro?]:

```

$\llbracket pc < \text{size } \tau s; c!pc \neq \perp \rrbracket \implies c!pc = \tau s!pc$   
**lemma** (in *lbv*) *le-top* [*simp*, *intro*]:  $x \leq_r \top$   
**lemma** (in *lbv*) *merge-mono*:  
   **assumes** *less*:  $\text{set } ss_2 \subseteq_r \text{set } ss_1$   
   **assumes** *x*:  $x \in A$   
   **assumes** *ss1*:  $\text{snd}'\text{set } ss_1 \subseteq A$   
   **assumes** *ss2*:  $\text{snd}'\text{set } ss_2 \subseteq A$   
   **shows**  $\text{merge } c \text{ pc } ss_2 \ x \subseteq_r \text{merge } c \text{ pc } ss_1 \ x$  (is  $?s_2 \subseteq_r ?s_1$ )  
**lemma** (in *lbvc*) *wti-mono*:  
   **assumes** *less*:  $s_2 \subseteq_r s_1$   
   **assumes** *pc*:  $pc < \text{size } \tau s$  **and** *s1*:  $s_1 \in A$  **and** *s2*:  $s_2 \in A$   
   **shows**  $\text{wti } c \text{ pc } s_2 \subseteq_r \text{wti } c \text{ pc } s_1$  (is  $?s_2' \subseteq_r ?s_1'$ )  
**lemma** (in *lbvc*) *wtc-mono*:  
   **assumes** *less*:  $s_2 \subseteq_r s_1$   
   **assumes** *pc*:  $pc < \text{size } \tau s$  **and** *s1*:  $s_1 \in A$  **and** *s2*:  $s_2 \in A$   
   **shows**  $\text{wtc } c \text{ pc } s_2 \subseteq_r \text{wtc } c \text{ pc } s_1$  (is  $?s_2' \subseteq_r ?s_1'$ )  
**lemma** (in *lbv*) *top-le-conv* [*simp*]:  $\top \subseteq_r x = (x = \top)$   
**lemma** (in *lbv*) *neq-top* [*simp*, *elim*]:  $\llbracket x \subseteq_r y; y \neq \top \rrbracket \implies x \neq \top$   
**lemma** (in *lbvc*) *stable-wti*:  
   **assumes** *stable*:  $\text{stable } r \text{ step } \tau s \text{ pc}$  **and** *pc*:  $pc < \text{size } \tau s$   
   **shows**  $\text{wti } c \text{ pc } (\tau s!pc) \neq \top$   
**lemma** (in *lbvc*) *wti-less*:  
   **assumes** *stable*:  $\text{stable } r \text{ step } \tau s \text{ pc}$  **and** *suc-pc*:  $\text{Suc } pc < \text{size } \tau s$   
   **shows**  $\text{wti } c \text{ pc } (\tau s!pc) \subseteq_r \tau s! \text{Suc } pc$  (is  $?wti \subseteq_r -$ )  
**lemma** (in *lbvc*) *stable-wtc*:  
   **assumes** *stable*:  $\text{stable } r \text{ step } \tau s \text{ pc}$  **and** *pc*:  $pc < \text{size } \tau s$   
   **shows**  $\text{wtc } c \text{ pc } (\tau s!pc) \neq \top$   
**lemma** (in *lbvc*) *wtc-less*:  
   **assumes** *stable*:  $\text{stable } r \text{ step } \tau s \text{ pc}$  **and** *suc-pc*:  $\text{Suc } pc < \text{size } \tau s$   
   **shows**  $\text{wtc } c \text{ pc } (\tau s!pc) \subseteq_r \tau s! \text{Suc } pc$  (is  $?wtc \subseteq_r -$ )  
**lemma** (in *lbvc*) *wt-step-wtl-lemma*:  
   **assumes** *wt-step*:  $\text{wt-step } r \top \text{ step } \tau s$   
   **shows**  $\bigwedge pc \ s. \text{pc} + \text{size } ls = \text{size } \tau s \implies s \subseteq_r \tau s!pc \implies s \in A \implies s \neq \top \implies$   
      $\text{wtl } ls \ c \text{ pc } s \neq \top$   
   (is  $\bigwedge pc \ s. - \implies - \implies - \implies - \implies ?wtl \ ls \ pc \ s \neq -$ )  
**theorem** (in *lbvc*) *wtl-complete*:  
   **assumes** *wt*:  $\text{wt-step } r \top \text{ step } \tau s$   
   **assumes** *s*:  $s \subseteq_r \tau s!0$   $s \in A$   $s \neq \top$  **and** *eq*:  $\text{size } ins = \text{size } \tau s$   
   **shows**  $\text{wtl } ins \ c \ 0 \ s \neq \top$   
**end**

## Chapter 3

# Concepts for all JinjaThreads Languages

### 3.1 JinjaThreads types

```
theory Type
imports
  ../Basic/Auxiliary
begin

type-synonym cname = String.literal — class names
type-synonym mname = String.literal — method name
type-synonym vname = String.literal — names for local/field variables

definition Object :: cname
where Object ≡ STR "java/lang/Object"

definition Thread :: cname
where Thread ≡ STR "java/lang/Thread"

definition Throwable :: cname
where Throwable ≡ STR "java/lang/Throwable"

definition this :: vname
where this ≡ STR "this"

definition run :: mname
where run ≡ STR "run() V"

definition start :: mname
where start ≡ STR "start() V"

definition wait :: mname
where wait ≡ STR "wait() V"

definition notify :: mname
where notify ≡ STR "notify() V"

definition notifyAll :: mname
```

**where** *notifyAll*  $\equiv$  *STR* "notifyAll() V"

**definition** *join* :: *mname*

**where** *join*  $\equiv$  *STR* "join() V"

**definition** *interrupt* :: *mname*

**where** *interrupt*  $\equiv$  *STR* "interrupt() V"

**definition** *isInterrupted* :: *mname*

**where** *isInterrupted*  $\equiv$  *STR* "isInterrupted() Z"

**definition** *hashCode* :: *mname*

**where** *hashCode* = *STR* "hashCode() I"

**definition** *clone* :: *mname*

**where** *clone* = *STR* "clone()Ljava/lang/Object;"

**definition** *print* :: *mname*

**where** *print* = *STR* "~print(I) V"

**definition** *currentThread* :: *mname*

**where** *currentThread* = *STR* "~Thread.currentThread()Ljava/lang/Thread;"

**definition** *interrupted* :: *mname*

**where** *interrupted* = *STR* "~Thread.interrupted() Z"

**definition** *yield* :: *mname*

**where** *yield* = *STR* "~Thread.yield() V"

**lemmas** *identifier-name-defs* [code-unfold] =

*this-def run-def start-def wait-def notify-def notifyAll-def join-def interrupt-def isInterrupted-def*  
*hashCode-def clone-def print-def currentThread-def interrupted-def yield-def*

**lemma** *Object-Thread-Throwable-neq* [simp]:

*Thread*  $\neq$  *Object* *Object*  $\neq$  *Thread*

*Object*  $\neq$  *Throwable* *Throwable*  $\neq$  *Object*

*Thread*  $\neq$  *Throwable* *Throwable*  $\neq$  *Thread*

**by**(auto simp add: *Thread-def Object-def Throwable-def*)

**lemma** *synth-method-names-neq-aux*:

*start*  $\neq$  *wait* *start*  $\neq$  *notify* *start*  $\neq$  *notifyAll* *start*  $\neq$  *join* *start*  $\neq$  *interrupt* *start*  $\neq$  *isInterrupted*

*start*  $\neq$  *hashCode* *start*  $\neq$  *clone* *start*  $\neq$  *print* *start*  $\neq$  *currentThread*

*start*  $\neq$  *interrupted* *start*  $\neq$  *yield* *start*  $\neq$  *run*

*wait*  $\neq$  *notify* *wait*  $\neq$  *notifyAll* *wait*  $\neq$  *join* *wait*  $\neq$  *interrupt* *wait*  $\neq$  *isInterrupted*

*wait*  $\neq$  *hashCode* *wait*  $\neq$  *clone* *wait*  $\neq$  *print* *wait*  $\neq$  *currentThread*

*wait*  $\neq$  *interrupted* *wait*  $\neq$  *yield* *wait*  $\neq$  *run*

*notify*  $\neq$  *notifyAll* *notify*  $\neq$  *join* *notify*  $\neq$  *interrupt* *notify*  $\neq$  *isInterrupted*

*notify*  $\neq$  *hashCode* *notify*  $\neq$  *clone* *notify*  $\neq$  *print* *notify*  $\neq$  *currentThread*

*notify*  $\neq$  *interrupted* *notify*  $\neq$  *yield* *notify*  $\neq$  *run*

*notifyAll*  $\neq$  *join* *notifyAll*  $\neq$  *interrupt* *notifyAll*  $\neq$  *isInterrupted*

*notifyAll*  $\neq$  *hashCode* *notifyAll*  $\neq$  *clone* *notifyAll*  $\neq$  *print* *notifyAll*  $\neq$  *currentThread*

*notifyAll*  $\neq$  *interrupted* *notifyAll*  $\neq$  *yield* *notifyAll*  $\neq$  *run*

```

join ≠ interrupt join ≠ isInterrupted
join ≠ hashCode join ≠ clone join ≠ print join ≠ currentThread
join ≠ interrupted join ≠ yield join ≠ run
interrupt ≠ isInterrupted
interrupt ≠ hashCode interrupt ≠ clone interrupt ≠ print interrupt ≠ currentThread
interrupt ≠ interrupted interrupt ≠ yield interrupt ≠ run
isInterrupted ≠ hashCode isInterrupted ≠ clone isInterrupted ≠ print isInterrupted ≠ currentThread

isInterrupted ≠ interrupted isInterrupted ≠ yield isInterrupted ≠ run
hashCode ≠ clone hashCode ≠ print hashCode ≠ currentThread
hashCode ≠ interrupted hashCode ≠ yield hashCode ≠ run
clone ≠ print clone ≠ currentThread
clone ≠ interrupted clone ≠ yield clone ≠ run
print ≠ currentThread
print ≠ interrupted print ≠ yield print ≠ run
currentThread ≠ interrupted currentThread ≠ yield currentThread ≠ run
interrupted ≠ yield interrupted ≠ run
yield ≠ run
by(simp-all add: identifier-name-defs)

lemmas synth-method-names-neq [simp] = synth-method-names-neq-aux synth-method-names-neq-aux[symmetric]

— types
datatype ty
  = Void      — type of statements
  | Boolean
  | Integer
  | NT        — null type
  | Class cname — class type
  | Array ty   (-[] 95) — array type

context
  notes [[inductive-internals]]
begin

inductive is-refT :: ty ⇒ bool where
  is-refT NT
| is-refT (Class C)
| is-refT (A[])

declare is-refT.intros[iff]

end

lemmas refTE [consumes 1, case-names NT Class Array] = is-refT.cases

lemma not-refTE [consumes 1, case-names Void Boolean Integer]:
  ⟦ ¬is-refT T; T = Void ⇒ P; T = Boolean ⇒ P; T = Integer ⇒ P ⟧ ⇒ P
by (cases T, auto)

fun ground-type :: ty ⇒ ty where
  ground-type (Array T) = ground-type T
| ground-type T = T

```

**abbreviation** *is-NT-Array* :: *ty*  $\Rightarrow$  *bool* **where**  
*is-NT-Array* *T*  $\equiv$  *ground-type* *T* = *NT*

**primrec** *the-Class* :: *ty*  $\Rightarrow$  *cname*  
**where**  
*the-Class* (*Class* *C*) = *C*

**primrec** *the-Array* :: *ty*  $\Rightarrow$  *ty*  
**where**  
*the-Array* (*T*[]) = *T*

**datatype** *htype* =  
*Class-type* *cname*  
| *Array-type* *ty* *nat*

**primrec** *ty-of-htype* :: *htype*  $\Rightarrow$  *ty*  
**where**  
*ty-of-htype* (*Class-type* *C*) = *Class* *C*  
| *ty-of-htype* (*Array-type* *T* *n*) = *Array* *T*

**primrec** *alen-of-htype* :: *htype*  $\Rightarrow$  *nat*  
**where**  
*alen-of-htype* (*Array-type* *T* *n*) = *n*

**primrec** *class-type-of* :: *htype*  $\Rightarrow$  *cname*  
**where**  
*class-type-of* (*Class-type* *C*) = *C*  
| *class-type-of* (*Array-type* *T* *n*) = *Object*

**fun** *class-type-of'* :: *ty*  $\Rightarrow$  *cname* *option*  
**where**  
*class-type-of'* (*Class* *C*) = [ *C* ]  
| *class-type-of'* (*Array* *T*) = [ *Object* ]  
| *class-type-of'* - = *None*

**lemma** *rec-htype-is-case* [*simp*]: *rec-htype* = *case-htype*  
**by**(*auto simp add: fun-eq-iff split: htype.split*)

**lemma** *ty-of-htype-eq-convs* [*simp*]:  
**shows** *ty-of-htype-eq-Boolean*: *ty-of-htype* *hT*  $\neq$  *Boolean*  
**and** *ty-of-htype-eq-Void*: *ty-of-htype* *hT*  $\neq$  *Void*  
**and** *ty-of-htype-eq-Integer*: *ty-of-htype* *hT*  $\neq$  *Integer*  
**and** *ty-of-htype-eq-NT*: *ty-of-htype* *hT*  $\neq$  *NT*  
**and** *ty-of-htype-eq-Class*: *ty-of-htype* *hT* = *Class* *C*  $\longleftrightarrow$  *hT* = *Class-type* *C*  
**and** *ty-of-htype-eq-Array*: *ty-of-htype* *hT* = *Array* *T*  $\longleftrightarrow$  ( $\exists$  *n*. *hT* = *Array-type* *T* *n*)  
**by**(*case-tac* [!] *hT*) *simp-all*

**lemma** *class-type-of-eq*:  
*class-type-of* *hT* =  
(*case* *hT* *of* *Class-type* *C*  $\Rightarrow$  *C* | *Array-type* *T* *n*  $\Rightarrow$  *Object*)  
**by**(*simp split: htype.split*)

**lemma** *class-type-of'-ty-of-htype* [*simp*]:

```

class-type-of' (ty-of-hType hT) = [class-type-of hT]
by(cases hT) simp-all

```

```

fun is-Array :: ty ⇒ bool
where
  is-Array (Array T) = True
| is-Array - = False

```

```

lemma is-Array-conv [simp]: is-Array T ⟷ (∃ U. T = Array U)
by(cases T) simp-all

```

```

fun is-Class :: ty ⇒ bool
where
  is-Class (Class C) = True
| is-Class - = False

```

```

lemma is-Class-conv [simp]: is-Class T ⟷ (∃ C. T = Class C)
by(cases T) simp-all

```

### 3.1.1 Code generator setup

```
code-pred is-refT .
```

```
end
```

## 3.2 Class Declarations and Programs

```

theory Decl
imports
  Type
begin

```

```
type-synonym volatile = bool
```

```

record fmod =
  volatile :: volatile

```

```

type-synonym fdecl = vname × ty × fmod — field declaration
type-synonym 'm mdecl = mname × ty list × ty × 'm — method = name, arg. types, return
type, body
type-synonym 'm mdecl' = mname × ty list × ty × 'm option — method = name, arg. types,
return type, possible body
type-synonym 'm class = cname × fdecl list × 'm mdecl' list — class = superclass, fields,
methods
type-synonym 'm cdecl = cname × 'm class — class declaration

```

```

datatype
  'm prog = Program 'm cdecl list

```

```

translations
  (type) fdecl <= (type) String.literal × ty × fmod
  (type) 'c mdecl <= (type) String.literal × ty list × ty × 'c
  (type) 'c mdecl' <= (type) String.literal × ty list × ty × 'c option
  (type) 'c class <= (type) String.literal × fdecl list × ('c mdecl) list

```

$(type) \text{ 'c cdecl} \leq (type) \text{ String.literal} \times (\text{'c class})$

**notation**  $(input) \text{ None } (Native)$

**primrec**  $classes :: 'm \text{ prog} \Rightarrow 'm \text{ cdecl list}$

**where**

$classes (Program P) = P$

**primrec**  $class :: 'm \text{ prog} \Rightarrow cname \rightarrow 'm \text{ class}$

**where**

$class (Program p) = \text{map-of } p$

**locale**  $prog =$

**fixes**  $P :: 'm \text{ prog}$

**definition**  $is-class :: 'm \text{ prog} \Rightarrow cname \Rightarrow bool$

**where**

$is-class P C \equiv class P C \neq None$

**lemma**  $finite-is-class: finite \{C. is-class P C\}$

**primrec**  $is-type :: 'm \text{ prog} \Rightarrow ty \Rightarrow bool$

**where**

$is-type-void: is-type P Void = True$

|  $is-type-bool: is-type P Boolean = True$

|  $is-type-int: is-type P Integer = True$

|  $is-type-nt: is-type P NT = True$

|  $is-type-class: is-type P (Class C) = is-class P C$

|  $is-type-array: is-type P (A[]) = (\text{case ground-type } A \text{ of } NT \Rightarrow False \mid \text{Class } C \Rightarrow is-class P C \mid - \Rightarrow True)$

**lemma**  $is-type-ArrayD: is-type P (T[]) \Longrightarrow is-type P T$

**by**( $induct T$ )  $auto$

**lemma**  $is-type-ground-type:$

$is-type P T \Longrightarrow is-type P (\text{ground-type } T)$

**by**( $induct T$ )( $auto,metis is-type-ArrayD is-type-array$ )

**abbreviation**  $types :: 'm \text{ prog} \Rightarrow ty \text{ set}$

**where**  $types P \equiv \{T. is-type P T\}$

**abbreviation**  $is-htype :: 'm \text{ prog} \Rightarrow htype \Rightarrow bool$

**where**  $is-htype P hT \equiv is-type P (\text{ty-of-htype } hT)$

### 3.2.1 Code generation

**lemma**  $is-class-intros [code-pred-intro]:$

$class P C \neq None \Longrightarrow is-class P C$

**by**( $auto simp add: is-class-def$ )

**code-pred**

$(modes: i \Rightarrow i \Rightarrow bool)$

$is-class$

**unfolding**  $is-class-def$  **by**  $simp$



**declare** *is-class-def*[code]

**end**

### 3.3 Relations between Jinja Types

**theory** *TypeRel*

**imports**

*Decl*

**begin**

#### 3.3.1 The subclass relations

**inductive** *subcls1* :: '*m prog*  $\Rightarrow$  *cname*  $\Rightarrow$  *cname*  $\Rightarrow$  *bool* ( $- \vdash - \prec^1 -$  [71, 71, 71] 70)

**for** *P* :: '*m prog*

**where** *subcls1I*:  $\llbracket \text{class } P \ C = \text{Some } (D, \text{rest}); C \neq \text{Object} \rrbracket \Longrightarrow P \vdash C \prec^1 D$

**abbreviation** *subcls* :: '*m prog*  $\Rightarrow$  *cname*  $\Rightarrow$  *cname*  $\Rightarrow$  *bool* ( $- \vdash - \preceq^* -$  [71, 71, 71] 70)

**where**  $P \vdash C \preceq^* D \equiv (\text{subcls1 } P)^{**} C D$

**lemma** *subcls1D*:

$P \vdash C \prec^1 D \Longrightarrow C \neq \text{Object} \wedge (\exists fs \ ms. \text{class } P \ C = \text{Some } (D, fs, ms))$

**by**(*auto elim: subcls1.cases*)

**lemma** *Object-subcls1* [*iff*]:  $\neg P \vdash \text{Object} \prec^1 C$

**by**(*simp add: subcls1.simps*)

**lemma** *Object-subcls-conv* [*iff*]:  $(P \vdash \text{Object} \preceq^* C) = (C = \text{Object})$

**by**(*auto elim: converse-rtranclpE*)

**lemma** *finite-subcls1*: *finite*  $\{(C, D). P \vdash C \prec^1 D\}$

**proof** –

**let** *?A* = *SIGMA* *C*: $\{C. \text{is-class } P \ C\}. \{D. C \neq \text{Object} \wedge \text{fst } (\text{the } (\text{class } P \ C)) = D\}$

**have** *finite ?A* **by**(*rule finite-SigmaI [OF finite-is-class]*) *auto*

**also have** *?A* =  $\{(C, D). P \vdash C \prec^1 D\}$

**by**(*fastforce simp:is-class-def dest: subcls1D elim: subcls1I*)

**finally show** *?thesis* .

**qed**

**lemma** *finite-subcls1'*:

*finite*  $(\{(D, C). P \vdash C \prec^1 D\})$

**by**(*subst finite-converse[symmetric]*)

(*simp add: converse-unfold finite-subcls1 del: finite-converse*)

**lemma** *subcls-is-class*:  $(\text{subcls1 } P)^{++} C D \Longrightarrow \text{is-class } P \ C$

**by**(*auto elim: converse-tranclpE dest!: subcls1D simp add: is-class-def*)

**lemma** *subcls-is-class1*:  $\llbracket P \vdash C \preceq^* D; \text{is-class } P \ D \rrbracket \Longrightarrow \text{is-class } P \ C$

**by**(*auto elim: converse-rtranclpE dest!: subcls1D simp add: is-class-def*)

#### 3.3.2 The subtype relations

**inductive** *widen* :: '*m prog*  $\Rightarrow$  *ty*  $\Rightarrow$  *ty*  $\Rightarrow$  *bool* ( $- \vdash - \leq -$  [71, 71, 71] 70)

**for** *P* :: '*m prog*

where

*widen-refl*[*iff*]:  $P \vdash T \leq T$   
 $|$  *widen-subcls*:  $P \vdash C \preceq^* D \implies P \vdash \text{Class } C \leq \text{Class } D$   
 $|$  *widen-null*[*iff*]:  $P \vdash NT \leq \text{Class } C$   
 $|$  *widen-null-array*[*iff*]:  $P \vdash NT \leq \text{Array } A$   
 $|$  *widen-array-object*:  $P \vdash \text{Array } A \leq \text{Class } \text{Object}$   
 $|$  *widen-array-array*:  $P \vdash A \leq B \implies P \vdash \text{Array } A \leq \text{Array } B$

abbreviation

*widens* :: 'm prog  $\Rightarrow$  ty list  $\Rightarrow$  ty list  $\Rightarrow$  bool (-  $\vdash$  - [ $\leq$ ] - [71,71,71] 70)

where

$P \vdash Ts \leq Ts' == \text{list-all2 } (\text{widen } P) \text{ } Ts \text{ } Ts'$

**lemma** [*iff*]:  $(P \vdash T \leq \text{Void}) = (T = \text{Void})$

**lemma** [*iff*]:  $(P \vdash T \leq \text{Boolean}) = (T = \text{Boolean})$

**lemma** [*iff*]:  $(P \vdash T \leq \text{Integer}) = (T = \text{Integer})$

**lemma** [*iff*]:  $(P \vdash \text{Void} \leq T) = (T = \text{Void})$

**lemma** [*iff*]:  $(P \vdash \text{Boolean} \leq T) = (T = \text{Boolean})$

**lemma** [*iff*]:  $(P \vdash \text{Integer} \leq T) = (T = \text{Integer})$

**lemma** *Class-widen*:  $P \vdash \text{Class } C \leq T \implies \exists D. T = \text{Class } D$

**by**(*erule widen.cases, auto*)

**lemma** *Array-Array-widen*:

$P \vdash \text{Array } T \leq \text{Array } U \implies P \vdash T \leq U$

**by**(*auto elim: widen.cases*)

**lemma** *widen-Array*:  $(P \vdash T \leq U[]) \longleftrightarrow (T = NT \vee (\exists V. T = V[] \wedge P \vdash V \leq U))$

**by**(*induct T*)(*auto dest: Array-Array-widen elim: widen.cases intro: widen-array-array*)

**lemma** *Array-widen*:  $P \vdash \text{Array } A \leq T \implies (\exists B. T = \text{Array } B \wedge P \vdash A \leq B) \vee T = \text{Class } \text{Object}$

**by**(*auto elim: widen.cases*)

**lemma** [*iff*]:  $(P \vdash T \leq NT) = (T = NT)$

**by**(*induct T*)(*auto dest: Class-widen Array-widen*)

**lemma** *Class-widen-Class* [*iff*]:  $(P \vdash \text{Class } C \leq \text{Class } D) = (P \vdash C \preceq^* D)$

**by** (*auto elim: widen-subcls widen.cases*)

**lemma** *widen-Class*:  $(P \vdash T \leq \text{Class } C) = (T = NT \vee (\exists D. T = \text{Class } D \wedge P \vdash D \preceq^* C) \vee (C = \text{Object} \wedge (\exists A. T = \text{Array } A)))$

**by**(*induct T*)(*auto dest: Array-widen intro: widen-array-object*)

**lemma** *NT-widen*:

$P \vdash NT \leq T = (T = NT \vee (\exists C. T = \text{Class } C) \vee (\exists U. T = U[]))$

**by**(*cases T*) *auto*

**lemma** *Class-widen2*:  $P \vdash \text{Class } C \leq T = (\exists D. T = \text{Class } D \wedge P \vdash C \preceq^* D)$

**by** (*cases T, auto elim: widen.cases*)

**lemma** *Object-widen*:  $P \vdash \text{Class } \text{Object} \leq T \implies T = \text{Class } \text{Object}$

**by**(*cases T, auto elim: widen.cases*)

**lemma** *NT-Array-widen-Object*:

*is-NT-Array*  $T \implies P \vdash T \leq \text{Class } \text{Object}$

```

by(induct T, auto intro: widen-array-object)

lemma widen-trans[trans]:
  assumes  $P \vdash S \leq U$   $P \vdash U \leq T$ 
  shows  $P \vdash S \leq T$ 
using assms
proof(induct arbitrary: T)
  case (widen-refl T T') thus  $P \vdash T \leq T'$  .
next
  case (widen-subcls C D T)
  then obtain E where  $T = \text{Class } E$  by (blast dest: Class-widen)
  with widen-subcls show  $P \vdash \text{Class } C \leq T$  by (auto elim: rtrancl-trans)
next
  case (widen-null C RT)
  then obtain D where  $RT = \text{Class } D$  by (blast dest: Class-widen)
  thus  $P \vdash NT \leq RT$  by auto
next
  case widen-null-array thus ?case by(auto dest: Array-widen)
next
  case (widen-array-object A T)
  hence  $T = \text{Class Object}$  by(rule Object-widen)
  with widen-array-object show  $P \vdash A[] \leq T$ 
  by(auto intro: widen.widen-array-object)
next
  case widen-array-array thus ?case
  by(auto dest!: Array-widen intro: widen.widen-array-array widen-array-object)
qed

lemma widens-trans:  $\llbracket P \vdash Ss \leq Ts; P \vdash Ts \leq Us \rrbracket \implies P \vdash Ss \leq Us$ 
by (rule list-all2-trans)(rule widen-trans)

lemma class-type-of'-widenD:
  class-type-of'  $T = \lfloor C \rfloor \implies P \vdash T \leq \text{Class } C$ 
by(cases T)(auto intro: widen-array-object)

lemma widen-is-class-type-of:
  assumes class-type-of'  $T = \lfloor C \rfloor$   $P \vdash T' \leq T$   $T' \neq NT$ 
  obtains  $C'$  where class-type-of'  $T' = \lfloor C' \rfloor$   $P \vdash C' \preceq^* C$ 
using assms by(cases T)(auto simp add: widen-Class widen-Array)

lemma widens-refl:  $P \vdash Ts \leq Ts$ 
by(rule list-all2-refl[OF widen-refl])

lemma widen-append1:
   $P \vdash (xs @ ys) \leq Ts \iff (\exists Ts1 Ts2. Ts = Ts1 @ Ts2 \wedge \text{length } xs = \text{length } Ts1 \wedge \text{length } ys = \text{length } Ts2 \wedge P \vdash xs \leq Ts1 \wedge P \vdash ys \leq Ts2)$ 
unfolding list-all2-append1 by fastforce

lemmas widens-Cons [iff] = list-all2-Cons1 [of widen P] for P

lemma widens-lengthD:
   $P \vdash xs \leq ys \implies \text{length } xs = \text{length } ys$ 
by(rule list-all2-lengthD)

```

**lemma** *widen-refT*:  $\llbracket \text{is-refT } T; P \vdash U \leq T \rrbracket \implies \text{is-refT } U$   
**by**(*erule refTE*)(*auto simp add: widen-Class widen-Array*)

**lemma** *refT-widen*:  $\llbracket \text{is-refT } T; P \vdash T \leq U \rrbracket \implies \text{is-refT } U$   
**by**(*erule widen.cases*) *auto*

**inductive** *is-lub* :: *'m prog*  $\Rightarrow$  *ty*  $\Rightarrow$  *ty*  $\Rightarrow$  *ty*  $\Rightarrow$  *bool* ( $- \vdash \text{lub}''((-,-)') = -$  [51,51,51,51] 50)  
**for** *P* :: *'m prog* **and** *U* :: *ty* **and** *V* :: *ty* **and** *T* :: *ty*

**where**

$\llbracket P \vdash U \leq T; P \vdash V \leq T; \\ \bigwedge T'. \llbracket P \vdash U \leq T'; P \vdash V \leq T' \rrbracket \implies P \vdash T \leq T' \rrbracket \\ \implies P \vdash \text{lub}(U, V) = T$

**lemma** *is-lub-upper*:

$P \vdash \text{lub}(U, V) = T \implies P \vdash U \leq T \wedge P \vdash V \leq T$

**by**(*auto elim: is-lub.cases*)

**lemma** *is-lub-least*:

$\llbracket P \vdash \text{lub}(U, V) = T; P \vdash U \leq T'; P \vdash V \leq T' \rrbracket \implies P \vdash T \leq T'$

**by**(*auto elim: is-lub.cases*)

**lemma** *is-lub-Void* [*iff*]:

$P \vdash \text{lub}(\text{Void}, \text{Void}) = T \longleftrightarrow T = \text{Void}$

**by**(*auto intro: is-lub.intros elim: is-lub.cases*)

**lemma** *is-lubI* [*code-pred-intro*]:

$\llbracket P \vdash U \leq T; P \vdash V \leq T; \forall T'. P \vdash U \leq T' \longrightarrow P \vdash V \leq T' \longrightarrow P \vdash T \leq T' \rrbracket \implies P \vdash \text{lub}(U, V) = T$

**by**(*blast intro: is-lub.intros*)

### 3.3.3 Method lookup

**inductive** *Methods* :: *'m prog*  $\Rightarrow$  *cname*  $\Rightarrow$  (*mname*  $\rightarrow$  (*ty list*  $\times$  *ty*  $\times$  *'m option*)  $\times$  *cname*)  $\Rightarrow$  *bool*  
 ( $- \vdash - \text{sees}'\text{-methods} -$  [51,51,51] 50)

**for** *P* :: *'m prog*

**where**

*sees-methods-Object*:

$\llbracket \text{class } P \text{ Object} = \text{Some}(D, fs, ms); Mm = \text{map-option } (\lambda m. (m, \text{Object})) \circ \text{map-of } ms \rrbracket \\ \implies P \vdash \text{Object sees-methods } Mm$

| *sees-methods-rec*:

$\llbracket \text{class } P \text{ C} = \text{Some}(D, fs, ms); C \neq \text{Object}; P \vdash D \text{ sees-methods } Mm; \\ Mm' = Mm ++ (\text{map-option } (\lambda m. (m, C)) \circ \text{map-of } ms) \rrbracket \\ \implies P \vdash C \text{ sees-methods } Mm'$

**lemma** *sees-methods-fun*:

**assumes**  $P \vdash C \text{ sees-methods } Mm$

**shows**  $P \vdash C \text{ sees-methods } Mm' \implies Mm' = Mm$

**using** *assms*

**proof**(*induction arbitrary: Mm'*)

**case** *sees-methods-Object* **thus** ?*case* **by**(*auto elim: Methods.cases*)

**next**

**case** (*sees-methods-rec* *C D fs ms Dres Cres Cres'*)

**from**  $\langle P \vdash C \text{ sees-methods } Cres' \rangle \langle C \neq \text{Object} \rangle \langle \text{class } P \text{ C} = [(D, fs, ms)] \rangle$

**obtain** *Dres'* **where** *Dmethods'*:  $P \vdash D \text{ sees-methods } Dres'$

**and**  $Cres'$ :  $Cres' = Dres' ++ (map-option (\lambda m. (m, C)) \circ map-of ms)$   
**by** *cases auto*  
**from** *sees-methods-rec.IH[OF Dmethods']*  $\langle Cres = Dres ++ (map-option (\lambda m. (m, C)) \circ map-of ms) \rangle$   $Cres'$   
**show** *?case by simp*  
**qed**

**lemma** *visible-methods-exist*:

$P \vdash C \text{ sees-methods } Mm \implies Mm \ M = Some(m, D) \implies$   
 $(\exists D' fs ms. class \ P \ D = Some(D', fs, ms) \wedge map-of ms \ M = Some \ m)$   
**by** *(induct rule: Methods.induct) auto*

**lemma** *sees-methods-decl-above*:

**assumes**  $P \vdash C \text{ sees-methods } Mm$   
**shows**  $Mm \ M = Some(m, D) \implies P \vdash C \preceq^* D$   
**using** *assms*  
**by** *induct(auto elim: converse-rtrancpl-into-rtrancpl[where r = subcls1 P, OF subcls1I])*

**lemma** *sees-methods-idemp*:

**assumes**  $P \vdash C \text{ sees-methods } Mm$  **and**  $Mm \ M = Some(m, D)$   
**shows**  $\exists Mm'. (P \vdash D \text{ sees-methods } Mm') \wedge Mm' \ M = Some(m, D)$   
**using** *assms*  
**by** *(induct arbitrary: m D)(fastforce dest: Methods.intros)+*

**lemma** *sees-methods-decl-mono*:

**assumes** *sub*:  $P \vdash C' \preceq^* C$  **and**  $P \vdash C \text{ sees-methods } Mm$   
**shows**  $\exists Mm' Mm2. P \vdash C' \text{ sees-methods } Mm' \wedge Mm' = Mm ++ Mm2 \wedge (\forall M m D. Mm2 \ M = Some(m, D) \longrightarrow P \vdash D \preceq^* C)$   
 $(\text{is } \exists Mm' Mm2. ?Q \ C' \ C \ Mm' \ Mm2)$

**using** *assms*

**proof** *(induction rule: converse-rtrancpl-induct)*

**case** *base*

**hence**  $?Q \ C \ C \ Mm \ Map.empty$  **by** *simp*

**thus**  $\exists Mm' Mm2. ?Q \ C \ C \ Mm' \ Mm2$  **by** *blast*

**next**

**case** *(step C'' C')*

**note**  $sub1 = \langle P \vdash C'' \prec^1 C' \rangle$  **and**  $sub = \langle P \vdash C' \preceq^* C \rangle$

**and**  $Csees = \langle P \vdash C \text{ sees-methods } Mm \rangle$

**from** *step.IH[OF Csees]* **obtain**  $Mm' Mm2$  **where**  $C'sees: P \vdash C' \text{ sees-methods } Mm'$

**and**  $Mm': Mm' = Mm ++ Mm2$

**and**  $subC: \forall M m D. Mm2 \ M = Some(m, D) \longrightarrow P \vdash D \preceq^* C$  **by** *blast*

**obtain**  $fs \ ms$  **where**  $class: class \ P \ C'' = Some(C', fs, ms) \ C'' \neq Object$

**using** *subcls1D[OF sub1]* **by** *blast*

**let**  $?Mm3 = map-option (\lambda m. (m, C'')) \circ map-of ms$

**have**  $P \vdash C'' \text{ sees-methods } (Mm ++ Mm2) ++ ?Mm3$

**using** *sees-methods-rec[OF class C'sees refl]*  $Mm'$  **by** *simp*

**hence**  $?Q \ C'' \ C \ ((Mm ++ Mm2) ++ ?Mm3) \ (Mm2 ++ ?Mm3)$

**using** *converse-rtrancpl-into-rtrancpl[OF sub1 sub]*

**by** *simp (simp add: map-add-def subC split: option.split)*

**thus**  $\exists Mm' Mm2. ?Q \ C'' \ C \ Mm' \ Mm2$  **by** *blast*

**qed**

**definition** *Method* ::  $'m \ prog \Rightarrow cname \Rightarrow mname \Rightarrow ty \ list \Rightarrow ty \Rightarrow 'm \ option \Rightarrow cname \Rightarrow bool$   
 $(- \vdash - \text{ sees } - : - \longrightarrow - \text{ in } - [51, 51, 51, 51, 51, 51, 51] \ 50)$

**where**

$$P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D \equiv \\ \exists Mm. P \vdash C \text{ sees-methods } Mm \wedge Mm M = \text{Some}((Ts, T, m), D)$$

Output translation to replace *None* with its notation *Native* when used as method body in *Method*.

**abbreviation (output)**

$$\text{Method-native} :: 'm \text{ prog} \Rightarrow \text{cname} \Rightarrow \text{mname} \Rightarrow \text{ty list} \Rightarrow \text{ty} \Rightarrow \text{cname} \Rightarrow \text{bool} \\ (- \vdash - \text{ sees } - : - \rightarrow - = \text{Native in } - [51, 51, 51, 51, 51, 51] 50)$$

**where**  $\text{Method-native } P \ C \ M \ Ts \ T \ D \equiv \text{Method } P \ C \ M \ Ts \ T \ \text{Native } D$

**definition**  $\text{has-method} :: 'm \text{ prog} \Rightarrow \text{cname} \Rightarrow \text{mname} \Rightarrow \text{bool} \ (- \vdash - \text{ has } - [51, 0, 51] 50)$

**where**

$$P \vdash C \text{ has } M \equiv \exists Ts \ T \ m \ D. P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D$$

**lemma** *has-methodI*:

$$P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D \Longrightarrow P \vdash C \text{ has } M$$

**by** (*unfold has-method-def*) *blast*

**lemma** *sees-method-fun*:

$$\llbracket P \vdash C \text{ sees } M: TS \rightarrow T = m \text{ in } D; P \vdash C \text{ sees } M: TS' \rightarrow T' = m' \text{ in } D' \rrbracket \\ \Longrightarrow TS' = TS \wedge T' = T \wedge m' = m \wedge D' = D$$

**lemma** *sees-method-decl-above*:

$$P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D \Longrightarrow P \vdash C \preceq^* D$$

**lemma** *visible-method-exists*:

$$P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D \Longrightarrow \\ \exists D' \ fs \ ms. \text{class } P \ D = \text{Some}(D', fs, ms) \wedge \text{map-of } ms \ M = \text{Some}(Ts, T, m)$$

**lemma** *sees-method-idemp*:

$$P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D \Longrightarrow P \vdash D \text{ sees } M: Ts \rightarrow T = m \text{ in } D$$

**lemma** *sees-method-decl-mono*:

$$\llbracket P \vdash C' \preceq^* C; P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D; \\ P \vdash C' \text{ sees } M: Ts' \rightarrow T' = m' \text{ in } D' \rrbracket \Longrightarrow P \vdash D' \preceq^* D$$

**apply**(*frule sees-method-decl-above*)

**apply**(*unfold Method-def*)

**apply** *clarsimp*

**apply**(*drule* (1) *sees-methods-decl-mono*)

**apply** *clarsimp*

**apply**(*drule* (1) *sees-methods-fun*)

**apply** *clarsimp*

**apply**(*blast intro: rtrancplp-trans*)

**done**

**lemma** *sees-method-is-class*:

$$P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D \Longrightarrow \text{is-class } P \ C$$

**by** (*auto simp add: is-class-def Method-def elim: Methods.cases*)

### 3.3.4 Field lookup

**inductive** *Fields* ::  $'m \text{ prog} \Rightarrow \text{cname} \Rightarrow ((\text{vname} \times \text{cname}) \times (\text{ty} \times \text{fmod})) \text{ list} \Rightarrow \text{bool}$   
 $(- \vdash - \text{ has'-fields } - [51, 51, 51] 50)$

```

for  $P :: 'm \text{ prog}$ 
where
  has-fields-rec:
     $\llbracket \text{class } P \ C = \text{Some}(D, fs, ms); C \neq \text{Object}; P \vdash D \text{ has-fields } FDTs;$ 
     $FDTs' = \text{map } (\lambda(F, Tm). ((F, C), Tm)) \ fs \ @ \ FDTs \rrbracket$ 
     $\implies P \vdash C \text{ has-fields } FDTs'$ 

  | has-fields-Object:
     $\llbracket \text{class } P \ \text{Object} = \text{Some}(D, fs, ms); FDTs = \text{map } (\lambda(F, T). ((F, \text{Object}), T)) \ fs \rrbracket$ 
     $\implies P \vdash \text{Object} \text{ has-fields } FDTs$ 

lemma has-fields-fun:
  assumes  $P \vdash C \text{ has-fields } FDTs$  and  $P \vdash C \text{ has-fields } FDTs'$ 
  shows  $FDTs' = FDTs$ 
using assms
proof(induction arbitrary: FDTs')
  case has-fields-Object thus ?case by(auto elim: Fields.cases)
next
  case (has-fields-rec  $C \ D \ fs \ ms \ Dres \ Cres \ Cres'$ )
  from  $\langle P \vdash C \text{ has-fields } Cres' \rangle \langle C \neq \text{Object} \rangle \langle \text{class } P \ C = \text{Some } (D, fs, ms) \rangle$ 
  obtain  $Dres'$  where  $DFields': P \vdash D \text{ has-fields } Dres'$ 
  and  $Cres': Cres' = \text{map } (\lambda(F, Tm). ((F, C), Tm)) \ fs \ @ \ Dres'$ 
  by cases auto
  from has-fields-rec.IH[OF DFields']  $\langle Cres = \text{map } (\lambda(F, Tm). ((F, C), Tm)) \ fs \ @ \ Dres \rangle \langle Cres' \rangle$ 
  show ?case by simp
qed

lemma all-fields-in-has-fields:
  assumes  $P \vdash C \text{ has-fields } FDTs$ 
  and  $P \vdash C \preceq^* D \text{ class } P \ D = \text{Some}(D', fs, ms) \ (F, Tm) \in \text{set } fs$ 
  shows  $((F, D), Tm) \in \text{set } FDTs$ 
using assms
by induct (auto 4 3 elim: converse-rtrancplE dest: subcls1D)

lemma has-fields-decl-above:
  assumes  $P \vdash C \text{ has-fields } FDTs \ ((F, D), Tm) \in \text{set } FDTs$ 
  shows  $P \vdash C \preceq^* D$ 
using assms
by induct (auto intro: converse-rtrancpl-into-rtrancpl subcls1I)

lemma subcls-notin-has-fields:
  assumes  $P \vdash C \text{ has-fields } FDTs \ ((F, D), Tm) \in \text{set } FDTs$ 
  shows  $\neg (\text{subcls1 } P)^{++} \ D \ C$ 
using assms apply(induct)
  prefer 2 apply(fastforce dest: trancplD)
apply clarsimp
apply(erule disjE)
apply(clarsimp simp add:image-def)
apply(erule trancplD)
apply clarify
apply(erule subcls1D)
apply(fastforce dest:trancplD all-fields-in-has-fields)
apply(blast dest:subcls1I trancpl.trancpl-into-trancpl)
done

```

**lemma** *has-fields-mono-lem*:  
 assumes  $P \vdash D \preceq^* C \vdash C$  *has-fields FDTs*  
 shows  $\exists \text{pre}. P \vdash D$  *has-fields*  $\text{pre}@FDTs \wedge \text{dom}(\text{map-of pre}) \cap \text{dom}(\text{map-of FDTs}) = \{\}$   
**using** *assms*  
**apply**(*induct rule:converse-rtrancp-induct*)  
**apply**(*rule-tac*  $x = []$  **in** *exI*)  
**apply** *simp*  
**apply** *clarsimp*  
**apply**(*rename-tac*  $D' D$  *pre*)  
**apply**(*subgoal-tac* (*subcls1*  $P$ )<sup>++</sup>  $D' C$ )  
**prefer** 2 **apply**(*erule* (1) *rtrancp-into-trancp2*)  
**apply**(*drule* *subcls1D*)  
**apply** *clarsimp*  
**apply**(*rename-tac*  $fs$   $ms$ )  
**apply**(*drule* (2) *has-fields-rec*)  
**apply**(*rule* *refl*)  
**apply**(*rule-tac*  $x = \text{map } (\lambda(F, Tm). ((F, D'), Tm)) fs @ pre$  **in** *exI*)  
**apply** *simp*  
**apply**(*simp* *add: Int-Un-distrib2*)  
**apply**(*rule* *equalsOI*)  
**apply**(*auto* *dest: subcls-notin-has-fields simp: dom-map-of-conv-image-fst image-def*)  
**done**

**lemma** *has-fields-is-class*:  
 $P \vdash C$  *has-fields FDTs*  $\implies$  *is-class*  $P C$   
**by** (*auto simp add: is-class-def elim: Fields.cases*)

**lemma** *Object-has-fields-Object*:  
 assumes  $P \vdash \text{Object}$  *has-fields FDTs*  
 shows  $\text{snd } 'fst \text{ ' set FDTs} \subseteq \{\text{Object}\}$   
**using** *assms* **by** *cases auto*

**definition**  
 $\text{has-field} :: 'm \text{ prog} \Rightarrow \text{cname} \Rightarrow \text{vname} \Rightarrow \text{ty} \Rightarrow \text{fmod} \Rightarrow \text{cname} \Rightarrow \text{bool}$   
 $(- \vdash - \text{ has } - :- '(-) \text{ in } - [51, 51, 51, 51, 51, 51] 50)$

**where**  
 $P \vdash C$  *has*  $F:T$  (*fm*) *in*  $D \equiv$   
 $\exists FDTs. P \vdash C$  *has-fields FDTs*  $\wedge \text{map-of FDTs } (F, D) = \text{Some } (T, fm)$

**lemma** *has-field-mono*:  
 $\llbracket P \vdash C$  *has*  $F:T$  (*fm*) *in*  $D; P \vdash C' \preceq^* C \rrbracket \implies P \vdash C'$  *has*  $F:T$  (*fm*) *in*  $D$   
**by**(*fastforce simp: has-field-def map-add-def dest: has-fields-mono-lem*)

**lemma** *has-field-is-class*:  
 $P \vdash C$  *has*  $M:T$  (*fm*) *in*  $D \implies$  *is-class*  $P C$   
**by** (*auto simp add: is-class-def has-field-def elim: Fields.cases*)

**lemma** *has-field-decl-above*:  
 $P \vdash C$  *has*  $F:T$  (*fm*) *in*  $D \implies P \vdash C \preceq^* D$   
**unfolding** *has-field-def*  
**by**(*auto* *dest: map-of-SomeD has-fields-decl-above*)

**lemma** *has-field-fun*:



$\llbracket P \vdash C \text{ has } F:T (fm) \text{ in } D; P \vdash C \text{ has } F:T' (fm') \text{ in } D \rrbracket \implies T' = T \wedge fm = fm'$   
**by**(*auto simp:has-field-def dest:has-fields-fun*)

**definition**

*sees-field* :: 'm prog  $\Rightarrow$  cname  $\Rightarrow$  vname  $\Rightarrow$  ty  $\Rightarrow$  fmod  $\Rightarrow$  cname  $\Rightarrow$  bool  
 (-  $\vdash$  - *sees* :- '(-) in - [51,51,51,51,51,51] 50)

**where**

$P \vdash C \text{ sees } F:T (fm) \text{ in } D \equiv$   
 $\exists FDTs. P \vdash C \text{ has-fields } FDTs \wedge$   
 $\text{map-of } (\text{map } (\lambda((F,D),Tm). (F,(D,Tm))) FDTs) F = \text{Some}(D,T,fm)$

**lemma** *map-of-remap-SomeD*:

$\text{map-of } (\text{map } (\lambda((k,k'),x). (k,(k',x))) t) k = \text{Some } (k',x) \implies \text{map-of } t (k, k') = \text{Some } x$   
**by** (*induct t (auto simp:fun-upd-apply split: if-split-asm)*)

**lemma** *has-visible-field*:

$P \vdash C \text{ sees } F:T (fm) \text{ in } D \implies P \vdash C \text{ has } F:T (fm) \text{ in } D$   
**by**(*auto simp add:has-field-def sees-field-def map-of-remap-SomeD*)

**lemma** *sees-field-fun*:

$\llbracket P \vdash C \text{ sees } F:T (fm) \text{ in } D; P \vdash C \text{ sees } F:T' (fm') \text{ in } D' \rrbracket \implies T' = T \wedge D' = D \wedge fm = fm'$   
**by**(*fastforce simp:sees-field-def dest:has-fields-fun*)

**lemma** *sees-field-decl-above*:

$P \vdash C \text{ sees } F:T (fm) \text{ in } D \implies P \vdash C \preceq^* D$   
**by**(*clarsimp simp add: sees-field-def*)  
 (*blast intro: has-fields-decl-above map-of-SomeD map-of-remap-SomeD*)

**lemma** *sees-field-idemp*:

**assumes**  $P \vdash C \text{ sees } F:T (fm) \text{ in } D$   
**shows**  $P \vdash D \text{ sees } F:T (fm) \text{ in } D$   
**proof** –  
**from** *assms* **obtain** *FDTs* **where** *has*:  $P \vdash C \text{ has-fields } FDTs$   
**and** *F*:  $\text{map-of } (\text{map } (\lambda((F, D), Tm). (F, D, Tm)) FDTs) F = \lfloor (D, T, fm) \rfloor$   
**unfolding** *sees-field-def* **by** *blast*  
**thus** ?thesis  
**proof** *induct*  
**case** *has-fields-rec* **thus** ?case **unfolding** *sees-field-def*  
**by**(*auto*)(*fastforce dest: map-of-SomeD intro!: exI intro: Fields.has-fields-rec*)  
**next**  
**case** *has-fields-Object* **thus** ?case **unfolding** *sees-field-def*  
**by**(*fastforce dest: map-of-SomeD intro: Fields.has-fields-Object intro!: exI*)  
**qed**  
**qed**

### 3.3.5 Functional lookup

**definition** *method* :: 'm prog  $\Rightarrow$  cname  $\Rightarrow$  mname  $\Rightarrow$  cname  $\times$  ty list  $\times$  ty  $\times$  'm option  
**where** *method*  $P C M \equiv \text{THE } (D, Ts, T, m). P \vdash C \text{ sees } M:Ts \rightarrow T = m \text{ in } D$

**definition** *field* :: 'm prog  $\Rightarrow$  cname  $\Rightarrow$  vname  $\Rightarrow$  cname  $\times$  ty  $\times$  fmod

**where** *field*  $P C F \equiv \text{THE } (D, T, fm). P \vdash C \text{ sees } F:T (fm) \text{ in } D$

**definition** *fields* :: 'm prog  $\Rightarrow$  cname  $\Rightarrow$  ((vname  $\times$  cname)  $\times$  (ty  $\times$  fmod)) list  
**where** *fields* P C  $\equiv$  THE FDTs. P  $\vdash$  C has-fields FDTs

**lemma** [simp]: P  $\vdash$  C has-fields FDTs  $\Longrightarrow$  fields P C = FDTs

**lemma** field-def2 [simp]: P  $\vdash$  C sees F:T (fm) in D  $\Longrightarrow$  field P C F = (D, T, fm)

**lemma** method-def2 [simp]: P  $\vdash$  C sees M: Ts $\rightarrow$ T = m in D  $\Longrightarrow$  method P C M = (D, Ts, T, m)

**lemma** has-fields-b-fields:

P  $\vdash$  C has-fields FDTs  $\Longrightarrow$  fields P C = FDTs

**unfolding** fields-def

**by** (blast intro: the-equality has-fields-fun)

**lemma** has-field-map-of-fields [simp]:

P  $\vdash$  C has F:T (fm) in D  $\Longrightarrow$  map-of (fields P C) (F, D) = [(T, fm)]

**by**(auto simp add: has-field-def)

### 3.3.6 Code generation

New introduction rules for subcls1

**code-pred**

— Disallow mode *i-o-o* to force *code-pred* in subsequent predicates not to use this inefficient mode

(modes:  $i \Rightarrow i \Rightarrow i \Rightarrow \text{bool}$ ,  $i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}$ )

subcls1

.

Introduce proper constant *subcls'* for *subcls* and generate executable equation for *subcls'*

**definition** *subcls'* **where** *subcls'* = *subcls*

**code-pred**

(modes:  $i \Rightarrow i \Rightarrow i \Rightarrow \text{bool}$ ,  $i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}$ )

[inductify]

subcls'

.

**lemma** subcls-conv-subcls' [code-unfold]:

(subcls1 P)  $\hat{=}$  subcls' P

**by**(simp add: subcls'-def)

Change rule  $?P \vdash ?A[] \leq \text{Class Object}$  such that predicate compiler tests on class *Object* first. Otherwise *widen-i-o-i* never terminates.

**lemma** widen-array-object-code:

C = Object  $\Longrightarrow$  P  $\vdash$  Array A  $\leq$  Class C

**by**(auto intro: widen.intros)

**lemmas** [code-pred-intro] =

widen-refl widen-subcls widen-null widen-null-array widen-array-object-code widen-array-array

**code-pred**

(modes:  $i \Rightarrow i \Rightarrow i \Rightarrow \text{bool}$ )

widen

**by**(erule widen.cases) auto

Readjust the code equations for *widen* such that *widen-i-i-i* is guaranteed to contain () at most once (even in the code representation!). This is important for the scheduler and the small-step semantics because of the weaker code equations for *the*.

A similar problem cannot hit the subclass relation because, for acyclic subclass hierarchies, the paths in the hierarchy are unique and cycle-free.

**definition** *widen-i-i-i'* **where** *widen-i-i-i' = widen-i-i-i*

**declare** *widen.equation* [code del]

**lemmas** *widen-i-i-i'-equation* [code] = *widen.equation*[folded *widen-i-i-i'-def*]

**lemma** *widen-i-i-i-code* [code]:

*widen-i-i-i P T T' = (if P ⊢ T ≤ T' then Predicate.single () else bot)*

**by**(auto intro!: pred-eqI intro: widen-i-i-iI elim: widen-i-i-iE)

**code-pred**

(modes: *i* ⇒ *i* ⇒ *i* ⇒ bool, *i* ⇒ *i* ⇒ *o* ⇒ bool)

Methods

.

**code-pred**

(modes: *i* ⇒ *i* ⇒ *i* ⇒ *o* ⇒ *o* ⇒ *o* ⇒ *o* ⇒ bool, *i* ⇒ *i* ⇒ *i* ⇒ *o* ⇒ *o* ⇒ *o* ⇒ *i* ⇒ bool)

[inductify]

Method

.

**code-pred**

(modes: *i* ⇒ *i* ⇒ *i* ⇒ bool)

[inductify]

has-method

.

**declare** *fun-upd-def* [code-pred-inline]

**code-pred**

(modes: *i* ⇒ *i* ⇒ *o* ⇒ bool)

Fields

.

**code-pred**

(modes: *i* ⇒ *i* ⇒ *i* ⇒ *o* ⇒ *o* ⇒ *i* ⇒ bool)

[inductify, skip-proof]

has-field

.

**code-pred**

(modes: *i* ⇒ *i* ⇒ *i* ⇒ *o* ⇒ *o* ⇒ *o* ⇒ bool, *i* ⇒ *i* ⇒ *i* ⇒ *o* ⇒ *o* ⇒ *i* ⇒ bool)

[inductify, skip-proof]

sees-field

.

**lemma** *eval-Method-i-i-i-o-o-o-o-conv*:

*Predicate.eval (Method-i-i-i-o-o-o-o P C M) = (λ(Ts, T, m, D). P ⊢ C sees M:Ts→T=m in D)*

**by**(auto intro: Method-i-i-i-o-o-o-oI elim: Method-i-i-i-o-o-o-oE intro!: ext)

**lemma** *method-code* [code]:

*method P C M =*

```

    Predicate.the (Predicate.bind (Method-i-i-i-o-o-o P C M) ( $\lambda(Ts, T, m, D).$  Predicate.single (D, Ts,
    T, m)))
  apply (rule sym, rule the-eqI)
  apply (simp add: method-def eval-Method-i-i-i-o-o-o-conv)
  apply (rule arg-cong [where f=The])
  apply (auto simp add: Sup-fun-def Sup-bool-def fun-eq-iff)
done

```

**lemma** *eval-sees-field-i-i-i-o-o-conv*:

```

    Predicate.eval (sees-field-i-i-i-o-o-o P C F) = ( $\lambda(T, fm, D).$  P  $\vdash$  C sees F:T (fm) in D)
  by(auto intro!: ext intro: sees-field-i-i-i-o-o-oI elim: sees-field-i-i-i-o-o-oE)

```

**lemma** *eval-sees-field-i-i-i-o-i-conv*:

```

    Predicate.eval (sees-field-i-i-i-o-o-i P C F D) = ( $\lambda(T, fm).$  P  $\vdash$  C sees F:T (fm) in D)
  by(auto intro!: ext intro: sees-field-i-i-i-o-o-iI elim: sees-field-i-i-i-o-o-iE)

```

**lemma** *field-code* [code]:

```

    field P C F = Predicate.the (Predicate.bind (sees-field-i-i-i-o-o-o P C F) ( $\lambda(T, fm, D).$  Predi-
    cate.single (D, T, fm)))
  apply (rule sym, rule the-eqI)
  apply (simp add: field-def eval-sees-field-i-i-i-o-o-o-conv)
  apply (rule arg-cong [where f=The])
  apply (auto simp add: Sup-fun-def Sup-bool-def fun-eq-iff)
done

```

**lemma** *eval-Fields-conv*:

```

    Predicate.eval (Fields-i-i-o P C) = ( $\lambda FDTs.$  P  $\vdash$  C has-fields FDTs)
  by(auto intro: Fields-i-i-oI elim: Fields-i-i-oE intro!: ext)

```

**lemma** *fields-code* [code]:

```

    fields P C = Predicate.the (Fields-i-i-o P C)
  by(simp add: fields-def Predicate.the-def eval-Fields-conv)

```

**code-identifier**

```

  code-module TypeRel  $\rightarrow$ 
    (SML) TypeRel and (Haskell) TypeRel and (OCaml) TypeRel
| code-module Decl  $\rightarrow$ 
    (SML) TypeRel and (Haskell) TypeRel and (OCaml) TypeRel

```

**end**

### 3.4 Jinja Values

**theory** *Value*

**imports**

  TypeRel

  HOL-Library.Word

**begin**

**no-notation** *floor* ( $\lfloor \cdot \rfloor$ )

**type-synonym** *word32* = 32 word

**datatype** *'addr val*  
 = *Unit* — dummy result value of void expressions  
 | *Null* — null reference  
 | *Bool bool* — Boolean value  
 | *Intg word32* — integer value  
 | *Addr 'addr* — addresses of objects, arrays and threads in the heap

**primrec** *default-val* :: *ty*  $\Rightarrow$  *'addr val* — default value for all types

**where**

*default-val Void* = *Unit*  
 | *default-val Boolean* = *Bool False*  
 | *default-val Integer* = *Intg 0*  
 | *default-val NT* = *Null*  
 | *default-val (Class C)* = *Null*  
 | *default-val (Array A)* = *Null*

**lemma** *default-val-not-Addr*: *default-val T*  $\neq$  *Addr a*  
**by**(*cases T*)(*simp-all*)

**lemma** *Addr-not-default-val*: *Addr a*  $\neq$  *default-val T*  
**by**(*cases T*)(*simp-all*)

**primrec** *the-Intg* :: *'addr val*  $\Rightarrow$  *word32*

**where**

*the-Intg (Intg i)* = *i*

**primrec** *the-Addr* :: *'addr val*  $\Rightarrow$  *'addr*

**where**

*the-Addr (Addr a)* = *a*

**fun** *is-Addr* :: *'addr val*  $\Rightarrow$  *bool*

**where**

*is-Addr (Addr a)* = *True*  
 | *is-Addr -* = *False*

**lemma** *is-AddrE* [*elim!*]:

$\llbracket \text{is-Addr } v; \bigwedge a. v = \text{Addr } a \implies \text{thesis} \rrbracket \implies \text{thesis}$   
**by**(*cases v, auto*)

**fun** *is-Intg* :: *'addr val*  $\Rightarrow$  *bool*

**where**

*is-Intg (Intg i)* = *True*  
 | *is-Intg -* = *False*

**lemma** *is-IntgE* [*elim!*]:

$\llbracket \text{is-Intg } v; \bigwedge i. v = \text{Intg } i \implies \text{thesis} \rrbracket \implies \text{thesis}$   
**by**(*cases v, auto*)

**fun** *is-Bool* :: *'addr val*  $\Rightarrow$  *bool*

**where**

*is-Bool (Bool b)* = *True*  
 | *is-Bool -* = *False*

**lemma** *is-BoolE* [*elim!*]:

$\llbracket \text{is-Bool } v; \bigwedge a. v = \text{Bool } a \implies \text{thesis} \rrbracket \implies \text{thesis}$   
**by**(*cases v, auto*)

**definition** *is-Ref* :: 'addr val  $\Rightarrow$  bool  
**where** *is-Ref* *v*  $\equiv v = \text{Null} \vee \text{is-Addr } v$

**lemma** *is-Ref-def2*:  
*is-Ref* *v* = (*v* = *Null*  $\vee$  ( $\exists a. v = \text{Addr } a$ ))  
**by** (*cases v*) (*auto simp add: is-Ref-def*)

**lemma** [*iff*]: *is-Ref Null* **by** (*simp add: is-Ref-def2*)

**definition** *undefined-value* :: 'addr val **where** *undefined-value* = *Unit*

**lemma** *undefined-value-not-Addr*:  
*undefined-value*  $\neq \text{Addr } a$  *Addr a*  $\neq \text{undefined-value}$   
**by**(*simp-all add: undefined-value-def*)

**class** *addr* =  
**fixes** *hash-addr* :: 'a  $\Rightarrow$  int  
**and** *monitor-funfun-to-list* :: ('a  $\Rightarrow$  f nat)  $\Rightarrow$  'a list  
**assumes** *set* (*monitor-funfun-to-list* *f*) = *Collect* ((*\$*) (*funfun-dom* *f*))

**locale** *addr-base* =  
**fixes** *addr2thread-id* :: 'addr  $\Rightarrow$  'thread-id  
**and** *thread-id2addr* :: 'thread-id  $\Rightarrow$  'addr

**end**

## 3.5 Exceptions

**theory** *Exceptions*  
**imports**  
*Value*  
**begin**

**definition** *NullPointer* :: *cname*  
**where** [*code-unfold*]: *NullPointer* = *STR "java/lang/NullPointerException"*

**definition** *ClassCast* :: *cname*  
**where** [*code-unfold*]: *ClassCast* = *STR "java/lang/ClassCastException"*

**definition** *OutOfMemory* :: *cname*  
**where** [*code-unfold*]: *OutOfMemory* = *STR "java/lang/OutOfMemoryError"*

**definition** *ArrayIndexOutOfBounds* :: *cname*  
**where** [*code-unfold*]: *ArrayIndexOutOfBounds* = *STR "java/lang/ArrayIndexOutOfBoundsException"*

**definition** *ArrayStore* :: *cname*  
**where** [*code-unfold*]: *ArrayStore* = *STR "java/lang/ArrayStoreException"*

**definition** *NegativeArraySize* :: *cname*  
**where** [*code-unfold*]: *NegativeArraySize* = *STR "java/lang/NegativeArraySizeException"*

**definition** *ArithmeticException* :: cname

**where** [code-unfold]: *ArithmeticException* = STR "java/lang/ArithmeticException"

**definition** *IllegalMonitorState* :: cname

**where** [code-unfold]: *IllegalMonitorState* = STR "java/lang/IllegalMonitorStateException"

**definition** *IllegalThreadState* :: cname

**where** [code-unfold]: *IllegalThreadState* = STR "java/lang/IllegalThreadStateException"

**definition** *InterruptedException* :: cname

**where** [code-unfold]: *InterruptedException* = STR "java/lang/InterruptedException"

**definition** *sys-xcpts-list* :: cname list

**where**

*sys-xcpts-list* =  
 [NullPointer, ClassCast, OutOfMemory, ArrayIndexOutOfBounds, ArrayStore, NegativeArraySize,  
*ArithmeticException*,  
*IllegalMonitorState*, *IllegalThreadState*, *InterruptedException*]

**definition** *sys-xcpts* :: cname set

**where** [code-unfold]: *sys-xcpts* = set *sys-xcpts-list*

**definition** *wf-syscls* :: 'm prog  $\Rightarrow$  bool

**where** *wf-syscls*  $P \equiv (\forall C \in \{\text{Object}, \text{Throwable}, \text{Thread}\}. \text{is-class } P \ C) \wedge (\forall C \in \text{sys-xcpts}. P \vdash C \preceq^* \text{Throwable})$

### 3.5.1 System exceptions

**lemma** [simp]:

*NullPointer*  $\in$  *sys-xcpts*  $\wedge$   
*OutOfMemory*  $\in$  *sys-xcpts*  $\wedge$   
*ClassCast*  $\in$  *sys-xcpts*  $\wedge$   
*ArrayIndexOutOfBounds*  $\in$  *sys-xcpts*  $\wedge$   
*ArrayStore*  $\in$  *sys-xcpts*  $\wedge$   
*NegativeArraySize*  $\in$  *sys-xcpts*  $\wedge$   
*IllegalMonitorState*  $\in$  *sys-xcpts*  $\wedge$   
*IllegalThreadState*  $\in$  *sys-xcpts*  $\wedge$   
*InterruptedException*  $\in$  *sys-xcpts*  $\wedge$   
*ArithmeticException*  $\in$  *sys-xcpts*

**by**(simp add: *sys-xcpts-def sys-xcpts-list-def*)

**lemma** *sys-xcpts-cases* [consumes 1, cases set]:

$\llbracket C \in \text{sys-xcpts}; P \text{ NullPointer}; P \text{ OutOfMemory}; P \text{ ClassCast};$   
 $P \text{ ArrayIndexOutOfBounds}; P \text{ ArrayStore}; P \text{ NegativeArraySize};$   
 $P \text{ ArithmeticException};$   
 $P \text{ IllegalMonitorState}; P \text{ IllegalThreadState}; P \text{ InterruptedException} \rrbracket$   
 $\implies P \ C$

**by** (auto simp add: *sys-xcpts-def sys-xcpts-list-def*)

**lemma** *OutOfMemory-not-Object*[simp]: *OutOfMemory*  $\neq$  *Object*

**by**(simp add: *OutOfMemory-def Object-def*)

**lemma** *ClassCast-not-Object*[simp]: *ClassCast*  $\neq$  *Object*

**by**(*simp* add: *ClassCast-def* *Object-def*)

**lemma** *NullPointerException-not-Object*[*simp*]: *NullPointerException*  $\neq$  *Object*

**by**(*simp* add: *NullPointerException-def* *Object-def*)

**lemma** *ArrayIndexOutOfBounds-not-Object*[*simp*]: *ArrayIndexOutOfBounds*  $\neq$  *Object*

**by**(*simp* add: *ArrayIndexOutOfBounds-def* *Object-def*)

**lemma** *ArrayStore-not-Object*[*simp*]: *ArrayStore*  $\neq$  *Object*

**by**(*simp* add: *ArrayStore-def* *Object-def*)

**lemma** *NegativeArraySize-not-Object*[*simp*]: *NegativeArraySize*  $\neq$  *Object*

**by**(*simp* add: *NegativeArraySize-def* *Object-def*)

**lemma** *ArithmeticException-not-Object*[*simp*]: *ArithmeticException*  $\neq$  *Object*

**by**(*simp* add: *ArithmeticException-def* *Object-def*)

**lemma** *IllegalMonitorState-not-Object*[*simp*]: *IllegalMonitorState*  $\neq$  *Object*

**by**(*simp* add: *IllegalMonitorState-def* *Object-def*)

**lemma** *IllegalThreadState-not-Object*[*simp*]: *IllegalThreadState*  $\neq$  *Object*

**by**(*simp* add: *IllegalThreadState-def* *Object-def*)

**lemma** *InterruptedException-not-Object*[*simp*]: *InterruptedException*  $\neq$  *Object*

**by**(*simp* add: *InterruptedException-def* *Object-def*)

**lemma** *sys-xcpts-neqs-aux*:

*NullPointerException*  $\neq$  *ClassCast* *NullPointerException*  $\neq$  *OutOfMemory* *NullPointerException*  $\neq$  *ArrayIndexOutOfBounds*  
*NullPointerException*  $\neq$  *ArrayStore* *NullPointerException*  $\neq$  *NegativeArraySize* *NullPointerException*  $\neq$  *IllegalMonitorState*  
*NullPointerException*  $\neq$  *IllegalThreadState* *NullPointerException*  $\neq$  *InterruptedException* *NullPointerException*  $\neq$  *ArithmeticException*

*ClassCast*  $\neq$  *OutOfMemory* *ClassCast*  $\neq$  *ArrayIndexOutOfBounds*

*ClassCast*  $\neq$  *ArrayStore* *ClassCast*  $\neq$  *NegativeArraySize* *ClassCast*  $\neq$  *IllegalMonitorState*

*ClassCast*  $\neq$  *IllegalThreadState* *ClassCast*  $\neq$  *InterruptedException* *ClassCast*  $\neq$  *ArithmeticException*  
*OutOfMemory*  $\neq$  *ArrayIndexOutOfBounds*

*OutOfMemory*  $\neq$  *ArrayStore* *OutOfMemory*  $\neq$  *NegativeArraySize* *OutOfMemory*  $\neq$  *IllegalMonitorState*

*OutOfMemory*  $\neq$  *IllegalThreadState* *OutOfMemory*  $\neq$  *InterruptedException*

*OutOfMemory*  $\neq$  *ArithmeticException*

*ArrayIndexOutOfBounds*  $\neq$  *ArrayStore* *ArrayIndexOutOfBounds*  $\neq$  *NegativeArraySize* *ArrayIndexOutOfBounds*  $\neq$  *IllegalMonitorState*

*ArrayIndexOutOfBounds*  $\neq$  *IllegalThreadState* *ArrayIndexOutOfBounds*  $\neq$  *InterruptedException* *ArrayIndexOutOfBounds*  $\neq$  *ArithmeticException*

*ArrayStore*  $\neq$  *NegativeArraySize* *ArrayStore*  $\neq$  *IllegalMonitorState*

*ArrayStore*  $\neq$  *IllegalThreadState* *ArrayStore*  $\neq$  *InterruptedException*

*ArrayStore*  $\neq$  *ArithmeticException*

*NegativeArraySize*  $\neq$  *IllegalMonitorState*

*NegativeArraySize*  $\neq$  *IllegalThreadState* *NegativeArraySize*  $\neq$  *InterruptedException*

*NegativeArraySize*  $\neq$  *ArithmeticException*

*IllegalMonitorState*  $\neq$  *IllegalThreadState* *IllegalMonitorState*  $\neq$  *InterruptedException*

*IllegalMonitorState*  $\neq$  *ArithmeticException*

*IllegalThreadState*  $\neq$  *InterruptedException*

*IllegalThreadState*  $\neq$  *ArithmeticException*

*InterruptedException*  $\neq$  *ArithmeticException*



**by**(*simp-all add: NullPointerException-def ClassCast-def OutOfMemory-def ArrayIndexOutOfBoundsException-def ArrayStore-def NegativeArraySize-def IllegalMonitorState-def IllegalThreadState-def InterruptedException-def ArithmeticException-def*)

**lemmas** *sys-xcpts-neqs = sys-xcpts-neqs-aux sys-xcpts-neqs-aux[symmetric]*

**lemma** *Thread-neq-sys-xcpts-aux:*

*Thread*  $\neq$  *NullPointerException*  
*Thread*  $\neq$  *ClassCast*  
*Thread*  $\neq$  *OutOfMemory*  
*Thread*  $\neq$  *ArrayIndexOutOfBoundsException*  
*Thread*  $\neq$  *ArrayStore*  
*Thread*  $\neq$  *NegativeArraySize*  
*Thread*  $\neq$  *ArithmeticException*  
*Thread*  $\neq$  *IllegalMonitorState*  
*Thread*  $\neq$  *IllegalThreadState*  
*Thread*  $\neq$  *InterruptedException*

**by**(*simp-all add: Thread-def NullPointerException-def ClassCast-def OutOfMemory-def ArrayIndexOutOfBoundsException-def ArrayStore-def NegativeArraySize-def IllegalMonitorState-def IllegalThreadState-def InterruptedException-def ArithmeticException-def*)

**lemmas** *Thread-neq-sys-xcpts = Thread-neq-sys-xcpts-aux Thread-neq-sys-xcpts-aux[symmetric]*

### 3.5.2 Well-formedness for system classes and exceptions

**lemma**

**assumes** *wf-syscls P*  
**shows** *wf-syscls-class-Object:  $\exists C fs ms. \text{class } P \text{ Object} = \text{Some } (C, fs, ms)$*   
**and** *wf-syscls-class-Thread:  $\exists C fs ms. \text{class } P \text{ Thread} = \text{Some } (C, fs, ms)$*   
**using** *assms*  
**by**(*auto simp: map-of-SomeI wf-syscls-def is-class-def*)

**lemma** [*simp*]:

**assumes** *wf-syscls P*  
**shows** *wf-syscls-is-class-Object: is-class P Object*  
**and** *wf-syscls-is-class-Thread: is-class P Thread*  
**using** *assms* **by**(*simp-all add: is-class-def wf-syscls-class-Object wf-syscls-class-Thread*)

**lemma** *wf-syscls-xcpt-subcls-Throwable:*

$\llbracket C \in \text{sys-xcpts}; \text{wf-syscls } P \rrbracket \implies P \vdash C \preceq^* \text{Throwable}$   
**by**(*simp add: wf-syscls-def is-class-def class-def*)

**lemma** *wf-syscls-is-class-Throwable:*

*wf-syscls P  $\implies$  is-class P Throwable*  
**by**(*auto simp add: wf-syscls-def is-class-def class-def map-of-SomeI*)

**lemma** *wf-syscls-is-class-sub-Throwable:*

$\llbracket \text{wf-syscls } P; P \vdash C \preceq^* \text{Throwable} \rrbracket \implies \text{is-class } P C$   
**by**(*erule subcls-is-classI*)(*erule wf-syscls-is-class-Throwable*)

**lemma** *wf-syscls-is-class-xcpt:*

$\llbracket C \in \text{sys-xcpts}; \text{wf-syscls } P \rrbracket \implies \text{is-class } P C$   
**by**(*blast intro: wf-syscls-is-class-sub-Throwable wf-syscls-xcpt-subcls-Throwable*)

```

lemma wf-syscls-code [code]:
  wf-syscls P  $\longleftrightarrow$ 
    ( $\forall C \in \text{set } [\text{Object}, \text{Throwable}, \text{Thread}]. \text{is-class } P \ C) \wedge (\forall C \in \text{sys-xcpts}. P \vdash C \preceq^* \text{Throwable})$ 
by(simp only: wf-syscls-def) simp

end

```

### 3.6 System Classes

**theory** *SystemClasses*

**imports**

*Exceptions*

**begin**

This theory provides definitions for the *Object* class, and the system exceptions.

Inline *SystemClasses* definition because they are polymorphic values that violate ML's value restriction.

*Object* has actually superclass, but we set it to the empty string for code generation. Any other class name (like *undefined*) would do as well except for code generation.

```

definition ObjectC :: 'm cdecl
where [code-unfold]:
  ObjectC =
    (Object, (STR "", [],
      [(wait, [], Void, Native),
       (notify, [], Void, Native),
       (notifyAll, [], Void, Native),
       (hashCode, [], Integer, Native),
       (clone, [], Class Object, Native),
       (print, [Integer], Void, Native),
       (currentThread, [], Class Thread, Native),
       (interrupted, [], Boolean, Native),
       (yield, [], Void, Native)
      ]))

```

```

definition ThrowableC :: 'm cdecl
where [code-unfold]: ThrowableC  $\equiv$  (Throwable, (Object, [], []))

```

```

definition NullPointerC :: 'm cdecl
where [code-unfold]: NullPointerC  $\equiv$  (NullPointer, (Throwable, [], []))

```

```

definition ClassCastC :: 'm cdecl
where [code-unfold]: ClassCastC  $\equiv$  (ClassCast, (Throwable, [], []))

```

```

definition OutOfMemoryC :: 'm cdecl
where [code-unfold]: OutOfMemoryC  $\equiv$  (OutOfMemory, (Throwable, [], []))

```

```

definition ArrayIndexOutOfBoundsC :: 'm cdecl
where [code-unfold]: ArrayIndexOutOfBoundsC  $\equiv$  (ArrayIndexOutOfBounds, (Throwable, [], []))

```

```

definition ArrayStoreC :: 'm cdecl
where [code-unfold]: ArrayStoreC  $\equiv$  (ArrayStore, (Throwable, [], []))

```

```

definition NegativeArraySizeC :: 'm cdecl

```

```

where [code-unfold]: NegativeArraySizeC  $\equiv$  (NegativeArraySize, (Throwable,[],[]))

definition ArithmeticExceptionC :: 'm cdecl
where [code-unfold]: ArithmeticExceptionC  $\equiv$  (ArithmeticException, (Throwable,[],[]))

definition IllegalMonitorStateC :: 'm cdecl
where [code-unfold]: IllegalMonitorStateC  $\equiv$  (IllegalMonitorState, (Throwable,[],[]))

definition IllegalThreadStateC :: 'm cdecl
where [code-unfold]: IllegalThreadStateC  $\equiv$  (IllegalThreadState, (Throwable,[],[]))

definition InterruptedExceptionC :: 'm cdecl
where [code-unfold]: InterruptedExceptionC  $\equiv$  (InterruptedException, (Throwable,[],[]))

definition SystemClasses :: 'm cdecl list
where [code-unfold]:
  SystemClasses  $\equiv$ 
  [ObjectC, ThrowableC, NullPointerExceptionC, ClassCastC, OutOfMemoryC,
   ArrayIndexOutOfBoundsExceptionC, ArrayStoreC, NegativeArraySizeC,
   ArithmeticExceptionC,
   IllegalMonitorStateC, IllegalThreadStateC, InterruptedExceptionC]

end

```

### 3.7 An abstract heap model

```

theory Heap
imports
  Value
begin

primrec typeof :: 'addr val  $\rightarrow$  ty
where
  typeof Unit    = Some Void
| typeof Null    = Some NT
| typeof (Bool b) = Some Boolean
| typeof (Intg i) = Some Integer
| typeof (Addr a) = None

datatype addr-loc =
  CField cname vname
| ACell nat

lemma rec-addr-loc [simp]: rec-addr-loc = case-addr-loc
by(auto simp add: fun-eq-iff split: addr-loc.splits)

primrec is-volatile :: 'm prog  $\Rightarrow$  addr-loc  $\Rightarrow$  bool
where
  is-volatile P (ACell n) = False
| is-volatile P (CField D F) = volatile (snd (snd (field P D F)))

locale heap-base =
  addr-base addr2thread-id thread-id2addr

```

```

for addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
+
fixes spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
begin

```

```

fun typeof-h :: 'heap ⇒ 'addr val ⇒ ty option (typeof -)
where
  typeofh (Addr a) = map-option ty-of-htype (typeof-addr h a)
| typeofh v = typeof v

```

```

definition cname-of :: 'heap ⇒ 'addr ⇒ cname
where cname-of h a = the-Class (ty-of-htype (the (typeof-addr h a)))

```

```

definition hext :: 'heap ⇒ 'heap ⇒ bool (- ≤ - [51,51] 50)
where
  h ≤ h' ≡ typeof-addr h ⊆m typeof-addr h'

```

```

context
  notes [[inductive-internals]]
begin

```

```

inductive addr-loc-type :: 'm prog ⇒ 'heap ⇒ 'addr ⇒ addr-loc ⇒ ty ⇒ bool
  (-, ⊢ -@- : - [50, 50, 50, 50, 50] 51)

```

```

for P :: 'm prog and h :: 'heap and a :: 'addr
where

```

```

  addr-loc-type-field:
  [[ typeof-addr h a = [U]; P ⊢ class-type-of U has F:T (fm) in D ]]
  ⇒ P, h ⊢ a@CField D F : T

```

```

| addr-loc-type-cell:
  [[ typeof-addr h a = [Array-type T n']; n < n' ]]
  ⇒ P, h ⊢ a@ACell n : T

```

**end**

```

definition typeof-addr-loc :: 'm prog ⇒ 'heap ⇒ 'addr ⇒ addr-loc ⇒ ty
where typeof-addr-loc P h a al = (THE T. P, h ⊢ a@al : T)

```

```

definition deterministic-heap-ops :: bool
where

```

```

  deterministic-heap-ops ⇔
  (∀ h ad al v v'. heap-read h ad al v → heap-read h ad al v' → v = v') ∧
  (∀ h ad al v h' h''. heap-write h ad al v h' → heap-write h ad al v h'' → h' = h'') ∧
  (∀ hT h' a h'' a'. (h', a) ∈ allocate h hT → (h'', a') ∈ allocate h hT → h' = h'' ∧ a = a') ∧
  ¬ spurious-wakeups

```

**end**

**lemma** *typeof-lit-eq-Boolean* [simp]: (*typeof* *v* = *Some Boolean*) = ( $\exists b. v = \text{Bool } b$ )  
**by**(*cases v*)(*auto*)

**lemma** *typeof-lit-eq-Integer* [simp]: (*typeof* *v* = *Some Integer*) = ( $\exists i. v = \text{Intg } i$ )  
**by**(*cases v*)(*auto*)

**lemma** *typeof-lit-eq-NT* [simp]: (*typeof* *v* = *Some NT*) = (*v* = *Null*)  
**by**(*cases v*)(*auto*)

**lemma** *typeof-lit-eq-Void* [simp]: *typeof* *v* = *Some Void*  $\longleftrightarrow$  *v* = *Unit*  
**by**(*cases v*)(*auto*)

**lemma** *typeof-lit-neq-Class* [simp]: *typeof* *v*  $\neq$  *Some (Class C)*  
**by**(*cases v*) *auto*

**lemma** *typeof-lit-neq-Array* [simp]: *typeof* *v*  $\neq$  *Some (Array T)*  
**by**(*cases v*) *auto*

**lemma** *typeof-NoneD* [simp,dest]:  
*typeof* *v* = *Some x*  $\implies \neg \text{is-Addr } v$   
**by** (*cases v*) *auto*

**lemma** *typeof-lit-is-type*:  
*typeof* *v* = *Some T*  $\implies \text{is-type } P \ T$   
**by**(*cases v*) *auto*

**context** *heap-base* **begin**

**lemma** *typeof-h-eq-Boolean* [simp]: (*typeof<sub>h</sub>* *v* = *Some Boolean*) = ( $\exists b. v = \text{Bool } b$ )  
**by**(*cases v*)(*auto*)

**lemma** *typeof-h-eq-Integer* [simp]: (*typeof<sub>h</sub>* *v* = *Some Integer*) = ( $\exists i. v = \text{Intg } i$ )  
**by**(*cases v*)(*auto*)

**lemma** *typeof-h-eq-NT* [simp]: (*typeof<sub>h</sub>* *v* = *Some NT*) = (*v* = *Null*)  
**by**(*cases v*)(*auto*)

**lemma** *hextI*:  
 $\llbracket \bigwedge a \ C. \text{typeof-addr } h \ a = \lfloor \text{Class-type } C \rfloor \implies \text{typeof-addr } h' \ a = \lfloor \text{Class-type } C \rfloor;$   
 $\bigwedge a \ T \ n. \text{typeof-addr } h \ a = \lfloor \text{Array-type } T \ n \rfloor \implies \text{typeof-addr } h' \ a = \lfloor \text{Array-type } T \ n \rfloor \rrbracket$   
 $\implies h \trianglelefteq h'$

**unfolding** *hext-def*  
**by**(*rule map-leI*)(*case-tac v, simp-all*)

**lemma** *hext-objD*:  
**assumes**  $h \trianglelefteq h'$   
**and**  $\text{typeof-addr } h \ a = \lfloor \text{Class-type } C \rfloor$   
**shows**  $\text{typeof-addr } h' \ a = \lfloor \text{Class-type } C \rfloor$   
**using** *assms* **unfolding** *hext-def* **by**(*auto dest: map-le-SomeD*)

**lemma** *hext-arrD*:  
**assumes**  $h \trianglelefteq h'$   $\text{typeof-addr } h \ a = \lfloor \text{Array-type } T \ n \rfloor$   
**shows**  $\text{typeof-addr } h' \ a = \lfloor \text{Array-type } T \ n \rfloor$

**using** *assms* **unfolding** *hext-def* **by**(*blast dest: map-le-SomeD*)

**lemma** *hext-refl* [*iff*]:  $h \trianglelefteq h$   
**by** (*rule hextI*) *blast*+

**lemma** *hext-trans* [*trans*]:  $\llbracket h \trianglelefteq h'; h' \trianglelefteq h'' \rrbracket \implies h \trianglelefteq h''$   
**unfolding** *hext-def* **by**(*rule map-le-trans*)

**lemma** *typeof-lit-typeof*:  
 $\text{typeof } v = \lfloor T \rfloor \implies \text{typeof}_h v = \lfloor T \rfloor$   
**by**(*cases v*)(*simp-all*)

**lemma** *addr-loc-type-fun*:  
 $\llbracket P, h \vdash a@al : T; P, h \vdash a@al : T' \rrbracket \implies T = T'$   
**by**(*auto elim!: addr-loc-type.cases dest: has-field-fun*)

**lemma** *THE-addr-loc-type*:  
 $P, h \vdash a@al : T \implies (\text{THE } T. P, h \vdash a@al : T) = T$   
**by**(*rule the-equality*)(*auto dest: addr-loc-type-fun*)

**lemma** *typeof-addr-locI* [*simp*]:  
 $P, h \vdash a@al : T \implies \text{typeof-addr-loc } P \ h \ a \ al = T$   
**by**(*auto simp add: typeof-addr-loc-def dest: addr-loc-type-fun*)

**lemma** *deterministic-heap-opsI*:  
 $\llbracket \bigwedge h \ ad \ al \ v \ v'. \llbracket \text{heap-read } h \ ad \ al \ v; \text{heap-read } h \ ad \ al \ v' \rrbracket \implies v = v';$   
 $\bigwedge h \ ad \ al \ v \ h' \ h''. \llbracket \text{heap-write } h \ ad \ al \ v \ h'; \text{heap-write } h \ ad \ al \ v \ h'' \rrbracket \implies h' = h'';$   
 $\bigwedge h \ hT \ h' \ a \ h'' \ a'. \llbracket (h', a) \in \text{allocate } h \ hT; (h'', a') \in \text{allocate } h \ hT \rrbracket \implies h' = h'' \wedge a = a';$   
 $\neg \text{spurious-wakeups} \rrbracket$   
 $\implies \text{deterministic-heap-ops}$   
**unfolding** *deterministic-heap-ops-def* **by** *blast*

**lemma** *deterministic-heap-ops-readD*:  
 $\llbracket \text{deterministic-heap-ops}; \text{heap-read } h \ ad \ al \ v; \text{heap-read } h \ ad \ al \ v' \rrbracket \implies v = v'$   
**unfolding** *deterministic-heap-ops-def* **by** *blast*

**lemma** *deterministic-heap-ops-writeD*:  
 $\llbracket \text{deterministic-heap-ops}; \text{heap-write } h \ ad \ al \ v \ h'; \text{heap-write } h \ ad \ al \ v \ h'' \rrbracket \implies h' = h''$   
**unfolding** *deterministic-heap-ops-def* **by** *blast*

**lemma** *deterministic-heap-ops-allocateD*:  
 $\llbracket \text{deterministic-heap-ops}; (h', a) \in \text{allocate } h \ hT; (h'', a') \in \text{allocate } h \ hT \rrbracket \implies h' = h'' \wedge a = a'$   
**unfolding** *deterministic-heap-ops-def* **by** *blast*

**lemma** *deterministic-heap-ops-no-spurious-wakeups*:  
 $\text{deterministic-heap-ops} \implies \neg \text{spurious-wakeups}$   
**unfolding** *deterministic-heap-ops-def* **by** *blast*

**end**

**locale** *addr-conv* =  
*heap-base*  
*addr2thread-id thread-id2addr*  
*spurious-wakeups*

```

    empty-heap allocate typeof-addr heap-read heap-write
+
prog P
for addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype
and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
and P :: 'm prog
+
assumes addr2thread-id-inverse:
 $\llbracket \text{typeof-addr } h \ a = \lfloor \text{Class-type } C \rfloor; P \vdash C \preceq^* \text{Thread} \rrbracket \Longrightarrow \text{thread-id2addr } (\text{addr2thread-id } a) = a$ 
begin

lemma typeof-addr-thread-id2-addr-addr2thread-id [simp]:
 $\llbracket \text{typeof-addr } h \ a = \lfloor \text{Class-type } C \rfloor; P \vdash C \preceq^* \text{Thread} \rrbracket \Longrightarrow \text{typeof-addr } h \ (\text{thread-id2addr } (\text{addr2thread-id } a)) = \lfloor \text{Class-type } C \rfloor$ 
by(simp add: addr2thread-id-inverse)

end

locale heap =
  addr-conv
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  P
  for addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
  and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
  and spurious-wakeups :: bool
  and empty-heap :: 'heap
  and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
  and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype
  and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
  and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
  and P :: 'm prog
+
  assumes allocate-SomeD:  $\llbracket (h', a) \in \text{allocate } h \ hT; \text{is-htype } P \ hT \rrbracket \Longrightarrow \text{typeof-addr } h' \ a = \text{Some } hT$ 

  and hext-allocate:  $\bigwedge a. (h', a) \in \text{allocate } h \ hT \Longrightarrow h \sqsubseteq h'$ 

  and hext-heap-write:
    heap-write  $h \ a \ al \ v \ h' \Longrightarrow h \sqsubseteq h'$ 

begin

lemmas hext-heap-ops = hext-allocate hext-heap-write

lemma typeof-addr-hext-mono:
 $\llbracket h \sqsubseteq h'; \text{typeof-addr } h \ a = \lfloor hT \rfloor \rrbracket \Longrightarrow \text{typeof-addr } h' \ a = \lfloor hT \rfloor$ 

```

**unfolding** *hext-def* **by**(*rule map-le-SomeD*)

**lemma** *hext-typeof-mono*:

$\llbracket h \trianglelefteq h'; \text{typeof}_h v = \text{Some } T \rrbracket \implies \text{typeof}_{h'} v = \text{Some } T$   
**by** (*cases v*)(*auto intro: typeof-addr-hext-mono*)

**lemma** *addr-loc-type-hext-mono*:

$\llbracket P, h \vdash a@al : T; h \trianglelefteq h' \rrbracket \implies P, h' \vdash a@al : T$   
**by**(*force elim!*: *addr-loc-type.cases intro: addr-loc-type.intros elim: typeof-addr-hext-mono dest: hext-arrD*)

**lemma** *type-of-hext-type-of*: — FIXME: What's this rule good for?

$\llbracket \text{typeof}_h w = \lfloor T \rfloor; \text{hext } h \ h' \rrbracket \implies \text{typeof}_{h'} w = \lfloor T \rfloor$   
**by**(*rule hext-typeof-mono*)

**lemma** *hext-None*:  $\llbracket h \trianglelefteq h'; \text{typeof-addr } h' \ a = \text{None} \rrbracket \implies \text{typeof-addr } h \ a = \text{None}$

**by**(*rule ccontr*)(*auto dest: typeof-addr-hext-mono*)

**lemma** *map-typeof-hext-mono*:

$\llbracket \text{map } \text{typeof}_h \text{ vs} = \text{map } \text{Some } Ts; h \trianglelefteq h' \rrbracket \implies \text{map } \text{typeof}_{h'} \text{ vs} = \text{map } \text{Some } Ts$   
**apply**(*induct vs arbitrary: Ts*)  
**apply**(*auto simp add: Cons-eq-map-conv intro: hext-typeof-mono*)  
**done**

**lemma** *hext-typeof-addr-map-le*:

$h \trianglelefteq h' \implies \text{typeof-addr } h \subseteq_m \text{typeof-addr } h'$   
**by**(*auto simp add: map-le-def dest: typeof-addr-hext-mono*)

**lemma** *hext-dom-typeof-addr-subset*:

$h \trianglelefteq h' \implies \text{dom } (\text{typeof-addr } h) \subseteq \text{dom } (\text{typeof-addr } h')$   
**by** (*metis hext-typeof-addr-map-le map-le-implies-dom-le*)

**end**

**declare** *heap-base.typeof-h.simps* [*code*]

**declare** *heap-base.cname-of-def* [*code*]

**end**

## 3.8 Observable events in JinjaThreads

**theory** *Observable-Events*

**imports**

*Heap*

*../Framework/FWState*

**begin**

**datatype** (*'addr, 'thread-id*) *obs-event* =

*ExternalCall 'addr mname 'addr val list 'addr val*  
 $|$  *ReadMem 'addr addr-loc 'addr val*  
 $|$  *WriteMem 'addr addr-loc 'addr val*  
 $|$  *NewHeapElem 'addr htype*  
 $|$  *ThreadStart 'thread-id*  
 $|$  *ThreadJoin 'thread-id*



```

| SyncLock 'addr
| SyncUnlock 'addr
| ObsInterrupt 'thread-id
| ObsInterrupted 'thread-id

instance obs-event :: (type, type) obs-action
proof qed

type-synonym
('addr, 'thread-id, 'x, 'heap) Jinja-thread-action =
('addr, 'thread-id, 'x, 'heap, 'addr, ('addr, 'thread-id) obs-event) thread-action

print-translation <
let
  fun tr'
    [ a1, t1, x, h, a2
      , Const (@{type-syntax obs-event}, -) $ a3 $ t2] =
    if a1 = a2 andalso a2 = a3 andalso t1 = t2 then Syntax.const @{type-syntax Jinja-thread-action}
    $ a1 $ t1 $ x $ h
    else raise Match;
  in [(@{type-syntax thread-action}, K tr')]
end
>
typ ('addr, 'thread-id, 'x, 'heap) Jinja-thread-action

lemma range-ty-of-htype: range ty-of-htype  $\subseteq$  range Class  $\cup$  range Array
apply(rule subsetI)
apply(erule rangeE)
apply(rename-tac ht)
apply(case-tac ht)
apply auto
done

lemma some-choice:  $(\exists a. \forall b. P b (a b)) \longleftrightarrow (\forall b. \exists a. P b a)$ 
by metis

definition convert-RA :: 'addr released-locks  $\Rightarrow$  ('addr :: addr, 'thread-id) obs-event list
where  $\bigwedge ln. \text{convert-RA } ln = \text{concat } (\text{map } (\lambda ad. \text{replicate } (ln \$ ad) (\text{SyncLock } ad)) (\text{monitor-funfun-to-list } ln))$ 

lemma set-convert-RA-not-New [simp]:
 $\bigwedge ln. \text{NewHeapElem } a \text{ CTn} \notin \text{set } (\text{convert-RA } ln)$ 
by(auto simp add: convert-RA-def)

lemma set-convert-RA-not-Read [simp]:
 $\bigwedge ln. \text{ReadMem } ad \text{ al } v \notin \text{set } (\text{convert-RA } ln)$ 
by(auto simp add: convert-RA-def)

end

```

### 3.9 The initial configuration

**theory** *StartConfig*

**imports**

*Exceptions*

*Observable-Events*

**begin**

**definition** *initialization-list* :: *cname list*

**where**

*initialization-list* = *Thread* # *sys-xcpts-list*

**context** *heap-base* **begin**

**definition** *create-initial-object* :: *'heap* × *'addr list* × *bool* ⇒ *cname* ⇒ *'heap* × *'addr list* × *bool*

**where**

*create-initial-object* =

(λ(*h*, *ads*, *b*) *C*.

  if *b*

  then let *HA* = *allocate h (Class-type C)*

    in if *HA* = {} then (*h*, *ads*, *False*)

      else let (*h'*, *a''*) = *SOME ha. ha* ∈ *HA* in (*h'*, *ads* @ [*a''*], *True*)

  else (*h*, *ads*, *False*))

**definition** *start-heap-data* :: *'heap* × *'addr list* × *bool*

**where**

*start-heap-data* = *foldl create-initial-object (empty-heap, [], True) initialization-list*

**definition** *start-heap* :: *'heap*

**where** *start-heap* = *fst start-heap-data*

**definition** *start-heap-ok* :: *bool*

**where** *start-heap-ok* = *snd (snd (start-heap-data))*

**definition** *start-heap-obs* :: (*'addr*, *'thread-id*) *obs-event list*

**where**

*start-heap-obs* =

*map* (λ(*C*, *a*). *NewHeapElem a (Class-type C)*) (*zip initialization-list (fst (snd start-heap-data))*)

**definition** *start-addr* :: *'addr list*

**where** *start-addr* = *fst (snd start-heap-data)*

**definition** *addr-of-sys-xcpt* :: *cname* ⇒ *'addr*

**where** *addr-of-sys-xcpt C* = *the (map-of (zip initialization-list start-addr) C)*

**definition** *start-tid* :: *'thread-id*

**where** *start-tid* = *addr2thread-id (hd start-addr)*

**definition** *start-state* :: (*cname* ⇒ *mname* ⇒ *ty list* ⇒ *ty* ⇒ *'m* ⇒ *'addr val list* ⇒ *'x*) ⇒ *'m prog*  
⇒ *cname* ⇒ *mname* ⇒ *'addr val list* ⇒ (*'addr*, *'thread-id*, *'x*, *'heap*, *'addr*) *state*

**where**

*start-state f P C M vs* ≡

  let (*D*, *Ts*, *T*, *m*) = *method P C M*

  in (*K\$ None*, ([*start-tid* ↦ (*f D M Ts T (the m) vs*, *no-wait-locks*)], *start-heap*), *Map.empty*, {})

**lemma** *create-initial-object-simps*:

*create-initial-object* ( $h$ ,  $ads$ ,  $b$ )  $C =$   
 (if  $b$   
   then let  $HA = \text{allocate } h \text{ (Class-type } C)$   
     in if  $HA = \{\}$  then ( $h$ ,  $ads$ ,  $False$ )  
       else let  $(h', a'') = \text{SOME } ha. ha \in HA$  in ( $h'$ ,  $ads @ [a'']$ ,  $True$ )  
   else ( $h$ ,  $ads$ ,  $False$ ))

**unfolding** *create-initial-object-def* **by** *simp*

**lemma** *create-initial-object-False* [*simp*]:

*create-initial-object* ( $h$ ,  $ads$ ,  $False$ )  $C = (h, ads, False)$

**by**(*simp add: create-initial-object-simps*)

**lemma** *foldl-create-initial-object-False* [*simp*]:

*foldl create-initial-object* ( $h$ ,  $ads$ ,  $False$ )  $Cs = (h, ads, False)$

**by**(*induct Cs*) *simp-all*

**lemma** *NewHeapElem-start-heap-obs-start-addrD*:

*NewHeapElem* a  $CTh \in \text{set start-heap-obs} \implies a \in \text{set start-addr}$

**unfolding** *start-heap-obs-def start-addr-def*

**by**(*auto dest: set-zip-rightD*)

**lemma** *shr-start-state*: *shr* (*start-state*  $f P C M vs$ ) = *start-heap*

**by**(*simp add: start-state-def split-beta*)

**lemma** *start-heap-obs-not-Read*:

*ReadMem*  $ad \text{ al } v \notin \text{set start-heap-obs}$

**unfolding** *start-heap-obs-def* **by** *auto*

**lemma** *length-initialization-list-le-length-start-addr*:

*length initialization-list*  $\geq \text{length start-addr}$

**proof** –

{ **fix**  $h ads xs$   
**have** *length* (*fst* (*snd* (*foldl create-initial-object* ( $h$ ,  $ads$ ,  $True$ )  $xs$ )))  $\leq \text{length } ads + \text{length } xs$   
**proof**(*induct xs arbitrary: h ads*)  
  **case Nil thus ?case by simp**  
**next**  
  **case** (*Cons*  $x xs$ )  
  **from** *this*[*of fst* (*SOME*  $ha. ha \in \text{allocate } h \text{ (Class-type } x)$ )  $ads @ [\text{snd} (\text{SOME } ha. ha \in \text{allocate } h \text{ (Class-type } x)]$ )]  
  **show** ?*case* **by**(*clarsimp simp add: create-initial-object-simps split-beta*)  
**qed** }  
**from** *this*[*of empty-heap [] initialization-list*]  
**show** ?*thesis* **unfolding** *start-heap-def start-addr-def start-heap-data-def* **by** *simp*  
**qed**

**lemma** (**in** –) *distinct-initialization-list*:

*distinct initialization-list*

**by**(*simp add: initialization-list-def sys-xcpts-list-def sys-xcpts-neqs Thread-neq-sys-xcpts*)

**lemma** (**in** –) *wf-syscls-initialization-list-is-class*:

$\llbracket \text{wf-syscls } P; C \in \text{set initialization-list} \rrbracket \implies \text{is-class } P C$

**by**(*auto simp add: initialization-list-def sys-xcpts-list-def wf-syscls-is-class-xcpt*)

**lemma** *start-addr-NewHeapElem-start-heap-obsD*:

$a \in \text{set start-addr} \implies \exists CTn. \text{NewHeapElem } a \ CTn \in \text{set start-heap-obs}$

**using** *length-initialization-list-le-length-start-addr*

**unfolding** *start-heap-obs-def start-addr-def*

**by**(*force simp add: set-zip in-set-conv-nth intro: rev-image-eqI*)

**lemma** *in-set-start-addr-conv-NewHeapElem*:

$a \in \text{set start-addr} \iff (\exists CTn. \text{NewHeapElem } a \ CTn \in \text{set start-heap-obs})$

**by**(*blast dest: start-addr-NewHeapElem-start-heap-obsD intro: NewHeapElem-start-heap-obs-start-addrD*)

### 3.9.1 preallocated

**definition** *preallocated* :: 'heap  $\Rightarrow$  bool

**where** *preallocated*  $h \equiv \forall C \in \text{sys-xcpts}. \text{typeof-addr } h \ (\text{addr-of-sys-xcpt } C) = \lfloor \text{Class-type } C \rfloor$

**lemma** *typeof-addr-sys-xcp*:

$\llbracket \text{preallocated } h; C \in \text{sys-xcpts} \rrbracket \implies \text{typeof-addr } h \ (\text{addr-of-sys-xcpt } C) = \lfloor \text{Class-type } C \rfloor$

**by**(*simp add: preallocated-def*)

**lemma** *typeof-sys-xcp*:

$\llbracket \text{preallocated } h; C \in \text{sys-xcpts} \rrbracket \implies \text{typeof}_h \ (\text{Addr } (\text{addr-of-sys-xcpt } C)) = \lfloor \text{Class } C \rfloor$

**by**(*simp add: typeof-addr-sys-xcp*)

**lemma** *addr-of-sys-xcpt-start-addr*:

$\llbracket \text{start-heap-ok}; C \in \text{sys-xcpts} \rrbracket \implies \text{addr-of-sys-xcpt } C \in \text{set start-addr}$

**unfolding** *start-heap-ok-def start-heap-data-def initialization-list-def sys-xcpts-list-def preallocated-def start-heap-def start-addr-def*

**apply**(*simp split: prod.split-asm if-split-asm add: create-initial-object-simps*)

**apply**(*erule sys-xcpts-cases*)

**apply**(*simp-all add: addr-of-sys-xcpt-def start-addr-def start-heap-data-def initialization-list-def sys-xcpts-list-def create-initial-object-simps*)

**done**

**lemma** [*simp*]:

**assumes** *preallocated*  $h$

**shows** *typeof-ClassCast*:  $\text{typeof-addr } h \ (\text{addr-of-sys-xcpt } \text{ClassCast}) = \text{Some}(\text{Class-type } \text{ClassCast})$

**and** *typeof-OutOfMemory*:  $\text{typeof-addr } h \ (\text{addr-of-sys-xcpt } \text{OutOfMemory}) = \text{Some}(\text{Class-type } \text{OutOfMemory})$

**and** *typeof-NullPointer*:  $\text{typeof-addr } h \ (\text{addr-of-sys-xcpt } \text{NullPointer}) = \text{Some}(\text{Class-type } \text{NullPointer})$

**and** *typeof-ArrayIndexOutOfBounds*:

$\text{typeof-addr } h \ (\text{addr-of-sys-xcpt } \text{ArrayIndexOutOfBounds}) = \text{Some}(\text{Class-type } \text{ArrayIndexOutOfBounds})$

**and** *typeof-ArrayStore*:  $\text{typeof-addr } h \ (\text{addr-of-sys-xcpt } \text{ArrayStore}) = \text{Some}(\text{Class-type } \text{ArrayStore})$

**and** *typeof-NegativeArraySize*:  $\text{typeof-addr } h \ (\text{addr-of-sys-xcpt } \text{NegativeArraySize}) = \text{Some}(\text{Class-type } \text{NegativeArraySize})$

**and** *typeof-ArithmeticException*:  $\text{typeof-addr } h \ (\text{addr-of-sys-xcpt } \text{ArithmeticException}) = \text{Some}(\text{Class-type } \text{ArithmeticException})$

**and** *typeof-IllegalMonitorState*:  $\text{typeof-addr } h \ (\text{addr-of-sys-xcpt } \text{IllegalMonitorState}) = \text{Some}(\text{Class-type } \text{IllegalMonitorState})$

**and** *typeof-IllegalThreadState*:  $\text{typeof-addr } h \ (\text{addr-of-sys-xcpt } \text{IllegalThreadState}) = \text{Some}(\text{Class-type } \text{IllegalThreadState})$

**and** *typeof-InterruptedException*:  $\text{typeof-addr } h \ (\text{addr-of-sys-xcpt } \text{InterruptedException}) = \text{Some}(\text{Class-type } \text{InterruptedException})$

*InterruptedException*)

**using** *assms*

**by**(*simp-all add: typeof-addr-sys-xcp*)

**lemma** *cname-of-xcp* [*simp*]:

$\llbracket \text{preallocated } h; C \in \text{sys-xcpts} \rrbracket \implies \text{cname-of } h \text{ (addr-of-sys-xcpt } C) = C$

**by**(*drule (1) typeof-addr-sys-xcp*)(*simp add: cname-of-def*)

**lemma** *preallocated-hext*:

$\llbracket \text{preallocated } h; h \trianglelefteq h' \rrbracket \implies \text{preallocated } h'$

**by**(*auto simp add: preallocated-def dest: hext-objD*)

**end**

**context** *heap* **begin**

**lemma** *preallocated-heap-ops*:

**assumes** *preallocated h*

**shows** *preallocated-allocate*:  $\bigwedge a. (h', a) \in \text{allocate } h \text{ } hT \implies \text{preallocated } h'$

**and** *preallocated-write-field*: *heap-write h a al v h'  $\implies$  preallocated h'*

**using** *preallocated-hext[OF assms, of h']*

**by**(*blast intro: hext-heap-ops*)**+**

**lemma** *not-empty-pairE*:  $\llbracket A \neq \{\}; \bigwedge a b. (a, b) \in A \implies \text{thesis} \rrbracket \implies \text{thesis}$

**by** *auto*

**lemma** *allocate-not-emptyI*:  $(h', a) \in \text{allocate } h \text{ } hT \implies \text{allocate } h \text{ } hT \neq \{\}$

**by** *auto*

**lemma** *allocate-Eps*:

$\llbracket (h'', a'') \in \text{allocate } h \text{ } hT; (\text{SOME } ha. ha \in \text{allocate } h \text{ } hT) = (h', a') \rrbracket \implies (h', a') \in \text{allocate } h \text{ } hT$

**by**(*drule sym*)(*auto intro: someI*)

**lemma** *preallocated-start-heap*:

$\llbracket \text{start-heap-ok}; \text{wf-syscls } P \rrbracket \implies \text{preallocated start-heap}$

**unfolding** *start-heap-ok-def start-heap-data-def initialization-list-def sys-xcpts-list-def*  
*preallocated-def start-heap-def start-addr-def*

**apply**(*clarsimp split: prod.split-asm if-split-asm simp add: create-initial-object-simps*)

**apply**(*erule not-empty-pairE*)**+**

**apply**(*drule (1) allocate-Eps*)

**apply**(*drule (1) allocate-Eps*)

**apply**(*drule (1) allocate-Eps*)

**apply**(*drule (1) allocate-Eps*)

**apply**(*drule (1) allocate-Eps*)

**apply**(*drule (1) allocate-Eps*)

**apply**(*drule (1) allocate-Eps*)

**apply**(*drule (1) allocate-Eps*)

**apply**(*drule (1) allocate-Eps*)

**apply**(*drule (1) allocate-Eps*)

**apply**(*drule (1) allocate-Eps*)

**apply**(*rotate-tac 13*)

**apply**(*frule allocate-SomeD, simp add: wf-syscls-is-class-xcpt, frule hext-allocate, rotate-tac 1*)

**apply**(*frule allocate-SomeD, simp add: wf-syscls-is-class-xcpt, frule hext-allocate, rotate-tac 1*)

**apply**(*frule allocate-SomeD, simp add: wf-syscls-is-class-xcpt, frule hext-allocate, rotate-tac 1*)

```

apply(frule allocate-SomeD, simp add: wf-syscls-is-class-xcpt, frule hext-allocate, rotate-tac 1)
apply(frule allocate-SomeD, simp add: wf-syscls-is-class-xcpt, frule hext-allocate, rotate-tac 1)
apply(frule allocate-SomeD, simp add: wf-syscls-is-class-xcpt, frule hext-allocate, rotate-tac 1)
apply(frule allocate-SomeD, simp add: wf-syscls-is-class-xcpt, frule hext-allocate, rotate-tac 1)
apply(frule allocate-SomeD, simp add: wf-syscls-is-class-xcpt, frule hext-allocate, rotate-tac 1)
apply(frule allocate-SomeD, simp add: wf-syscls-is-class-xcpt, frule hext-allocate, rotate-tac 1)
apply(frule allocate-SomeD, simp add: wf-syscls-is-class-xcpt, frule hext-allocate, rotate-tac 1)
apply(frule allocate-SomeD, simp add: wf-syscls-is-class-xcpt, frule hext-allocate, rotate-tac 1)
apply(erule sys-xcpts-cases)
apply(simp-all add: addr-of-sys-xcpt-def initialization-list-def sys-xcpts-list-def sys-xcpts-neqs Thread-neq-sys-xcpts
start-heap-data-def start-addr-def create-initial-object-simps allocate-not-emptyI split del: if-split)
apply(assumption|erule typeof-addr-hext-mono)+
done

```

**lemma** start-tid-start-addr:

```

  [| wf-syscls P; start-heap-ok |]  $\impl$  thread-id2addr start-tid  $\in$  set start-addr
unfolding start-heap-ok-def start-heap-data-def initialization-list-def sys-xcpts-list-def
  preallocated-def start-heap-def start-addr-def
apply(simp split: prod.split-asm if-split-asm add: create-initial-object-simps addr-of-sys-xcpt-def start-addr-def
start-tid-def start-heap-data-def initialization-list-def sys-xcpts-list-def)
apply(erule not-empty-pairE)+
apply(drule (1) allocate-Eps)
apply(rotate-tac -1)
apply(drule allocate-SomeD, simp)
apply(auto intro: addr2thread-id-inverse)
done

```

**lemma**

```

  assumes wf-syscls P
  shows dom-typeof-addr-start-heap: set start-addr  $\subseteq$  dom (typeof-addr start-heap)
  and distinct-start-addr: distinct start-addr

```

**proof** –

```

  { fix h ads b and Cs xs :: cname list
    assume set ads  $\subseteq$  dom (typeof-addr h) and distinct (Cs @ xs) and length Cs = length ads
      and  $\bigwedge C a. (C, a) \in \text{set } (\text{zip } Cs \text{ ads}) \implies \text{typeof-addr } h \ a = \lfloor \text{Class-type } C \rfloor$ 
      and  $\bigwedge C. C \in \text{set } xs \implies \text{is-class } P \ C$ 
    hence set (fst (snd (foldl create-initial-object (h, ads, b) xs)))  $\subseteq$ 
      dom (typeof-addr (fst (foldl create-initial-object (h, ads, b) xs)))  $\wedge$ 
      (distinct ads  $\longrightarrow$  distinct (fst (snd (foldl create-initial-object (h, ads, b) xs))))
    (is ?concl xs h ads b Cs)
    proof(induct xs arbitrary: h ads b Cs)
      case Nil thus ?case by auto
    next
      case (Cons x xs)
      note ads =  $\langle \text{set ads} \subseteq \text{dom } (\text{typeof-addr } h) \rangle$ 
      note dist =  $\langle \text{distinct } (Cs @ x \# xs) \rangle$ 
      note len =  $\langle \text{length } Cs = \text{length ads} \rangle$ 
      note type =  $\langle \bigwedge C a. (C, a) \in \text{set } (\text{zip } Cs \text{ ads}) \implies \text{typeof-addr } h \ a = \lfloor \text{Class-type } C \rfloor \rangle$ 
      note is-class =  $\langle \bigwedge C. C \in \text{set } (x \# xs) \implies \text{is-class } P \ C \rangle$ 
      show ?case
      proof(cases b  $\wedge$  allocate h (Class-type x)  $\neq$  {})
        case False thus ?thesis
          using ads len by(auto simp add: create-initial-object-simps zip-append1)
      next

```

```

case [simp]: True
obtain  $h' a'$  where  $h'a': (SOME\ ha.\ ha \in allocate\ h\ (Class\text{-}type\ x)) = (h', a')$ 
  by(cases  $SOME\ ha.\ ha \in allocate\ h\ (Class\text{-}type\ x)$ )
with True have new-obj:  $(h', a') \in allocate\ h\ (Class\text{-}type\ x)$ 
  by(auto simp del: True intro: allocate-Eps)
hence hext:  $h \sqsubseteq h'$  by(rule hext-allocate)
with ads new-obj have ads': set ads  $\subseteq dom\ (typeof\text{-}addr\ h')$ 
  by(auto dest: typeof-addr-hext-mono[OF hext-allocate])
moreover {
  from new-obj ads' is-class[of x]
  have set (ads @ [a'])  $\subseteq dom\ (typeof\text{-}addr\ h')$ 
    by(auto dest: allocate-SomeD)
  moreover from dist have distinct ((Cs @ [x]) @ xs) by simp
  moreover have length (Cs @ [x]) = length (ads @ [a']) using len by simp
  moreover {
    fix C a
    assume (C, a)  $\in set\ (zip\ (Cs\ @\ [x])\ (ads\ @\ [a']))$ 
    hence typeof-addr  $h' a = \lfloor Class\text{-}type\ C \rfloor$ 
      using hext new-obj type[of C a] len is-class
    by(auto dest: allocate-SomeD hext-objD) }
  note type' = this
  moreover have is-class':  $\bigwedge C.\ C \in set\ xs \implies is\text{-}class\ P\ C$  using is-class by simp
  ultimately have ?concl xs  $h' (ads\ @\ [a'])\ True\ (Cs\ @\ [x])$  by(rule Cons)
  moreover have  $a' \notin set\ ads$ 
  proof
    assume  $a': a' \in set\ ads$ 
    then obtain C where (C, a')  $\in set\ (zip\ Cs\ ads)$  C  $\in set\ Cs$ 
      using len unfolding set-zip in-set-conv-nth by auto
    hence typeof-addr  $h\ a' = \lfloor Class\text{-}type\ C \rfloor$  by-(rule type)
    with hext have typeof-addr  $h' a' = \lfloor Class\text{-}type\ C \rfloor$  by(rule typeof-addr-hext-mono)
    moreover from new-obj is-class
    have typeof-addr  $h' a' = \lfloor Class\text{-}type\ x \rfloor$  by(auto dest: allocate-SomeD)
    ultimately have  $C = x$  by simp
    with dist  $\langle C \in set\ Cs \rangle$  show False by simp
  qed
  moreover note calculation }
ultimately show ?thesis by(simp add: create-initial-object-simps new-obj  $h'a'$ )
qed
qed }
from this[of [] empty-heap [] initialization-list True]
  distinct-initialization-list wf-syscls-initialization-list-is-class[OF assms]
show set start-addr  $\subseteq dom\ (typeof\text{-}addr\ start\text{-}heap)$ 
  and distinct start-addr
  unfolding start-heap-def start-addr-def start-heap-data-def by auto
qed

lemma NewHeapElem-start-heap-obsD:
  assumes wf-syscls P
  and NewHeapElem a  $hT \in set\ start\text{-}heap\text{-}obs$ 
  shows typeof-addr start-heap a =  $\lfloor hT \rfloor$ 
proof -
  show ?thesis
  proof(cases  $hT$ )
    case (Class-type C)

```

```

{ fix h ads b xs Cs
  assume (C, a) ∈ set (zip (Cs @ xs) (fst (snd (foldl create-initial-object (h, ads, b) xs))))
    and ∀(C, a) ∈ set (zip Cs ads). typeof-addr h a = [Class-type C]
    and length Cs = length ads
    and ∀ C ∈ set xs. is-class P C
  hence typeof-addr (fst (foldl create-initial-object (h, ads, b) xs)) a = [Class-type C]
  proof(induct xs arbitrary: h ads b Cs)
    case Nil thus ?case by auto
  next
    case (Cons x xs)
    note inv = ⟨∀(C, a) ∈ set (zip Cs ads). typeof-addr h a = [Class-type C]⟩
    and Ca = ⟨(C, a) ∈ set (zip (Cs @ x # xs) (fst (snd (foldl create-initial-object (h, ads, b) (x
# xs))))))⟩
    and len = ⟨length Cs = length ads⟩
    and is-class = ⟨∀ C ∈ set (x # xs). is-class P C⟩
  show ?case
  proof(cases b ∧ allocate h (Class-type x) ≠ {})
    case False thus ?thesis
      using inv Ca len by(auto simp add: create-initial-object-simps zip-append1 split: if-split-asm)
  next
    case [simp]: True
    obtain h' a' where h'a': (SOME ha. ha ∈ allocate h (Class-type x)) = (h', a')
      by(cases SOME ha. ha ∈ allocate h (Class-type x))
    with True have new-obj: (h', a') ∈ allocate h (Class-type x)
      by(auto simp del: True intro: allocate-Eps)
    hence hext: h ≤ h' by(rule hext-allocate)

    have (C, a) ∈ set (zip ((Cs @ [x]) @ xs) (fst (snd (foldl create-initial-object (h', ads @ [a'],
True) xs))))
      using Ca new-obj by(simp add: create-initial-object-simps h'a')
    moreover have ∀(C, a) ∈ set (zip (Cs @ [x]) (ads @ [a'])). typeof-addr h' a = [Class-type C]
    proof(clarify)
      fix C a
      assume (C, a) ∈ set (zip (Cs @ [x]) (ads @ [a']))
      thus typeof-addr h' a = [Class-type C]
        using inv len hext new-obj is-class by(auto dest: allocate-SomeD typeof-addr-hext-mono)
    qed
    moreover have length (Cs @ [x]) = length (ads @ [a']) using len by simp
    moreover have ∀ C ∈ set xs. is-class P C using is-class by simp
    ultimately have typeof-addr (fst (foldl create-initial-object (h', ads @ [a'], True) xs)) a =
[Class-type C]
      by(rule Cons)
    thus ?thesis using new-obj by(simp add: create-initial-object-simps h'a')
  qed
qed }
from this[of [] initialization-list empty-heap [] True] assms wf-syscls-initialization-list-is-class[of P]

  show ?thesis by(auto simp add: start-heap-obs-def start-heap-data-def start-heap-def Class-type)
next
  case Array-type thus ?thesis using assms
    by(auto simp add: start-heap-obs-def start-heap-data-def start-heap-def)
qed
qed

```



end

### 3.9.2 Code generation

**definition** *pick-addr* :: ('heap × 'addr) set ⇒ 'heap × 'addr  
**where** *pick-addr* HA = (SOME ha. ha ∈ HA)

**lemma** *pick-addr-code* [code]:  
*pick-addr* (set [ha]) = ha  
**by**(*simp* add: *pick-addr-def*)

**lemma** (**in** *heap-base*) *start-heap-data-code*:  
*start-heap-data* =  
 (let  
 (h, ads, b) = foldl  
 (λ(h, ads, b) C.  
   if b then  
     let HA = allocate h (Class-type C)  
     in if HA = {} then (h, ads, False)  
       else let (h', a'') = pick-addr HA in (h', a'' # ads, True)  
   else (h, ads, False))  
 (empty-heap, [], True)  
 initialization-list  
 in (h, rev ads, b))  
**unfolding** *start-heap-data-def* *create-initial-object-def* *pick-addr-def*  
**by**(rule *rev-induct*)(*simp-all* add: *split-beta*)

**lemmas** [code] =  
*heap-base.start-heap-data-code*  
*heap-base.start-heap-def*  
*heap-base.start-heap-ok-def*  
*heap-base.start-heap-obs-def*  
*heap-base.start-addr-def*  
*heap-base.addr-of-sys-xcpt-def*  
*heap-base.start-tid-def*  
*heap-base.start-state-def*

end

## 3.10 Conformance Relations for Type Soundness Proofs

**theory** *Conform*  
**imports**  
*StartConfig*  
**begin**

**context** *heap-base* **begin**

**definition** *conf* :: 'm prog ⇒ 'heap ⇒ 'addr val ⇒ ty ⇒ bool   (·, · ⊢ · :≤ - [51,51,51,51] 50)  
**where** *P, h* ⊢ *v* :≤ *T* ≡ ∃ *T'*. *typeof<sub>h</sub>* *v* = Some *T'* ∧ *P* ⊢ *T'* ≤ *T*

**definition** *lconf* :: 'm prog ⇒ 'heap ⇒ (vname → 'addr val) ⇒ (vname → ty) ⇒ bool   (·, · ⊢ · '(≤)  
 - [51,51,51,51] 50)  
**where** *P, h* ⊢ *l* (:≤) *E* ≡ ∀ *V* *v*. *l V* = Some *v* → (∃ *T*. *E V* = Some *T* ∧ *P, h* ⊢ *v* :≤ *T*)

**abbreviation**  $\text{confs} :: 'm \text{ prog} \Rightarrow 'heap \Rightarrow 'addr \text{ val list} \Rightarrow ty \text{ list} \Rightarrow bool \ (-, - \vdash - [\leq]) - [51, 51, 51, 51] 50)$

**where**  $P, h \vdash vs [\leq] Ts == list\text{-}all2 \ (conf \ P \ h) \ vs \ Ts$

**definition**  $tconf :: 'm \text{ prog} \Rightarrow 'heap \Rightarrow 'thread\text{-}id \Rightarrow bool \ (-, - \vdash - \sqrt{t} [51, 51, 51] 50)$

**where**  $P, h \vdash t \sqrt{t} \equiv \exists C. \text{typeof}\text{-}addr \ h \ (thread\text{-}id2addr \ t) = [Class\text{-}type \ C] \wedge P \vdash C \preceq^* Thread$

**end**

**locale**  $heap\text{-}conf\text{-}base =$

$heap\text{-}base +$

**constraints**  $addr2thread\text{-}id :: ('addr :: addr) \Rightarrow 'thread\text{-}id$

**and**  $thread\text{-}id2addr :: 'thread\text{-}id \Rightarrow 'addr$

**and**  $spurious\text{-}wakeups :: bool$

**and**  $empty\text{-}heap :: 'heap$

**and**  $allocate :: 'heap \Rightarrow htype \Rightarrow ('heap \times 'addr) \text{ set}$

**and**  $typeof\text{-}addr :: 'heap \Rightarrow 'addr \rightarrow htype$

**and**  $heap\text{-}read :: 'heap \Rightarrow 'addr \Rightarrow addr\text{-}loc \Rightarrow 'addr \text{ val} \Rightarrow bool$

**and**  $heap\text{-}write :: 'heap \Rightarrow 'addr \Rightarrow addr\text{-}loc \Rightarrow 'addr \text{ val} \Rightarrow 'heap \Rightarrow bool$

**fixes**  $hconf :: 'heap \Rightarrow bool$

**and**  $P :: 'm \text{ prog}$

**sublocale**  $heap\text{-}conf\text{-}base < prog \ P \ .$

**locale**  $heap\text{-}conf =$

$heap$

$addr2thread\text{-}id \ thread\text{-}id2addr$

$spurious\text{-}wakeups$

$empty\text{-}heap \ allocate \ typeof\text{-}addr \ heap\text{-}read \ heap\text{-}write$

$P$

$+$

$heap\text{-}conf\text{-}base$

$addr2thread\text{-}id \ thread\text{-}id2addr$

$spurious\text{-}wakeups$

$empty\text{-}heap \ allocate \ typeof\text{-}addr \ heap\text{-}read \ heap\text{-}write$

$hconf \ P$

**for**  $addr2thread\text{-}id :: ('addr :: addr) \Rightarrow 'thread\text{-}id$

**and**  $thread\text{-}id2addr :: 'thread\text{-}id \Rightarrow 'addr$

**and**  $spurious\text{-}wakeups :: bool$

**and**  $empty\text{-}heap :: 'heap$

**and**  $allocate :: 'heap \Rightarrow htype \Rightarrow ('heap \times 'addr) \text{ set}$

**and**  $typeof\text{-}addr :: 'heap \Rightarrow 'addr \rightarrow htype$

**and**  $heap\text{-}read :: 'heap \Rightarrow 'addr \Rightarrow addr\text{-}loc \Rightarrow 'addr \text{ val} \Rightarrow bool$

**and**  $heap\text{-}write :: 'heap \Rightarrow 'addr \Rightarrow addr\text{-}loc \Rightarrow 'addr \text{ val} \Rightarrow 'heap \Rightarrow bool$

**and**  $hconf :: 'heap \Rightarrow bool$

**and**  $P :: 'm \text{ prog}$

$+$

**assumes**  $hconf\text{-}empty \ [iff]: hconf \ empty\text{-}heap$

**and**  $typeof\text{-}addr\text{-}is\text{-}type: \llbracket typeof\text{-}addr \ h \ a = [hT]; hconf \ h \rrbracket \Longrightarrow is\text{-}type \ P \ (ty\text{-}of\text{-}htype \ hT)$

**and**  $hconf\text{-}allocate\text{-}mono: \bigwedge a. \llbracket (h', a) \in allocate \ h \ hT; hconf \ h; is\text{-}htype \ P \ hT \rrbracket \Longrightarrow hconf \ h'$

**and**  $hconf\text{-}heap\text{-}write\text{-}mono:$

$\bigwedge T. \llbracket heap\text{-}write \ h \ a \ al \ v \ h'; hconf \ h; P, h \vdash a@al : T; P, h \vdash v : \leq T \rrbracket \Longrightarrow hconf \ h'$

```

locale heap-progress =
  heap-conf
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  hconf P
for addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype
and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
and hconf :: 'heap  $\Rightarrow$  bool
and P :: 'm prog
+
assumes heap-read-total:  $\llbracket \text{hconf } h; P, h \vdash a@al : T \rrbracket \Longrightarrow \exists v. \text{heap-read } h \ a \ al \ v \wedge P, h \vdash v : \leq T$ 
and heap-write-total:  $\llbracket \text{hconf } h; P, h \vdash a@al : T; P, h \vdash v : \leq T \rrbracket \Longrightarrow \exists h'. \text{heap-write } h \ a \ al \ v \ h'$ 

locale heap-conf-read =
  heap-conf
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  hconf P
for addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype
and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
and hconf :: 'heap  $\Rightarrow$  bool
and P :: 'm prog
+
assumes heap-read-conf:  $\llbracket \text{heap-read } h \ a \ al \ v; P, h \vdash a@al : T; \text{hconf } h \rrbracket \Longrightarrow P, h \vdash v : \leq T$ 

locale heap-typesafe =
  heap-conf-read +
  heap-progress +
constrains addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype
and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
and hconf :: 'heap  $\Rightarrow$  bool
and P :: 'm prog

context heap-conf begin

```

**lemmas** *hconf-heap-ops-mono* =  
   *hconf-allocate-mono*  
   *hconf-heap-write-mono*

**end**

### 3.10.1 Value conformance $:\leq$

**context** *heap-base* **begin**

**lemma** *conf-Null* [*simp*]:  $P, h \vdash \text{Null} :\leq T = P \vdash NT \leq T$   
**unfolding** *conf-def* **by** (*simp* (*no-asm*))

**lemma** *typeof-conf* [*simp*]:  $\text{typeof}_h v = \text{Some } T \implies P, h \vdash v :\leq T$   
**unfolding** *conf-def* **by** (*cases* *v*) *auto*

**lemma** *typeof-lit-conf* [*simp*]:  $\text{typeof } v = \text{Some } T \implies P, h \vdash v :\leq T$   
**by** (*rule* *typeof-conf* [*OF typeof-lit-typeof*])

**lemma** *defval-conf* [*simp*]:  $P, h \vdash \text{default-val } T :\leq T$   
**unfolding** *conf-def* **by** (*cases* *T*) *auto*

**lemma** *conf-widen*:  $P, h \vdash v :\leq T \implies P \vdash T \leq T' \implies P, h \vdash v :\leq T'$   
**unfolding** *conf-def* **by** (*cases* *v*) (*auto intro: widen-trans*)

**lemma** *conf-sys-xcpt*:  
 $\llbracket \text{preallocated } h; C \in \text{sys-xcpts} \rrbracket \implies P, h \vdash \text{Addr } (\text{addr-of-sys-xcpt } C) :\leq \text{Class } C$   
**by** (*simp add: conf-def typeof-addr-sys-xcp*)

**lemma** *conf-NT* [*iff*]:  $P, h \vdash v :\leq NT = (v = \text{Null})$   
**by** (*auto simp add: conf-def*)

**lemma** *is-IntgI*:  $P, h \vdash v :\leq \text{Integer} \implies \text{is-Intg } v$   
**by** (*unfold conf-def*) *auto*

**lemma** *is-BoolI*:  $P, h \vdash v :\leq \text{Boolean} \implies \text{is-Bool } v$   
**by** (*unfold conf-def*) *auto*

**lemma** *is-RefI*:  $P, h \vdash v :\leq T \implies \text{is-refT } T \implies \text{is-Ref } v$   
**by** (*cases* *v*) (*auto elim: is-refT.cases simp add: conf-def is-Ref-def*)

**lemma** *non-npD*:  
 $\llbracket v \neq \text{Null}; P, h \vdash v :\leq \text{Class } C; C \neq \text{Object} \rrbracket$   
 $\implies \exists a \ C'. v = \text{Addr } a \wedge \text{typeof-addr } h \ a = \llbracket \text{Class-type } C' \rrbracket \wedge P \vdash C' \preceq^* C$   
**by** (*cases* *v*) (*auto simp add: conf-def widen-Class*)

**lemma** *non-npD2*:  
 $\llbracket v \neq \text{Null}; P, h \vdash v :\leq \text{Class } C \rrbracket$   
 $\implies \exists a \ hT. v = \text{Addr } a \wedge \text{typeof-addr } h \ a = \llbracket hT \rrbracket \wedge P \vdash \text{class-type-of } hT \preceq^* C$   
**by** (*cases* *v*) (*auto simp add: conf-def widen-Class*)

**end**

**context** *heap* **begin**

**lemma** *conf-hext*:  $\llbracket h \sqsubseteq h'; P, h \vdash v : \leq T \rrbracket \implies P, h' \vdash v : \leq T$   
**unfolding** *conf-def* **by**(*cases v*)(*auto dest: typeof-addr-hext-mono*)

**lemma** *conf-heap-ops-mono*:  
**assumes**  $P, h \vdash v : \leq T$   
**shows** *conf-allocate-mono*:  $(h', a) \in \text{allocate } h \ hT \implies P, h' \vdash v : \leq T$   
**and** *conf-heap-write-mono*:  $\text{heap-write } h \ a \ al \ v' \ h' \implies P, h' \vdash v : \leq T$   
**using** *assms*  
**by**(*auto intro: conf-hext dest: hext-heap-ops*)

**end**

### 3.10.2 Value list conformance $[:\leq]$

**context** *heap-base* **begin**

**lemma** *confs-widens* [*trans*]:  $\llbracket P, h \vdash vs : [\leq] Ts; P \vdash Ts [\leq] Ts' \rrbracket \implies P, h \vdash vs : [\leq] Ts'$   
**by** (*rule list-all2-trans*)(*rule conf-widen*)

**lemma** *confs-rev*:  $P, h \vdash \text{rev } s : [\leq] t = (P, h \vdash s : [\leq] \text{rev } t)$   
**by**(*rule list-all2-rev1*)

**lemma** *confs-conv-map*:  
 $P, h \vdash vs : [\leq] Ts' = (\exists Ts. \text{map } \text{typeof}_h \ vs = \text{map } \text{Some } Ts \wedge P \vdash Ts [\leq] Ts')$   
**apply**(*induct vs arbitrary: Ts'*)  
**apply** *simp*  
**apply**(*case-tac Ts'*)  
**apply**(*auto simp add: conf-def*)  
**apply**(*rule-tac x = T' # Ts in exI*)  
**apply**(*simp add: fun-of-def*)  
**done**

**lemma** *confs-Cons2*:  $P, h \vdash xs : [\leq] y \# ys = (\exists z \ zs. xs = z \# zs \wedge P, h \vdash z : \leq y \wedge P, h \vdash zs : [\leq] ys)$   
**by** (*rule list-all2-Cons2*)

**end**

**context** *heap* **begin**

**lemma** *confs-hext*:  $P, h \vdash vs : [\leq] Ts \implies h \sqsubseteq h' \implies P, h' \vdash vs : [\leq] Ts$   
**by** (*erule list-all2-mono, erule conf-hext, assumption*)

**end**

### 3.10.3 Local variable conformance

**context** *heap-base* **begin**

**lemma** *lconf-upd*:  
 $\llbracket P, h \vdash l (: \leq) E; P, h \vdash v : \leq T; E \ V = \text{Some } T \rrbracket \implies P, h \vdash l(V \mapsto v) (: \leq) E$   
**unfolding** *lconf-def* **by** *auto*

**lemma** *lconf-empty* [iff]:  $P, h \vdash \text{Map.empty} \text{ } (\leq) E$   
**by**(*simp add:lconf-def*)

**lemma** *lconf-upd2*:  $\llbracket P, h \vdash l \text{ } (\leq) \text{ } E; P, h \vdash v \text{ } \leq \text{ } T \rrbracket \implies P, h \vdash l(V \mapsto v) \text{ } (\leq) \text{ } E(V \mapsto T)$   
**by** (*simp add:lconf-def*)

end

context *heap* begin

**lemma** *lconf-hext*:  $\llbracket P, h \vdash l \text{ } (: \leq) \text{ } E; h \sqsubseteq h' \rrbracket \implies P, h' \vdash l \text{ } (: \leq) \text{ } E$   
**unfolding** *lconf-def* **by** (*fast elim*: *conf-hext*)

end

### 3.10.4 Thread object conformance

context *heap-base* begin

**lemma** *tconfI*:  $\llbracket \text{typeof-addr } h \text{ (thread-id2addr } t) = \lfloor \text{Class-type } C \rfloor; P \vdash C \preceq^* \text{Thread} \rrbracket \Longrightarrow P, h \vdash t$   
 $\sqrt{t}$   
**by**(*simp add: tconf-def*)

**lemma** *tconfD*:  $P, h \vdash t \checkmark t \implies \exists C. \text{typeof\_addr } h \text{ (thread-id2addr } t) = \lfloor \text{Class-type } C \rfloor \wedge P \vdash C \preceq^* \text{Thread}$   
**by**(*auto simp add: tconf-def*)

end

**context** *heap* **begin**

**lemma** *tconf-hext-mono*:  $\llbracket P, h \vdash t \sqrt{t}; h \sqsubseteq h' \rrbracket \implies P, h' \vdash t \sqrt{t}$   
**by**(*auto simp add: tconf-def dest: typeof-addr-hext-mono*)

```

lemma tconf-heap-ops-mono:
  assumes  $P, h \vdash t \sqrt{t}$ 
  shows tconf-allocate-mono:  $(h', a) \in \text{allocate } h \ hT \implies P, h' \vdash t \sqrt{t}$ 
  and tconf-heap-write-mono:  $\text{heap-write } h \ a \ al \ v \ h' \implies P, h' \vdash t \sqrt{t}$ 
using tconf-hext-mono[OF asms, of h']
by(blast intro: hext-heap-ops)+

```

[illegible]

```

apply(drule (1) allocate-Eps)
apply(drule (1) allocate-Eps)
apply(drule (1) allocate-Eps)
apply(drule (1) allocate-Eps)
apply(drule allocate-SomeD[where hT=Class-type Thread])
  apply simp
apply(rule tconfI)
  apply(erule typeof-addr-hext-mono[OF hext-allocate])+
  apply simp
apply blast
done

```

```

lemma start-heap-write-typeable:
  assumes WriteMem ad al v ∈ set start-heap-obs
  shows  $\exists T. P, \text{start-heap} \vdash \text{ad}@al : T \wedge P, \text{start-heap} \vdash v : \leq T$ 
using assms
unfolding start-heap-obs-def start-heap-def
by clarsimp

```

**end**

### 3.10.5 Well-formed start state

```
context heap-base begin
```

```

inductive wf-start-state :: 'm prog  $\Rightarrow$  cname  $\Rightarrow$  mname  $\Rightarrow$  'addr val list  $\Rightarrow$  bool
for P :: 'm prog and C :: cname and M :: mname and vs :: 'addr val list
where
  wf-start-state:
   $\llbracket P \vdash C \text{ sees } M : Ts \rightarrow T = \llbracket \text{meth} \rrbracket \text{ in } D; \text{start-heap-ok}; P, \text{start-heap} \vdash vs \llbracket : \leq \rrbracket Ts \rrbracket$ 
   $\implies \text{wf-start-state } P \ C \ M \ vs$ 

```

**end**

**end**

## 3.11 Semantics of method calls that cannot be defined inside JinjaThreads

```
theory ExternalCall
```

```
imports
```

```
  ../Framework/FWSemantics
```

```
  Conform
```

```
begin
```

```
type-synonym
```

```

  ('addr, 'thread-id, 'heap) external-thread-action = ('addr, 'thread-id, cname  $\times$  mname  $\times$  'addr, 'heap)
  Jinja-thread-action

```

```
print-translation  $\langle$ 
```

```
  let
```

```
    fun tr'
```

```

    [a1, t
    , Const (@{type-syntax prod}, -) $ Const (@{type-syntax String.literal}, -) $
      (Const (@{type-syntax prod}, -) $ Const (@{type-syntax String.literal}, -) $ a2)
    , h] =
    if a1 = a2 then Syntax.const @{type-syntax external-thread-action} $ a1 $ t $ h
    else raise Match;
    in [(@{type-syntax Jinja-thread-action}, K tr')]
  end
>
typ ('addr,'thread-id,'heap) external-thread-action

```

### 3.11.1 Typing of external calls

**inductive** *external-WT-defs* :: *cname*  $\Rightarrow$  *mname*  $\Rightarrow$  *ty list*  $\Rightarrow$  *ty*  $\Rightarrow$  *bool* ((*--'(-)*) :: - [50, 0, 0, 50] 60)

**where**

```

  Thread.start([]) :: Void
| Thread.join([]) :: Void
| Thread.interrupt([]) :: Void
| Thread.isInterrupted([]) :: Boolean
| Object.wait([]) :: Void
| Object.notify([]) :: Void
| Object.notifyAll([]) :: Void
| Object.clone([]) :: Class Object
| Object.hashCode([]) :: Integer
| Object.print([Integer]) :: Void
| Object.currentThread([]) :: Class Thread
| Object.interrupted([]) :: Boolean
| Object.yield([]) :: Void

```

**inductive-cases** *external-WT-defs-cases*:

```

  a.start(vs) :: T
  a.join(vs) :: T
  a.interrupt(vs) :: T
  a.isInterrupted(vs) :: T
  a.wait(vs) :: T
  a.notify(vs) :: T
  a.notifyAll(vs) :: T
  a.clone(vs) :: T
  a.hashCode(vs) :: T
  a.print(vs) :: T
  a.currentThread(vs) :: T
  a.interrupted([]) :: T
  a.yield(vs) :: T

```

**inductive** *is-native* :: '*m prog*  $\Rightarrow$  *hType*  $\Rightarrow$  *mname*  $\Rightarrow$  *bool*

**for** *P* :: '*m prog* **and** *hT* :: *hType* **and** *M* :: *mname*

**where**  $\llbracket P \vdash \text{class-type-of } hT \text{ sees } M:Ts \rightarrow T = \text{Native in } D; D \cdot M(Ts) :: T \rrbracket \Longrightarrow \text{is-native } P \ hT \ M$

**lemma** *is-nativeD*: *is-native* *P hT M*  $\Longrightarrow \exists Ts \ T \ D. P \vdash \text{class-type-of } hT \text{ sees } M:Ts \rightarrow T = \text{Native in } D \wedge D \cdot M(Ts) :: T$

**by** (*simp add: is-native.simps*)

**inductive** (**in** *heap-base*) *external-WT'* :: '*m prog*  $\Rightarrow$  '*heap*  $\Rightarrow$  '*addr*  $\Rightarrow$  *mname*  $\Rightarrow$  '*addr val list*  $\Rightarrow$  *ty*



$\Rightarrow \text{bool}$   
 $(-, - \vdash (-)'(-)) : - [50, 0, 0, 0, 50] \ 60)$   
**for**  $P :: 'm \text{ prog}$  **and**  $h :: 'heap$  **and**  $a :: 'addr$  **and**  $M :: mname$  **and**  $vs :: 'addr \text{ val list}$  **and**  $U :: ty$   
**where**  
 $\llbracket \text{typeof-addr } h \ a = \lfloor hT \rfloor; \text{map typeof}_h \ vs = \text{map Some } Ts; P \vdash \text{class-type-of } hT \text{ sees } M:Ts' \rightarrow U = \text{Native in } D;$   
 $P \vdash Ts [\leq] Ts' \rrbracket$   
 $\Rightarrow P, h \vdash a \cdot M(vs) : U$

**context** *heap-base* **begin**

**lemma** *external-WT'-iff*:

$P, h \vdash a \cdot M(vs) : U \iff$   
 $(\exists hT \ Ts \ Ts' \ D. \text{typeof-addr } h \ a = \lfloor hT \rfloor \wedge \text{map typeof}_h \ vs = \text{map Some } Ts \wedge P \vdash \text{class-type-of } hT$   
 $\text{sees } M:Ts' \rightarrow U = \text{Native in } D \wedge P \vdash Ts [\leq] Ts')$   
**by**(*simp add: external-WT'.simps*)

**end**

**context** *heap* **begin**

**lemma** *external-WT'-hext-mono*:

$\llbracket P, h \vdash a \cdot M(vs) : T; h \trianglelefteq h' \rrbracket \Rightarrow P, h' \vdash a \cdot M(vs) : T$   
**by**(*auto 5 2 simp add: external-WT'-iff dest: typeof-addr-hext-mono map-typeof-hext-mono*)

**end**

### 3.11.2 Semantics of external calls

**datatype** *'addr extCallRet* =

$\text{RetVal } 'addr \text{ val}$   
 $| \text{RetExc } 'addr$   
 $| \text{RetStaySame}$

**lemma** *rec-extCallRet [simp]*: *rec-extCallRet* = *case-extCallRet*

**by**(*auto simp add: fun-eq-iff split: extCallRet.split*)

**context** *heap-base* **begin**

**abbreviation** *RetEXC* :: *cname*  $\Rightarrow$  *'addr extCallRet*

**where** *RetEXC* *C*  $\equiv \text{RetExc (addr-of-sys-xcpt } C)$

**inductive** *heap-copy-loc* :: *'addr*  $\Rightarrow$  *'addr*  $\Rightarrow$  *addr-loc*  $\Rightarrow$  *'heap*  $\Rightarrow$  (*'addr*, *'thread-id*) *obs-event list*  $\Rightarrow$  *'heap*  $\Rightarrow$  *bool*

**for**  $a :: 'addr$  **and**  $a' :: 'addr$  **and**  $al :: \text{addr-loc}$  **and**  $h :: 'heap$

**where**

$\llbracket \text{heap-read } h \ a \ al \ v; \text{heap-write } h \ a' \ al \ v \ h' \rrbracket$   
 $\Rightarrow \text{heap-copy-loc } a \ a' \ al \ h \ ([\text{ReadMem } a \ al \ v, \text{WriteMem } a' \ al \ v]) \ h'$

**inductive** *heap-copies* :: *'addr*  $\Rightarrow$  *'addr*  $\Rightarrow$  *addr-loc list*  $\Rightarrow$  *'heap*  $\Rightarrow$  (*'addr*, *'thread-id*) *obs-event list*  $\Rightarrow$  *'heap*  $\Rightarrow$  *bool*

**for**  $a :: 'addr$  **and**  $a' :: 'addr$

**where**

*Nil*: *heap-copies*  $a \ a' \ [] \ h \ [] \ h$

| *Cons*:  

$$\llbracket \text{heap-copy-loc } a \ a' \ al \ h \ ob \ h'; \text{ heap-copies } a \ a' \ als \ h' \ obs \ h'' \rrbracket$$

$$\implies \text{heap-copies } a \ a' \ (al \ \# \ als) \ h \ (ob \ @ \ obs) \ h''$$

**inductive-cases** *heap-copies-cases*:

*heap-copies*  $a \ a' \ [] \ h \ ops \ h'$   
*heap-copies*  $a \ a' \ (al \ \# \ als) \ h \ ops \ h'$

Contrary to Sun's JVM 1.6.0\_07, cloning an interrupted thread does not yield an interrupted thread, because the interrupt flag is not stored inside the thread object. Starting a clone of a started thread with Sun JVM 1.6.0\_07 raises an illegal thread state exception, we just start another thread. The thread at <http://mail.openjdk.java.net/pipermail/core-libs-dev/2010-August/004715.html> discusses the general problem of thread cloning and argues against that. The bug report [http://bugs.sun.com/bugdatabase/view\\_bug.do?bug\\_id=6968584](http://bugs.sun.com/bugdatabase/view_bug.do?bug_id=6968584) changes the Thread class implementation such that `Object.clone()` can no longer be accessed for Thread and subclasses in Java 7.

Array cells are never volatile themselves.

**inductive** *heap-clone* :: 'm prog  $\Rightarrow$  'heap  $\Rightarrow$  'addr  $\Rightarrow$  'heap  $\Rightarrow$  (('addr, 'thread-id) obs-event list  $\times$  'addr) option  $\Rightarrow$  bool

**for**  $P :: 'm \text{ prog}$  **and**  $h :: 'heap$  **and**  $a :: 'addr$

**where**

*CloneFail*:

$\llbracket \text{typeof-addr } h \ a = [hT]; \text{ allocate } h \ hT = \{\} \rrbracket$   
 $\implies \text{heap-clone } P \ h \ a \ h \ \text{None}$

| *ObjClone*:

$\llbracket \text{typeof-addr } h \ a = [\text{Class-type } C]; (h', a') \in \text{allocate } h \ (\text{Class-type } C);$   
 $P \vdash C \text{ has-fields } FDTs; \text{ heap-copies } a \ a' \ (\text{map } (\lambda((F, D), Tfm). CField \ D \ F) \ FDTs) \ h' \ obs \ h'' \rrbracket$   
 $\implies \text{heap-clone } P \ h \ a \ h'' \ [(\text{NewHeapElem } a' \ (\text{Class-type } C) \ \# \ obs, a')]$

| *ArrClone*:

$\llbracket \text{typeof-addr } h \ a = [\text{Array-type } T \ n]; (h', a') \in \text{allocate } h \ (\text{Array-type } T \ n); P \vdash \text{Object has-fields } FDTs;$

$\text{heap-copies } a \ a' \ (\text{map } (\lambda((F, D), Tfm). CField \ D \ F) \ FDTs \ @ \ \text{map } ACell \ [0..<n]) \ h' \ obs \ h'' \rrbracket$   
 $\implies \text{heap-clone } P \ h \ a \ h'' \ [(\text{NewHeapElem } a' \ (\text{Array-type } T \ n) \ \# \ obs, a')]$

**inductive** *red-external* ::

'm prog  $\Rightarrow$  'thread-id  $\Rightarrow$  'heap  $\Rightarrow$  'addr  $\Rightarrow$  mname  $\Rightarrow$  'addr val list  
 $\Rightarrow$  ('addr, 'thread-id, 'heap) external-thread-action  $\Rightarrow$  'addr extCallRet  $\Rightarrow$  'heap  $\Rightarrow$  bool

**and** *red-external-syntax* ::

'm prog  $\Rightarrow$  'thread-id  $\Rightarrow$  'addr  $\Rightarrow$  mname  $\Rightarrow$  'addr val list  $\Rightarrow$  'heap  
 $\Rightarrow$  ('addr, 'thread-id, 'heap) external-thread-action  $\Rightarrow$  'addr extCallRet  $\Rightarrow$  'heap  $\Rightarrow$  bool  
 $(-, - \vdash ((\dots(-)), /(-)) \dashrightarrow \text{ext } ((-), /(-))) \ [50, 0, 0, 0, 0, 0, 0, 0, 0] \ 51)$

**for**  $P :: 'm \text{ prog}$  **and**  $t :: 'thread-id$  **and**  $h :: 'heap$  **and**  $a :: 'addr$

**where**

$P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow \text{ext } \langle va, h' \rangle \equiv \text{red-external } P \ t \ h \ a \ M \ vs \ ta \ va \ h'$

| *RedNewThread*:

$\llbracket \text{typeof-addr } h \ a = [\text{Class-type } C]; P \vdash C \preceq^* \text{Thread} \rrbracket$   
 $\implies P, t \vdash \langle a \cdot \text{start}([], h) \rangle -\llbracket \text{NewThread } (\text{addr2thread-id } a) \ (C, \text{run}, a) \ h, \text{ThreadStart } (\text{addr2thread-id } a) \rrbracket \rightarrow \text{ext } \langle \text{RetVal } \text{Unit}, h \rangle$

| *RedNewThreadFail*:

$\llbracket \text{typeof-addr } h \ a = [\text{Class-type } C]; P \vdash C \preceq^* \text{Thread} \rrbracket$   
 $\implies P, t \vdash \langle a \cdot \text{start}([], h) \rangle -\llbracket \text{ThreadExists } (\text{addr2thread-id } a) \ \text{True} \rrbracket \rightarrow \text{ext } \langle \text{RetEXC } \text{IllegalThreadState},$

$h\rangle$

| *RedJoin*:

$\llbracket \text{typeof-addr } h \ a = \lfloor \text{Class-type } C \rfloor; P \vdash C \preceq^* \text{Thread} \rrbracket$   
 $\implies P, t \vdash \langle a.\text{join}(\lfloor \rfloor), h \rangle - \llbracket \text{Join } (\text{addr2thread-id } a), \text{IsInterrupted } t \ \text{False}, \text{ThreadJoin } (\text{addr2thread-id } a) \rrbracket \rightarrow \text{ext} \langle \text{RetVal Unit}, h \rangle$

| *RedJoinInterrupt*:

$\llbracket \text{typeof-addr } h \ a = \lfloor \text{Class-type } C \rfloor; P \vdash C \preceq^* \text{Thread} \rrbracket$   
 $\implies P, t \vdash \langle a.\text{join}(\lfloor \rfloor), h \rangle - \llbracket \text{IsInterrupted } t \ \text{True}, \text{ClearInterrupt } t, \text{ObsInterrupted } t \rrbracket \rightarrow \text{ext} \langle \text{RetEXC InterruptedException}, h \rangle$

— Interruption should produce inter-thread actions (JLS 17.4.4) for the synchronizes-with order. They should synchronize with the inter-thread actions that determine whether a thread has been interrupted. Hence, interruption generates an *ObsInterrupt* action.

Although *WakeUp* causes the interrupted thread to raise an *InterruptedException* independent of the interrupt status, the interrupt flag must be set with *Interrupt* such that other threads observe the interrupted thread as interrupted while it competes for the monitor lock again.

Interrupting a thread which has not yet been started does not set the interrupt flag (tested with Sun HotSpot JVM 1.6.0\_07).

| *RedInterrupt*:

$\llbracket \text{typeof-addr } h \ a = \lfloor \text{Class-type } C \rfloor; P \vdash C \preceq^* \text{Thread} \rrbracket$   
 $\implies P, t \vdash \langle a.\text{interrupt}(\lfloor \rfloor), h \rangle$   
 $\quad - \llbracket \text{ThreadExists } (\text{addr2thread-id } a) \ \text{True}, \text{WakeUp } (\text{addr2thread-id } a),$   
 $\quad \text{Interrupt } (\text{addr2thread-id } a), \text{ObsInterrupt } (\text{addr2thread-id } a) \rrbracket \rightarrow \text{ext}$   
 $\quad \langle \text{RetVal Unit}, h \rangle$

| *RedInterruptInexist*:

$\llbracket \text{typeof-addr } h \ a = \lfloor \text{Class-type } C \rfloor; P \vdash C \preceq^* \text{Thread} \rrbracket$   
 $\implies P, t \vdash \langle a.\text{interrupt}(\lfloor \rfloor), h \rangle$   
 $\quad - \llbracket \text{ThreadExists } (\text{addr2thread-id } a) \ \text{False} \rrbracket \rightarrow \text{ext}$   
 $\quad \langle \text{RetVal Unit}, h \rangle$

| *RedIsInterruptedTrue*:

$\llbracket \text{typeof-addr } h \ a = \lfloor \text{Class-type } C \rfloor; P \vdash C \preceq^* \text{Thread} \rrbracket$   
 $\implies P, t \vdash \langle a.\text{isInterrupted}(\lfloor \rfloor), h \rangle - \llbracket \text{IsInterrupted } (\text{addr2thread-id } a) \ \text{True}, \text{ObsInterrupted } (\text{addr2thread-id } a) \rrbracket \rightarrow \text{ext}$   
 $\quad \langle \text{RetVal (Bool True)}, h \rangle$

| *RedIsInterruptedFalse*:

$\llbracket \text{typeof-addr } h \ a = \lfloor \text{Class-type } C \rfloor; P \vdash C \preceq^* \text{Thread} \rrbracket$   
 $\implies P, t \vdash \langle a.\text{isInterrupted}(\lfloor \rfloor), h \rangle - \llbracket \text{IsInterrupted } (\text{addr2thread-id } a) \ \text{False} \rrbracket \rightarrow \text{ext} \langle \text{RetVal (Bool False)}, h \rangle$

— The JLS leaves unspecified whether *wait* first checks for the monitor state (whether the thread holds a lock on the monitor) or for the interrupt flag of the current thread. Sun Hotspot JVM 1.6.0\_07 seems to check for the monitor state first, so we do it here, too.

| *RedWaitInterrupt*:

$P, t \vdash \langle a.\text{wait}(\lfloor \rfloor), h \rangle - \llbracket \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a, \text{IsInterrupted } t \ \text{True}, \text{ClearInterrupt } t, \text{ObsInterrupted } t \rrbracket \rightarrow \text{ext}$   
 $\quad \langle \text{RetEXC InterruptedException}, h \rangle$

| *RedWait*:

$P, t \vdash \langle a \cdot \text{wait}(\square), h \rangle - \llbracket \text{Suspend } a, \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a, \text{ReleaseAcquire} \rightarrow a, \text{IsInterrupted } t \text{ False}, \text{SyncUnlock } a \rrbracket \rightarrow \text{ext} \langle \text{RetStaySame}, h \rangle$

| *RedWaitFail*:

$P, t \vdash \langle a \cdot \text{wait}(\square), h \rangle - \llbracket \text{UnlockFail} \rightarrow a \rrbracket \rightarrow \text{ext} \langle \text{RetEXC IllegalMonitorState}, h \rangle$

| *RedWaitNotified*:

$P, t \vdash \langle a \cdot \text{wait}(\square), h \rangle - \llbracket \text{Notified} \rrbracket \rightarrow \text{ext} \langle \text{RetVal Unit}, h \rangle$

— This rule does NOT check that the interrupted flag is set, but still clears it. The semantics will be that only the executing thread clears its interrupt.

| *RedWaitInterrupted*:

$P, t \vdash \langle a \cdot \text{wait}(\square), h \rangle - \llbracket \text{WokenUp}, \text{ClearInterrupt } t, \text{ObsInterrupted } t \rrbracket \rightarrow \text{ext} \langle \text{RetEXC InterruptedException}, h \rangle$

— Calls to wait may decide to immediately wake up spuriously. This is indistinguishable from waking up spuriously any time before being notified or interrupted. Spurious wakeups are configured by the *spurious-wakeup* parameter of the *heap-base* locale.

| *RedWaitSpurious*:

$\text{spurious-wakeups} \implies$

$P, t \vdash \langle a \cdot \text{wait}(\square), h \rangle - \llbracket \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a, \text{ReleaseAcquire} \rightarrow a, \text{IsInterrupted } t \text{ False}, \text{SyncUnlock } a \rrbracket \rightarrow \text{ext} \langle \text{RetVal Unit}, h \rangle$

— *notify* and *notifyAll* do not perform synchronization inter-thread actions because they only tests whether the thread holds a lock, but do not change the lock state.

| *RedNotify*:

$P, t \vdash \langle a \cdot \text{notify}(\square), h \rangle - \llbracket \text{Notify } a, \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a \rrbracket \rightarrow \text{ext} \langle \text{RetVal Unit}, h \rangle$

| *RedNotifyFail*:

$P, t \vdash \langle a \cdot \text{notify}(\square), h \rangle - \llbracket \text{UnlockFail} \rightarrow a \rrbracket \rightarrow \text{ext} \langle \text{RetEXC IllegalMonitorState}, h \rangle$

| *RedNotifyAll*:

$P, t \vdash \langle a \cdot \text{notifyAll}(\square), h \rangle - \llbracket \text{NotifyAll } a, \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a \rrbracket \rightarrow \text{ext} \langle \text{RetVal Unit}, h \rangle$

| *RedNotifyAllFail*:

$P, t \vdash \langle a \cdot \text{notifyAll}(\square), h \rangle - \llbracket \text{UnlockFail} \rightarrow a \rrbracket \rightarrow \text{ext} \langle \text{RetEXC IllegalMonitorState}, h \rangle$

| *RedClone*:

$\text{heap-clone } P \ h \ a \ h' \ \llbracket (\text{obs}, a') \rrbracket$

$\implies P, t \vdash \langle a \cdot \text{clone}(\square), h \rangle - (K\$ \square, \square, \square, \square, \square, \text{obs}) \rightarrow \text{ext} \langle \text{RetVal (Addr } a'), h' \rangle$

| *RedCloneFail*:

$\text{heap-clone } P \ h \ a \ h' \ \text{None} \implies P, t \vdash \langle a \cdot \text{clone}(\square), h \rangle - \varepsilon \rightarrow \text{ext} \langle \text{RetEXC OutOfMemory}, h' \rangle$

| *RedHashCode*:

$P, t \vdash \langle a \cdot \text{hashCode}(\square), h \rangle - \llbracket \rrbracket \rightarrow \text{ext} \langle \text{RetVal (Intg (word-of-int (hash-addr } a)) \rangle, h \rangle$

| *RedPrint*:

$P, t \vdash \langle a \cdot \text{print}(vs), h \rangle - \llbracket \text{ExternalCall } a \text{ print } vs \text{ Unit} \rrbracket \rightarrow \text{ext} \langle \text{RetVal Unit}, h \rangle$

| *RedCurrentThread*:

$P, t \vdash \langle a \cdot \text{currentThread}(\square), h \rangle - \llbracket \text{ext} \rrbracket \rightarrow \text{ext} \langle \text{RetVal } (\text{Addr } (\text{thread-id2addr } t)), h \rangle$   
 | *RedInterruptedTrue*:  
 $P, t \vdash \langle a \cdot \text{interrupted}(\square), h \rangle - \llbracket \text{IsInterrupted } t \text{ True, ClearInterrupt } t, \text{ObsInterrupted } t \rrbracket \rightarrow \text{ext} \langle \text{RetVal } (\text{Bool True}), h \rangle$   
 | *RedInterruptedFalse*:  
 $P, t \vdash \langle a \cdot \text{interrupted}(\square), h \rangle - \llbracket \text{IsInterrupted } t \text{ False} \rrbracket \rightarrow \text{ext} \langle \text{RetVal } (\text{Bool False}), h \rangle$   
 | *RedYield*:  
 $P, t \vdash \langle a \cdot \text{yield}(\square), h \rangle - \llbracket \text{Yield} \rrbracket \rightarrow \text{ext} \langle \text{RetVal Unit}, h \rangle$

### 3.11.3 Aggressive formulation for external calls

**definition** *red-external-aggr* ::

$'m \text{ prog} \Rightarrow 'thread\text{-}id \Rightarrow 'addr \Rightarrow mname \Rightarrow 'addr \text{ val list} \Rightarrow 'heap \Rightarrow$   
 $(( 'addr, 'thread\text{-}id, 'heap) \text{ external-thread-action} \times 'addr \text{ extCallRet} \times 'heap) \text{ set}$   
**where**  
 $\text{red-external-aggr } P \ t \ a \ M \text{ vs } h =$   
 (if  $M = \text{wait}$  then  
   let  $ad\text{-}t = \text{thread-id2addr } t$   
   in  $\{(\llbracket \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a, \text{IsInterrupted } t \text{ True, ClearInterrupt } t, \text{ObsInterrupted } t \rrbracket, \text{RetEXC InterruptedException}, h),$   
      $(\llbracket \text{Suspend } a, \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a, \text{ReleaseAcquire} \rightarrow a, \text{IsInterrupted } t \text{ False, SyncUnlock } a \rrbracket,$   
      $\text{RetStaySame}, h),$   
      $(\llbracket \text{UnlockFail} \rightarrow a \rrbracket, \text{RetEXC IllegalMonitorState}, h),$   
      $(\llbracket \text{Notified} \rrbracket, \text{RetVal Unit}, h),$   
      $(\llbracket \text{WokenUp, ClearInterrupt } t, \text{ObsInterrupted } t \rrbracket, \text{RetEXC InterruptedException}, h)\} \cup$   
   (if *spurious-wakeups* then  $\{(\llbracket \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a, \text{ReleaseAcquire} \rightarrow a, \text{IsInterrupted } t \text{ False, SyncUnlock } a \rrbracket,$   
      $\text{RetVal Unit}, h)\}$  else  $\{\}$ )  
   else if  $M = \text{notify}$  then  $\{(\llbracket \text{Notify } a, \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a \rrbracket, \text{RetVal Unit}, h),$   
      $(\llbracket \text{UnlockFail} \rightarrow a \rrbracket, \text{RetEXC IllegalMonitorState}, h)\}$   
   else if  $M = \text{notifyAll}$  then  $\{(\llbracket \text{NotifyAll } a, \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a \rrbracket, \text{RetVal Unit}, h),$   
      $(\llbracket \text{UnlockFail} \rightarrow a \rrbracket, \text{RetEXC IllegalMonitorState}, h)\}$   
   else if  $M = \text{clone}$  then  
      $\{((K\$ \square, \square, \square, \square, \square, \text{obs}), \text{RetVal } (\text{Addr } a'), h') | \text{obs } a' h'. \text{heap-clone } P \ h \ a \ h' \ [(\text{obs}, a')]\}$   
      $\cup \{(\llbracket \rrbracket, \text{RetEXC OutOfMemory}, h') | h'. \text{heap-clone } P \ h \ a \ h' \ \text{None}\}$   
   else if  $M = \text{hashCode}$  then  $\{(\llbracket \rrbracket, \text{RetVal } (\text{Intg } (\text{word-of-int } (\text{hash-addr } a))), h)\}$   
   else if  $M = \text{print}$  then  $\{(\llbracket \text{ExternalCall } a \ M \text{ vs Unit} \rrbracket, \text{RetVal Unit}, h)\}$   
   else if  $M = \text{currentThread}$  then  $\{(\llbracket \rrbracket, \text{RetVal } (\text{Addr } (\text{thread-id2addr } t))), h)\}$   
   else if  $M = \text{interrupted}$  then  $\{(\llbracket \text{IsInterrupted } t \text{ True, ClearInterrupt } t, \text{ObsInterrupted } t \rrbracket, \text{RetVal } (\text{Bool True}), h),$   
      $(\llbracket \text{IsInterrupted } t \text{ False} \rrbracket, \text{RetVal } (\text{Bool False}), h)\}$   
   else if  $M = \text{yield}$  then  $\{(\llbracket \text{Yield} \rrbracket, \text{RetVal Unit}, h)\}$   
   else  
     let  $hT = \text{the } (\text{typeof-addr } h \ a)$   
     in if  $P \vdash \text{ty-of-htype } hT \leq \text{Class Thread}$  then  
       let  $t\text{-}a = \text{addr2thread-id } a$   
       in if  $M = \text{start}$  then  
          $\{(\llbracket \text{NewThread } t\text{-}a \ (the\text{-}Class \ (\text{ty-of-htype } hT), \text{run}, a) \ h, \text{ThreadStart } t\text{-}a \rrbracket, \text{RetVal Unit}, h),$   
          $(\llbracket \text{ThreadExists } t\text{-}a \text{ True} \rrbracket, \text{RetEXC IllegalThreadState}, h)\}$   
       else if  $M = \text{join}$  then  
          $\{(\llbracket \text{Join } t\text{-}a, \text{IsInterrupted } t \text{ False, ThreadJoin } t\text{-}a \rrbracket, \text{RetVal Unit}, h),$

$(\llbracket \text{IsInterrupted } t \text{ True, ClearInterrupt } t, \text{ObsInterrupted } t \rrbracket, \text{RetEXC InterruptedException, } h) \rrbracket$   
 else if  $M = \text{interrupt}$  then  
 $\{(\llbracket \text{ThreadExists } t\text{-a True, WakeUp } t\text{-a, Interrupt } t\text{-a, ObsInterrupt } t\text{-a} \rrbracket, \text{RetVal Unit, } h),$   
 $(\llbracket \text{ThreadExists } t\text{-a False} \rrbracket, \text{RetVal Unit, } h)\}$   
 else if  $M = \text{isInterrupted}$  then  
 $\{(\llbracket \text{IsInterrupted } t\text{-a False} \rrbracket, \text{RetVal (Bool False), } h),$   
 $(\llbracket \text{IsInterrupted } t\text{-a True, ObsInterrupted } t\text{-a} \rrbracket, \text{RetVal (Bool True), } h)\}$   
 else  $\{(\llbracket \rrbracket, \text{undefined})\}$   
 else  $\{(\llbracket \rrbracket, \text{undefined})\}$

**lemma** *red-external-imp-red-external-aggr*:

$P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle \implies (ta, va, h') \in \text{red-external-aggr } P \ t \ a \ M \ vs \ h$

**unfolding** *red-external-aggr-def*

**by**(*auto elim!*: *red-external.cases split del: if-split simp add: split-beta*)

**end**

**context** *heap begin*

**lemma** *hext-heap-copy-loc*:

$\text{heap-copy-loc } a \ a' \ al \ h \ obs \ h' \implies h \trianglelefteq h'$

**by**(*blast elim: heap-copy-loc.cases dest: hext-heap-ops*)

**lemma** *hext-heap-copies*:

**assumes** *heap-copies*  $a \ a' \ als \ h \ obs \ h'$

**shows**  $h \trianglelefteq h'$

**using** *assms* **by** *induct(blast intro: hext-heap-copy-loc hext-trans)+*

**lemma** *hext-heap-clone*:

**assumes** *heap-clone*  $P \ h \ a \ h' \ res$

**shows**  $h \trianglelefteq h'$

**using** *assms* **by**(*blast elim: heap-clone.cases dest: hext-heap-ops hext-heap-copies intro: hext-trans*)

**theorem** *red-external-hext*:

**assumes**  $P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle$

**shows** *hext*  $h \ h'$

**using** *assms*

**by**(*cases*)(*blast intro: hext-heap-ops hext-heap-clone*)**+**

**lemma** *red-external-preserves-tconf*:

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle; P, h \vdash t' \sqrt{t} \rrbracket \implies P, h' \vdash t' \sqrt{t}$

**by**(*drule red-external-hext*)(*rule tconf-hext-mono*)

**end**

**context** *heap-conf begin*

**lemma** *typeof-addr-heap-clone*:

**assumes** *heap-clone*  $P \ h \ a \ h' \ [(obs, a')]$

**and** *hconf*  $h$

**shows** *typeof-addr*  $h' \ a' = \text{typeof-addr } h \ a$

**using** *assms*

**by** *cases (auto dest!: allocate-SomeD hext-heap-copies dest: typeof-addr-hext-mono typeof-addr-is-type*

*is-type-ArrayD*)

**end**

**context** *heap-base* **begin**

**lemma** *red-ext-new-thread-heap*:

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle; NewThread\ t' \text{ ex } h'' \in set\ \{\{ta\}_t\} \rrbracket \implies h'' = h'$   
**by**(*auto elim: red-external.cases simp add: ta-upd-simps*)

**lemma** *red-ext-aggr-new-thread-heap*:

$\llbracket (ta, va, h') \in red\text{-}external\text{-}aggr\ P\ t\ a\ M\ vs\ h; NewThread\ t' \text{ ex } h'' \in set\ \{\{ta\}_t\} \rrbracket \implies h'' = h'$   
**by**(*auto simp add: red-external-aggr-def is-native.simps split-beta ta-upd-simps split: if-split-asm*)

**end**

**context** *addr-conv* **begin**

**lemma** *red-external-new-thread-exists-thread-object*:

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle; NewThread\ t' \text{ ex } h'' \in set\ \{\{ta\}_t\} \rrbracket$   
 $\implies \exists C. \text{typeof-addr } h' (\text{thread-id2addr } t') = \lfloor \text{Class-type } C \rfloor \wedge P \vdash C \preceq^* Thread$   
**by**(*auto elim!: red-external.cases dest!: Array-widen simp add: ta-upd-simps*)

**lemma** *red-external-aggr-new-thread-exists-thread-object*:

$\llbracket (ta, va, h') \in red\text{-}external\text{-}aggr\ P\ t\ a\ M\ vs\ h; \text{typeof-addr } h\ a \neq None; \\ NewThread\ t' \text{ ex } h'' \in set\ \{\{ta\}_t\} \rrbracket$   
 $\implies \exists C. \text{typeof-addr } h' (\text{thread-id2addr } t') = \lfloor \text{Class-type } C \rfloor \wedge P \vdash C \preceq^* Thread$   
**by**(*auto simp add: red-external-aggr-def is-native.simps split-beta ta-upd-simps widen-Class split: if-split-asm dest!: Array-widen*)

**end**

**context** *heap* **begin**

**lemma** *red-external-aggr-hext*:

$\llbracket (ta, va, h') \in red\text{-}external\text{-}aggr\ P\ t\ a\ M\ vs\ h; is\text{-}native\ P\ (the\ (typeof\text{-}addr\ h\ a))\ M \rrbracket \implies h \trianglelefteq h'$   
**apply**(*auto simp add: red-external-aggr-def split-beta is-native.simps elim!: external-WT-defs-cases hext-heap-clone split: if-split-asm*)  
**apply**(*auto elim!: external-WT-defs.cases dest!: sees-method-decl-above intro: widen-trans simp add: class-type-of-eq split: htype.split-asm*)  
**done**

**lemma** *red-external-aggr-preserves-tconf*:

$\llbracket (ta, va, h') \in red\text{-}external\text{-}aggr\ P\ t\ a\ M\ vs\ h; is\text{-}native\ P\ (the\ (typeof\text{-}addr\ h\ a))\ M; P, h \vdash t' \sqrt{t} \rrbracket$   
 $\implies P, h' \vdash t' \sqrt{t}$   
**by**(*blast dest: red-external-aggr-hext intro: tconf-hext-mono*)

**end**

**context** *heap-base* **begin**

**lemma** *red-external-Wakeup-no-Join-no-Lock-no-Interrupt*:

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle; Notified \in set\ \{\{ta\}_w\} \vee WokenUp \in set\ \{\{ta\}_w\} \rrbracket \implies$   
 $collect\text{-}locks\ \{\{ta\}_l\} = \{\} \wedge collect\text{-}cond\text{-}actions\ \{\{ta\}_c\} = \{\} \wedge collect\text{-}interrupts\ \{\{ta\}_i\} = \{\}$

**by**(*auto elim!*: *red-external.cases simp add: ta-upd-simps collect-locks-def collect-interrupts-def*)

**lemma** *red-external-aggr-Wakeup-no-Join*:

$\llbracket (ta, va, h') \in \text{red-external-aggr } P \ t \ a \ M \ vs \ h;$   
 $\text{Notified} \in \text{set } \llbracket ta \rrbracket_w \vee \text{WokenUp} \in \text{set } \llbracket ta \rrbracket_w \rrbracket$   
 $\implies \text{collect-locks } \llbracket ta \rrbracket_l = \{\} \wedge \text{collect-cond-actions } \llbracket ta \rrbracket_c = \{\} \wedge \text{collect-interrupts } \llbracket ta \rrbracket_i = \{\}$

**by**(*auto simp add: red-external-aggr-def split-beta ta-upd-simps collect-locks-def collect-interrupts-def split: if-split-asm*)

**lemma** *red-external-Suspend-StaySame*:

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow \text{ext } \langle va, h' \rangle; \text{Suspend } w \in \text{set } \llbracket ta \rrbracket_w \rrbracket \implies va = \text{RetStaySame}$

**by**(*auto elim!*: *red-external.cases simp add: ta-upd-simps*)

**lemma** *red-external-aggr-Suspend-StaySame*:

$\llbracket (ta, va, h') \in \text{red-external-aggr } P \ t \ a \ M \ vs \ h; \text{Suspend } w \in \text{set } \llbracket ta \rrbracket_w \rrbracket \implies va = \text{RetStaySame}$

**by**(*auto simp add: red-external-aggr-def split-beta ta-upd-simps split: if-split-asm*)

**lemma** *red-external-Suspend-waitD*:

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow \text{ext } \langle va, h' \rangle; \text{Suspend } w \in \text{set } \llbracket ta \rrbracket_w \rrbracket \implies M = \text{wait}$

**by**(*auto elim!*: *red-external.cases simp add: ta-upd-simps*)

**lemma** *red-external-aggr-Suspend-waitD*:

$\llbracket (ta, va, h') \in \text{red-external-aggr } P \ t \ a \ M \ vs \ h; \text{Suspend } w \in \text{set } \llbracket ta \rrbracket_w \rrbracket \implies M = \text{wait}$

**by**(*auto simp add: red-external-aggr-def split-beta ta-upd-simps split: if-split-asm*)

**lemma** *red-external-new-thread-sub-thread*:

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow \text{ext } \langle va, h' \rangle; \text{NewThread } t' \ (C, M', a') \ h'' \in \text{set } \llbracket ta \rrbracket_t \rrbracket$   
 $\implies \text{typeof-addr } h' \ a' = \lfloor \text{Class-type } C \rfloor \wedge P \vdash C \preceq^* \text{Thread} \wedge M' = \text{run}$

**by**(*auto elim!*: *red-external.cases simp add: widen-Class ta-upd-simps*)

**lemma** *red-external-aggr-new-thread-sub-thread*:

$\llbracket (ta, va, h') \in \text{red-external-aggr } P \ t \ a \ M \ vs \ h; \text{typeof-addr } h \ a \neq \text{None};$

$\text{NewThread } t' \ (C, M', a') \ h'' \in \text{set } \llbracket ta \rrbracket_t \rrbracket$

$\implies \text{typeof-addr } h' \ a' = \lfloor \text{Class-type } C \rfloor \wedge P \vdash C \preceq^* \text{Thread} \wedge M' = \text{run}$

**by**(*auto simp add: red-external-aggr-def split-beta ta-upd-simps widen-Class split: if-split-asm dest!: Array-widen*)

**lemma** *heap-copy-loc-length*:

**assumes** *heap-copy-loc* *a a' al h obs h'*

**shows** *length obs = 2*

**using** *assms by(cases) simp*

**lemma** *heap-copies-length*:

**assumes** *heap-copies* *a a' als h obs h'*

**shows** *length obs = 2 \* length als*

**using** *assms by(induct)(auto dest!: heap-copy-loc-length)*

**end**

### 3.11.4 $\tau$ -moves

**inductive**  *$\tau$ external-defs* :: *cname*  $\Rightarrow$  *mname*  $\Rightarrow$  *bool*

**where**



$\tau_{\text{external-defs}}$  Object hashCode  
 |  $\tau_{\text{external-defs}}$  Object currentThread

**definition**  $\tau_{\text{external}} :: 'm \text{ prog} \Rightarrow h\text{type} \Rightarrow m\text{name} \Rightarrow \text{bool}$   
**where**  $\tau_{\text{external}} P hT M \longleftrightarrow (\exists Ts \ Tr \ D. P \vdash \text{class-type-of } hT \text{ sees } M:Ts \rightarrow Tr = \text{Native in } D \wedge \tau_{\text{external-defs}} D M)$

**context** heap-base **begin**

**definition**  $\tau_{\text{external}}' :: 'm \text{ prog} \Rightarrow 'heap \Rightarrow 'addr \Rightarrow m\text{name} \Rightarrow \text{bool}$   
**where**  $\tau_{\text{external}}' P h a M \longleftrightarrow (\exists hT. \text{typeof-addr } h a = \text{Some } hT \wedge \tau_{\text{external}} P hT M)$

**lemma**  $\tau_{\text{external}}'\text{-red-external-heap-unchanged}$ :

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle; \tau_{\text{external}}' P h a M \rrbracket \Longrightarrow h' = h$   
**by**(auto elim!: red-external.cases  $\tau_{\text{external-defs}}$ .cases simp add:  $\tau_{\text{external-def}}$   $\tau_{\text{external}}'\text{-def}$ )

**lemma**  $\tau_{\text{external}}'\text{-red-external-aggr-heap-unchanged}$ :

$\llbracket (ta, va, h') \in \text{red-external-aggr } P t a M \text{ vs } h; \tau_{\text{external}}' P h a M \rrbracket \Longrightarrow h' = h$   
**by**(auto elim!:  $\tau_{\text{external-defs}}$ .cases simp add:  $\tau_{\text{external-def}}$   $\tau_{\text{external}}'\text{-def}$  red-external-aggr-def)

**lemma**  $\tau_{\text{external}}'\text{-red-external-TA-empty}$ :

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle; \tau_{\text{external}}' P h a M \rrbracket \Longrightarrow ta = \varepsilon$   
**by**(auto elim!: red-external.cases  $\tau_{\text{external-defs}}$ .cases simp add:  $\tau_{\text{external-def}}$   $\tau_{\text{external}}'\text{-def}$ )

**lemma**  $\tau_{\text{external}}'\text{-red-external-aggr-TA-empty}$ :

$\llbracket (ta, va, h') \in \text{red-external-aggr } P t a M \text{ vs } h; \tau_{\text{external}}' P h a M \rrbracket \Longrightarrow ta = \varepsilon$   
**by**(auto elim!:  $\tau_{\text{external-defs}}$ .cases simp add:  $\tau_{\text{external-def}}$   $\tau_{\text{external}}'\text{-def}$  red-external-aggr-def)

**lemma** red-external-new-thread-addr-conf:

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle; \text{NewThread } t (C, M, a') h'' \in \text{set } \{ta\}_t \rrbracket$   
 $\Longrightarrow P, h' \vdash \text{Addr } a : \leq \text{Class Thread}$   
**by**(auto elim!: red-external.cases simp add: conf-def ta-upd-simps)

**lemma**  $\tau_{\text{external-red-external-aggr-heap-unchanged}$ :

$\llbracket (ta, va, h') \in \text{red-external-aggr } P t a M \text{ vs } h; \tau_{\text{external}} P (\text{the } (\text{typeof-addr } h a)) M \rrbracket \Longrightarrow h' = h$   
**by**(auto elim!:  $\tau_{\text{external-defs}}$ .cases simp add:  $\tau_{\text{external-def}}$  red-external-aggr-def)

**lemma**  $\tau_{\text{external-red-external-aggr-TA-empty}$ :

$\llbracket (ta, va, h') \in \text{red-external-aggr } P t a M \text{ vs } h; \tau_{\text{external}} P (\text{the } (\text{typeof-addr } h a)) M \rrbracket \Longrightarrow ta = \varepsilon$   
**by**(auto elim!:  $\tau_{\text{external-defs}}$ .cases simp add:  $\tau_{\text{external-def}}$  red-external-aggr-def)

**end**

### 3.11.5 Code generation

**code-pred**

(modes:

$i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow \text{bool},$   
 $i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool},$   
 $i \Rightarrow i \Rightarrow o \Rightarrow o \Rightarrow \text{bool},$   
 $o \Rightarrow i \Rightarrow o \Rightarrow o \Rightarrow \text{bool})$

external-WT-defs

.

**code-pred**

(modes:  $i \Rightarrow i \Rightarrow i \Rightarrow \text{bool}$ )  
 [inductify, skip-proof]  
 is-native

.

**declare** heap-base.heap-copy-loc.intros[code-pred-intro]

**code-pred**

(modes:  $(i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow (i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow o \Rightarrow \text{bool}$ )

heap-base.heap-copy-loc

**proof** –

case heap-copy-loc

from heap-copy-loc.premis show thesis

by(rule heap-base.heap-copy-loc.cases)(rule heap-copy-loc.that[OF refl refl refl refl refl refl])

qed

**declare** heap-base.heap-copies.intros [code-pred-intro]

**code-pred**

(modes:  $(i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow (i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow o \Rightarrow \text{bool}$ )

heap-base.heap-copies

**proof** –

case heap-copies

from heap-copies.premis show thesis

by(rule heap-base.heap-copies.cases)(erule (3) heap-copies.that[OF refl refl refl refl]|assumption)+

qed

**declare** heap-base.heap-clone.intros [folded Predicate-Compile.contains-def, code-pred-intro]

**code-pred**

(modes:  $i \Rightarrow i \Rightarrow (i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow (i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}$ )

heap-base.heap-clone

**proof** –

case heap-clone

from heap-clone.premis show thesis

by(rule heap-base.heap-clone.cases[folded Predicate-Compile.contains-def])(erule (3) heap-clone.that[OF refl refl refl refl refl refl]|assumption)+

qed

code\_pred in Isabelle2012 cannot handle boolean parameters as premises properly, so this replacement rule explicitly tests for *True*

**lemma** (in heap-base) RedWaitSpurious-Code:

spurious-wakeups = True  $\implies$

$P, t \vdash \langle a \cdot \text{wait}([], h), h \rangle - \{ \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a, \text{ReleaseAcquire} \rightarrow a, \text{IsInterrupted } t \text{ False}, \text{SyncUnlock } a \} \rightarrow \text{ext } \langle \text{RetVal Unit}, h \rangle$

by(rule RedWaitSpurious) simp

**lemmas** [code-pred-intro] =

heap-base.RedNewThread heap-base.RedNewThreadFail

heap-base.RedJoin heap-base.RedJoinInterrupt

```

heap-base.RedInterrupt heap-base.RedInterruptInexist heap-base.RedIsInterruptedTrue heap-base.RedIsInterruptedFalse
heap-base.RedWaitInterrupt heap-base.RedWait heap-base.RedWaitFail heap-base.RedWaitNotified heap-base.RedWaitInterrupt
declare heap-base.RedWaitSpurious-Code [code-pred-intro RedWaitSpurious]
lemmas [code-pred-intro] =
  heap-base.RedNotify heap-base.RedNotifyFail heap-base.RedNotifyAll heap-base.RedNotifyAllFail
  heap-base.RedClone heap-base.RedCloneFail
  heap-base.RedHashcode heap-base.RedPrint heap-base.RedCurrentThread
  heap-base.RedInterruptedTrue heap-base.RedInterruptedFalse
  heap-base.RedYield

code-pred
  (modes:  $i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow (i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow (i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow i$ 
 $\Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow o \Rightarrow o \Rightarrow \text{bool}$ )
  heap-base.red-external
proof –
  case red-external
  from red-external.premis show ?thesis
  apply(rule heap-base.red-external.cases)
  apply(erule (4) red-external.that[OF refl refl refl refl refl refl refl refl refl refl refl refl])|assumption|erule
eqTrueI)+
  done
qed

end

```

### 3.12 Generic Well-formedness of programs

```
theory WellForm
imports
  SystemClasses
  ExternalCall
begin
```

This theory defines global well-formedness conditions for programs but does not look inside method bodies. Hence it works for both Jinja and JVM programs. Well-typing of expressions is defined elsewhere (in theory *WellType*).

Because JinjaThreads does not have method overloading, its policy for method overriding is the classical one: *covariant in the result type but contravariant in the argument types*. This means the result type of the overriding method becomes more specific, the argument types become more general.

**type-synonym** *'m wf-mdecl-test* = *'m prog*  $\Rightarrow$  *cname*  $\Rightarrow$  *'m mdecl*  $\Rightarrow$  *bool*

**definition**  $wf\text{-}fdecl :: 'm \text{ prog} \Rightarrow fdecl \Rightarrow bool$   
**where**  $wf\text{-}fdecl P \equiv \lambda(F, T, fm). \text{is-type } P \ T$

$$\begin{aligned} \textbf{definition } wf\text{-mdecl} &:: 'm \text{ wf-mdecl-test} \Rightarrow 'm \text{ prog} \Rightarrow cname \Rightarrow 'm \text{ mdecl}' \Rightarrow bool \textbf{ where} \\ wf\text{-mdecl } wf\text{-md } P \ C &\equiv \\ \lambda(M, Ts, T, m). &(\forall T \in set \ Ts. \ is\text{-type } P \ T) \wedge is\text{-type } P \ T \wedge \\ &(case \ m \ of \ Native \Rightarrow C.M(Ts) :: T \mid |mb| \Rightarrow wf\text{-md } P \ C \ (M, Ts, T, mb)) \end{aligned}$$

**fun** *wf-overriding* :: 'm prog  $\Rightarrow$  cname  $\Rightarrow$  'm mdecl'  $\Rightarrow$  bool  
**where**  
*wf-overriding* P D (M, Ts, T, m) =

$(\forall D' Ts' T' m'. P \vdash D \text{ sees } M:Ts' \rightarrow T' = m' \text{ in } D' \longrightarrow P \vdash Ts' [\leq] Ts \wedge P \vdash T \leq T')$

**definition**  $wf\text{-}cdecl :: 'm \text{ wf-mdecl-test} \Rightarrow 'm \text{ prog} \Rightarrow 'm \text{ cdecl} \Rightarrow \text{bool}$

**where**

$wf\text{-}cdecl \text{ wf-md } P \equiv \lambda(C, (D, fs, ms)).$   
 $(\forall f \in \text{set } fs. \text{ wf-fdecl } P f) \wedge \text{distinct-fst } fs \wedge$   
 $(\forall m \in \text{set } ms. \text{ wf-mdecl } \text{ wf-md } P C m) \wedge$   
 $\text{distinct-fst } ms \wedge$   
 $(C \neq \text{Object} \longrightarrow$   
 $\text{is-class } P D \wedge \neg P \vdash D \preceq^* C \wedge$   
 $(\forall m \in \text{set } ms. \text{ wf-overriding } P D m)) \wedge$   
 $(C = \text{Thread} \longrightarrow (\exists m. (\text{run}, [], \text{Void}, m) \in \text{set } ms))$

**definition**  $wf\text{-}prog :: 'm \text{ wf-mdecl-test} \Rightarrow 'm \text{ prog} \Rightarrow \text{bool}$

**where**

$wf\text{-}prog \text{ wf-md } P \longleftrightarrow \text{wf-syscls } P \wedge \text{distinct-fst } (\text{classes } P) \wedge (\forall c \in \text{set } (\text{classes } P). \text{ wf-cdecl } \text{ wf-md } P c)$

**lemma**  $wf\text{-}prog\text{-}def2$ :

$wf\text{-}prog \text{ wf-md } P \longleftrightarrow \text{wf-syscls } P \wedge (\forall C \text{ rest. class } P C = [\text{rest}] \longrightarrow \text{wf-cdecl } \text{ wf-md } P (C, \text{rest}))$   
 $\wedge \text{distinct-fst } (\text{classes } P)$

**by**(cases P)(auto simp add: wf-prog-def dest: map-of-SomeD map-of-SomeI)

### 3.12.1 Well-formedness lemmas

**lemma**  $wf\text{-}prog\text{-}wf\text{-}syscls$ :  $wf\text{-}prog \text{ wf-md } P \Longrightarrow \text{wf-syscls } P$

**by**(simp add: wf-prog-def)

**lemma**  $\text{class-wf}$ :

$\llbracket \text{class } P C = \text{Some } c; \text{ wf-prog } \text{ wf-md } P \rrbracket \Longrightarrow \text{wf-cdecl } \text{ wf-md } P (C, c)$

**by** (cases P) (fastforce dest: map-of-SomeD simp add: wf-prog-def)

**lemma** [simp]:

**assumes**  $\text{wf-prog } \text{ wf-md } P$   
**shows**  $\text{class-Object}: \exists C fs ms. \text{ class } P \text{ Object} = \text{Some } (C, fs, ms)$   
**and**  $\text{class-Thread}: \exists C fs ms. \text{ class } P \text{ Thread} = \text{Some } (C, fs, ms)$

**using**  $\text{wf-prog-wf-syscls}[OF \text{ assms}]$

**by**(rule wf-syscls-class-Object wf-syscls-class-Thread)+

**lemma** [simp]:

**assumes**  $\text{wf-prog } \text{ wf-md } P$   
**shows**  $\text{is-class-Object}: \text{is-class } P \text{ Object}$   
**and**  $\text{is-class-Thread}: \text{is-class } P \text{ Thread}$

**using**  $\text{wf-prog-wf-syscls}[OF \text{ assms}]$  **by** simp-all

**lemma**  $\text{xcpt-subcls-Throwable}$ :

$\llbracket C \in \text{sys-xcpts}; \text{ wf-prog } \text{ wf-md } P \rrbracket \Longrightarrow P \vdash C \preceq^* \text{Throwable}$

**by**(simp add: wf-prog-wf-syscls wf-syscls-xcpt-subcls-Throwable)

**lemma**  $\text{is-class-Throwable}$ :

$\text{wf-prog } \text{ wf-md } P \Longrightarrow \text{is-class } P \text{ Throwable}$

**by**(rule wf-prog-wf-syscls wf-syscls-is-class-Throwable)+

**lemma**  $\text{is-class-sub-Throwable}$ :

$\llbracket \text{wf-prog wf-md } P; P \vdash C \preceq^* \text{Throwable} \rrbracket \implies \text{is-class } P \ C$   
**by**(rule wf-syscls-is-class-sub-Throwable[OF wf-prog-wf-syscls])

**lemma** *is-class-xcpt*:

$\llbracket C \in \text{sys-xcpts}; \text{wf-prog wf-md } P \rrbracket \implies \text{is-class } P \ C$   
**by**(rule wf-syscls-is-class-xcpt[OF - wf-prog-wf-syscls])

**context** *heap-base* **begin**

**lemma** *wf-preallocatedE*:

**assumes** *wf-prog wf-md P*

**and** *preallocated h*

**and**  $C \in \text{sys-xcpts}$

**obtains** *typeof-addr h (addr-of-sys-xcpt C) = [Class-type C] P ⊢ C ≼\* Throwable*

**proof** –

**from**  $\langle \text{preallocated } h \rangle \langle C \in \text{sys-xcpts} \rangle$  **have** *typeof-addr h (addr-of-sys-xcpt C) = [Class-type C]*

**by**(rule *typeof-addr-sys-xcp*)

**moreover from**  $\langle C \in \text{sys-xcpts} \rangle \langle \text{wf-prog wf-md } P \rangle$  **have**  $P \vdash C \preceq^* \text{Throwable}$  **by**(rule *xcpt-subcls-Throwable*)

**ultimately show thesis by**(rule *that*)

**qed**

**lemma** *wf-preallocatedD*:

**assumes** *wf-prog wf-md P*

**and** *preallocated h*

**and**  $C \in \text{sys-xcpts}$

**shows** *typeof-addr h (addr-of-sys-xcpt C) = [Class-type C] ∧ P ⊢ C ≼\* Throwable*

**using** *assms*

**by**(rule *wf-preallocatedE*) *blast*

**end**

**lemma** (**in** *heap-conf*) *hconf-start-heap*:

*wf-prog wf-md P ⟹ hconf start-heap*

**unfolding** *start-heap-def start-heap-data-def initialization-list-def sys-xcpts-list-def*

**using** *hconf-empty*

**by** –(*simp add: create-initial-object-simps del: hconf-empty, clarsimp split: prod.split elim!: not-empty-pairE*  
*simp del: hconf-empty, drule (1) allocate-Eps, drule (1) hconf-allocate-mono, simp add: is-class-xcpt*)+

**lemma** *subcls1-wfD*:

$\llbracket P \vdash C \prec^1 D; \text{wf-prog wf-md } P \rrbracket \implies D \neq C \wedge \neg (\text{subcls1 } P)^{++} D \ C$

**apply**( *frule tranclp.r-into-trancl*[**where**  $r = \text{subcls1 } P$ ])

**apply**( *drule subcls1D*)

**apply**(*clarify*)

**apply**( *drule (1) class-wf*)

**apply**( *unfold wf-cdecl-def*)

**apply**(rule *conjI*)

**apply**(*force*)

**apply**(*unfold reflcp-tranclp*[*symmetric*, **where**  $r = \text{subcls1 } P$ ])

**apply**(*blast*)

**done**

**lemma** *wf-cdecl-supD*:

$\llbracket \text{wf-cdecl wf-md } P \ (C, D, r); C \neq \text{Object} \rrbracket \implies \text{is-class } P \ D$

**lemma** *subcls-asymp*:

$$\llbracket \text{wf-prog wf-md } P; (\text{subcls1 } P)^{++} C D \rrbracket \Longrightarrow \neg (\text{subcls1 } P)^{++} D C$$

**lemma** *subcls-irrefl*:

$$\llbracket \text{wf-prog wf-md } P; (\text{subcls1 } P)^{++} C D \rrbracket \Longrightarrow C \neq D$$

**lemma** *acyclicP-def*:

$$\text{acyclicP } r \longleftrightarrow (\forall x. \neg r^{++} x x)$$

**unfolding** *acyclic-def trancl-def*

**by**(*auto*)

**lemma** *acyclic-subcls1*:

$$\text{wf-prog wf-md } P \Longrightarrow \text{acyclicP } (\text{subcls1 } P)$$

**by**(*unfold acyclicP-def*)(*fast dest: subcls-irrefl*)

**lemma** *finite-conversep*:  $\text{finite } \{(x, y). r^{-1-1} x y\} = \text{finite } \{(x, y). r x y\}$

**by**(*subst finite-converse[unfolded converse-unfold, symmetric]*) *simp*

**lemma** *acyclicP-wf-subcls1*:

$$\text{acyclicP } (\text{subcls1 } P) \Longrightarrow \text{wfP } ((\text{subcls1 } P)^{-1-1})$$

**unfolding** *wfP-def*

**by**(*rule finite-acyclic-wf*)(*simp-all only: finite-conversep finite-subcls1 acyclicP-converse*)

**lemma** *wf-subcls1*:

$$\text{wf-prog wf-md } P \Longrightarrow \text{wfP } ((\text{subcls1 } P)^{-1-1})$$

**by**(*rule acyclicP-wf-subcls1*)(*rule acyclic-subcls1*)

**lemma** *single-valued-subcls1*:

$$\text{wf-prog wf-md } G \Longrightarrow \text{single-valuedp } (\text{subcls1 } G)$$

**lemma** *subcls-induct*:

$$\llbracket \text{wf-prog wf-md } P; \bigwedge C. \forall D. (\text{subcls1 } P)^{++} C D \longrightarrow Q D \Longrightarrow Q C \rrbracket \Longrightarrow Q C$$

**lemma** *subcls1-induct-aux*:

$$\llbracket \text{is-class } P C; \text{wf-prog wf-md } P; Q \text{ Object};$$

$$\bigwedge C D \text{ fs ms.}$$

$$\llbracket C \neq \text{Object}; \text{is-class } P C; \text{class } P C = \text{Some } (D, \text{fs}, \text{ms}) \wedge$$

$$\text{wf-cdecl wf-md } P (C, D, \text{fs}, \text{ms}) \wedge P \vdash C \prec^1 D \wedge \text{is-class } P D \wedge Q D \rrbracket \Longrightarrow Q C \rrbracket$$

$$\Longrightarrow Q C$$

**lemma** *subcls1-induct* [*consumes 2, case-names Object Subcls*]:

$$\llbracket \text{wf-prog wf-md } P; \text{is-class } P C; Q \text{ Object};$$

$$\bigwedge C D. \llbracket C \neq \text{Object}; P \vdash C \prec^1 D; \text{is-class } P D; Q D \rrbracket \Longrightarrow Q C \rrbracket$$

$$\Longrightarrow Q C$$

**lemma** *subcls-C-Object*:

$$\llbracket \text{is-class } P C; \text{wf-prog wf-md } P \rrbracket \Longrightarrow P \vdash C \preceq^* \text{Object}$$

**lemma** *converse-subcls-is-class*:

**assumes** *wf*: *wf-prog wf-md P*

**shows**  $\llbracket P \vdash C \preceq^* D; \text{is-class } P C \rrbracket \Longrightarrow \text{is-class } P D$

**proof**(*induct rule: rtranclp-induct*)

**assume** *is-class P C*

**thus** *is-class P C* .

**next**

**fix** *D E*

**assume** *PDE*:  $P \vdash D \prec^1 E$

**and** *IH*: *is-class P C*  $\Longrightarrow$  *is-class P D*

```

  and iPC: is-class P C
  have is-class P D by (rule IH[OF iPC])
  with PDE obtain fsD MsD where classD: class P D = [(E, fsD, MsD)]
    by (auto simp add: is-class-def elim!: subcls1.cases)
  thus is-class P E using wf PDE
    by (auto elim!: subcls1.cases dest: class-wf simp: wf-cdecl-def)
qed

```

**lemma** *is-class-is-subcls*:

*wf-prog* *m* *P*  $\implies$  *is-class* *P* *C* = *P*  $\vdash$  *C*  $\preceq^*$  *Object*

**lemma** *subcls-antisym*:

$\llbracket \text{wf-prog } m \text{ } P; P \vdash C \preceq^* D; P \vdash D \preceq^* C \rrbracket \implies C = D$

**apply**(*drule* *acyclic-subcls1*)

**apply**(*drule* *acyclic-impl-antisym-rtrancl*)

**apply**(*drule* *antisymD*)

**apply**(*unfold* *Transitive-Closure.rtrancl-def*)

**apply**(*auto*)

**done**

**lemma** *is-type-pTs*:

$\llbracket \text{wf-prog wf-md } P; \text{ class } P \text{ } C = [(S, fs, ms)]; (M, Ts, T, m) \in \text{set } ms \rrbracket \implies \text{set } Ts \subseteq \text{types } P$   
**by**(*fastforce* *dest*: *class-wf* *simp* *add*: *wf-cdecl-def* *wf-mdecl-def*)

**lemma** *widen-asm-1*:

**assumes** *wfP*: *wf-prog* *wf-md* *P*

**shows** *P*  $\vdash$  *C*  $\leq$  *D*  $\implies$  *C* = *D*  $\vee$   $\neg$  (*P*  $\vdash$  *D*  $\leq$  *C*)

**proof** (*erule* *widen.induct*)

**fix** *T*

**show** *T* = *T*  $\vee$   $\neg$  (*P*  $\vdash$  *T*  $\leq$  *T*) **by** *simp*

**next**

**fix** *C* *D*

**assume** *CscD*: *P*  $\vdash$  *C*  $\preceq^*$  *D*

**then have** *CpscD*: *C* = *D*  $\vee$  (*C*  $\neq$  *D*  $\wedge$  (*subcls1* *P*)<sup>++</sup> *C* *D*) **by** (*simp* *add*: *rtranclpD*)

{ **assume** *P*  $\vdash$  *D*  $\preceq^*$  *C*

**then have** *DpscC*: *D* = *C*  $\vee$  (*D*  $\neq$  *C*  $\wedge$  (*subcls1* *P*)<sup>++</sup> *D* *C*) **by** (*simp* *add*: *rtranclpD*)

{ **assume** (*subcls1* *P*)<sup>++</sup> *D* *C*

**with** *wfP* **have** *CnscD*:  $\neg$  (*subcls1* *P*)<sup>++</sup> *C* *D* **by** (*rule* *subcls-asm*)

**with** *CpscD* **have** *C* = *D* **by** *simp*

}

**with** *DpscC* **have** *C* = *D* **by** *blast*

}

**hence** *Class* *C* = *Class* *D*  $\vee$   $\neg$  (*P*  $\vdash$  *D*  $\preceq^*$  *C*) **by** *blast*

**thus** *Class* *C* = *Class* *D*  $\vee$   $\neg$  *P*  $\vdash$  *Class* *D*  $\leq$  *Class* *C* **by** *simp*

**next**

**fix** *C*

**show** *NT* = *Class* *C*  $\vee$   $\neg$  *P*  $\vdash$  *Class* *C*  $\leq$  *NT* **by** *simp*

**next**

**fix** *A*

{ **assume** *P*  $\vdash$  *A*[]  $\leq$  *NT*

**hence** *A*[] = *NT* **by** *fastforce*

**hence** *False* **by** *simp* }

**hence**  $\neg$  (*P*  $\vdash$  *A*[]  $\leq$  *NT*) **by** *blast*

**thus** *NT* = *A*[]  $\vee$   $\neg$  *P*  $\vdash$  *A*[]  $\leq$  *NT* **by** *simp*

**next**

```

fix A
show  $A[] = \text{Class Object} \vee \neg P \vdash \text{Class Object} \leq A[]$ 
  by(auto dest: Object-widen)
next
fix A B
assume AsU:  $P \vdash A \leq B$  and BnpscA:  $A = B \vee \neg P \vdash B \leq A$ 
{ assume  $P \vdash B[] \leq A[]$ 
  hence  $P \vdash B \leq A$  by (auto dest: Array-Array-widen)
  with BnpscA have  $A = B$  by blast
  hence  $A[] = B[]$  by simp }
thus  $A[] = B[] \vee \neg P \vdash B[] \leq A[]$  by blast
qed

```

**lemma** *widen-asy*:  $\llbracket \text{wf-prog wf-md } P; P \vdash C \leq D; C \neq D \rrbracket \implies \neg (P \vdash D \leq C)$

**proof** –

```

assume wfP: wf-prog wf-md P and CsD:  $P \vdash C \leq D$  and CneqD:  $C \neq D$ 
from wfP CsD have  $C = D \vee \neg (P \vdash D \leq C)$  by (rule widen-asy-1)
with CneqD show ?thesis by simp

```

**qed**

**lemma** *widen-antisym*:

$\llbracket \text{wf-prog } m \text{ } P; P \vdash T \leq U; P \vdash U \leq T \rrbracket \implies T = U$

**by**(*auto dest: widen-asy*)

**lemma** *widen-C-Object*:  $\llbracket \text{wf-prog wf-md } P; \text{is-class } P \text{ } C \rrbracket \implies P \vdash \text{Class } C \leq \text{Class Object}$   
**by**(*simp add: subcls-C-Object*)

**lemma** *is-refType-widen-Object*:

**assumes** *wfP*: *wf-prog wfmc P*

**shows**  $\llbracket \text{is-type } P \text{ } A; \text{is-refT } A \rrbracket \implies P \vdash A \leq \text{Class Object}$

**by**(*induct A*)(*auto elim: refTE intro: subcls-C-Object[OF - wfP] widen-array-object*)

**lemma** *is-lub-unique*:

**assumes** *wf*: *wf-prog wf-md P*

**shows**  $\llbracket P \vdash \text{lub}(U, V) = T; P \vdash \text{lub}(U, V) = T' \rrbracket \implies T = T'$

**by**(*auto elim!: is-lub.cases intro: widen-antisym[OF wf]*)

### 3.12.2 Well-formedness and method lookup

**lemma** *sees-wf-mdecl*:

$\llbracket \text{wf-prog wf-md } P; P \vdash C \text{ sees } M:Ts \rightarrow T = m \text{ in } D \rrbracket \implies \text{wf-mdecl wf-md } P \text{ } D (M, Ts, T, m)$

**lemma** *sees-method-mono* [*rule-format (no-asm)*]:

$\llbracket P \vdash C' \preceq^* C; \text{wf-prog wf-md } P \rrbracket \implies$

$\forall D \text{ } Ts \text{ } T \text{ } m. P \vdash C \text{ sees } M:Ts \rightarrow T = m \text{ in } D \longrightarrow$

$(\exists D' \text{ } Ts' \text{ } T' \text{ } m'. P \vdash C' \text{ sees } M:Ts' \rightarrow T' = m' \text{ in } D' \wedge P \vdash Ts \leq Ts' \wedge P \vdash T' \leq T)$

**apply**( *drule rtranclpD*)

**apply**( *erule disjE*)

**apply**( *fastforce intro: widen-refl widens-refl*)

**apply**( *erule conjE*)

**apply**( *erule tranclp-trans-induct*)

**prefer** 2

**apply**( *clarify*)

**apply**( *drule spec, drule spec, drule spec, drule spec, erule (1) impE*)



```

apply clarify
apply( fast elim: widen-trans widens-trans)
apply( clarify)
apply( drule subcls1D)
apply( clarify)
apply(clarsimp simp:Method-def)
apply(frule (2) sees-methods-rec)
apply(rule refl)
apply(case-tac map-of ms M)
apply(rule-tac x = D in exI)
apply(rule-tac x = Ts in exI)
apply(rule-tac x = T in exI)
apply(clarsimp simp add: widens-refl)
apply(rule-tac x = m in exI)
apply(fastforce simp add:map-add-def split:option.split)
apply clarsimp
apply(rename-tac Ts' T' m')
apply( drule (1) class-wf)
apply( unfold wf-cdecl-def Method-def)
apply( frule map-of-SomeD)
apply(clarsimp)
apply(drule (1) bspec)+
apply clarsimp
apply(erule-tac x=D in allE)
apply(erule-tac x=Ts in allE)
apply(rotate-tac -1)
apply(erule-tac x=T in allE)
apply(fastforce simp:map-add-def Method-def split:option.split)
done

```

**lemma** *sees-method-mono2*:

$$\begin{aligned}
& \llbracket P \vdash C' \preceq^* C; \text{wf-prog wf-md } P; \\
& \quad P \vdash C \text{ sees } M:Ts \rightarrow T = m \text{ in } D; P \vdash C' \text{ sees } M:Ts' \rightarrow T' = m' \text{ in } D' \rrbracket \\
& \implies P \vdash Ts \leq Ts' \wedge P \vdash T' \leq T
\end{aligned}$$

**lemma** *mdecls-visible*:

**assumes** *wf: wf-prog wf-md P and class: is-class P C*  
**shows**  $\bigwedge D \text{ fs ms. class } P \text{ } C = \text{Some}(D, \text{fs}, \text{ms})$   
 $\implies \exists Mm. P \vdash C \text{ sees-methods } Mm \wedge (\forall (M, Ts, T, m) \in \text{set ms. } Mm \text{ } M = \text{Some}((Ts, T, m), C))$

**using** *wf class*

**proof** (*induct rule:subcls1-induct*)

**case** *Object*

**with** *wf* **have** *distinct-fst ms*

**by**(*auto dest: class-wf simp add: wf-cdecl-def*)

**with** *Object* **show** *?case* **by**(*fastforce intro!: sees-methods-Object map-of-SomeI*)

**next**

**case** *Subcls*

**with** *wf* **have** *distinct-fst ms*

**by**(*auto dest: class-wf simp add: wf-cdecl-def*)

**with** *Subcls* **show** *?case*

**by**(*fastforce elim:sees-methods-rec dest:subcls1D map-of-SomeI*  
*simp:is-class-def*)

**qed**

**lemma** *mdecl-visible*:

**assumes** *wf*: *wf-prog wf-md P* **and** *C*: *class P C = [(S,fs,ms)]* **and** *m*:  $(M, Ts, T, m) \in \text{set } ms$   
**shows**  $P \vdash C \text{ sees } M:Ts \rightarrow T = m \text{ in } C$

**proof** –

**from** *C* **have** *is-class P C* **by**(*auto simp:is-class-def*)

**with** *assms* **show** *?thesis*

**by**(*bestsimp simp:Method-def dest:mdecls-visible*)

**qed**

**lemma** *sees-wf-native*:

$\llbracket wf\text{-prog } wf\text{-md } P; P \vdash C \text{ sees } M:Ts \rightarrow T = \text{Native in } D \rrbracket \Longrightarrow D \cdot M(Ts) :: T$

**apply**(*drule* (1) *sees-wf-mdecl*)

**apply**(*simp add: wf-mdecl-def*)

**done**

**lemma** *Call-lemma*:

$\llbracket P \vdash C \text{ sees } M:Ts \rightarrow T = m \text{ in } D; P \vdash C' \preceq^* C; wf\text{-prog } wf\text{-md } P \rrbracket$

$\Longrightarrow \exists D' Ts' T' m'.$

$P \vdash C' \text{ sees } M:Ts' \rightarrow T' = m' \text{ in } D' \wedge P \vdash Ts [\leq] Ts' \wedge P \vdash T' \leq T \wedge P \vdash C' \preceq^* D'$

$\wedge \text{is-type } P T' \wedge (\forall T \in \text{set } Ts'. \text{is-type } P T) \wedge (m' \neq \text{Native} \longrightarrow wf\text{-md } P D' (M, Ts', T', \text{the } m'))$

**apply**(*frule* (2) *sees-method-mono*)

**apply**(*fastforce intro:sees-method-decl-above dest:sees-wf-mdecl*

*simp: wf-mdecl-def*)

**done**

**lemma** *sub-Thread-sees-run*:

**assumes** *wf*: *wf-prog wf-md P*

**and** *PCThread*:  $P \vdash C \preceq^* \text{Thread}$

**shows**  $\exists D \text{ mthd}. P \vdash C \text{ sees run: } [] \rightarrow \text{Void} = \llbracket \text{mthd} \rrbracket \text{ in } D$

**proof** –

**from** *class-Thread[OF wf]* **obtain** *T' fsT MsT*

**where** *classT*: *class P Thread = [(T', fsT, MsT)]* **by** *blast*

**hence** *wfcThread*: *wf-cdecl wf-md P (Thread, T', fsT, MsT)* **using** *wf* **by**(*rule class-wf*)

**then obtain** *mrunT* **where** *runThread*:  $(\text{run}, [], \text{Void}, \text{mrunT}) \in \text{set } MsT$

**by**(*auto simp add: wf-cdecl-def*)

**moreover have**  $\exists MmT. P \vdash \text{Thread sees-methods } MmT \wedge$

$(\forall (M, Ts, T, m) \in \text{set } MsT. MmT M = \text{Some}((Ts, T, m), \text{Thread}))$

**by**(*rule mdecls-visible[OF wf is-class-Thread[OF wf] classT]*)

**then obtain** *MmT* **where** *ThreadMmT*:  $P \vdash \text{Thread sees-methods } MmT$

**and** *MmT*:  $\forall (M, Ts, T, m) \in \text{set } MsT. MmT M = \text{Some}((Ts, T, m), \text{Thread})$

**by** *blast*

**ultimately obtain** *mthd*

**where**  $MmT \text{ run} = \llbracket ([], \text{Void}, \text{mthd}), \text{Thread} \rrbracket$

**by**(*fastforce*)

**with** *ThreadMmT* **have** *Tseesrun*:  $P \vdash \text{Thread sees run: } [] \rightarrow \text{Void} = \text{mthd in Thread}$

**by**(*auto simp add: Method-def*)

**from** *sees-method-mono[OF PCThread wf Tseesrun]*

**obtain** *D' m'* **where**  $P \vdash C \text{ sees run: } [] \rightarrow \text{Void} = m' \text{ in } D'$  **by** *auto*

**moreover have**  $m' \neq \text{None}$

**proof**

**assume**  $m' = \text{None}$

**with** *wf*  $\langle P \vdash C \text{ sees run: } [] \rightarrow \text{Void} = m' \text{ in } D' \rangle$  **have**  $D' \cdot \text{run}([]) :: \text{Void}$

```

    by(auto intro: sees-wf-native)
  thus False by cases auto
qed
ultimately show ?thesis by auto
qed

lemma wf-prog-lift:
  assumes wf: wf-prog ( $\lambda P C$  bd.  $A P C$  bd)  $P$ 
  and rule:
     $\bigwedge wf\text{-md } C M Ts C T m.$ 
     $\llbracket wf\text{-prog } wf\text{-md } P; P \vdash C \text{ sees } M:Ts \rightarrow T = \lfloor m \rfloor \text{ in } C; \text{ is-class } P C; \text{ set } Ts \subseteq \text{types } P; A P C$ 
     $(M, Ts, T, m) \rrbracket$ 
     $\implies B P C (M, Ts, T, m)$ 
  shows wf-prog ( $\lambda P C$  bd.  $B P C$  bd)  $P$ 
proof(cases  $P$ )
  case (Program  $P'$ )
  thus ?thesis using wf
  apply(clarsimp simp add: wf-prog-def wf-cdecl-def)
  apply(drule (1) bspec)
  apply(rename-tac  $C D fs ms$ )
  apply(subgoal-tac is-class  $P C$ )
  prefer 2
  apply(simp add: is-class-def)
  apply(drule weak-map-of-SomeI)
  apply(simp add: Program)
  apply(clarsimp simp add: Program wf-mdecl-def split del: option.split)
  apply(drule (1) bspec)
  apply clarsimp
  apply(rule conjI)
  apply clarsimp
  apply clarsimp
  apply(frule (1) map-of-SomeI)
  apply(rule rule[OF wf, unfolded Program])
  apply(clarsimp simp add: is-class-def)
  apply(rule mdecl-visible[OF wf[unfolded Program]])
  apply(fastforce intro: is-type-pTs [OF wf, unfolded Program])+
  done
qed

```

### 3.12.3 Well-formedness and field lookup

**lemma** *wf-Fields-Ex*:  
 $\llbracket wf\text{-prog } wf\text{-md } P; \text{ is-class } P C \rrbracket \implies \exists FDTs. P \vdash C \text{ has-fields } FDTs$

**lemma** *has-fields-types*:  
 $\llbracket P \vdash C \text{ has-fields } FDTs; (FD, T, fm) \in \text{set } FDTs; wf\text{-prog } wf\text{-md } P \rrbracket \implies \text{is-type } P T$

**lemma** *sees-field-is-type*:  
 $\llbracket P \vdash C \text{ sees } F:T (fm) \text{ in } D; wf\text{-prog } wf\text{-md } P \rrbracket \implies \text{is-type } P T$   
**by**(fastforce simp: sees-field-def  
 elim: has-fields-types map-of-SomeD[OF map-of-remap-SomeD])

**lemma** *wf-has-field-mono2*:  
 assumes wf: wf-prog wf-md  $P$

```

    and has:  $P \vdash C \text{ has } F:T \text{ (fm) in } E$ 
    shows  $\llbracket P \vdash C \preceq^* D; P \vdash D \preceq^* E \rrbracket \implies P \vdash D \text{ has } F:T \text{ (fm) in } E$ 
  proof(induct rule: rtranclp-induct)
    case base show ?case using has .
  next
    case (step  $D D'$ )
    note  $D_{\text{sub}}D' = \langle P \vdash D \prec^1 D' \rangle$ 
    from  $D_{\text{sub}}D'$  obtain rest where classD:  $\text{class } P \ D = \lfloor (D', \text{rest}) \rfloor$ 
    and DObj:  $D \neq \text{Object}$  by(auto elim!: subcls1.cases)
    from  $D_{\text{sub}}D' \langle P \vdash D' \preceq^* E \rangle$  have  $D_{\text{sub}}E: P \vdash D \preceq^* E$  and  $D_{\text{sub}}E2: (\text{subcls1 } P)^{++} D \ E$ 
    by(rule converse-rtranclp-into-rtranclp rtranclp-into-tranclp2)+
    from wf  $D_{\text{sub}}E2$  have  $D_nE: D \neq E$  by(rule subcls-irrefl)
    from  $D_{\text{sub}}E$  have hasD:  $P \vdash D \text{ has } F:T \text{ (fm) in } E$  by(rule  $\langle P \vdash D \preceq^* E \implies P \vdash D \text{ has } F:T \text{ (fm) in } E \rangle$ )
    then obtain FDTs where hasf:  $P \vdash D \text{ has-fields FDTs}$  and FE:  $\text{map-of FDTs } (F, E) = \lfloor (T, \text{fm}) \rfloor$ 
    unfolding has-field-def by blast
    from hasf show ?case
  proof cases
    case has-fields-Object with DObj show ?thesis by simp
  next
    case (has-fields-rec  $DD' \text{ fs ms FDTs'}$ )
    with classD have [simp]:  $DD' = D' \text{ rest} = (\text{fs}, \text{ms})$ 
    and hasf':  $P \vdash D' \text{ has-fields FDTs'}$ 
    and FDTs:  $\text{FDTs} = \text{map } (\lambda(F, Tm). ((F, D), Tm)) \text{ fs} @ \text{FDTs'}$  by auto
    from FDTs FE  $D_nE$  hasf' show ?thesis by(auto dest: map-of-SomeD simp add: has-field-def)
  qed
qed

lemma wf-has-field-idemp:
   $\llbracket \text{wf-prog wf-md } P; P \vdash C \text{ has } F:T \text{ (fm) in } D \rrbracket \implies P \vdash D \text{ has } F:T \text{ (fm) in } D$ 
  apply(frul has-field-decl-above)
  apply(erule (2) wf-has-field-mono2)
  apply(rule rtranclp.rtrancl-refl)
  done

lemma map-of-remap-conv:
   $\llbracket \text{distinct-fst fs; map-of } (\text{map } (\lambda(F, y). ((F, D), y)) \text{ fs}) (F, D) = \lfloor T \rfloor \rrbracket$ 
   $\implies \text{map-of } (\text{map } (\lambda((F, D), T). (F, D, T)) (\text{map } (\lambda(F, y). ((F, D), y)) \text{ fs})) F = \lfloor (D, T) \rfloor$ 
  apply(induct fs)
  apply auto
  done

lemma has-field-idemp-sees-field:
  assumes wf: wf-prog wf-md  $P$ 
  and has:  $P \vdash D \text{ has } F:T \text{ (fm) in } D$ 
  shows  $P \vdash D \text{ sees } F:T \text{ (fm) in } D$ 
  proof -
    from has obtain FDTs where hasf:  $P \vdash D \text{ has-fields FDTs}$ 
    and FD:  $\text{map-of FDTs } (F, D) = \lfloor (T, \text{fm}) \rfloor$  unfolding has-field-def by blast
    from hasf have map-of  $(\text{map } (\lambda((F, D), T). (F, D, T)) \text{ FDTs}) F = \lfloor (D, T, \text{fm}) \rfloor$ 
    proof cases
      case (has-fields-Object  $D' \text{ fs ms}$ )
      from  $\langle \text{class } P \ \text{Object} = \lfloor (D', \text{fs}, \text{ms}) \rfloor \rangle$  wf
      have wf-cdecl wf-md  $P \ (\text{Object}, D', \text{fs}, \text{ms})$  by(rule class-wf)
    end
  end

```

```

hence distinct-fst fs by(simp add: wf-cdecl-def)
with FD has-fields-Object show ?thesis by(auto intro: map-of-remap-conv simp del: map-map)
next
case (has-fields-rec D' fs ms FDTs')
hence [simp]: FDTs = map ( $\lambda(F, Tm). ((F, D), Tm)$ ) fs @ FDTs'
  and classD: class P D =  $\lfloor(D', fs, ms)\rfloor$  and DnObj: D  $\neq$  Object
  and hasf': P  $\vdash$  D' has-fields FDTs' by auto
from  $\langle \text{class } P \ D = \lfloor(D', fs, ms)\rfloor \rangle$  wf
have wf-cdecl wf-md P (D, D', fs, ms) by(rule class-wf)
hence distinct-fst fs by(simp add: wf-cdecl-def)
moreover have map-of FDTs' (F, D) = None
proof(rule ccontr)
  assume map-of FDTs' (F, D)  $\neq$  None
  then obtain T' fm' where map-of FDTs' (F, D) =  $\lfloor(T', fm')\rfloor$  by(auto)
  with hasf' have P  $\vdash$  D'  $\preceq^*$  D by(auto dest!: map-of-SomeD intro: has-fields-decl-above)
  with classD DnObj have (subcls1 P)++ D D
    by(auto intro: subcls1.intros rtranclp-into-tranclp2)
  with wf show False by(auto dest: subcls-irrefl)
qed
ultimately show ?thesis using FD hasf'
  by(auto simp add: map-add-Some-iff intro: map-of-remap-conv simp del: map-map)
qed
with hasf show ?thesis unfolding sees-field-def by blast
qed

```

**lemma** *has-fields-distinct*:

```

assumes wf: wf-prog wf-md P
and P  $\vdash$  C has-fields FDTs
shows distinct (map fst FDTs)
using  $\langle P \vdash C \text{ has-fields } FDTs \rangle$ 
proof(induct)
case (has-fields-Object D fs ms FDTs)
have eq: map (fst  $\circ$  ( $\lambda(F, y). ((F, Object), y))) fs$  = map (( $\lambda F. (F, Object)$ )  $\circ$  fst) fs by(auto)
from  $\langle \text{class } P \ \text{Object} = \lfloor(D, fs, ms)\rfloor \rangle$  wf
have wf-cdecl wf-md P (Object, D, fs, ms) by(rule class-wf)
hence distinct (map fst fs) by(simp add: wf-cdecl-def distinct-fst-def)
hence distinct (map (fst  $\circ$  ( $\lambda(F, y). ((F, Object), y))) fs)$ 
  unfolding eq distinct-map by(auto intro: comp-inj-on inj-onI)
thus ?case using  $\langle FDTs = \text{map } (\lambda(F, T). ((F, Object), T)) fs \rangle$  by(simp)
next
case (has-fields-rec C D fs ms FDTs FDTs')
have eq: map (fst  $\circ$  ( $\lambda(F, y). ((F, C), y))) fs$  = map (( $\lambda F. (F, C)$ )  $\circ$  fst) fs by(auto)
from  $\langle \text{class } P \ C = \lfloor(D, fs, ms)\rfloor \rangle$  wf
have wf-cdecl wf-md P (C, D, fs, ms) by(rule class-wf)
hence distinct (map fst fs) by(simp add: wf-cdecl-def distinct-fst-def)
hence distinct (map (fst  $\circ$  ( $\lambda(F, y). ((F, C), y))) fs)$ 
  unfolding eq distinct-map by(auto intro: comp-inj-on inj-onI)
moreover from  $\langle \text{class } P \ C = \lfloor(D, fs, ms)\rfloor \rangle$   $\langle C \neq \text{Object} \rangle$ 
have P  $\vdash$  C  $\prec^1$  D by(rule subcls1.intros)
with  $\langle P \vdash D \text{ has-fields } FDTs \rangle$ 
have (fst  $\circ$  ( $\lambda(F, y). ((F, C), y)))$  ‘set fs  $\cap$  fst ‘set FDTs = {}
  by(auto dest: subcls-notin-has-fields)
ultimately show ?case using  $\langle FDTs' = \text{map } (\lambda(F, T). ((F, C), T)) fs @ FDTs \rangle$   $\langle \text{distinct (map fst FDTs)} \rangle$  by simp

```

qed

### 3.12.4 Code generation

code-pred

(modes:  $i \Rightarrow i \Rightarrow i \Rightarrow \text{bool}$ )  
 [inductify]  
 wf-overriding  
 .

Separate subclass acyclicity from class declaration check. Otherwise, cyclic class hierarchies might lead to non-termination as *Methods* recurses over the class hierarchy.

**definition** *acyclic-class-hierarchy* :: 'm prog  $\Rightarrow$  bool

where

*acyclic-class-hierarchy*  $P \longleftrightarrow$   
 $(\forall (C, D, fs, ml) \in \text{set } (\text{classes } P). C \neq \text{Object} \longrightarrow \neg P \vdash D \preceq^* C)$

**definition** *wf-cdecl'* :: 'm wf-mdecl-test  $\Rightarrow$  'm prog  $\Rightarrow$  'm cdecl  $\Rightarrow$  bool

where

*wf-cdecl'* wf-md  $P = (\lambda(C, (D, fs, ms)).$   
 $(\forall f \in \text{set } fs. \text{wf-fdecl } P f) \wedge \text{distinct-fst } fs \wedge$   
 $(\forall m \in \text{set } ms. \text{wf-mdecl } P C m) \wedge$   
 $\text{distinct-fst } ms \wedge$   
 $(C \neq \text{Object} \longrightarrow \text{is-class } P D \wedge (\forall m \in \text{set } ms. \text{wf-overriding } P D m)) \wedge$   
 $(C = \text{Thread} \longrightarrow (\exists m. (\text{run}, [], \text{Void}, m) \in \text{set } ms)))$

**lemma** *acyclic-class-hierarchy-code* [code]:

*acyclic-class-hierarchy*  $P \longleftrightarrow (\forall (C, D, fs, ml) \in \text{set } (\text{classes } P). C \neq \text{Object} \longrightarrow \neg \text{subcls}' P D C)$

by(simp add: *acyclic-class-hierarchy-def* *subcls'-def*)

**lemma** *wf-cdecl'-code* [code]:

*wf-cdecl'* wf-md  $P = (\lambda(C, (D, fs, ms)).$   
 $(\forall f \in \text{set } fs. \text{wf-fdecl } P f) \wedge \text{distinct-fst } fs \wedge$   
 $(\forall m \in \text{set } ms. \text{wf-mdecl } P C m) \wedge$   
 $\text{distinct-fst } ms \wedge$   
 $(C \neq \text{Object} \longrightarrow \text{is-class } P D \wedge (\forall m \in \text{set } ms. \text{wf-overriding } P D m)) \wedge$   
 $(C = \text{Thread} \longrightarrow ((\text{run}, [], \text{Void}) \in \text{set } (\text{map } (\lambda(M, Ts, T, b). (M, Ts, T)) ms))))$

by(auto simp add: *wf-cdecl'-def* intro!: ext intro: rev-image-eqI)

**declare** *set-append* [symmetric, code-unfold]

**lemma** *wf-prog-code* [code]:

*wf-prog* wf-md  $P \longleftrightarrow$   
 $\text{acyclic-class-hierarchy } P \wedge$   
 $\text{wf-syscls } P \wedge \text{distinct-fst } (\text{classes } P) \wedge$   
 $(\forall c \in \text{set } (\text{classes } P). \text{wf-cdecl}' \text{ wf-md } P c)$

**unfolding** *wf-prog-def* *wf-cdecl-def* *wf-cdecl'-def* *acyclic-class-hierarchy-def* *split-def*

by blast

end

## 3.13 Properties of external calls in well-formed programs

**theory** *ExternalCallWF*

```

imports
  WellForm
  ../Framework/FWSemantics
begin

lemma external-WT-defs-is-type:
  assumes wf-prog wf-md P and C.M(Ts) :: T
  shows is-class P C and is-type P T set Ts ⊆ types P
using assms by(auto elim: external-WT-defs.cases)

context heap-base begin

lemma WT-red-external-aggr-imp-red-external:
  
$$\llbracket wf\text{-prog } wf\text{-md } P; (ta, va, h') \in red\text{-external-aggr } P \text{ } t \text{ } a \text{ } M \text{ } vs \text{ } h; P, h \vdash a.M(vs) : U; P, h \vdash t \sqrt{t} \rrbracket$$


$$\implies P, t \vdash \langle a.M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle$$

apply(drule tconfD)
apply(erule external-WT'.cases)
apply(clarsimp)
apply(drule (1) sees-wf-native)
apply(erule external-WT-defs.cases)
apply(case-tac [!] hT)
apply(auto 4 4 simp add: red-external-aggr-def widen-Class intro: red-external.intros heap-base.red-external.intros where
addr2thread-id=addr2thread-id and thread-id2addr=thread-id2addr and spurious-wakeups=True and
empty-heap=empty-heap and allocate=allocate and typeof-addr=typeof-addr and heap-read=heap-read
and heap-write=heap-write heap-base.red-external.intros where addr2thread-id=addr2thread-id and
thread-id2addr=thread-id2addr and spurious-wakeups=False and empty-heap=empty-heap and al-
locate=allocate and typeof-addr=typeof-addr and heap-read=heap-read and heap-write=heap-write
split: if-split-asm dest: sees-method-decl-above)
done

lemma WT-red-external-list-conv:
  
$$\llbracket wf\text{-prog } wf\text{-md } P; P, h \vdash a.M(vs) : U; P, h \vdash t \sqrt{t} \rrbracket$$


$$\implies P, t \vdash \langle a.M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle \longleftrightarrow (ta, va, h') \in red\text{-external-aggr } P \text{ } t \text{ } a \text{ } M \text{ } vs \text{ } h$$

by(blast intro: WT-red-external-aggr-imp-red-external red-external-imp-red-external-aggr)

lemma red-external-new-thread-sees:
  
$$\llbracket wf\text{-prog } wf\text{-md } P; P, t \vdash \langle a.M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle; NewThread \text{ } t' \text{ } (C, M', a') \text{ } h'' \in set \{ta\}_t \rrbracket$$


$$\implies typeof\text{-addr } h' \text{ } a' = \lfloor Class\text{-type } C \rfloor \wedge (\exists T \text{ } meth \text{ } D. P \vdash C \text{ } sees \text{ } M':[] \rightarrow T = \lfloor meth \rfloor \text{ } in \text{ } D)$$

by(fastforce elim!: red-external.cases simp add: widen-Class ta-upd-simps dest: sub-Thread-sees-run)

end

```

### 3.13.1 Preservation of heap conformance

```

context heap-conf-read begin

```

```

lemma hconf-heap-copy-loc-mono:
  assumes heap-copy-loc a a' al h obs h'
  and hconf h
  and P, h ⊢ a@al : T P, h ⊢ a'@al : T
  shows hconf h'
proof –
  from ⟨heap-copy-loc a a' al h obs h'⟩ obtain v
  where read: heap-read h a al v

```

and write: heap-write  $h$   $a'$   $al$   $v$   $h'$  by cases auto  
 from read  $\langle P, h \vdash a@al : T \rangle \langle hconf\ h \rangle$  have  $P, h \vdash v : \leq T$   
 by(rule heap-read-conf)  
 with write  $\langle hconf\ h \rangle \langle P, h \vdash a'@al : T \rangle$  show ?thesis  
 by(rule hconf-heap-write-mono)  
 qed

lemma hconf-heap-copies-mono:

assumes heap-copies  $a$   $a'$  als  $h$  obs  $h'$   
 and hconf  $h$   
 and list-all2  $(\lambda al\ T.\ P, h \vdash a@al : T)$  als  $Ts$   
 and list-all2  $(\lambda al\ T.\ P, h \vdash a'@al : T)$  als  $Ts$   
 shows hconf  $h'$   
 using assms  
 proof(induct arbitrary:  $Ts$ )  
 case Nil thus ?case by simp  
 next  
 case (Cons  $al\ h\ ob\ h'$  als obs  $h''$ )  
 note step =  $\langle heap-copy-loc\ a\ a'\ al\ h\ ob\ h' \rangle$   
 from  $\langle list-all2\ (\lambda al\ T.\ P, h \vdash a@al : T)\ (al\ \# \text{ als})\ Ts \rangle$   
 obtain  $T\ Ts'$  where [simp]:  $Ts = T \# Ts'$   
 and  $P, h \vdash a@al : T$  list-all2  $(\lambda al\ T.\ P, h \vdash a@al : T)$  als  $Ts'$   
 by(auto simp add: list-all2-Cons1)  
 from  $\langle list-all2\ (\lambda al\ T.\ P, h \vdash a'@al : T)\ (al\ \# \text{ als})\ Ts \rangle$   
 have  $P, h \vdash a'@al : T$  list-all2  $(\lambda al\ T.\ P, h \vdash a'@al : T)$  als  $Ts'$  by simp-all  
 from step  $\langle hconf\ h \rangle \langle P, h \vdash a@al : T \rangle \langle P, h \vdash a'@al : T \rangle$   
 have hconf  $h'$  by(rule hconf-heap-copy-loc-mono)  
 moreover from step have  $h \trianglelefteq h'$  by(rule hext-heap-copy-loc)  
 from  $\langle list-all2\ (\lambda al\ T.\ P, h \vdash a@al : T)\ als\ Ts' \rangle$   
 have list-all2  $(\lambda al\ T.\ P, h' \vdash a@al : T)$  als  $Ts'$   
 by(rule list-all2-mono)(rule addr-loc-type-hext-mono[OF -  $\langle h \trianglelefteq h' \rangle$ ])  
 moreover from  $\langle list-all2\ (\lambda al\ T.\ P, h \vdash a'@al : T)\ als\ Ts' \rangle$   
 have list-all2  $(\lambda al\ T.\ P, h' \vdash a'@al : T)$  als  $Ts'$   
 by(rule list-all2-mono)(rule addr-loc-type-hext-mono[OF -  $\langle h \trianglelefteq h' \rangle$ ])  
 ultimately show ?case by(rule Cons)  
 qed

lemma hconf-heap-clone-mono:

assumes heap-clone  $P\ h\ a\ h'$  res  
 and hconf  $h$   
 shows hconf  $h'$   
 using  $\langle heap-clone\ P\ h\ a\ h'\ res \rangle$   
 proof cases  
 case CloneFail thus ?thesis using  $\langle hconf\ h \rangle$   
 by(fastforce intro: hconf-heap-ops-mono dest: typeof-addr-is-type)  
 next  
 case (ObjClone  $C\ h''\ a'\ FDTs\ obs$ )  
 note FDTs =  $\langle P \vdash C\ has-fields\ FDTs \rangle$   
 let ?als = map  $(\lambda((F, D), Tfm).\ CField\ D\ F)\ FDTs$   
 let ?Ts = map  $(\lambda(FD, T).\ fst\ (the\ (map-of\ FDTs\ FD)))\ FDTs$   
 note  $\langle heap-copies\ a\ a'\ ?als\ h''\ obs\ h' \rangle$   
 moreover from  $\langle typeof-addr\ h\ a = \lfloor Class-type\ C \rfloor \rangle \langle hconf\ h \rangle$  have is-class  $P\ C$   
 by(auto dest: typeof-addr-is-type)  
 from  $\langle (h'', a') \in allocate\ h\ (Class-type\ C) \rangle$  have  $h \trianglelefteq h''$  hconf  $h''$



```

  by(rule hext-heap-ops hconf-allocate-mono)+(simp-all add: ⟨hconf h⟩ ⟨is-class P C⟩)
note ⟨hconf h'⟩
moreover
from ⟨typeof-addr h a = [Class-type C]⟩ FDTs
have list-all2 (λal T. P, h ⊢ a@al : T) ?als ?Ts
  unfolding list-all2-map1 list-all2-map2 list-all2-same
  by(fastforce intro: addr-loc-type.intros simp add: has-field-def dest: weak-map-of-SomeI)
hence list-all2 (λal T. P, h' ⊢ a@al : T) ?als ?Ts
  by(rule list-all2-mono)(rule addr-loc-type-hext-mono[OF - ⟨h ≤ h'⟩])
moreover from ⟨(h'', a') ∈ allocate h (Class-type C)⟩ ⟨is-class P C⟩
have typeof-addr h'' a' = [Class-type C] by(auto dest: allocate-SomeD)
with FDTs have list-all2 (λal T. P, h'' ⊢ a'@al : T) ?als ?Ts
  unfolding list-all2-map1 list-all2-map2 list-all2-same
  by(fastforce intro: addr-loc-type.intros simp add: has-field-def dest: weak-map-of-SomeI)
ultimately have hconf h' by(rule hconf-heap-copies-mono)
thus ?thesis using ObjClone by simp
next
case (ArrClone T n h'' a' FDTs obs)
let ?als = map (λ((F, D), Tfm). CField D F) FDTs @ map ACell [0..<n]
let ?Ts = map (λ(FD, T). fst (the (map-of FDTs FD))) FDTs @ replicate n T
note ⟨heap-copies a a' ?als h'' obs h'⟩
moreover from ⟨typeof-addr h a = [Array-type T n]⟩ ⟨hconf h⟩ have is-type P (T[])
  by(auto dest: typeof-addr-is-type)
from ⟨(h'', a') ∈ allocate h (Array-type T n)⟩ have h ≤ h'' hconf h''
  by(rule hext-heap-ops hconf-allocate-mono)+(simp-all add: ⟨hconf h⟩ ⟨is-type P (T[])⟩[simplified])
note ⟨hconf h''⟩
moreover from ⟨h ≤ h''⟩ ⟨typeof-addr h a = [Array-type T n]⟩
have type'a: typeof-addr h'' a = [Array-type T n] by(auto intro: hext-arrD)
note FDTs = ⟨P ⊢ Object has-fields FDTs⟩
from type'a FDTs have list-all2 (λal T. P, h'' ⊢ a@al : T) ?als ?Ts
  by(fastforce intro: list-all2-all-nthI addr-loc-type.intros simp add: has-field-def distinct-fst-def list-all2-append
list-all2-map1 list-all2-map2 list-all2-same dest: weak-map-of-SomeI)
moreover from ⟨(h'', a') ∈ allocate h (Array-type T n)⟩ ⟨is-type P (T[])⟩
have typeof-addr h'' a' = [Array-type T n] by(auto dest: allocate-SomeD)
hence list-all2 (λal T. P, h'' ⊢ a'@al : T) ?als ?Ts using FDTs
  by(fastforce intro: list-all2-all-nthI addr-loc-type.intros simp add: has-field-def distinct-fst-def list-all2-append
list-all2-map1 list-all2-map2 list-all2-same dest: weak-map-of-SomeI)
ultimately have hconf h' by(rule hconf-heap-copies-mono)
thus ?thesis using ArrClone by simp
qed

theorem external-call-hconf:
  assumes major: P, t ⊢ ⟨a.M(vs), h⟩ -ta→ext ⟨va, h'⟩
  and minor: P, h ⊢ a.M(vs) : U hconf h
  shows hconf h'
using major minor
by cases(fastforce intro: hconf-heap-clone-mono)+

end

context heap-base begin

primrec conf-extRet :: 'm prog ⇒ 'heap ⇒ 'addr extCallRet ⇒ ty ⇒ bool where
  conf-extRet P h (RetVal v) T = (P, h ⊢ v :≤ T)

```

| *conf-extRet*  $P\ h\ (RetExc\ a)\ T = (P, h \vdash Addr\ a : \leq Class\ Throwable)$   
 | *conf-extRet*  $P\ h\ RetStaySame\ T = True$

**end**

**context** *heap-conf* **begin**

**lemma** *red-external-conf-extRet*:

**assumes** *wf*: *wf-prog wf-md P*

**shows**  $\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext\ \langle va, h' \rangle; P, h \vdash a \cdot M(vs) : U; hconf\ h; preallocated\ h; P, h \vdash t \sqrt{t} \rrbracket$

$\implies conf-extRet\ P\ h'\ va\ U$

**using** *wf* **apply** –

**apply**(*frule red-external-heap*)

**apply**(*drule* (1) *preallocated-heap*)

**apply**(*auto elim!*: *red-external.cases external-WT'.cases external-WT-defs-cases dest!*: *sees-wf-native*[*OF wf*])

**apply**(*auto simp add*: *conf-def tconf-def intro*: *xcpt-subcls-Throwable dest!*: *heap-heap-write*)

**apply**(*case-tac hT*)

**apply**(*auto* 4 4 *dest!*: *typeof-addr-heap-clone dest*: *typeof-addr-is-type intro*: *widen-array-object subcls-C-Object*)

**done**

**end**

### 3.13.2 Progress theorems for external calls

**context** *heap-progress* **begin**

**lemma** *heap-copy-loc-progress*:

**assumes** *hconf*: *hconf h*

**and** *alconfa*:  $P, h \vdash a @ al : T$

**and** *alconfa'*:  $P, h \vdash a' @ al : T$

**shows**  $\exists v\ h'. heap-copy-loc\ a\ a'\ al\ h\ ([ReadMem\ a\ al\ v,\ WriteMem\ a'\ al\ v])\ h' \wedge P, h \vdash v : \leq T \wedge hconf\ h'$

**proof** –

**from** *heap-read-total*[*OF hconf alconfa*]

**obtain** *v* **where** *heap-read h a al v P, h*  $\vdash v : \leq T$  **by** *blast*

**moreover from** *heap-write-total*[*OF hconf alconfa'*  $\langle P, h \vdash v : \leq T \rangle$ ] **obtain** *h'* **where** *heap-write h a' al v h'* ..

**moreover hence** *hconf h'* **using** *hconf alconfa'*  $\langle P, h \vdash v : \leq T \rangle$  **by**(*rule hconf-heap-write-mono*)

**ultimately show** *?thesis* **by**(*blast intro*: *heap-copy-loc.intros*)

**qed**

**lemma** *heap-copies-progress*:

**assumes** *hconf* *h*

**and** *list-all2* ( $\lambda al\ T.\ P, h \vdash a @ al : T$ ) *als Ts*

**and** *list-all2* ( $\lambda al\ T.\ P, h \vdash a' @ al : T$ ) *als Ts*

**shows**  $\exists vs\ h'. heap-copies\ a\ a'\ als\ h\ (concat\ (map\ (\lambda(al, v).\ [ReadMem\ a\ al\ v,\ WriteMem\ a'\ al\ v])\ (zip\ als\ vs)))\ h' \wedge hconf\ h'$

**using** *assms*

**proof**(*induct als arbitrary*: *h Ts*)

**case** *Nil* **thus** *?case* **by**(*auto intro*: *heap-copies.Nil*)

**next**

```

case (Cons al als)
from  $\langle \text{list-all2 } (\lambda al T. P, h \vdash a @ al : T) (al \# als) Ts \rangle$ 
obtain  $T' Ts'$  where [simp]:  $Ts = T' \# Ts'$ 
  and  $P, h \vdash a @ al : T' \text{ list-all2 } (\lambda al T. P, h \vdash a @ al : T) als Ts'$ 
  by (auto simp add: list-all2-Cons1)
from  $\langle \text{list-all2 } (\lambda al T. P, h \vdash a' @ al : T) (al \# als) Ts \rangle$ 
have  $P, h \vdash a' @ al : T'$  and  $\text{list-all2 } (\lambda al T. P, h \vdash a' @ al : T) als Ts'$  by simp-all
from  $\langle hconf h \rangle \langle P, h \vdash a @ al : T' \rangle \langle P, h \vdash a' @ al : T' \rangle$ 
obtain  $v h'$  where heap-copy-loc  $a a' al h$  [ReadMem  $a al v$ , WriteMem  $a' al v$ ]  $h'$ 
  and  $hconf h'$  by (fastforce dest: heap-copy-loc-progress)
moreover hence  $h \sqsubseteq h'$  by  $-(\text{rule } hext\text{-heap-copy-loc})$ 
{
  note  $\langle hconf h' \rangle$ 
  moreover from  $\langle \text{list-all2 } (\lambda al T. P, h \vdash a @ al : T) als Ts' \rangle$ 
  have  $\text{list-all2 } (\lambda al T. P, h' \vdash a @ al : T) als Ts'$ 
    by (rule list-all2-mono) (rule addr-loc-type-hext-mono [OF -  $\langle h \sqsubseteq h' \rangle$ ])
  moreover from  $\langle \text{list-all2 } (\lambda al T. P, h \vdash a' @ al : T) als Ts' \rangle$ 
  have  $\text{list-all2 } (\lambda al T. P, h' \vdash a' @ al : T) als Ts'$ 
    by (rule list-all2-mono) (rule addr-loc-type-hext-mono [OF -  $\langle h \sqsubseteq h' \rangle$ ])
  ultimately have  $\exists vs h''. \text{heap-copies } a a' als h' (\text{concat } (\text{map } (\lambda(al, v). [\text{ReadMem } a al v, \text{WriteMem } a' al v]) (\text{zip } als vs)))) h'' \wedge hconf h''$ 
    by (rule Cons) }
  then obtain  $vs h''$ 
    where heap-copies  $a a' als h' (\text{concat } (\text{map } (\lambda(al, v). [\text{ReadMem } a al v, \text{WriteMem } a' al v]) (\text{zip } als vs)))) h''$ 
    and  $hconf h''$  by blast
  ultimately
    have heap-copies  $a a' (al \# als) h ([\text{ReadMem } a al v, \text{WriteMem } a' al v] @ (\text{concat } (\text{map } (\lambda(al, v). [\text{ReadMem } a al v, \text{WriteMem } a' al v]) (\text{zip } als vs)))) h''$ 
      by  $-(\text{rule } \text{heap-copies.Cons})$ 
    also have  $[\text{ReadMem } a al v, \text{WriteMem } a' al v] @ (\text{concat } (\text{map } (\lambda(al, v). [\text{ReadMem } a al v, \text{WriteMem } a' al v]) (\text{zip } als vs)))) =$ 
       $(\text{concat } (\text{map } (\lambda(al, v). [\text{ReadMem } a al v, \text{WriteMem } a' al v]) (\text{zip } (al \# als) (v \# vs))))$  by
simp
    finally show ?case using  $\langle hconf h'' \rangle$  by blast
qed

```

**lemma** *heap-clone-progress*:

```

assumes wf: wf-prog wf-md  $P$ 
and typea: typeof-addr  $h a = \lfloor hT \rfloor$ 
and hconf:  $hconf h$ 
shows  $\exists h' res. \text{heap-clone } P h a h' res$ 
proof –
  from typea hconf have is-htype  $P hT$  by (rule typeof-addr-is-type)
  show ?thesis
  proof (cases allocate h hT = \{\})
    case True
      with typea CloneFail [of h a hT P]
      show ?thesis by auto
    next
      case False
      then obtain  $h' a'$  where new:  $(h', a') \in \text{allocate } h hT$  by (rule not-empty-pairE)
      hence  $h \sqsubseteq h'$  by (rule hext-allocate)
      have  $hconf h'$  using new hconf  $\langle \text{is-htype } P hT \rangle$  by (rule hconf-allocate-mono)

```

```

show ?thesis
proof(cases hT)
  case [simp]: (Class-type C)
  from <is-htype P hT> have is-class P C by simp
  from wf-Fields-Ex[OF wf this]
  obtain FDTs where FDTs: P ⊢ C has-fields FDTs ..
  let ?als = map (λ((F, D), Tfm). CField D F) FDTs
  let ?Ts = map (λ(FD, T). fst (the (map-of FDTs FD))) FDTs
  from typea FDTs have list-all2 (λal T. P, h ⊢ a@al : T) ?als ?Ts
    unfolding list-all2-map1 list-all2-map2 list-all2-same
    by(fastforce intro: addr-loc-type.intros simp add: has-field-def dest: weak-map-of-SomeI)
  hence list-all2 (λal T. P, h' ⊢ a@al : T) ?als ?Ts
    by(rule list-all2-mono)(simp add: addr-loc-type-hext-mono[OF - <h ≤ h'>] split-def)
  moreover from new <is-class P C>
  have typeof-addr h' a' = [Class-type C] by(auto dest: allocate-SomeD)
  with FDTs have list-all2 (λal T. P, h' ⊢ a'@al : T) ?als ?Ts
    unfolding list-all2-map1 list-all2-map2 list-all2-same
    by(fastforce intro: addr-loc-type.intros map-of-SomeI simp add: has-field-def dest: weak-map-of-SomeI)
  ultimately obtain obs h'' where heap-copies a a' ?als h' obs h'' hconf h''
    by(blast dest: heap-copies-progress[OF <hconf h'>])
  with typea new FDTs ObjClone[of h a C h' a' P FDTs obs h'']
  show ?thesis by auto
next
case [simp]: (Array-type T n)
from wf obtain FDTs where FDTs: P ⊢ Object has-fields FDTs
  by(blast dest: wf-Fields-Ex is-class-Object)
let ?als = map (λ((F, D), Tfm). CField D F) FDTs @ map ACell [0..<n]
let ?Ts = map (λ(FD, T). fst (the (map-of FDTs FD))) FDTs @ replicate n T
from <h ≤ h'> typea have type'a: typeof-addr h' a = [Array-type T n]
  by(auto intro: hext-arrD)
from type'a FDTs have list-all2 (λal T. P, h' ⊢ a@al : T) ?als ?Ts
  by(fastforce intro: list-all2-all-nthI addr-loc-type.intros simp add: has-field-def list-all2-append
list-all2-map1 list-all2-map2 list-all2-same dest: weak-map-of-SomeI)
moreover from new <is-htype P hT>
have typeof-addr h' a' = [Array-type T n]
  by(auto dest: allocate-SomeD)
hence list-all2 (λal T. P, h' ⊢ a'@al : T) ?als ?Ts using FDTs
  by(fastforce intro: list-all2-all-nthI addr-loc-type.intros simp add: has-field-def list-all2-append
list-all2-map1 list-all2-map2 list-all2-same dest: weak-map-of-SomeI)
ultimately obtain obs h'' where heap-copies a a' ?als h' obs h'' hconf h''
  by(blast dest: heap-copies-progress[OF <hconf h'>])
with typea new FDTs ArrClone[of h a T n h' a' P FDTs obs h'']
show ?thesis by auto
qed
qed
qed

theorem external-call-progress:
  assumes wf: wf-prog wf-md P
  and wt: P, h ⊢ a.M(vs) : U
  and hconf: hconf h
  shows ∃ ta va h'. P, t ⊢ ⟨a.M(vs), h⟩ -ta→ext ⟨va, h'⟩
proof -
  note [simp del] = split-paired-Ex

```

```

from wt obtain hT Ts Ts' D
  where T: typeof-addr h a =  $\lfloor hT \rfloor$  and Ts: map typeofh vs = map Some Ts
  and P ⊢ class-type-of hT sees M:Ts'→U = Native in D and subTs: P ⊢ Ts [≤] Ts'
  unfolding external-WT'-iff by blast
from wf ⟨P ⊢ class-type-of hT sees M:Ts'→U = Native in D⟩
have D·M(Ts') :: U by (rule sees-wf-native)
moreover from ⟨P ⊢ class-type-of hT sees M:Ts'→U = Native in D⟩
have P ⊢ ty-of-hType hT ≤ Class D
  by (cases hT) (auto dest: sees-method-decl-above intro: widen-trans widen-array-object)
ultimately show ?thesis using T Ts subTs
proof cases
  assume [simp]: D = Object M = clone Ts' = [] U = Class Object
  from heap-clone-progress[OF wf T hconf] obtain h' res where heap-clone P h a h' res by blast
  thus ?thesis using subTs Ts by (cases res) (auto intro: red-external.intros)
qed (auto simp add: widen-Class intro: red-external.intros)
qed
end

```

### 3.13.3 Lemmas for preservation of deadlocked threads

**context** *heap-progress* **begin**

**lemma** *red-external-wt-hconf-hext*:

```

assumes wf: wf-prog wf-md P
and red: P, t ⊢ ⟨a·M(vs), h⟩ -ta→ext ⟨va, h⟩
and hext: h'' ≤ h
and wt: P, h'' ⊢ a·M(vs) : U
and tconf: P, h'' ⊢ t √t
and hconf: hconf h''
shows ∃ ta' va' h'''. P, t ⊢ ⟨a·M(vs), h''⟩ -ta'→ext ⟨va', h'''⟩ ∧
  collect-locks {ta}l = collect-locks {ta'}l ∧
  collect-cond-actions {ta}c = collect-cond-actions {ta'}c ∧
  collect-interrupts {ta}i = collect-interrupts {ta'}i

```

**using** *red wt hext*

**proof cases**

```

case (RedClone obs a')
from wt obtain hT C Ts Ts' D
  where T: typeof-addr h'' a =  $\lfloor hT \rfloor$ 
  unfolding external-WT'-iff by blast
from heap-clone-progress[OF wf T hconf]
obtain h''' res where heap-clone P h'' a h''' res by blast
thus ?thesis using RedClone
  by (cases res) (fastforce intro: red-external.intros) +

```

**next**

```

case RedCloneFail
from wt obtain hT Ts Ts'
  where T: typeof-addr h'' a =  $\lfloor hT \rfloor$ 
  unfolding external-WT'-iff by blast
from heap-clone-progress[OF wf T hconf]
obtain h''' res where heap-clone P h'' a h''' res by blast
thus ?thesis using RedCloneFail
  by (cases res) (fastforce intro: red-external.intros) +

```

**qed** (*fastforce simp add: ta-upd-simps elim!: external-WT'.cases intro: red-external.intros[simplified]*)

*dest: typeof-addr-hext-mono*)+

**lemma** *red-external-wf-red*:

**assumes** *wf*: *wf-prog wf-md P*

**and** *red*:  $P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle$

**and** *tconf*:  $P, h \vdash t \sqrt{t}$

**and** *hconf*: *hconf h*

**and** *wst*:  $wset\ s\ t = None \vee (M = wait \wedge (\exists w. wset\ s\ t = \lfloor PostWS\ w \rfloor))$

**obtains**  $ta' va' h''$

**where**  $P, t \vdash \langle a \cdot M(vs), h \rangle -ta' \rightarrow ext \langle va', h'' \rangle$

**and** *final-thread.actions-ok* *final s t ta'*  $\vee$  *final-thread.actions-ok'* *s t ta'*  $\wedge$  *final-thread.actions-subset* *ta' ta*

**proof**(*atomize-elim*)

**let** *?a-t* = *thread-id2addr t*

**let** *?t-a* = *addr2thread-id a*

**from** *tconf* **obtain** *C* **where** *ht*: *typeof-addr h ?a-t* =  $\lfloor Class-type\ C \rfloor$

**and** *sub*:  $P \vdash C \preceq^* Thread$  **by**(*fastforce dest: tconfD*)

**show**  $\exists ta' va' h'. P, t \vdash \langle a \cdot M(vs), h \rangle -ta' \rightarrow ext \langle va', h' \rangle \wedge (final-thread.actions-ok\ final\ s\ t\ ta' \vee final-thread.actions-ok'\ s\ t\ ta' \wedge final-thread.actions-subset\ ta'\ ta)$

**proof**(*cases final-thread.actions-ok' s t ta*)

**case** *True*

**have** *final-thread.actions-subset ta ta* **by**(*rule final-thread.actions-subset-refl*)

**with** *True red* **show** *?thesis* **by** *blast*

**next**

**case** *False*

**note** [*simp*] = *final-thread.actions-ok'-iff lock-ok-las'-def final-thread.cond-action-oks'-subset-Join final-thread.actions-subset-iff ta-upd-simps collect-cond-actions-def collect-interrupts-def*

**note** [*rule del*] = *subsetI*

**note** [*intro*] = *collect-locks'-subset-collect-locks red-external.intros[simplified]*

**show** *?thesis*

**proof**(*cases wset s t*)

**case** [*simp*]: (*Some w*)

**with** *wst* **obtain** *w'* **where** [*simp*]:  $w = PostWS\ w'\ M = wait$  **by** *auto*

**from** *red* **have** [*simp*]:  $vs = []$  **by**(*auto elim: red-external.cases*)

**show** *?thesis*

**proof**(*cases w'*)

**case** [*simp*]: *WSWokenUp*

**let** *?ta'* =  $\{\{ WokenUp, ClearInterrupt\ t, ObsInterrupted\ t \}\}$

**have** *final-thread.actions-ok' s t ?ta'* **by**(*simp add: wset-actions-ok-def*)

**moreover** **have** *final-thread.actions-subset ?ta' ta*

**by**(*auto simp add: collect-locks'-def finfun-upd-apply*)

**moreover** **from** *RedWaitInterrupted*

**have**  $\exists va\ h'. P, t \vdash \langle a \cdot M(vs), h \rangle -?ta' \rightarrow ext \langle va, h' \rangle$  **by** *auto*

**ultimately** **show** *?thesis* **by** *blast*

**next**

**case** [*simp*]: *WSNotified*

**let** *?ta'* =  $\{\{ Notified \}\}$

**have** *final-thread.actions-ok' s t ?ta'* **by**(*simp add: wset-actions-ok-def*)

**moreover** **have** *final-thread.actions-subset ?ta' ta*

**by**(*auto simp add: collect-locks'-def finfun-upd-apply*)

**moreover** **from** *RedWaitNotified*

```

    have  $\exists va\ h'. P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow ext \langle va, h' \rangle$  by auto
    ultimately show ?thesis by blast
qed
next
case None

from red False show ?thesis
proof cases
  case (RedNewThread C)
  note  $ta = \langle ta = \{\{NewThread\ ?t-a\ (C, run, a)\ h, ThreadStart\ ?t-a\}\} \rangle$ 
  let  $?ta' = \{\{ThreadExists\ ?t-a\ True\}\}$ 
  from  $ta$  False None have  $final-thread.actions-ok'\ s\ t\ ?ta'$  by (auto)
  moreover from  $ta$  have  $final-thread.actions-subset\ ?ta'\ ta$  by (auto)
  ultimately show ?thesis using RedNewThread by (fastforce)
next
case RedNewThreadFail
then obtain  $va'\ h'\ x$  where  $P, t \vdash \langle a \cdot M(vs), h \rangle - \{\{NewThread\ ?t-a\ x\ h', ThreadStart\ ?t-a\}\} \rightarrow ext$ 
 $\langle va', h' \rangle$ 
  by (fastforce)
  moreover from  $\langle ta = \{\{ThreadExists\ ?t-a\ True\}\} \rangle$  False None
  have  $final-thread.actions-ok'\ s\ t\ \{\{NewThread\ ?t-a\ x\ h', ThreadStart\ ?t-a\}\}$  by (auto)
  moreover from  $\langle ta = \{\{ThreadExists\ ?t-a\ True\}\} \rangle$ 
  have  $final-thread.actions-subset\ \{\{NewThread\ ?t-a\ x\ h', ThreadStart\ ?t-a\}\} ta$  by (auto)
  ultimately show ?thesis by blast
next
case RedJoin
let  $?ta = \{\{IsInterrupted\ t\ True, ClearInterrupt\ t, ObsInterrupted\ t\}\}$ 
  from  $\langle ta = \{\{Join\ (addr2thread-id\ a), IsInterrupted\ t\ False, ThreadJoin\ (addr2thread-id\ a)\}\} \rangle$ 
None False
  have  $t \in interrupts\ s$  by (auto)
  hence  $final-thread.actions-ok\ final\ s\ t\ ?ta$ 
    using None by (auto simp add: final-thread.actions-ok-iff final-thread.cond-action-oks.simps)
  moreover obtain  $va\ h'$  where  $P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta \rightarrow ext \langle va, h' \rangle$  using RedJoinInterrupt
RedJoin by auto
  ultimately show ?thesis by blast
next
case RedJoinInterrupt
  hence False using False None by (auto)
  thus ?thesis ..
next
case RedInterrupt
let  $?ta = \{\{ThreadExists\ (addr2thread-id\ a)\ False\}\}$ 
from RedInterrupt None False
have  $free-thread-id\ (thr\ s)\ (addr2thread-id\ a)$  by (auto simp add: wset-actions-ok-def)
hence  $final-thread.actions-ok\ final\ s\ t\ ?ta$  using None
  by (auto simp add: final-thread.actions-ok-iff final-thread.cond-action-oks.simps)
moreover obtain  $va\ h'$  where  $P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta \rightarrow ext \langle va, h' \rangle$  using RedInterruptInexist
RedInterrupt by auto
  ultimately show ?thesis by blast
next
case RedInterruptInexist
  let  $?ta = \{\{ThreadExists\ (addr2thread-id\ a)\ True, WakeUp\ (addr2thread-id\ a), Interrupt$ 
 $(addr2thread-id\ a), ObsInterrupt\ (addr2thread-id\ a)\}\}$ 
  from RedInterruptInexist None False

```

```

have  $\neg$  free-thread-id (thr s) (addr2thread-id a) by(auto simp add: wset-actions-ok-def)
hence final-thread.actions-ok final s t ?ta using None
by(auto simp add: final-thread.actions-ok-iff final-thread.cond-action-oks.simps wset-actions-ok-def)
moreover obtain va h' where  $P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta \rightarrow ext \langle va, h' \rangle$  using RedInterruptInexist
RedInterrupt by auto
ultimately show ?thesis by blast
next
case (RedIsInterruptedTrue C)
let ?ta' =  $\llbracket IsInterrupted ?t-a False \rrbracket$ 
from RedIsInterruptedTrue False None have  $?t-a \notin interrupts\ s$  by(auto)
hence final-thread.actions-ok' s t ?ta' using None by auto
moreover from RedIsInterruptedTrue have final-thread.actions-subset ?ta' ta by auto
moreover from RedIsInterruptedTrue RedIsInterruptedFalse obtain va h'
  where  $P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow ext \langle va, h' \rangle$  by auto
ultimately show ?thesis by blast
next
case (RedIsInterruptedFalse C)
let ?ta' =  $\llbracket IsInterrupted ?t-a True, ObsInterrupted ?t-a \rrbracket$ 
from RedIsInterruptedFalse have  $?t-a \in interrupts\ s$ 
  using False None by(auto)
hence final-thread.actions-ok final s t ?ta'
  using None by(auto simp add: final-thread.actions-ok-iff final-thread.cond-action-oks.simps)
moreover obtain va h' where  $P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow ext \langle va, h' \rangle$ 
  using RedIsInterruptedFalse RedIsInterruptedTrue by auto
ultimately show ?thesis by blast
next
case RedWaitInterrupt
note ta =  $\langle ta = \llbracket Unlock \rightarrow a, Lock \rightarrow a, IsInterrupted\ t\ True, ClearInterrupt\ t, ObsInterrupted\ t \rrbracket \rangle$ 
from ta False None have hli:  $\neg has-lock (locks\ s\ \$\ a)\ t \vee t \notin interrupts\ s$ 
by(fastforce simp add: lock-actions-ok'-iff finfun-upd-apply split: if-split-asm dest: may-lock-t-may-lock-unlock
dest: has-lock-may-lock)
show ?thesis
proof(cases has-lock (locks s $ a) t)
  case True
  let ?ta' =  $\llbracket Suspend\ a, Unlock \rightarrow a, Lock \rightarrow a, ReleaseAcquire \rightarrow a, IsInterrupted\ t\ False, SyncUnlock\ a \rrbracket$ 
  from True hli have  $t \notin interrupts\ s$  by simp
  with True False have final-thread.actions-ok' s t ?ta' using None
  by(auto simp add: lock-actions-ok'-iff finfun-upd-apply wset-actions-ok-def Cons-eq-append-conv)
  moreover from ta have final-thread.actions-subset ?ta' ta
    by(auto simp add: collect-locks'-def finfun-upd-apply)
  moreover from RedWait RedWaitInterrupt obtain va h' where  $P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow ext \langle va, h' \rangle$ 
  by auto
  ultimately show ?thesis by blast
next
case False
let ?ta' =  $\llbracket UnlockFail \rightarrow a \rrbracket$ 
from False have final-thread.actions-ok' s t ?ta' using None
  by(auto simp add: lock-actions-ok'-iff finfun-upd-apply)
moreover from ta have final-thread.actions-subset ?ta' ta
  by(auto simp add: collect-locks'-def finfun-upd-apply)
moreover from RedWaitInterrupt obtain va h' where  $P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow ext \langle va, h' \rangle$ 
by(fastforce)

```



ultimately show ?thesis by blast  
 qed  
 next  
 case RedWait  
 note  $ta = \langle ta = \{\text{Suspend } a, \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a, \text{ReleaseAcquire} \rightarrow a, \text{IsInterrupted } t \text{ False}, \text{SyncUnlock } a\} \rangle$   
  
 from  $ta \text{ False None}$  have  $hli: \neg \text{has-lock } (\text{locks } s \ \$ \ a) \ t \vee t \in \text{interrupts } s$   
 by(auto simp add: lock-actions-ok'-iff finfun-upd-apply wset-actions-ok-def Cons-eq-append-conv  
 split: if-split-asm dest: may-lock-t-may-lock-unlock-lock-t dest: has-lock-may-lock)  
 show ?thesis  
 proof(cases has-lock (locks s \$ a) t)  
 case True  
 let  $?ta' = \{\text{Unlock} \rightarrow a, \text{Lock} \rightarrow a, \text{IsInterrupted } t \text{ True}, \text{ClearInterrupt } t, \text{ObsInterrupted } t\}$   
 from True hli have  $t \in \text{interrupts } s$  by simp  
 with True False have final-thread.actions-ok final s t ?ta' using None  
 by(auto simp add: final-thread.actions-ok-iff final-thread.cond-action-oks.simps lock-ok-las-def  
 finfun-upd-apply has-lock-may-lock)  
 moreover from RedWait RedWaitInterrupt obtain  $va \ h'$  where  $P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow \text{ext}$   
 $\langle va, h' \rangle$  by auto  
 ultimately show ?thesis by blast  
 next  
 case False  
 let  $?ta' = \{\text{UnlockFail} \rightarrow a\}$   
 from False have final-thread.actions-ok' s t ?ta' using None  
 by(auto simp add: lock-actions-ok'-iff finfun-upd-apply)  
 moreover from ta have final-thread.actions-subset ?ta' ta  
 by(auto simp add: collect-locks'-def finfun-upd-apply)  
 moreover from RedWait RedWaitFail obtain  $va \ h'$  where  $P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow \text{ext}$   
 $\langle va, h' \rangle$  by(fastforce)  
 ultimately show ?thesis by blast  
 qed  
 next  
 case RedWaitFail  
 note  $ta = \langle ta = \{\text{UnlockFail} \rightarrow a\} \rangle$   
 let  $?ta' = \text{if } t \in \text{interrupts } s$   
     then  $\{\text{Unlock} \rightarrow a, \text{Lock} \rightarrow a, \text{IsInterrupted } t \text{ True}, \text{ClearInterrupt } t, \text{ObsInterrupted } t\}$   
     else  $\{\text{Suspend } a, \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a, \text{ReleaseAcquire} \rightarrow a, \text{IsInterrupted } t \text{ False},$   
 SyncUnlock a  $\}$   
 from  $ta \text{ False None}$  have has-lock (locks s \$ a) t  
 by(auto simp add: finfun-upd-apply split: if-split-asm)  
 hence final-thread.actions-ok final s t ?ta' using None  
 by(auto simp add: final-thread.actions-ok-iff final-thread.cond-action-oks.simps lock-ok-las-def  
 finfun-upd-apply has-lock-may-lock wset-actions-ok-def)  
 moreover from RedWaitFail RedWait RedWaitInterrupt  
 obtain  $va \ h'$  where  $P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow \text{ext } \langle va, h' \rangle$   
 by(cases  $t \in \text{interrupts } s$ ) (auto)  
 ultimately show ?thesis by blast  
 next  
 case RedWaitNotified  
 note  $ta = \langle ta = \{\text{Notified}\} \rangle$   
 let  $?ta' = \text{if has-lock } (\text{locks } s \ \$ \ a) \ t$   
     then (if  $t \in \text{interrupts } s$   
         then  $\{\text{Unlock} \rightarrow a, \text{Lock} \rightarrow a, \text{IsInterrupted } t \text{ True}, \text{ClearInterrupt } t, \text{ObsInterrupted } t$

$t\}$   
 $\text{else } \{\text{Suspend } a, \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a, \text{ReleaseAcquire} \rightarrow a, \text{IsInterrupted } t \text{ False},$   
 $\text{SyncUnlock } a\}$   
 $\text{else } \{\text{UnlockFail} \rightarrow a\}$   
**have**  $\text{final-thread.actions-ok final } s \ t \ ?ta' \text{ using None}$   
**by**  $(\text{auto simp add: final-thread.actions-ok-iff final-thread.cond-action-oks.simps lock-ok-las-def}$   
 $\text{finfun-upd-apply has-lock-may-lock wset-actions-ok-def})$   
**moreover from**  $\text{RedWaitNotified RedWait RedWaitInterrupt RedWaitFail}$   
**have**  $\exists va \ h'. P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow \text{ext } \langle va, h' \rangle$  **by**  $\text{auto}$   
**ultimately show**  $?thesis$  **by**  $\text{blast}$   
**next**  
**case**  $\text{RedWaitInterrupted}$   
**note**  $ta = \langle ta = \{\text{WokenUp}, \text{ClearInterrupt } t, \text{ObsInterrupted } t\} \rangle$   
**let**  $?ta' = \text{if has-lock (locks } s \ \$ \ a) \ t$   
 $\text{then (if } t \in \text{interrupts } s$   
 $\text{then } \{\text{Unlock} \rightarrow a, \text{Lock} \rightarrow a, \text{IsInterrupted } t \text{ True}, \text{ClearInterrupt } t, \text{ObsInterrupted}$   
 $t\}$   
 $\text{else } \{\text{Suspend } a, \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a, \text{ReleaseAcquire} \rightarrow a, \text{IsInterrupted } t \text{ False},$   
 $\text{SyncUnlock } a\}$   
 $\text{else } \{\text{UnlockFail} \rightarrow a\}$   
**have**  $\text{final-thread.actions-ok final } s \ t \ ?ta' \text{ using None}$   
**by**  $(\text{auto simp add: final-thread.actions-ok-iff final-thread.cond-action-oks.simps lock-ok-las-def}$   
 $\text{finfun-upd-apply has-lock-may-lock wset-actions-ok-def})$   
**moreover from**  $\text{RedWaitInterrupted RedWait RedWaitInterrupt RedWaitFail}$   
**have**  $\exists va \ h'. P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow \text{ext } \langle va, h' \rangle$  **by**  $\text{auto}$   
**ultimately show**  $?thesis$  **by**  $\text{blast}$   
**next**  
**case**  $\text{RedWaitSpurious}$   
**note**  $ta = \langle ta = \{\text{Unlock} \rightarrow a, \text{Lock} \rightarrow a, \text{ReleaseAcquire} \rightarrow a, \text{IsInterrupted } t \text{ False}, \text{SyncUnlock}$   
 $a\} \rangle$   
**from**  $ta \text{ False None have hli: } \neg \text{has-lock (locks } s \ \$ \ a) \ t \vee t \in \text{interrupts } s$   
**by**  $(\text{auto simp add: lock-actions-ok'-iff finfun-upd-apply wset-actions-ok-def Cons-eq-append-conv}$   
 $\text{split: if-split-asm dest: may-lock-t-may-lock-unlock-lock-t dest: has-lock-may-lock})$   
**show**  $?thesis$   
**proof**  $(\text{cases has-lock (locks } s \ \$ \ a) \ t)$   
**case**  $\text{True}$   
**let**  $?ta' = \{\text{Unlock} \rightarrow a, \text{Lock} \rightarrow a, \text{IsInterrupted } t \text{ True}, \text{ClearInterrupt } t, \text{ObsInterrupted } t\}$   
**from**  $\text{True hli have } t \in \text{interrupts } s$  **by**  $\text{simp}$   
**with**  $\text{True False have final-thread.actions-ok final } s \ t \ ?ta' \text{ using None}$   
**by**  $(\text{auto simp add: final-thread.actions-ok-iff final-thread.cond-action-oks.simps lock-ok-las-def}$   
 $\text{finfun-upd-apply has-lock-may-lock})$   
**moreover from**  $\text{RedWaitInterrupt RedWaitSpurious(1-5)}$   
**obtain**  $va \ h' \text{ where } P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow \text{ext } \langle va, h' \rangle$  **by**  $\text{auto}$   
**ultimately show**  $?thesis$  **by**  $\text{blast}$   
**next**  
**case**  $\text{False}$   
**let**  $?ta' = \{\text{UnlockFail} \rightarrow a\}$   
**from**  $\text{False have final-thread.actions-ok' } s \ t \ ?ta' \text{ using None}$   
**by**  $(\text{auto simp add: lock-actions-ok'-iff finfun-upd-apply})$   
**moreover from**  $ta$  **have**  $\text{final-thread.actions-subset } ?ta' \ ta$   
**by**  $(\text{auto simp add: collect-locks'-def finfun-upd-apply})$   
**moreover from**  $\text{RedWaitSpurious(1-5) RedWaitFail}$   
**obtain**  $va \ h' \text{ where } P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow \text{ext } \langle va, h' \rangle$  **by**  $(\text{fastforce})$   
**ultimately show**  $?thesis$  **by**  $\text{blast}$

qed

next

case *RedNotify*

note  $ta = \langle ta = \{\text{Notify } a, \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a\} \rangle$

let  $?ta' = \{\text{UnlockFail} \rightarrow a\}$

from  $ta$  *False None* have  $\neg \text{has-lock } (\text{locks } s \ \$ \ a) \ t$

by(*fastforce simp add: lock-actions-ok'-iff finfun-upd-apply wset-actions-ok-def Cons-eq-append-conv*

*split: if-split-asm dest: may-lock-t-may-lock-unlock-lock-t has-lock-may-lock*)

hence  $\text{final-thread.actions-ok}' \ s \ t \ ?ta'$  using *None*

by(*auto simp add: lock-actions-ok'-iff finfun-upd-apply*)

moreover from  $ta$  have  $\text{final-thread.actions-subset } ?ta' \ ta$

by(*auto simp add: collect-locks'-def finfun-upd-apply*)

moreover from *RedNotify* obtain  $va \ h'$  where  $P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow \text{ext } \langle va, h' \rangle$

by(*fastforce*)

ultimately show *?thesis* by *blast*

next

case *RedNotifyFail*

note  $ta = \langle ta = \{\text{UnlockFail} \rightarrow a\} \rangle$

let  $?ta' = \{\text{Notify } a, \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a\}$

from  $ta$  *False None* have  $\text{has-lock } (\text{locks } s \ \$ \ a) \ t$

by(*auto simp add: finfun-upd-apply split: if-split-asm*)

hence  $\text{final-thread.actions-ok}' \ s \ t \ ?ta'$  using *None*

by(*auto simp add: finfun-upd-apply simp add: wset-actions-ok-def intro: has-lock-may-lock*)

moreover from  $ta$  have  $\text{final-thread.actions-subset } ?ta' \ ta$

by(*auto simp add: collect-locks'-def finfun-upd-apply*)

moreover from *RedNotifyFail* obtain  $va \ h'$  where  $P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow \text{ext } \langle va, h' \rangle$

by(*fastforce*)

ultimately show *?thesis* by *blast*

next

case *RedNotifyAll*

note  $ta = \langle ta = \{\text{NotifyAll } a, \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a\} \rangle$

let  $?ta' = \{\text{UnlockFail} \rightarrow a\}$

from  $ta$  *False None* have  $\neg \text{has-lock } (\text{locks } s \ \$ \ a) \ t$

by(*auto simp add: lock-actions-ok'-iff finfun-upd-apply wset-actions-ok-def Cons-eq-append-conv*

*split: if-split-asm dest: may-lock-t-may-lock-unlock-lock-t*)

hence  $\text{final-thread.actions-ok}' \ s \ t \ ?ta'$  using *None*

by(*auto simp add: lock-actions-ok'-iff finfun-upd-apply*)

moreover from  $ta$  have  $\text{final-thread.actions-subset } ?ta' \ ta$

by(*auto simp add: collect-locks'-def finfun-upd-apply*)

moreover from *RedNotifyAll* obtain  $va \ h'$  where  $P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow \text{ext } \langle va, h' \rangle$

by(*fastforce*)

ultimately show *?thesis* by *blast*

next

case *RedNotifyAllFail*

note  $ta = \langle ta = \{\text{UnlockFail} \rightarrow a\} \rangle$

let  $?ta' = \{\text{NotifyAll } a, \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a\}$

from  $ta$  *False None* have  $\text{has-lock } (\text{locks } s \ \$ \ a) \ t$

by(*auto simp add: finfun-upd-apply split: if-split-asm*)

hence  $\text{final-thread.actions-ok}' \ s \ t \ ?ta'$  using *None*

by(*auto simp add: finfun-upd-apply wset-actions-ok-def intro: has-lock-may-lock*)

moreover from  $ta$  have  $\text{final-thread.actions-subset } ?ta' \ ta$

by(*auto simp add: collect-locks'-def finfun-upd-apply*)

moreover from *RedNotifyAllFail* obtain  $va \ h'$  where  $P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow \text{ext } \langle va, h' \rangle$

```

by(fastforce)
  ultimately show ?thesis by blast
next
  case RedInterruptedTrue
  let ?ta' = {IsInterrupted t False}
  from RedInterruptedTrue have final-thread.actions-ok final s t ?ta'
  using None False by(auto simp add: final-thread.actions-ok-iff final-thread.cond-action-oks.simps)
  moreover obtain va h' where  $P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow_{ext} \langle va, h' \rangle$ 
    using RedInterruptedFalse RedInterruptedTrue by auto
  ultimately show ?thesis by blast
next
  case RedInterruptedFalse
  let ?ta' = {IsInterrupted t True, ClearInterrupt t, ObsInterrupted t}
  from RedInterruptedFalse have final-thread.actions-ok final s t ?ta'
    using None False
    by(auto simp add: final-thread.actions-ok-iff final-thread.cond-action-oks.simps)
  moreover obtain va h' where  $P, t \vdash \langle a \cdot M(vs), h \rangle - ?ta' \rightarrow_{ext} \langle va, h' \rangle$ 
    using RedInterruptedFalse RedInterruptedTrue by auto
  ultimately show ?thesis by blast
qed(auto simp add: None)
qed
qed
qed
end

```

context heap-base begin

lemma red-external-ta-satisfiable:

```

  fixes final
  assumes  $P, t \vdash \langle a \cdot M(vs), h \rangle - ta \rightarrow_{ext} \langle va, h' \rangle$ 
  shows  $\exists s. \text{final-thread.actions-ok final s t ta}$ 
proof -
  note [simp] =
    final-thread.actions-ok-iff final-thread.cond-action-oks.simps final-thread.cond-action-ok.simps
    lock-ok-las-def finfun-upd-apply wset-actions-ok-def has-lock-may-lock
  and [intro] =
    free-thread-id.intros
  and [cong] = conj-cong

  from assms show ?thesis by cases(fastforce intro: exI[where x=(K$ None)(a $:= [(t, 0)])]
exI[where x=(K$ None)])+
qed

```

lemma red-external-aggr-ta-satisfiable:

```

  fixes final
  assumes  $(ta, va, h') \in \text{red-external-aggr } P \ t \ a \ M \ vs \ h$ 
  shows  $\exists s. \text{final-thread.actions-ok final s t ta}$ 
proof -
  note [simp] =
    final-thread.actions-ok-iff final-thread.cond-action-oks.simps final-thread.cond-action-ok.simps
    lock-ok-las-def finfun-upd-apply wset-actions-ok-def has-lock-may-lock
  and [intro] =
    free-thread-id.intros

```

and [cong] = conj-cong

from *assms* show *?thesis*

by(*fastforce simp add: red-external-aggr-def split-beta ta-upd-simps split: if-split-asm intro: exI[where x=Map.empty] exI[where x=(K\$ None)(a \$:= [(t, 0)])] exI[where x=K\$ None]*)  
qed

end

### 3.13.4 Determinism

context *heap-base* begin

lemma *heap-copy-loc-deterministic*:

assumes *det: deterministic-heap-ops*

and *copy: heap-copy-loc a a' al h ops h' heap-copy-loc a a' al h ops' h''*

shows *ops = ops'  $\wedge$  h' = h''*

using *copy*

by(*auto elim!: heap-copy-loc.cases dest: deterministic-heap-ops-readD[OF det] deterministic-heap-ops-writeD[OF det]*)

lemma *heap-copies-deterministic*:

assumes *det: deterministic-heap-ops*

and *copy: heap-copies a a' als h ops h' heap-copies a a' als h ops' h''*

shows *ops = ops'  $\wedge$  h' = h''*

using *copy*

apply(*induct arbitrary: ops' h''*)

apply(*fastforce elim!: heap-copies-cases*)

apply(*erule heap-copies-cases*)

apply *clarify*

apply(*drule (1) heap-copy-loc-deterministic[OF det]*)

apply *clarify*

apply(*unfold same-append-eq*)

apply *blast*

done

lemma *heap-clone-deterministic*:

assumes *det: deterministic-heap-ops*

and *clone: heap-clone P h a h' obs heap-clone P h a h'' obs'*

shows *h' = h''  $\wedge$  obs = obs'*

using *clone*

by(*auto 4 4 elim!: heap-clone.cases dest: heap-copies-deterministic[OF det] deterministic-heap-ops-allocateD[OF det] has-fields-fun*)

lemma *red-external-deterministic*:

fixes *final*

assumes *det: deterministic-heap-ops*

and *red: P, t  $\vdash$   $\langle a \cdot M(vs), (shr\ s) \rangle -ta \rightarrow ext \langle va, h' \rangle$  P, t  $\vdash$   $\langle a \cdot M(vs), (shr\ s) \rangle -ta' \rightarrow ext \langle va', h'' \rangle$*

and *aok: final-thread.actions-ok final s t ta final-thread.actions-ok final s t ta'*

shows *ta = ta'  $\wedge$  va = va'  $\wedge$  h' = h''*

using *red aok*

apply(*simp add: final-thread.actions-ok-iff lock-ok-las-def*)

apply(*erule red-external.cases*)

apply(*erule-tac [!] red-external.cases*)

```

apply simp-all
apply(auto simp add: finfun-upd-apply wset-actions-ok-def dest: heap-clone-deterministic[OF det] split:
if-split-asm)
using deterministic-heap-ops-no-spurious-wakeups[OF det]
apply simp-all
done

end

end

```

### 3.14 Conformance for threads

```

theory ConformThreaded
imports
  ../Framework/FWLifting
  ../Framework/FWWellform
  Conform
begin

```

Every thread must be represented as an object whose address is its thread ID

```
context heap-base begin
```

```

abbreviation thread-conf :: 'm prog  $\Rightarrow$  ('addr, 'thread-id, 'x) thread-info  $\Rightarrow$  'heap  $\Rightarrow$  bool
where thread-conf P  $\equiv$  ts-ok ( $\lambda t\ x\ m. P, m \vdash t \sqrt{t}$ )

```

```

lemma thread-confI:
  ( $\bigwedge t\ xln. ts\ t = \lfloor xln \rfloor \implies P, h \vdash t \sqrt{t} \implies thread-conf\ P\ ts\ h$ )
by(blast intro: ts-okI)

```

```

lemma thread-confD:
  assumes thread-conf P ts h ts t =  $\lfloor xln \rfloor$ 
  shows  $P, h \vdash t \sqrt{t}$ 
using assms by(cases xln)(auto dest: ts-okD)

```

```

lemma thread-conf-ts-upd-eq [simp]:
  assumes tst: ts t =  $\lfloor xln \rfloor$ 
  shows thread-conf P (ts( $t \mapsto xln'$ )) h  $\longleftrightarrow$  thread-conf P ts h

```

```

proof
  assume tc: thread-conf P (ts( $t \mapsto xln'$ )) h
  show thread-conf P ts h
  proof(rule thread-confI)
    fix T XLN
    assume ts T =  $\lfloor XLN \rfloor$ 
    with tc show  $P, h \vdash T \sqrt{t}$ 
    by(cases T = t)(auto dest: thread-confD)
  qed

```

```

next
  assume tc: thread-conf P ts h
  show thread-conf P (ts( $t \mapsto xln'$ )) h
  proof(rule thread-confI)
    fix T XLN
    assume (ts( $t \mapsto xln'$ )) T =  $\lfloor XLN \rfloor$ 
    with tst obtain XLN' where ts T =  $\lfloor XLN' \rfloor$ 

```

```

    by(cases T = t)(auto)
  with tc show P, h ⊢ T √t
  by(auto dest: thread-confD)
qed
qed

end

context heap begin

lemma thread-conf-hext:
   $\llbracket \text{thread-conf } P \text{ ts } h; h \trianglelefteq h' \rrbracket \implies \text{thread-conf } P \text{ ts } h'$ 
by(blast intro: thread-confI tconf-hext-mono dest: thread-confD)

lemma thread-conf-start-state:
   $\llbracket \text{start-heap-ok}; \text{wf-syscls } P \rrbracket \implies \text{thread-conf } P \text{ (thr (start-state f P C M vs)) (shr (start-state f P C M vs))}$ 
by(auto intro!: thread-confI simp add: start-state-def split-beta split: if-split-asm intro: tconf-start-heap-start-tid)

end

context heap-base begin

lemma lock-thread-ok-start-state:
  lock-thread-ok (locks (start-state f P C M vs)) (thr (start-state f P C M vs))
by(rule lock-thread-okI)(simp add: start-state-def split-beta)

lemma wset-thread-ok-start-state:
  wset-thread-ok (wset (start-state f P C M vs)) (thr (start-state f P C M vs))
by(auto simp add: wset-thread-ok-def start-state-def split-beta)

lemma wset-final-ok-start-state:
  final-thread.wset-final-ok final (wset (start-state f P C M vs)) (thr (start-state f P C M vs))
by(rule final-thread.wset-final-okI)(simp add: start-state-def split-beta)

end

end

```

### 3.15 Binary Operators

```

theory BinOp
imports
  WellForm Word-Lib.Bit-Shifts-Infix-Syntax
begin

datatype bop = — names of binary operations
  Eq
| NotEq
| LessThan
| LessOrEqual
| GreaterThan
| GreaterOrEqual

```

```

| Add
| Subtract
| Mult
| Div
| Mod
| BinAnd
| BinOr
| BinXor
| ShiftLeft
| ShiftRightZeros
| ShiftRightSigned

```

### 3.15.1 The semantics of binary operators

**context**

**includes** *bit-operations-syntax*

**begin**

**type-synonym** *'addr binop-ret* = *'addr val* + *'addr* — a value or the address of an exception

**fun** *binop-LessThan* :: *'addr val*  $\Rightarrow$  *'addr val*  $\Rightarrow$  *'addr binop-ret option*

**where**

*binop-LessThan* (*Intg i1*) (*Intg i2*) = *Some* (*Inl* (*Bool* (*i1* <*s* *i2*)))

| *binop-LessThan v1 v2* = *None*

**fun** *binop-LessOrEqual* :: *'addr val*  $\Rightarrow$  *'addr val*  $\Rightarrow$  *'addr binop-ret option*

**where**

*binop-LessOrEqual* (*Intg i1*) (*Intg i2*) = *Some* (*Inl* (*Bool* (*i1* <=*s* *i2*)))

| *binop-LessOrEqual v1 v2* = *None*

**fun** *binop-GreaterThan* :: *'addr val*  $\Rightarrow$  *'addr val*  $\Rightarrow$  *'addr binop-ret option*

**where**

*binop-GreaterThan* (*Intg i1*) (*Intg i2*) = *Some* (*Inl* (*Bool* (*i2* <*s* *i1*)))

| *binop-GreaterThan v1 v2* = *None*

**fun** *binop-GreaterOrEqual* :: *'addr val*  $\Rightarrow$  *'addr val*  $\Rightarrow$  *'addr binop-ret option*

**where**

*binop-GreaterOrEqual* (*Intg i1*) (*Intg i2*) = *Some* (*Inl* (*Bool* (*i2* <=*s* *i1*)))

| *binop-GreaterOrEqual v1 v2* = *None*

**fun** *binop-Add* :: *'addr val*  $\Rightarrow$  *'addr val*  $\Rightarrow$  *'addr binop-ret option*

**where**

*binop-Add* (*Intg i1*) (*Intg i2*) = *Some* (*Inl* (*Intg* (*i1* + *i2*)))

| *binop-Add v1 v2* = *None*

**fun** *binop-Subtract* :: *'addr val*  $\Rightarrow$  *'addr val*  $\Rightarrow$  *'addr binop-ret option*

**where**

*binop-Subtract* (*Intg i1*) (*Intg i2*) = *Some* (*Inl* (*Intg* (*i1* - *i2*)))

| *binop-Subtract v1 v2* = *None*

**fun** *binop-Mult* :: *'addr val*  $\Rightarrow$  *'addr val*  $\Rightarrow$  *'addr binop-ret option*

**where**

*binop-Mult* (*Intg i1*) (*Intg i2*) = *Some* (*Inl* (*Intg* (*i1* \* *i2*)))

| *binop-Mult v1 v2* = *None*



**fun** *binop-BinAnd* :: 'addr val  $\Rightarrow$  'addr val  $\Rightarrow$  'addr binop-ret option  
**where**

*binop-BinAnd* (Intg i1) (Intg i2) = Some (Inl (Intg (i1 AND i2)))  
| *binop-BinAnd* (Bool b1) (Bool b2) = Some (Inl (Bool (b1  $\wedge$  b2)))  
| *binop-BinAnd* v1 v2 = None

**fun** *binop-BinOr* :: 'addr val  $\Rightarrow$  'addr val  $\Rightarrow$  'addr binop-ret option  
**where**

*binop-BinOr* (Intg i1) (Intg i2) = Some (Inl (Intg (i1 OR i2)))  
| *binop-BinOr* (Bool b1) (Bool b2) = Some (Inl (Bool (b1  $\vee$  b2)))  
| *binop-BinOr* v1 v2 = None

**fun** *binop-BinXor* :: 'addr val  $\Rightarrow$  'addr val  $\Rightarrow$  'addr binop-ret option  
**where**

*binop-BinXor* (Intg i1) (Intg i2) = Some (Inl (Intg (i1 XOR i2)))  
| *binop-BinXor* (Bool b1) (Bool b2) = Some (Inl (Bool (b1  $\neq$  b2)))  
| *binop-BinXor* v1 v2 = None

**fun** *binop-ShiftLeft* :: 'addr val  $\Rightarrow$  'addr val  $\Rightarrow$  'addr binop-ret option  
**where**

*binop-ShiftLeft* (Intg i1) (Intg i2) = Some (Inl (Intg (i1 << unat (i2 AND 0x1f))))  
| *binop-ShiftLeft* v1 v2 = None

**fun** *binop-ShiftRightZeros* :: 'addr val  $\Rightarrow$  'addr val  $\Rightarrow$  'addr binop-ret option  
**where**

*binop-ShiftRightZeros* (Intg i1) (Intg i2) = Some (Inl (Intg (i1 >> unat (i2 AND 0x1f))))  
| *binop-ShiftRightZeros* v1 v2 = None

**fun** *binop-ShiftRightSigned* :: 'addr val  $\Rightarrow$  'addr val  $\Rightarrow$  'addr binop-ret option  
**where**

*binop-ShiftRightSigned* (Intg i1) (Intg i2) = Some (Inl (Intg (i1 >>> unat (i2 AND 0x1f))))  
| *binop-ShiftRightSigned* v1 v2 = None

Division on 'a word is unsigned, but JLS specifies signed division.

**definition** *word-sdiv* :: 'a :: len word  $\Rightarrow$  'a word  $\Rightarrow$  'a word (**infixl** *sdiv* 70)

**where** [code]:

*x sdiv y* =  
(let *x'* = sint *x*; *y'* = sint *y*;  
negative = (*x'* < 0)  $\neq$  (*y'* < 0);  
result = abs *x'* div abs *y'*  
in word-of-int (if negative then -result else result))

**definition** *word-smod* :: 'a :: len word  $\Rightarrow$  'a word  $\Rightarrow$  'a word (**infixl** *smod* 70)

**where** [code]:

*x smod y* =  
(let *x'* = sint *x*; *y'* = sint *y*;  
negative = (*x'* < 0);  
result = abs *x'* mod abs *y'*  
in word-of-int (if negative then -result else result))

**declare** *word-sdiv-def* [simp] *word-smod-def* [simp]

**lemma** *sdiv-smod-id*: (*a sdiv b*) \* *b* + (*a smod b*) = *a*

```

proof -
  have F5:  $\forall u::'a \text{ word. } - (- u) = u$ 
    by simp
  have F7:  $\forall v u::'a \text{ word. } u + v = v + u$ 
    by (simp add: ac-simps)
  have F8:  $\forall (w::'a \text{ word}) (v::\text{int}) u::\text{int. } \text{word-of-int } u + \text{word-of-int } v * w = \text{word-of-int } (u + v * \text{sint } w)$ 
    by simp
  have  $\exists u. u = - \text{sint } b \wedge \text{word-of-int } (\text{sint } a \bmod u + - (- u * (\text{sint } a \text{ div } u))) = a$ 
    using F5 by simp
  hence  $\text{word-of-int } (\text{sint } a \bmod - \text{sint } b + - (\text{sint } b * (\text{sint } a \text{ div } - \text{sint } b))) = a$ 
    by (metis equation-minus-iff)
  hence  $\text{word-of-int } (\text{sint } a \bmod - \text{sint } b) + \text{word-of-int } (- (\text{sint } a \text{ div } - \text{sint } b)) * b = a$ 
    using F8 by (simp add: ac-simps)
  hence eq:  $\text{word-of-int } (- (\text{sint } a \text{ div } - \text{sint } b)) * b + \text{word-of-int } (\text{sint } a \bmod - \text{sint } b) = a$ 
    using F7 by simp

  show ?thesis
  proof (cases  $\text{sint } a < 0$ )
    case True note a = this
    show ?thesis
    proof (cases  $\text{sint } b < 0$ )
      case True
      with a show ?thesis
        by simp (metis F7 F8 eq minus-equation-iff minus-mult-minus mod-div-mult-eq)
    next
      case False
      from eq have  $\text{word-of-int } (- (- \text{sint } a \text{ div } \text{sint } b)) * b + \text{word-of-int } (- (- \text{sint } a \bmod \text{sint } b))$ 
        = a
        by (metis div-minus-right mod-minus-right)
      with a False show ?thesis by simp
    qed
  next
    case False note a = this
    show ?thesis
    proof (cases  $\text{sint } b < 0$ )
      case True
      with a eq show ?thesis by simp
    next
      case False with a show ?thesis
        by (simp add: F7 F8)
    qed
  qed
qed
end

notepad begin
have 5  $\text{sdiv } (3 :: \text{word32}) = 1$ 
and 5  $\text{smod } (3 :: \text{word32}) = 2$ 
and 5  $\text{sdiv } (-3 :: \text{word32}) = -1$ 
and 5  $\text{smod } (-3 :: \text{word32}) = 2$ 
and  $(-5) \text{sdiv } (3 :: \text{word32}) = -1$ 
and  $(-5) \text{smod } (3 :: \text{word32}) = -2$ 

```

```

and (-5) sdiv (-3 :: word32) = 1
and (-5) smod (-3 :: word32) = -2
and -2147483648 sdiv 1 = (-2147483648 :: word32)
by eval+
end

context heap-base
begin

fun binop-Mod :: 'addr val  $\Rightarrow$  'addr val  $\Rightarrow$  'addr binop-ret option
where
  binop-Mod (Intg i1) (Intg i2) =
    Some (if i2 = 0 then Inr (addr-of-sys-xcpt ArithmeticException) else Inl (Intg (i1 smod i2)))
| binop-Mod v1 v2 = None

fun binop-Div :: 'addr val  $\Rightarrow$  'addr val  $\Rightarrow$  'addr binop-ret option
where
  binop-Div (Intg i1) (Intg i2) =
    Some (if i2 = 0 then Inr (addr-of-sys-xcpt ArithmeticException) else Inl (Intg (i1 sdiv i2)))
| binop-Div v1 v2 = None

primrec binop :: bop  $\Rightarrow$  'addr val  $\Rightarrow$  'addr val  $\Rightarrow$  'addr binop-ret option
where
  binop Eq v1 v2 = Some (Inl (Bool (v1 = v2)))
| binop NotEq v1 v2 = Some (Inl (Bool (v1  $\neq$  v2)))
| binop LessThan = binop-LessThan
| binop LessOrEqual = binop-LessOrEqual
| binop GreaterThan = binop-GreaterThan
| binop GreaterOrEqual = binop-GreaterOrEqual
| binop Add = binop-Add
| binop Subtract = binop-Subtract
| binop Mult = binop-Mult
| binop Mod = binop-Mod
| binop Div = binop-Div
| binop BinAnd = binop-BinAnd
| binop BinOr = binop-BinOr
| binop BinXor = binop-BinXor
| binop ShiftLeft = binop-ShiftLeft
| binop ShiftRightZeros = binop-ShiftRightZeros
| binop ShiftRightSigned = binop-ShiftRightSigned

end

context
includes bit-operations-syntax
begin

lemma [simp]:
  (binop-LessThan v1 v2 = Some va)  $\longleftrightarrow$ 
  ( $\exists$  i1 i2. v1 = Intg i1  $\wedge$  v2 = Intg i2  $\wedge$  va = Inl (Bool (i1 < i2)))
by(cases (v1, v2) rule: binop-LessThan.cases) auto

lemma [simp]:
  (binop-LessOrEqual v1 v2 = Some va)  $\longleftrightarrow$ 

```

$(\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Bool } (i1 \leq_s i2)))$   
**by**(cases (v1, v2) rule: binop-LessOrEqual.cases) auto

**lemma** [simp]:  
 $(\text{binop-GreaterThan } v1\ v2 = \text{Some } va) \longleftrightarrow$   
 $(\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Bool } (i2 <_s i1)))$   
**by**(cases (v1, v2) rule: binop-GreaterThan.cases) auto

**lemma** [simp]:  
 $(\text{binop-GreaterOrEqual } v1\ v2 = \text{Some } va) \longleftrightarrow$   
 $(\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Bool } (i2 \leq_s i1)))$   
**by**(cases (v1, v2) rule: binop-GreaterOrEqual.cases) auto

**lemma** [simp]:  
 $(\text{binop-Add } v1\ v2 = \text{Some } va) \longleftrightarrow$   
 $(\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Intg } (i1 + i2)))$   
**by**(cases (v1, v2) rule: binop-Add.cases) auto

**lemma** [simp]:  
 $(\text{binop-Subtract } v1\ v2 = \text{Some } va) \longleftrightarrow$   
 $(\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Intg } (i1 - i2)))$   
**by**(cases (v1, v2) rule: binop-Subtract.cases) auto

**lemma** [simp]:  
 $(\text{binop-Mult } v1\ v2 = \text{Some } va) \longleftrightarrow$   
 $(\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Intg } (i1 * i2)))$   
**by**(cases (v1, v2) rule: binop-Mult.cases) auto

**lemma** [simp]:  
 $(\text{binop-BinAnd } v1\ v2 = \text{Some } va) \longleftrightarrow$   
 $(\exists b1\ b2. v1 = \text{Bool } b1 \wedge v2 = \text{Bool } b2 \wedge va = \text{Inl } (\text{Bool } (b1 \wedge b2))) \vee$   
 $(\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Intg } (i1 \text{ AND } i2)))$   
**by**(cases (v1, v2) rule: binop-BinAnd.cases) auto

**lemma** [simp]:  
 $(\text{binop-BinOr } v1\ v2 = \text{Some } va) \longleftrightarrow$   
 $(\exists b1\ b2. v1 = \text{Bool } b1 \wedge v2 = \text{Bool } b2 \wedge va = \text{Inl } (\text{Bool } (b1 \vee b2))) \vee$   
 $(\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Intg } (i1 \text{ OR } i2)))$   
**by**(cases (v1, v2) rule: binop-BinOr.cases) auto

**lemma** [simp]:  
 $(\text{binop-BinXor } v1\ v2 = \text{Some } va) \longleftrightarrow$   
 $(\exists b1\ b2. v1 = \text{Bool } b1 \wedge v2 = \text{Bool } b2 \wedge va = \text{Inl } (\text{Bool } (b1 \neq b2))) \vee$   
 $(\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Intg } (i1 \text{ XOR } i2)))$   
**by**(cases (v1, v2) rule: binop-BinXor.cases) auto

**lemma** [simp]:  
 $(\text{binop-ShiftLeft } v1\ v2 = \text{Some } va) \longleftrightarrow$   
 $(\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Intg } (i1 << \text{unat } (i2 \text{ AND } 0x1f))))$   
**by**(cases (v1, v2) rule: binop-ShiftLeft.cases) auto

**lemma** [simp]:  
 $(\text{binop-ShiftRightZeros } v1\ v2 = \text{Some } va) \longleftrightarrow$   
 $(\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Intg } (i1 >> \text{unat } (i2 \text{ AND } 0x1f))))$

```

by(cases (v1, v2) rule: binop-ShiftRightZeros.cases) auto

lemma [simp]:
  (binop-ShiftRightSigned v1 v2 = Some va)  $\longleftrightarrow$ 
  ( $\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge va = \text{Inl } (\text{Intg } (i1 \gg \gg \text{unat } (i2 \text{ AND } 0x1f))))$ )
by(cases (v1, v2) rule: binop-ShiftRightSigned.cases) auto

end

context heap-base
begin

lemma [simp]:
  (binop-Mod v1 v2 = Some va)  $\longleftrightarrow$ 
  ( $\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge$ 
     $va = (\text{if } i2 = 0 \text{ then } \text{Inr } (\text{addr-of-sys-xcpt ArithmeticException}) \text{ else } \text{Inl } (\text{Intg } (i1 \text{ smod } i2))))$ )
by(cases (v1, v2) rule: binop-Mod.cases) auto

lemma [simp]:
  (binop-Div v1 v2 = Some va)  $\longleftrightarrow$ 
  ( $\exists i1\ i2. v1 = \text{Intg } i1 \wedge v2 = \text{Intg } i2 \wedge$ 
     $va = (\text{if } i2 = 0 \text{ then } \text{Inr } (\text{addr-of-sys-xcpt ArithmeticException}) \text{ else } \text{Inl } (\text{Intg } (i1 \text{ sdiv } i2))))$ )
by(cases (v1, v2) rule: binop-Div.cases) auto

end

```

### 3.15.2 Typing for binary operators

**inductive** WT-binop :: 'm prog  $\Rightarrow$  ty  $\Rightarrow$  bop  $\Rightarrow$  ty  $\Rightarrow$  ty  $\Rightarrow$  bool (-  $\vdash$  -«-»- :: - [51,0,0,0,51] 50)

**where**

```

  WT-binop-Eq:
     $P \vdash T1 \leq T2 \vee P \vdash T2 \leq T1 \implies P \vdash T1 \ll \text{Eq} \gg T2 :: \text{Boolean}$ 

| WT-binop-NotEq:
   $P \vdash T1 \leq T2 \vee P \vdash T2 \leq T1 \implies P \vdash T1 \ll \text{NotEq} \gg T2 :: \text{Boolean}$ 

| WT-binop-LessThan:
   $P \vdash \text{Integer} \ll \text{LessThan} \gg \text{Integer} :: \text{Boolean}$ 

| WT-binop-LessOrEqual:
   $P \vdash \text{Integer} \ll \text{LessOrEqual} \gg \text{Integer} :: \text{Boolean}$ 

| WT-binop-GreaterThan:
   $P \vdash \text{Integer} \ll \text{GreaterThan} \gg \text{Integer} :: \text{Boolean}$ 

| WT-binop-GreaterOrEqual:
   $P \vdash \text{Integer} \ll \text{GreaterOrEqual} \gg \text{Integer} :: \text{Boolean}$ 

| WT-binop-Add:
   $P \vdash \text{Integer} \ll \text{Add} \gg \text{Integer} :: \text{Integer}$ 

| WT-binop-Subtract:
   $P \vdash \text{Integer} \ll \text{Subtract} \gg \text{Integer} :: \text{Integer}$ 

```

| *WT-binop-Mult*:  
 $P \vdash \text{Integer} \llbracket \text{Mult} \rrbracket \text{Integer} :: \text{Integer}$

| *WT-binop-Div*:  
 $P \vdash \text{Integer} \llbracket \text{Div} \rrbracket \text{Integer} :: \text{Integer}$

| *WT-binop-Mod*:  
 $P \vdash \text{Integer} \llbracket \text{Mod} \rrbracket \text{Integer} :: \text{Integer}$

| *WT-binop-BinAnd-Bool*:  
 $P \vdash \text{Boolean} \llbracket \text{BinAnd} \rrbracket \text{Boolean} :: \text{Boolean}$

| *WT-binop-BinAnd-Int*:  
 $P \vdash \text{Integer} \llbracket \text{BinAnd} \rrbracket \text{Integer} :: \text{Integer}$

| *WT-binop-BinOr-Bool*:  
 $P \vdash \text{Boolean} \llbracket \text{BinOr} \rrbracket \text{Boolean} :: \text{Boolean}$

| *WT-binop-BinOr-Int*:  
 $P \vdash \text{Integer} \llbracket \text{BinOr} \rrbracket \text{Integer} :: \text{Integer}$

| *WT-binop-BinXor-Bool*:  
 $P \vdash \text{Boolean} \llbracket \text{BinXor} \rrbracket \text{Boolean} :: \text{Boolean}$

| *WT-binop-BinXor-Int*:  
 $P \vdash \text{Integer} \llbracket \text{BinXor} \rrbracket \text{Integer} :: \text{Integer}$

| *WT-binop-ShiftLeft*:  
 $P \vdash \text{Integer} \llbracket \text{ShiftLeft} \rrbracket \text{Integer} :: \text{Integer}$

| *WT-binop-ShiftRightZeros*:  
 $P \vdash \text{Integer} \llbracket \text{ShiftRightZeros} \rrbracket \text{Integer} :: \text{Integer}$

| *WT-binop-ShiftRightSigned*:  
 $P \vdash \text{Integer} \llbracket \text{ShiftRightSigned} \rrbracket \text{Integer} :: \text{Integer}$

**lemma** *WT-binopI* [intro]:

$P \vdash T1 \leq T2 \vee P \vdash T2 \leq T1 \implies P \vdash T1 \llbracket \text{Eq} \rrbracket T2 :: \text{Boolean}$   
 $P \vdash T1 \leq T2 \vee P \vdash T2 \leq T1 \implies P \vdash T1 \llbracket \text{NotEq} \rrbracket T2 :: \text{Boolean}$   
 $\text{bop} = \text{Add} \vee \text{bop} = \text{Subtract} \vee \text{bop} = \text{Mult} \vee \text{bop} = \text{Mod} \vee \text{bop} = \text{Div} \vee \text{bop} = \text{BinAnd} \vee \text{bop} = \text{BinOr} \vee \text{bop} = \text{BinXor} \vee$   
 $\text{bop} = \text{ShiftLeft} \vee \text{bop} = \text{ShiftRightZeros} \vee \text{bop} = \text{ShiftRightSigned}$   
 $\implies P \vdash \text{Integer} \llbracket \text{bop} \rrbracket \text{Integer} :: \text{Integer}$   
 $\text{bop} = \text{LessThan} \vee \text{bop} = \text{LessOrEqual} \vee \text{bop} = \text{GreaterThan} \vee \text{bop} = \text{GreaterOrEqual} \implies P \vdash$   
 $\text{Integer} \llbracket \text{bop} \rrbracket \text{Integer} :: \text{Boolean}$   
 $\text{bop} = \text{BinAnd} \vee \text{bop} = \text{BinOr} \vee \text{bop} = \text{BinXor} \implies P \vdash \text{Boolean} \llbracket \text{bop} \rrbracket \text{Boolean} :: \text{Boolean}$   
**by**(auto intro: *WT-binop.intros*)

**inductive-cases** [elim]:

$P \vdash T1 \llbracket \text{Eq} \rrbracket T2 :: T$   
 $P \vdash T1 \llbracket \text{NotEq} \rrbracket T2 :: T$   
 $P \vdash T1 \llbracket \text{LessThan} \rrbracket T2 :: T$   
 $P \vdash T1 \llbracket \text{LessOrEqual} \rrbracket T2 :: T$   
 $P \vdash T1 \llbracket \text{GreaterThan} \rrbracket T2 :: T$

$P \vdash T1 \llbracket \text{GreaterOrEqual} \rrbracket T2 :: T$   
 $P \vdash T1 \llbracket \text{Add} \rrbracket T2 :: T$   
 $P \vdash T1 \llbracket \text{Subtract} \rrbracket T2 :: T$   
 $P \vdash T1 \llbracket \text{Mult} \rrbracket T2 :: T$   
 $P \vdash T1 \llbracket \text{Div} \rrbracket T2 :: T$   
 $P \vdash T1 \llbracket \text{Mod} \rrbracket T2 :: T$   
 $P \vdash T1 \llbracket \text{BinAnd} \rrbracket T2 :: T$   
 $P \vdash T1 \llbracket \text{BinOr} \rrbracket T2 :: T$   
 $P \vdash T1 \llbracket \text{BinXor} \rrbracket T2 :: T$   
 $P \vdash T1 \llbracket \text{ShiftLeft} \rrbracket T2 :: T$   
 $P \vdash T1 \llbracket \text{ShiftRightZeros} \rrbracket T2 :: T$   
 $P \vdash T1 \llbracket \text{ShiftRightSigned} \rrbracket T2 :: T$

**lemma** *WT-binop-fun*:  $\llbracket P \vdash T1 \llbracket \text{bop} \rrbracket T2 :: T; P \vdash T1 \llbracket \text{bop} \rrbracket T2 :: T' \rrbracket \implies T = T'$   
**by**(cases bop)(auto)

**lemma** *WT-binop-is-type*:  
 $\llbracket P \vdash T1 \llbracket \text{bop} \rrbracket T2 :: T; \text{is-type } P \ T1; \text{is-type } P \ T2 \rrbracket \implies \text{is-type } P \ T$   
**by**(cases bop) auto

**inductive** *WTrt-binop* ::  $'m \text{ prog} \Rightarrow ty \Rightarrow bop \Rightarrow ty \Rightarrow ty \Rightarrow \text{bool} \ (- \vdash - \llbracket - \rrbracket - : - [51,0,0,0,51] \ 50)$   
**where**

*WTrt-binop-Eq*:  
 $P \vdash T1 \llbracket \text{Eq} \rrbracket T2 : \text{Boolean}$

| *WTrt-binop-NotEq*:  
 $P \vdash T1 \llbracket \text{NotEq} \rrbracket T2 : \text{Boolean}$

| *WTrt-binop-LessThan*:  
 $P \vdash \text{Integer} \llbracket \text{LessThan} \rrbracket \text{Integer} : \text{Boolean}$

| *WTrt-binop-LessOrEqual*:  
 $P \vdash \text{Integer} \llbracket \text{LessOrEqual} \rrbracket \text{Integer} : \text{Boolean}$

| *WTrt-binop-GreaterThan*:  
 $P \vdash \text{Integer} \llbracket \text{GreaterThan} \rrbracket \text{Integer} : \text{Boolean}$

| *WTrt-binop-GreaterOrEqual*:  
 $P \vdash \text{Integer} \llbracket \text{GreaterOrEqual} \rrbracket \text{Integer} : \text{Boolean}$

| *WTrt-binop-Add*:  
 $P \vdash \text{Integer} \llbracket \text{Add} \rrbracket \text{Integer} : \text{Integer}$

| *WTrt-binop-Subtract*:  
 $P \vdash \text{Integer} \llbracket \text{Subtract} \rrbracket \text{Integer} : \text{Integer}$

| *WTrt-binop-Mult*:  
 $P \vdash \text{Integer} \llbracket \text{Mult} \rrbracket \text{Integer} : \text{Integer}$

| *WTrt-binop-Div*:  
 $P \vdash \text{Integer} \llbracket \text{Div} \rrbracket \text{Integer} : \text{Integer}$

| *WTrt-binop-Mod*:  
 $P \vdash \text{Integer} \llbracket \text{Mod} \rrbracket \text{Integer} : \text{Integer}$

| *WTrt-binop-BinAnd-Bool*:  
 $P \vdash \text{Boolean} \llbracket \text{BinAnd} \rrbracket \text{Boolean} : \text{Boolean}$

| *WTrt-binop-BinAnd-Int*:  
 $P \vdash \text{Integer} \llbracket \text{BinAnd} \rrbracket \text{Integer} : \text{Integer}$

| *WTrt-binop-BinOr-Bool*:  
 $P \vdash \text{Boolean} \llbracket \text{BinOr} \rrbracket \text{Boolean} : \text{Boolean}$

| *WTrt-binop-BinOr-Int*:  
 $P \vdash \text{Integer} \llbracket \text{BinOr} \rrbracket \text{Integer} : \text{Integer}$

| *WTrt-binop-BinXor-Bool*:  
 $P \vdash \text{Boolean} \llbracket \text{BinXor} \rrbracket \text{Boolean} : \text{Boolean}$

| *WTrt-binop-BinXor-Int*:  
 $P \vdash \text{Integer} \llbracket \text{BinXor} \rrbracket \text{Integer} : \text{Integer}$

| *WTrt-binop-ShiftLeft*:  
 $P \vdash \text{Integer} \llbracket \text{ShiftLeft} \rrbracket \text{Integer} : \text{Integer}$

| *WTrt-binop-ShiftRightZeros*:  
 $P \vdash \text{Integer} \llbracket \text{ShiftRightZeros} \rrbracket \text{Integer} : \text{Integer}$

| *WTrt-binop-ShiftRightSigned*:  
 $P \vdash \text{Integer} \llbracket \text{ShiftRightSigned} \rrbracket \text{Integer} : \text{Integer}$

**lemma** *WTrt-binopI* [intro]:

$P \vdash T1 \llbracket \text{Eq} \rrbracket T2 : \text{Boolean}$   
 $P \vdash T1 \llbracket \text{NotEq} \rrbracket T2 : \text{Boolean}$   
 $\text{bop} = \text{Add} \vee \text{bop} = \text{Subtract} \vee \text{bop} = \text{Mult} \vee \text{bop} = \text{Div} \vee \text{bop} = \text{Mod} \vee \text{bop} = \text{BinAnd} \vee \text{bop} = \text{BinOr} \vee \text{bop} = \text{BinXor} \vee$   
 $\text{bop} = \text{ShiftLeft} \vee \text{bop} = \text{ShiftRightZeros} \vee \text{bop} = \text{ShiftRightSigned}$   
 $\implies P \vdash \text{Integer} \llbracket \text{bop} \rrbracket \text{Integer} : \text{Integer}$   
 $\text{bop} = \text{LessThan} \vee \text{bop} = \text{LessOrEqual} \vee \text{bop} = \text{GreaterThan} \vee \text{bop} = \text{GreaterOrEqual} \implies P \vdash$   
 $\text{Integer} \llbracket \text{bop} \rrbracket \text{Integer} : \text{Boolean}$   
 $\text{bop} = \text{BinAnd} \vee \text{bop} = \text{BinOr} \vee \text{bop} = \text{BinXor} \implies P \vdash \text{Boolean} \llbracket \text{bop} \rrbracket \text{Boolean} : \text{Boolean}$   
**by**(auto intro: *WTrt-binop.intros*)

**inductive-cases** *WTrt-binop-cases* [elim]:

$P \vdash T1 \llbracket \text{Eq} \rrbracket T2 : T$   
 $P \vdash T1 \llbracket \text{NotEq} \rrbracket T2 : T$   
 $P \vdash T1 \llbracket \text{LessThan} \rrbracket T2 : T$   
 $P \vdash T1 \llbracket \text{LessOrEqual} \rrbracket T2 : T$   
 $P \vdash T1 \llbracket \text{GreaterThan} \rrbracket T2 : T$   
 $P \vdash T1 \llbracket \text{GreaterOrEqual} \rrbracket T2 : T$   
 $P \vdash T1 \llbracket \text{Add} \rrbracket T2 : T$   
 $P \vdash T1 \llbracket \text{Subtract} \rrbracket T2 : T$   
 $P \vdash T1 \llbracket \text{Mult} \rrbracket T2 : T$   
 $P \vdash T1 \llbracket \text{Div} \rrbracket T2 : T$   
 $P \vdash T1 \llbracket \text{Mod} \rrbracket T2 : T$   
 $P \vdash T1 \llbracket \text{BinAnd} \rrbracket T2 : T$   
 $P \vdash T1 \llbracket \text{BinOr} \rrbracket T2 : T$



$P \vdash T1 \llcorner \text{BinXor} \gg T2 : T$   
 $P \vdash T1 \llcorner \text{ShiftLeft} \gg T2 : T$   
 $P \vdash T1 \llcorner \text{ShiftRightZeros} \gg T2 : T$   
 $P \vdash T1 \llcorner \text{ShiftRightSigned} \gg T2 : T$

**inductive-simps** *WTrt-binop-simps* [simp]:

$P \vdash T1 \llcorner \text{Eq} \gg T2 : T$   
 $P \vdash T1 \llcorner \text{NotEq} \gg T2 : T$   
 $P \vdash T1 \llcorner \text{LessThan} \gg T2 : T$   
 $P \vdash T1 \llcorner \text{LessOrEqual} \gg T2 : T$   
 $P \vdash T1 \llcorner \text{GreaterThan} \gg T2 : T$   
 $P \vdash T1 \llcorner \text{GreaterOrEqual} \gg T2 : T$   
 $P \vdash T1 \llcorner \text{Add} \gg T2 : T$   
 $P \vdash T1 \llcorner \text{Subtract} \gg T2 : T$   
 $P \vdash T1 \llcorner \text{Mult} \gg T2 : T$   
 $P \vdash T1 \llcorner \text{Div} \gg T2 : T$   
 $P \vdash T1 \llcorner \text{Mod} \gg T2 : T$   
 $P \vdash T1 \llcorner \text{BinAnd} \gg T2 : T$   
 $P \vdash T1 \llcorner \text{BinOr} \gg T2 : T$   
 $P \vdash T1 \llcorner \text{BinXor} \gg T2 : T$   
 $P \vdash T1 \llcorner \text{ShiftLeft} \gg T2 : T$   
 $P \vdash T1 \llcorner \text{ShiftRightZeros} \gg T2 : T$   
 $P \vdash T1 \llcorner \text{ShiftRightSigned} \gg T2 : T$

**fun** *binop-relevant-class* :: *bop*  $\Rightarrow$  'm prog  $\Rightarrow$  *cname*  $\Rightarrow$  *bool*

**where**

$\text{binop-relevant-class Div} = (\lambda P C. P \vdash \text{ArithmeticException} \preceq^* C)$   
 $\text{binop-relevant-class Mod} = (\lambda P C. P \vdash \text{ArithmeticException} \preceq^* C)$   
 $\text{binop-relevant-class -} = (\lambda P C. \text{False})$

**lemma** *WT-binop-WTrt-binop*:

$P \vdash T1 \llcorner \text{bop} \gg T2 :: T \implies P \vdash T1 \llcorner \text{bop} \gg T2 : T$

**by**(*auto elim*: *WT-binop.cases*)

**context** *heap* **begin**

**lemma** *binop-progress*:

$\llbracket \text{typeof}_h v1 = \lfloor T1 \rfloor; \text{typeof}_h v2 = \lfloor T2 \rfloor; P \vdash T1 \llcorner \text{bop} \gg T2 : T \rrbracket$   
 $\implies \exists va. \text{binop bop } v1 v2 = \lfloor va \rfloor$

**by**(*cases bop*)(*auto del*: *disjCI split del*: *if-split*)

**lemma** *binop-type*:

**assumes** *wf*: *wf-prog wf-md P*

**and** *pre*: *preallocated h*

**and** *type*:  $\text{typeof}_h v1 = \lfloor T1 \rfloor \text{typeof}_h v2 = \lfloor T2 \rfloor P \vdash T1 \llcorner \text{bop} \gg T2 : T$

**shows**  $\text{binop bop } v1 v2 = \lfloor \text{Inl } v \rfloor \implies P, h \vdash v : \leq T$

**and**  $\text{binop bop } v1 v2 = \lfloor \text{Inr } a \rfloor \implies P, h \vdash \text{Addr } a : \leq \text{Class Throwable}$

**using** *type*

**apply**(*case-tac* [!] *bop*)

**apply**(*auto split*: *if-split-asm simp add*: *conf-def wf-preallocatedD[OF wf pre]*)

**done**

**lemma** *binop-relevant-class*:

**assumes** *wf*: *wf-prog wf-md P*

**and** *pre*: *preallocated h*  
**and** *bop*: *binop bop v1 v2 = [Inr a]*  
**and** *sup*:  $P \vdash \text{cname-of } h \ a \preceq^* C$   
**shows** *binop-relevant-class bop P C*  
**using** *assms*  
**by**(*cases bop*)(*auto split: if-split-asm*)

**end**

**lemma** *WTrt-binop-fun*:  $\llbracket P \vdash T1 \llbracket bop \rrbracket T2 : T; P \vdash T1 \llbracket bop \rrbracket T2 : T' \rrbracket \implies T = T'$   
**by**(*cases bop*)(*auto*)

**lemma** *WTrt-binop-THE [simp]*:  $P \vdash T1 \llbracket bop \rrbracket T2 : T \implies \text{The } (WTrt\text{-binop } P \ T1 \ bop \ T2) = T$   
**by**(*auto dest: WTrt-binop-fun*)

**lemma** *WTrt-binop-widen-mono*:  
 $\llbracket P \vdash T1 \llbracket bop \rrbracket T2 : T; P \vdash T1' \leq T1; P \vdash T2' \leq T2 \rrbracket \implies \exists T'. P \vdash T1' \llbracket bop \rrbracket T2' : T' \wedge P \vdash T' \leq T$   
**by**(*cases bop*)(*auto elim!: WTrt-binop-cases*)

**lemma** *WTrt-binop-is-type*:  
 $\llbracket P \vdash T1 \llbracket bop \rrbracket T2 : T; \text{is-type } P \ T1; \text{is-type } P \ T2 \rrbracket \implies \text{is-type } P \ T$   
**by**(*cases bop*) *auto*

### 3.15.3 Code generator setup

**lemmas** [*code*] =  
*heap-base.binop-Div.simps*  
*heap-base.binop-Mod.simps*  
*heap-base.binop.simps*

**code-pred**  
*(modes: i  $\Rightarrow$  i  $\Rightarrow$  i  $\Rightarrow$  i  $\Rightarrow$  o  $\Rightarrow$  bool, i  $\Rightarrow$  i  $\Rightarrow$  i  $\Rightarrow$  i  $\Rightarrow$  i  $\Rightarrow$  bool)*  
*WT-binop*  
 .

**code-pred**  
*(modes: i  $\Rightarrow$  i  $\Rightarrow$  i  $\Rightarrow$  i  $\Rightarrow$  o  $\Rightarrow$  bool, i  $\Rightarrow$  i  $\Rightarrow$  i  $\Rightarrow$  i  $\Rightarrow$  i  $\Rightarrow$  bool)*  
*WTrt-binop*  
 .

**lemma** *eval-WTrt-binop-i-i-i-i-o*:  
*Predicate.eval (WTrt-binop-i-i-i-i-o P T1 bop T2) T  $\longleftrightarrow$  P  $\vdash$  T1  $\llbracket bop \rrbracket$  T2 : T*  
**by**(*auto elim: WTrt-binop-i-i-i-i-oE intro: WTrt-binop-i-i-i-i-oI*)

**lemma** *the-WTrt-binop-code*:  
*(THE T. P  $\vdash$  T1  $\llbracket bop \rrbracket$  T2 : T) = Predicate.the (WTrt-binop-i-i-i-i-o P T1 bop T2)*  
**by**(*simp add: Predicate.the-def eval-WTrt-binop-i-i-i-i-o*)

**end**

## 3.16 The Jinja Type System as a Semilattice

**theory** *SemiType*

```

imports
  WellForm
  ../DFA/Semilattices
begin

inductive-set
  widen1 :: 'a prog  $\Rightarrow$  (ty  $\times$  ty) set
  and widen1-syntax :: 'a prog  $\Rightarrow$  ty  $\Rightarrow$  ty  $\Rightarrow$  bool (-  $\vdash$  -  $<^1$  - [71,71,71] 70)
  for P :: 'a prog
where
  P  $\vdash$  C  $<^1$  D  $\equiv$  (C, D)  $\in$  widen1 P

| widen1-Array-Object:
  P  $\vdash$  Array (Class Object)  $<^1$  Class Object

| widen1-Array-Integer:
  P  $\vdash$  Array Integer  $<^1$  Class Object

| widen1-Array-Boolean:
  P  $\vdash$  Array Boolean  $<^1$  Class Object

| widen1-Array-Void:
  P  $\vdash$  Array Void  $<^1$  Class Object

| widen1-Class:
  P  $\vdash$  C  $\prec^1$  D  $\implies$  P  $\vdash$  Class C  $<^1$  Class D

| widen1-Array-Array:
   $\llbracket P \vdash T <^1 U; \neg \text{is-NT-Array } T \rrbracket \implies P \vdash \text{Array } T <^1 \text{Array } U$ 

abbreviation widen1-trancl :: 'a prog  $\Rightarrow$  ty  $\Rightarrow$  ty  $\Rightarrow$  bool (-  $\vdash$  -  $<^+$  - [71,71,71] 70) where
  P  $\vdash$  T  $<^+$  U  $\equiv$  (T, U)  $\in$  trancl (widen1 P)

abbreviation widen1-rtrancl :: 'a prog  $\Rightarrow$  ty  $\Rightarrow$  ty  $\Rightarrow$  bool (-  $\vdash$  -  $<^*$  - [71,71,71] 70) where
  P  $\vdash$  T  $<^*$  U  $\equiv$  (T, U)  $\in$  rtrancl (widen1 P)

inductive-simps widen1-simps1 [simp]:
  P  $\vdash$  Integer  $<^1$  T
  P  $\vdash$  Boolean  $<^1$  T
  P  $\vdash$  Void  $<^1$  T
  P  $\vdash$  Class Object  $<^1$  T
  P  $\vdash$  NT  $<^1$  U

inductive-simps widen1-simps [simp]:
  P  $\vdash$  Array (Class Object)  $<^1$  T
  P  $\vdash$  Array Integer  $<^1$  T
  P  $\vdash$  Array Boolean  $<^1$  T
  P  $\vdash$  Array Void  $<^1$  T
  P  $\vdash$  Class C  $<^1$  T
  P  $\vdash$  T  $<^1$  Array U

lemma is-type-widen1:
  assumes icO: is-class P Object
  shows P  $\vdash$  T  $<^1$  U  $\implies$  is-type P T

```

**by**(*induct rule: widen1.induct*)(*auto intro: subcls-is-class icO split: ty.split dest: is-type-ground-type*)

**lemma** *widen1-NT-Array*:

**assumes** *is-NT-Array T*

**shows**  $\neg P \vdash T[] <^1 U$

**proof**

**assume**  $P \vdash T[] <^1 U$  **thus** *False* **using** *assms*

**by**(*induct T[] U arbitrary: T*) *auto*

**qed**

**lemma** *widen1-is-type*:

**assumes** *wfP: wf-prog wfmd P*

**shows**  $(A, B) \in \text{widen1 } P \implies \text{is-type } P B$

**proof**(*induct rule: widen1.induct*)

**case** (*widen1-Class C D*)

**hence** *is-class P C is-class P D*

**by**(*auto intro: subcls-is-class converse-subcls-is-class[OF wfP]*)

**thus** *?case* **by** *simp*

**next**

**case** (*widen1-Array-Array T U*)

**thus** *?case* **by**(*cases U*)(*auto elim: widen1.cases*)

**qed**(*insert wfP, auto*)

**lemma** *widen1-trancl-is-type*:

**assumes** *wfP: wf-prog wfmd P*

**shows**  $(A, B) \in (\text{widen1 } P)^+ \implies \text{is-type } P B$

**apply**(*induct rule: trancl-induct*)

**apply**(*auto intro: widen1-is-type[OF wfP]*)

**done**

**lemma** *single-valued-widen1*:

**assumes** *wf: wf-prog wf-md P*

**shows** *single-valued (widen1 P)*

**proof**(*rule single-valuedI*)

**fix** *x y z*

**assume**  $P \vdash x <^1 y$   $P \vdash x <^1 z$

**thus**  $y = z$

**proof**(*induct arbitrary: z rule: widen1.induct*)

**case** *widen1-Class*

**with** *single-valued-subcls1[OF wf]* **show** *?case*

**by**(*auto dest: single-valuedpD*)

**next**

**case** (*widen1-Array-Array T U z*)

**from**  $\langle P \vdash T[] <^1 z \rangle \langle P \vdash T <^1 U \rangle \langle \neg \text{is-NT-Array } T \rangle$

**obtain**  $z'$  **where**  $z': z = z'[]$  **and**  $Tz': P \vdash T <^1 z'$

**by**(*auto elim: widen1.cases*)

**with**  $\langle P \vdash T <^1 z' \implies U = z' \rangle$  **have**  $U = z'$  **by** *blast*

**with**  $z'$  **show** *?case* **by** *simp*

**qed** *simp-all*

**qed**

**function** *inheritance-level* :: '*a prog*  $\Rightarrow$  *cname*  $\Rightarrow$  *nat* **where**

*inheritance-level P C =*

(*if acyclicP (subcls1 P)  $\wedge$  is-class P C  $\wedge$  C  $\neq$  Object*

```

    then Suc (inheritance-level P (fst (the (class P C))))
    else 0)
by(pat-completeness, auto)
termination
proof(relation same-fst (λP. acyclicP (subcls1 P)) (λP. {(C, C'). (subcls1 P)-1-1 C C'}))
  show wf (same-fst (λP. acyclicP (subcls1 P)) (λP. {(C, C'). (subcls1 P)-1-1 C C'}))
    by(rule wf-same-fst)(rule acyclicP-wf-subcls1[unfolded wfP-def])
qed(auto simp add: is-class-def intro: subcls1I)

fun subtype-measure :: 'a prog ⇒ ty ⇒ nat where
  subtype-measure P (Class C) = inheritance-level P C
| subtype-measure P (Array T) = 1 + subtype-measure P T
| subtype-measure P T = 0

lemma subtype-measure-measure:
  assumes acyclic: acyclicP (subcls1 P)
  and widen1: P ⊢ x <1 y
  shows subtype-measure P y < subtype-measure P x
using widen1
proof(induct rule: widen1.induct)
  case (widen1-Class C D)
  then obtain rest where is-class P C C ≠ Object class P C = [(D, rest)]
    by(auto elim!: subcls1.cases simp: is-class-def)
  thus ?case using acyclic by(simp)
qed(simp-all)

lemma wf-converse-widen1:
  assumes wfP: wf-prog wfmc P
  shows wf ((widen1 P)^-1)
proof(rule wf-subset)
  from wfP have acyclicP (subcls1 P) by(rule acyclic-subcls1)
  thus (widen1 P)-1 ⊆ measure (subtype-measure P)
    by(auto dest: subtype-measure-measure)
qed simp

fun super :: 'a prog ⇒ ty ⇒ ty
where
  super P (Array Integer) = Class Object
| super P (Array Boolean) = Class Object
| super P (Array Void) = Class Object
| super P (Array (Class C)) = (if C = Object then Class Object else Array (super P (Class C)))
| super P (Array (Array T)) = Array (super P (Array T))
| super P (Class C) = Class (fst (the (class P C)))

lemma superI:
  P ⊢ T <1 U ⇒ super P T = U
proof(induct rule: widen1.induct)
  case (widen1-Array-Array T U)
  thus ?case by(cases T) auto
qed(auto dest: subcls1D)

lemma Class-widen1-super:
  P ⊢ Class C' <1 U' ⇔ is-class P C' ∧ C' ≠ Object ∧ U' = super P (Class C')
  (is ?lhs ⇔ ?rhs)

```

```

proof(rule iffI)
  assume ?lhs thus ?rhs
  by(auto intro: subcls-is-class simp add: superI simp del: super.simps)
next
  assume ?rhs thus ?lhs
  by(auto simp add: is-class-def intro: subcls1.intros)
qed

lemma super-widen1:
  assumes icO: is-class P Object
  shows  $P \vdash T <^1 U \longleftrightarrow \text{is-type } P \ T \wedge (\text{case } T \text{ of } \text{Class } C \Rightarrow (C \neq \text{Object} \wedge U = \text{super } P \ T)$ 
     $\mid \text{Array } T' \Rightarrow U = \text{super } P \ T$ 
     $\mid - \Rightarrow \text{False})$ 
proof(induct T arbitrary: U)
  case Class thus ?case using Class-widen1-super by(simp)
next
  case (Array T' U')
  note IH = this
  have  $P \vdash T'[] <^1 U' = (\text{is-type } P \ (T'[]) \wedge U' = \text{super } P \ (T'[]))$ 
  proof(rule iffI)
    assume wd:  $P \vdash T'[] <^1 U'$ 
    with icO have is-type P (T'[]) by(rule is-type-widen1)
    moreover from wd have  $\text{super } P \ (T'[]) = U'$  by(rule superI)
    ultimately show is-type P (T'[])  $\wedge U' = \text{super } P \ (T'[])$  by simp
  next
    assume is-type P (T'[])  $\wedge U' = \text{super } P \ (T'[])$ 
    then obtain is-type P (T'[]) and U':  $U' = \text{super } P \ (T'[])$  ..
    thus  $P \vdash T'[] <^1 U'$ 
  proof(cases T')
    case (Class D)
    thus ?thesis using U' icO  $\langle \text{is-type } P \ (T'[]) \rangle$ 
    by(cases D = Object)(auto simp add: is-class-def intro: subcls1.intros)
  next
    case Array thus ?thesis
    using IH  $\langle \text{is-type } P \ (T'[]) \rangle$  U' by(auto simp add: ty.split-asm)
  qed simp-all
qed
thus ?case by(simp)
qed(simp-all)

```

**definition** sup :: 'c prog  $\Rightarrow$  ty  $\Rightarrow$  ty  $\Rightarrow$  ty err **where**

```

sup P T U  $\equiv$ 
  if is-refT T  $\wedge$  is-refT U
  then OK (if U = NT then T
    else if T = NT then U
    else exec-lub (widen1 P) (super P) T U)
  else if (T = U) then OK T else Err

```

**lemma** sup-def':

```

sup P = ( $\lambda T \ U.$ 
  if is-refT T  $\wedge$  is-refT U
  then OK (if U = NT then T
    else if T = NT then U
    else exec-lub (widen1 P) (super P) T U)

```

else if ( $T = U$ ) then OK  $T$  else Err)  
 by (simp add: fun-eq-iff sup-def)

**definition**  $esl :: 'm \text{ prog} \Rightarrow ty \text{ esl}$

**where**

$esl \ P = (types \ P, \ widen \ P, \ sup \ P)$

**lemma**  $order-widen \ [intro, simp]:$

$wf-prog \ m \ P \Longrightarrow order \ (widen \ P)$

**unfolding**  $Semilat.order-def \ lesub-def$

**by** (auto intro: widen-trans widen-antisym)

**lemma**  $subcls1-trancl-widen1-trancl:$

$(subcls1 \ P)^{++} \ C \ D \Longrightarrow P \vdash Class \ C <^+ Class \ D$

**by**(induct rule: tranclp-induct[consumes 1, case-names base step])

(auto intro: trancl-into-trancl)

**lemma**  $subcls-into-widen1-rtrancl:$

$P \vdash C \preceq^* D \Longrightarrow P \vdash Class \ C <^* Class \ D$

**by**(induct rule: rtranclp-induct)(auto intro: rtrancl-into-rtrancl)

**lemma**  $not-widen1-NT-Array:$

$P \vdash U <^1 T \Longrightarrow \neg is-NT-Array \ T$

**by**(induct rule: widen1.induct)(auto)

**lemma**  $widen1-trancl-into-Array-widen1-trancl:$

$\llbracket P \vdash A <^+ B; \neg is-NT-Array \ A \rrbracket \Longrightarrow P \vdash A[] <^+ B[]$

**by**(induct rule: converse-trancl-induct)

(auto intro: trancl-into-trancl2 widen1-Array-Array dest: not-widen1-NT-Array)

**lemma**  $widen1-rtrancl-into-Array-widen1-rtrancl:$

$\llbracket P \vdash A <^* B; \neg is-NT-Array \ A \rrbracket \Longrightarrow P \vdash A[] <^* B[]$

**by**(blast elim: rtranclE intro: trancl-into-rtrancl widen1-trancl-into-Array-widen1-trancl rtrancl-into-trancl1)

**lemma**  $Array-Object-widen1-trancl:$

**assumes**  $wf: wf-prog \ wmdc \ P$

**and**  $itA: is-type \ P \ (A[])$

**shows**  $P \vdash A[] <^+ Class \ Object$

**using**  $itA$

**proof**(induction  $A$ )

**case** (Class  $C$ )

**hence**  $is-class \ P \ C$  **by** simp

**hence**  $P \vdash C \preceq^* Object$  **by**(rule subcls-C-Object[OF - wf])

**hence**  $P \vdash Class \ C <^* Class \ Object$  **by**(rule subcls-into-widen1-rtrancl)

**hence**  $P \vdash Class \ C[] <^* Class \ Object[]$

**by**(rule widen1-rtrancl-into-Array-widen1-rtrancl) simp

**thus** ?case **by**(rule rtrancl-into-trancl1) simp

**next**

**case** (Array  $A$ )

**from**  $\langle is-type \ P \ (A[][]) \rangle$  **have**  $is-type \ P \ (A[])$  **by**(rule is-type-ArrayD)

**hence**  $P \vdash A[] <^+ Class \ Object$  **by**(rule Array.IH)

**moreover from**  $\langle is-type \ P \ (A[][]) \rangle$  **have**  $\neg is-NT-Array \ (A[])$  **by** auto

**ultimately have**  $P \vdash A[] <^+ Class \ Object[]$

**by**(rule widen1-trancl-into-Array-widen1-trancl)

**thus** ?case **by**(rule trancl-into-trancl) simp  
**qed** auto

**lemma** widen-into-widen1-trancl:

**assumes** wf: wf-prog wfmd P

**shows**  $\llbracket P \vdash A \leq B; A \neq B; A \neq NT; \text{is-type } P \ A \rrbracket \implies P \vdash A <^+ B$

**proof**(induct rule: widen.induct)

**case** (widen-subcls C D)

**from**  $\langle \text{Class } C \neq \text{Class } D \rangle \langle P \vdash C \preceq^* D \rangle$  **have** (subcls1 P)<sup>++</sup> C D

**by**(auto elim: rtranclp.cases intro: rtranclp-into-tranclp1)

**thus** ?case **by**(rule subcls1-trancl-widen1-trancl)

**next**

**case** widen-array-object **thus** ?case **by**(auto intro: Array-Object-widen1-trancl[OF wf])

**next**

**case** (widen-array-array A B)

**hence**  $P \vdash A <^+ B$  **by**(cases A) auto

**with**  $\langle \text{is-type } P \ (A[]) \rangle$  **show** ?case **by**(auto intro: widen1-trancl-into-Array-widen1-trancl)

**qed**(auto)

**lemma** wf-prog-impl-acc-widen:

**assumes** wfP: wf-prog wfmd P

**shows** acc (types P) (widen P)

**proof** –

**from** wf-converse-widen1[OF wfP]

**have** wf (((widen1 P)<sup>^-1</sup>)<sup>^+</sup>) **by**(rule wf-trancl)

**hence** wfw1t:  $\bigwedge M \ T. \ T \in M \implies (\exists z \in M. \forall y. (y, z) \in ((widen1 \ P)^{-1})^+ \longrightarrow y \notin M)$

**by**(auto simp only: wf-eq-minimal)

**have** wf  $\{(y, x). \text{is-type } P \ x \wedge \text{is-type } P \ y \wedge \text{widen } P \ x \ y \wedge x \neq y\}$

**unfolding** wf-eq-minimal

**proof**(intro strip)

**fix** M and T :: ty

**assume** TM:  $T \in M$

**show**  $\exists z \in M. \forall y. (y, z) \in \{(y, T). \text{is-type } P \ T \wedge \text{is-type } P \ y \wedge \text{widen } P \ T \ y \wedge T \neq y\} \longrightarrow y \notin M$

**proof**(cases  $(\exists C. \text{Class } C \in M \wedge \text{is-class } P \ C) \vee (\exists U. U[] \in M \wedge \text{is-type } P \ (U[]))$ )

**case** True

**have** BNTthesis:  $\bigwedge B. \llbracket B \in (M \cap \text{types } P) - \{NT\} \rrbracket \implies ?thesis$

**proof** –

**fix** B

**assume** BM:  $B \in M \cap \text{types } P - \{NT\}$

**from** wfw1t[OF BM] **obtain** z

**where** zM:  $z \in M$

**and** znnt:  $z \neq NT$

**and** itz:  $\text{is-type } P \ z$

**and** y:  $\bigwedge y. (y, z) \in ((widen1 \ P)^{-1})^+ \implies y \notin M \cap \text{types } P - \{NT\}$  **by** blast

**show** ?thesis B

**proof**(rule beXI[OF - zM], rule allI, rule impI)

**fix** y

**assume**  $(y, z) \in \{(y, T). \text{is-type } P \ T \wedge \text{is-type } P \ y \wedge \text{widen } P \ T \ y \wedge T \neq y\}$

**hence** Pzy:  $P \vdash z \leq y$  **and** zy:  $z \neq y$  **and**  $\text{is-type } P \ y$  **by** auto

**hence**  $P \vdash z <^+ y$  **using** znnt itz

**by** –(rule widen-into-widen1-trancl[OF wfP])

**hence** ynM:  $y \notin M \cap \text{types } P - \{NT\}$

**by** –(rule y, simp add: trancl-converse)



```

    thus  $y \notin M$  using  $Pzy$  znnt  $\langle is-type\ P\ y \rangle$  by auto
  qed
qed
from True show ?thesis by(fastforce intro: BNTthesis)
next
case False

hence not-is-class:  $\bigwedge C. Class\ C \in M \implies \neg is-class\ P\ C$ 
and not-is-array:  $\bigwedge U. U[] \in M \implies \neg is-type\ P\ (U[])$  by simp-all

show ?thesis
proof(cases  $\exists C. Class\ C \in M$ )
case True
then obtain  $C$  where  $Class\ C \in M$  ..
with not-is-class[of  $C$ ] show ?thesis
  by(blast dest: rtrancpD subcls-is-class Class-widen)
next
case False
show ?thesis
proof(cases  $\exists T. Array\ T \in M$ )
case True
then obtain  $U$  where  $U: Array\ U \in M$  ..
hence  $\neg is-type\ P\ (U[])$  by(rule not-is-array)
thus ?thesis using  $U$  by(auto simp del: is-type.simps)
next
case False
with  $\langle \neg (\exists C. Class\ C \in M) \rangle\ TM$ 
have  $\forall y. P \vdash T \leq y \wedge T \neq y \longrightarrow y \notin M$ 
  by(cases  $T$ )(fastforce simp add: NT-widen)+
thus ?thesis using  $TM$  by blast
qed
qed
qed
qed
thus ?thesis by(simp add: Semilat.acc-def lesssub-def lesub-def)
qed

lemmas wf-widen-acc = wf-prog-impl-acc-widen
declare wf-widen-acc [intro, simp]

lemma acyclic-widen1:
  wf-prog wfmc  $P \implies acyclic\ (widen1\ P)$ 
by(auto dest: wf-converse-widen1 wf-acyclic simp add: acyclic-converse)

lemma widen1-into-widen:
   $(A, B) \in widen1\ P \implies P \vdash A \leq B$ 
by(induct rule: widen1.induct)(auto intro: widen.intros)

lemma widen1-rtranc1-into-widen:
   $P \vdash A <^* B \implies P \vdash A \leq B$ 
by(induct rule: rtranc1-induct)(auto dest!: widen1-into-widen elim: widen-trans)

lemma widen-eq-widen1-tranc1:
   $\llbracket wf-prog\ wf-md\ P; T \neq NT; T \neq U; is-type\ P\ T \rrbracket \implies P \vdash T \leq U \longleftrightarrow P \vdash T <^+ U$ 

```

by(*blast intro: widen-into-widen1-trancl widen1-rtrancl-into-widen trancl-into-rtrancl*)

lemma *sup-is-type*:

assumes *wf*: *wf-prog wf-md P*

and *itA*: *is-type P A*

and *itB*: *is-type P B*

and *sup*: *sup P A B = OK T*

shows *is-type P T*

proof –

{ assume *ANT*: *A ≠ NT*

and *BNT*: *B ≠ NT*

and *AnB*: *A ≠ B*

and *RTA*: *is-refT A*

and *RTB*: *is-refT B*

with *itA itB* have *AObject*: *P ⊢ A ≤ Class Object*

and *BObject*: *P ⊢ B ≤ Class Object*

by(*auto intro: is-refType-widen-Object[OF wf]*)

have *is-type P (exec-lub (widen1 P) (super P) A B)*

proof(*cases A = Class Object ∨ B = Class Object*)

case *True*

hence *exec-lub (widen1 P) (super P) A B = Class Object*

proof(*rule disjE*)

assume *A*: *A = Class Object*

moreover

from *BObject BNT itB* have *P ⊢ B <\* Class Object*

by(*cases B = Class Object*)(*auto intro: trancl-into-rtrancl widen-into-widen1-trancl[OF wf]*)

hence *is-ub ((widen1 P)\* (Class Object) B (Class Object))*

by(*auto intro: is-ubI*)

hence *is-lub ((widen1 P)\* (Class Object) B (Class Object))*

by(*auto simp add: is-lub-def dest: is-ubD*)

with *acyclic-widen1[OF wf]*

have *exec-lub (widen1 P) (super P) (Class Object) B = Class Object*

by(*auto intro: exec-lub-conv superI*)

ultimately show *exec-lub (widen1 P) (super P) A B = Class Object* by *simp*

next

assume *B*: *B = Class Object*

moreover

from *AObject ANT itA*

have *(A, Class Object) ∈ (widen1 P)\**

by(*cases A = Class Object, auto intro: trancl-into-rtrancl widen-into-widen1-trancl[OF wf]*)

hence *is-ub ((widen1 P)\* (Class Object) A (Class Object))*

by(*auto intro: is-ubI*)

hence *is-lub ((widen1 P)\* (Class Object) A (Class Object))*

by(*auto simp add: is-lub-def dest: is-ubD*)

with *acyclic-widen1[OF wf]*

have *exec-lub (widen1 P) (super P) A (Class Object) = Class Object*

by(*auto intro: exec-lub-conv superI*)

ultimately show *exec-lub (widen1 P) (super P) A B = Class Object* by *simp*

qed

with *wf* show *?thesis* by(*simp*)

next

case *False*

hence *AnObject*: *A ≠ Class Object*

and *BnObject*: *B ≠ Class Object* by *auto*

```

from widen-into-widen1-trancl[OF wf AObject AnObject ANT itA]
have  $P \vdash A <^* \text{Class Object}$  by(rule trancl-into-rtrancl)
moreover from widen-into-widen1-trancl[OF wf BObject BnObject BNT itB]
have  $P \vdash B <^* \text{Class Object}$  by(rule trancl-into-rtrancl)
ultimately have is-lub  $((\text{widen1 } P)^*) A B$  (exec-lub (widen1 P) (super P) A B)
  by(rule is-lub-exec-lub[OF single-valued-widen1[OF wf] acyclic-widen1[OF wf]])(auto intro:
superI)
hence Aew1:  $P \vdash A <^* \text{exec-lub (widen1 P) (super P) A B}$ 
  by(auto simp add: is-lub-def dest!: is-ubD)
thus ?thesis
proof(rule rtranclE)
  assume  $A = \text{exec-lub (widen1 P) (super P) A B}$ 
  with itA show ?thesis by simp
next
  fix A'
  assume  $P \vdash A' <^1 \text{exec-lub (widen1 P) (super P) A B}$ 
  thus ?thesis by(rule widen1-is-type[OF wf])
qed
qed }
with is-class-Object[OF wf] sup itA itB show ?thesis unfolding sup-def
  by(cases A = B)(auto split: if-split-asm simp add: exec-lub-refl)
qed

```

**lemma** closed-err-types:

```

assumes wfP: wf-prog wf-mb P
shows closed (err (types P)) (lift2 (sup P))
proof –
  { fix A B
    assume it: is-type P A is-type P B
    and  $A \neq \text{NT } B \neq \text{NT } A \neq B$ 
    and is-refT A is-refT B
    hence is-type P (exec-lub (widen1 P) (super P) A B)
    using sup-is-type[OF wfP it] by(simp add: sup-def) }
with is-class-Object[OF wfP] show ?thesis
  unfolding closed-def plussub-def lift2-def sup-def'
  by(auto split: err.split ty.splits)(auto simp add: exec-lub-refl)
qed

```

**lemma** widen-into-widen1-rtrancl:

```

 $\llbracket \text{wf-prog wfmd } P; \text{widen } P A B; A \neq \text{NT}; \text{is-type } P A \rrbracket \implies (A, B) \in (\text{widen1 } P)^*$ 
by(cases A = B)(auto intro: trancl-into-rtrancl widen-into-widen1-trancl)

```

**lemma** sup-widen-greater:

```

assumes wfP: wf-prog wf-mb P
and it1: is-type P t1
and it2: is-type P t2
and sup: sup P t1 t2 = OK s
shows widen P t1 s  $\wedge$  widen P t2 s
proof –
  { assume t1: is-refT t1
    and t2: is-refT t2
    and t1NT: t1  $\neq \text{NT}$ 
    and t2NT: t2  $\neq \text{NT}$ 

```

**with**  $it1\ it2\ wfP$  **have**  $P \vdash t1 \leq \text{Class Object } P \vdash t2 \leq \text{Class Object}$   
**by** (*auto intro: is-refType-widen-Object*)  
**with**  $t1NT\ t2NT\ it1\ it2$   
**have**  $P \vdash t1 <^* \text{Class Object } P \vdash t2 <^* \text{Class Object}$   
**by** (*auto intro: widen-into-widen1-rtrancl[OF wfP]*)  
**with**  $\text{single-valued-widen1}[OF\ wfP]$   
**obtain**  $u$  **where**  $\text{is-lub } ((\text{widen1 } P)^\wedge^*)\ t1\ t2\ u$   
**by** (*blast dest: single-valued-has-lubs*)  
**hence**  $P \vdash t1 \leq \text{exec-lub } (\text{widen1 } P)\ (\text{super } P)\ t1\ t2 \wedge$   
 $P \vdash t2 \leq \text{exec-lub } (\text{widen1 } P)\ (\text{super } P)\ t1\ t2$   
**using**  $\text{acyclic-widen1}[OF\ wfP]\ \text{superI}[of\ -\ -\ P]$   
**by** (*simp add: exec-lub-conv*) (*blast dest: is-lubD is-ubD intro: widen1-rtrancl-into-widen*) }  
**with**  $it1\ it2\ sup$  **show**  $?thesis$   
**by** (*cases s*) (*auto simp add: sup-def split: if-split-asm elim: refTE*)  
**qed**

**lemma** *sup-widen-smallest:*

**assumes**  $wfP: wf\text{-prog } wf\text{-mb } P$   
**and**  $itT: is\text{-type } P\ T$   
**and**  $itU: is\text{-type } P\ U$   
**and**  $TwV: P \vdash T \leq V$   
**and**  $UwV: P \vdash U \leq V$   
**and**  $sup: sup\ P\ T\ U = OK\ W$   
**shows**  $widen\ P\ W\ V$

**proof** –

**{ assume**  $rT: is\text{-refT } T$   
**and**  $rU: is\text{-refT } U$   
**and**  $UNT: U \neq NT$   
**and**  $TNT: T \neq NT$   
**and**  $W: \text{exec-lub } (\text{widen1 } P)\ (\text{super } P)\ T\ U = W$   
**from**  $itU\ itT\ rT\ rU\ UNT\ TNT$  **have**  $P \vdash T \leq \text{Class Object } P \vdash U \leq \text{Class Object}$   
**by** (*auto intro: is-refType-widen-Object[OF wfP]*)  
**with**  $UNT\ TNT\ itT\ itU$   
**have**  $P \vdash T <^* \text{Class Object } P \vdash U <^* \text{Class Object}$   
**by** (*auto intro: widen-into-widen1-rtrancl[OF wfP]*)  
**with**  $\text{single-valued-widen1}[OF\ wfP]$   
**obtain**  $X$  **where**  $\text{lub: is-lub } ((\text{widen1 } P)^\wedge^*)\ T\ U\ X$   
**by** (*blast dest: single-valued-has-lubs*)  
**with**  $\text{acyclic-widen1}[OF\ wfP]$   
**have**  $\text{exec-lub } (\text{widen1 } P)\ (\text{super } P)\ T\ U = X$   
**by** (*blast intro: superI exec-lub-conv*)  
**also from**  $TwV\ TNT\ UwV\ UNT\ itT\ itU$  **have**  $P \vdash T <^* V\ P \vdash U <^* V$   
**by** (*auto intro: widen-into-widen1-rtrancl[OF wfP]*)  
**with**  $\text{lub}$  **have**  $P \vdash X <^* V$   
**by** (*clarsimp simp add: is-lub-def is-ub-def*)  
**finally have**  $P \vdash \text{exec-lub } (\text{widen1 } P)\ (\text{super } P)\ T\ U \leq V$   
**by** (*rule widen1-rtrancl-into-widen*)  
**with**  $W$  **have**  $P \vdash W \leq V$  **by** *simp* }  
**with**  $sup\ itT\ itU\ TwV\ UwV$  **show**  $?thesis$   
**by** (*simp add: sup-def split: if-split-asm*)

**qed**

**lemma** *sup-exists:*

$\llbracket \text{widen } P\ a\ c; \text{widen } P\ b\ c \rrbracket \implies \exists T. \text{sup } P\ a\ b = OK\ T$

**by**(cases  $b$  a rule:  $ty.exhaust[case-product\ ty.exhaust]$ )(auto simp add: sup-def)

**lemma** *err-semilat-JType-esl*:

**assumes**  $wf-prog$ :  $wf-prog\ wf-mb\ P$

**shows**  $err-semilat\ (esl\ P)$

**proof** –

**from**  $wf-prog$  **have**  $order\ (widen\ P)\ ..$

**moreover from**  $wf-prog$

**have**  $closed\ (err\ (types\ P))\ (lift2\ (sup\ P))$

**by** (rule  $closed-err-types$ )

**moreover**

**from**  $wf-prog$  **have**

$(\forall x \in err\ (types\ P). \forall y \in err\ (types\ P). x \sqsubseteq_{Err.le}\ (widen\ P)\ x \sqcup_{lift2}\ (sup\ P)\ y) \wedge$

$(\forall x \in err\ (types\ P). \forall y \in err\ (types\ P). y \sqsubseteq_{Err.le}\ (widen\ P)\ x \sqcup_{lift2}\ (sup\ P)\ y)$

**by**(auto simp add:  $lesub-def\ plussub-def\ Err.le-def\ lift2-def\ sup-widen-greater\ split: err.split$ )

**moreover from**  $wf-prog$  **have**

$\forall x \in err\ (types\ P). \forall y \in err\ (types\ P). \forall z \in err\ (types\ P).$

$x \sqsubseteq_{Err.le}\ (widen\ P)\ z \wedge y \sqsubseteq_{Err.le}\ (widen\ P)\ z \longrightarrow x \sqcup_{lift2}\ (sup\ P)\ y \sqsubseteq_{Err.le}\ (widen\ P)\ z$

**unfolding**  $lift2-def\ plussub-def\ lesub-def\ Err.le-def$

**by**(auto intro:  $sup-widen-smallest\ dest:sup-exists\ simp\ add: split: err.split$ )

**ultimately show**  $?thesis$  **by** (simp add:  $esl-def\ semilat-def\ sl-def\ Err.sl-def$ )

**qed**

### 3.16.1 Relation between $SemiType.sup\ P\ T\ U = OK\ V$ and $P \vdash lub(T, U) = V$

**lemma** *sup-is-lubI*:

**assumes**  $wf$ :  $wf-prog\ wf-md\ P$

**and**  $it$ :  $is-type\ P\ T\ is-type\ P\ U$

**and**  $sup$ :  $sup\ P\ T\ U = OK\ V$

**shows**  $P \vdash lub(T, U) = V$

**proof**

**from**  $sup-widen-greater[OF\ wf\ it\ sup]$

**show**  $P \vdash T \leq V\ P \vdash U \leq V$  **by**  $blast+$

**next**

**fix**  $T'$

**assume**  $P \vdash T \leq T'\ P \vdash U \leq T'$

**thus**  $P \vdash V \leq T'$  **using**  $sup$  **by**(rule  $sup-widen-smallest[OF\ wf\ it]$ )

**qed**

**lemma** *is-lub-subD*:

**assumes**  $wf$ :  $wf-prog\ wf-md\ P$

**and**  $it$ :  $is-type\ P\ T\ is-type\ P\ U$

**and**  $lub$ :  $P \vdash lub(T, U) = V$

**shows**  $sup\ P\ T\ U = OK\ V$

**proof** –

**from**  $lub$  **have**  $P \vdash T \leq V\ P \vdash U \leq V$  **by**( $blast\ dest: is-lub-upper$ ) $+$

**from**  $sup-exists[OF\ this]$  **obtain**  $W$  **where**  $sup\ P\ T\ U = OK\ W$  **by**  $blast$

**moreover**

**with**  $wf\ it$  **have**  $P \vdash lub(T, U) = W$  **by**(rule  $sup-is-lubI$ )

**with**  $lub$  **have**  $V = W$  **by**(auto  $dest: is-lub-unique[OF\ wf]$ )

**ultimately show**  $?thesis$  **by**  $simp$

**qed**

**lemma** *is-lub-is-type*:

$\llbracket wf\text{-}prog\ wf\text{-}md\ P; is\text{-}type\ P\ T; is\text{-}type\ P\ U; P \vdash lub(T, U) = V \rrbracket \implies is\text{-}type\ P\ V$   
**by**(*frule* (3) *is-lub-subD*)(*erule* (3) *sup-is-type*)

### 3.16.2 Code generator setup

**code-pred** *widen1p* .

**lemmas** [*code*] = *widen1-def*

**lemma** *eval-widen1p-i-i-o-conv*:

*Predicate.eval* (*widen1p-i-i-o* *P* *T*) = ( $\lambda U. P \vdash T <^1 U$ )

**by**(*auto elim*: *widen1p-i-i-oE* *intro*: *widen1p-i-i-oI* *simp add*: *widen1-def fun-eq-iff*)

**lemma** *rtrancl-widen1-code* [*code-unfold*]:

$(widen1\ P)^{\wedge*} = \{(a, b). Predicate.holds\ (rtrancl\text{-}tab\text{-}FioB\text{-}i\text{-}i\ (widen1p\text{-}i\text{-}i\text{-}o\ P)\ []\ a\ b)\}$

**by**(*auto simp add*: *fun-eq-iff Predicate.holds-eq widen1-def rtrancl-def rtranclp-eq-rtrancl-tab-nil eval-widen1p-i-i-o-conv*  
*intro*!: *rtrancl-tab-FioB-i-i-iE* *elim*!: *rtrancl-tab-FioB-i-i-iE*)

**declare** *exec-lub-def* [*code-unfold*]

**end**

**theory** *Common-Main*

**imports**

*../Basic/Auxiliary*

*../Framework/FWProgress*

*../Framework/FWBisimDeadlock*

*../Framework/FWBisimLift*

*../DFA/Abstract-BV*

*ExternalCallWF*

*ConformThreaded*

*BinOp*

*SemiType*

**begin**

**end**

## Chapter 4

# JinjaThreads source language

### 4.1 Program State

```
theory State
imports
  ../Common/Heap
begin

type-synonym
  'addr locals = vname  $\rightarrow$  'addr val    — local vars, incl. params and “this”
type-synonym
  ('addr, 'heap) Jstate = 'heap  $\times$  'addr locals    — the heap and the local vars

definition hp :: 'heap  $\times$  'x  $\Rightarrow$  'heap where hp  $\equiv$  fst

definition lcl :: 'heap  $\times$  'x  $\Rightarrow$  'x where lcl  $\equiv$  snd

lemma hp-conv [simp]: hp (h, l) = h
by(simp add: hp-def)

lemma lcl-conv [simp]: lcl (h, l) = l
by(simp add: lcl-def)

end
```

### 4.2 Expressions

```
theory Expr
imports
  ../Common/BinOp
begin

datatype (dead 'a, dead 'b, dead 'addr) exp
  = new cname      — class instance creation
  | newArray ty ('a,'b,'addr) exp (newA -[-] [99,0] 90)    — array instance creation: type, size in
outermost dimension
  | Cast ty ('a,'b,'addr) exp    — type cast
  | InstanceOf ('a,'b,'addr) exp ty (- instanceof - [99, 99] 90) — instance of
```

| *Val* 'addr *val* — value  
 | *BinOp* ('a,'b,'addr) *exp* *bop* ('a,'b,'addr) *exp* (- «-» - [80,0,81] 80) — binary operation  
 | *Var* 'a — local variable (incl. parameter)  
 | *LAss* 'a ('a,'b,'addr) *exp* (-:= - [90,90] 90) — local assignment  
 | *AAcc* ('a,'b,'addr) *exp* ('a,'b,'addr) *exp* (-[-] [99,0] 90) — array cell read  
 | *AAss* ('a,'b,'addr) *exp* ('a,'b,'addr) *exp* ('a,'b,'addr) *exp* (-[-] := - [10,99,90] 90) — array cell  
 assignment  
 | *ALen* ('a,'b,'addr) *exp* (--length [10] 90) — array length  
 | *FAcc* ('a,'b,'addr) *exp* *vname* *cname* (--{-} [10,90,99] 90) — field access  
 | *FAss* ('a,'b,'addr) *exp* *vname* *cname* ('a,'b,'addr) *exp* (--{-} := - [10,90,99,90] 90) — field  
 assignment  
 | *CompareAndSwap* ('a,'b,'addr) *exp* *cname* *vname* ('a,'b,'addr) *exp* ('a,'b,'addr) *exp* (--compareAndSwap('(-, -, -)) [10,90,90,90,90] 90) — compare and swap  
 | *Call* ('a,'b,'addr) *exp* *mname* ('a,'b,'addr) *exp* *list* (--'(-) [90,99,0] 90) — method call  
 | *Block* 'a *ty* 'addr *val* *option* ('a,'b,'addr) *exp* ('{-:=; -})  
 | *Synchronized* 'b ('a,'b,'addr) *exp* ('a,'b,'addr) *exp* (*sync*- '(-) - [99,99,90] 90)  
 | *InSynchronized* 'b 'addr ('a,'b,'addr) *exp* (*insync*- '(-) - [99,99,90] 90)  
 | *Seq* ('a,'b,'addr) *exp* ('a,'b,'addr) *exp* (-;;/ - [61,60] 60)  
 | *Cond* ('a,'b,'addr) *exp* ('a,'b,'addr) *exp* ('a,'b,'addr) *exp* (*if* '(-) -/ *else* - [80,79,79] 70)  
 | *While* ('a,'b,'addr) *exp* ('a,'b,'addr) *exp* (*while* '(-) - [80,79] 70)  
 | *throw* ('a,'b,'addr) *exp*  
 | *TryCatch* ('a,'b,'addr) *exp* *cname* 'a ('a,'b,'addr) *exp* (*try* -/ *catch* '(-) - [0,99,80,79] 70)

#### type-synonym

'addr *expr* = (*vname*, *unit*, 'addr) *exp* — Jinja expression

#### type-synonym

'addr *J-mb* = *vname* *list* × 'addr *expr* — Jinja method body: parameter names and expression

#### type-synonym

'addr *J-prog* = 'addr *J-mb* *prog* — Jinja program

#### translations

(*type*) 'addr *expr* ≤= (*type*) (*String.literal*, *unit*, 'addr) *exp*  
 (*type*) 'addr *J-prog* ≤= (*type*) (*String.literal* *list* × 'addr *expr*) *prog*

### 4.2.1 Syntactic sugar

**abbreviation** *unit* :: ('a,'b,'addr) *exp*

**where** *unit* ≡ *Val Unit*

**abbreviation** *null* :: ('a,'b,'addr) *exp*

**where** *null* ≡ *Val Null*

**abbreviation** *addr* :: 'addr ⇒ ('a,'b,'addr) *exp*

**where** *addr* *a* == *Val (Addr a)*

**abbreviation** *true* :: ('a,'b,'addr) *exp*

**where** *true* == *Val (Bool True)*

**abbreviation** *false* :: ('a,'b,'addr) *exp*

**where** *false* == *Val (Bool False)*

**abbreviation** *Throw* :: 'addr ⇒ ('a,'b,'addr) *exp*

**where** *Throw* *a* == *throw (Val (Addr a))*



**abbreviation** (in *heap-base*) *THROW* :: *cname*  $\Rightarrow$  ('a,'b,'addr) *exp*  
**where** *THROW* *xc* == *Throw* (*addr-of-sys-xcpt* *xc*)

**abbreviation** *sync-unit-syntax* :: ('a,unit,'addr) *exp*  $\Rightarrow$  ('a,unit,'addr) *exp*  $\Rightarrow$  ('a,unit,'addr) *exp*  
(*sync*'(-) - [99,90] 90)  
**where** *sync*(*e1*) *e2*  $\equiv$  *sync*<sub>()</sub> (*e1*) *e2*

**abbreviation** *insync-unit-syntax* :: 'addr  $\Rightarrow$  ('a,unit,'addr) *exp*  $\Rightarrow$  ('a,unit,'addr) *exp* (*insync*'(-) - [99,90] 90)  
**where** *insync*(*a*) *e2*  $\equiv$  *insync*<sub>()</sub> (*a*) *e2*

Java syntax for binary operators

**abbreviation** *BinOp-Eq* :: ('a, 'b, 'c) *exp*  $\Rightarrow$  ('a, 'b, 'c) *exp*  $\Rightarrow$  ('a, 'b, 'c) *exp*  
(- «==» - [80,81] 80)  
**where** *e* «==» *e'*  $\equiv$  *e* «Eq» *e'*

**abbreviation** *BinOp-NotEq* :: ('a, 'b, 'c) *exp*  $\Rightarrow$  ('a, 'b, 'c) *exp*  $\Rightarrow$  ('a, 'b, 'c) *exp*  
(- «!=» - [80,81] 80)  
**where** *e* «!=» *e'*  $\equiv$  *e* «NotEq» *e'*

**abbreviation** *BinOp-LessThan* :: ('a, 'b, 'c) *exp*  $\Rightarrow$  ('a, 'b, 'c) *exp*  $\Rightarrow$  ('a, 'b, 'c) *exp*  
(- «<» - [80,81] 80)  
**where** *e* «<» *e'*  $\equiv$  *e* «LessThan» *e'*

**abbreviation** *BinOp-LessOrEqual* :: ('a, 'b, 'c) *exp*  $\Rightarrow$  ('a, 'b, 'c) *exp*  $\Rightarrow$  ('a, 'b, 'c) *exp*  
(- «<=» - [80,81] 80)  
**where** *e* «<=» *e'*  $\equiv$  *e* «LessOrEqual» *e'*

**abbreviation** *BinOp-GreaterThan* :: ('a, 'b, 'c) *exp*  $\Rightarrow$  ('a, 'b, 'c) *exp*  $\Rightarrow$  ('a, 'b, 'c) *exp*  
(- «>» - [80,81] 80)  
**where** *e* «>» *e'*  $\equiv$  *e* «GreaterThan» *e'*

**abbreviation** *BinOp-GreaterOrEqual* :: ('a, 'b, 'c) *exp*  $\Rightarrow$  ('a, 'b, 'c) *exp*  $\Rightarrow$  ('a, 'b, 'c) *exp*  
(- «>=» - [80,81] 80)  
**where** *e* «>=» *e'*  $\equiv$  *e* «GreaterOrEqual» *e'*

**abbreviation** *BinOp-Add* :: ('a, 'b, 'c) *exp*  $\Rightarrow$  ('a, 'b, 'c) *exp*  $\Rightarrow$  ('a, 'b, 'c) *exp*  
(- «+» - [80,81] 80)  
**where** *e* «+» *e'*  $\equiv$  *e* «Add» *e'*

**abbreviation** *BinOp-Subtract* :: ('a, 'b, 'c) *exp*  $\Rightarrow$  ('a, 'b, 'c) *exp*  $\Rightarrow$  ('a, 'b, 'c) *exp*  
(- «-» - [80,81] 80)  
**where** *e* «-» *e'*  $\equiv$  *e* «Subtract» *e'*

**abbreviation** *BinOp-Mult* :: ('a, 'b, 'c) *exp*  $\Rightarrow$  ('a, 'b, 'c) *exp*  $\Rightarrow$  ('a, 'b, 'c) *exp*  
(- «\*» - [80,81] 80)  
**where** *e* «\*» *e'*  $\equiv$  *e* «Mult» *e'*

**abbreviation** *BinOp-Div* :: ('a, 'b, 'c) *exp*  $\Rightarrow$  ('a, 'b, 'c) *exp*  $\Rightarrow$  ('a, 'b, 'c) *exp*  
(- «/» - [80,81] 80)  
**where** *e* «/» *e'*  $\equiv$  *e* «Div» *e'*

**abbreviation** *BinOp-Mod* :: ('a, 'b, 'c) *exp*  $\Rightarrow$  ('a, 'b, 'c) *exp*  $\Rightarrow$  ('a, 'b, 'c) *exp*  
(- «%» - [80,81] 80)

**where**  $e \llbracket \% \rrbracket e' \equiv e \llbracket Mod \rrbracket e'$

**abbreviation**  $BinOp\text{-}BinAnd :: ('a, 'b, 'c) exp \Rightarrow ('a, 'b, 'c) exp \Rightarrow ('a, 'b, 'c) exp$   
 $(- \llbracket \& \rrbracket - [80,81] 80)$

**where**  $e \llbracket \& \rrbracket e' \equiv e \llbracket BinAnd \rrbracket e'$

**abbreviation**  $BinOp\text{-}BinOr :: ('a, 'b, 'c) exp \Rightarrow ('a, 'b, 'c) exp \Rightarrow ('a, 'b, 'c) exp$   
 $(- \llbracket | \rrbracket - [80,81] 80)$

**where**  $e \llbracket | \rrbracket e' \equiv e \llbracket BinOr \rrbracket e'$

**abbreviation**  $BinOp\text{-}BinXor :: ('a, 'b, 'c) exp \Rightarrow ('a, 'b, 'c) exp \Rightarrow ('a, 'b, 'c) exp$   
 $(- \llbracket \wedge \rrbracket - [80,81] 80)$

**where**  $e \llbracket \wedge \rrbracket e' \equiv e \llbracket BinXor \rrbracket e'$

**abbreviation**  $BinOp\text{-}ShiftLeft :: ('a, 'b, 'c) exp \Rightarrow ('a, 'b, 'c) exp \Rightarrow ('a, 'b, 'c) exp$   
 $(- \llbracket << \rrbracket - [80,81] 80)$

**where**  $e \llbracket << \rrbracket e' \equiv e \llbracket ShiftLeft \rrbracket e'$

**abbreviation**  $BinOp\text{-}ShiftRightZeros :: ('a, 'b, 'c) exp \Rightarrow ('a, 'b, 'c) exp \Rightarrow ('a, 'b, 'c) exp$   
 $(- \llbracket >>> \rrbracket - [80,81] 80)$

**where**  $e \llbracket >>> \rrbracket e' \equiv e \llbracket ShiftRightZeros \rrbracket e'$

**abbreviation**  $BinOp\text{-}ShiftRightSigned :: ('a, 'b, 'c) exp \Rightarrow ('a, 'b, 'c) exp \Rightarrow ('a, 'b, 'c) exp$   
 $(- \llbracket >> \rrbracket - [80,81] 80)$

**where**  $e \llbracket >> \rrbracket e' \equiv e \llbracket ShiftRightSigned \rrbracket e'$

**abbreviation**  $BinOp\text{-}CondAnd :: ('a, 'b, 'c) exp \Rightarrow ('a, 'b, 'c) exp \Rightarrow ('a, 'b, 'c) exp$   
 $(- \llbracket \&\& \rrbracket - [80,81] 80)$

**where**  $e \llbracket \&\& \rrbracket e' \equiv \text{if } (e) \text{ } e' \text{ else false}$

**abbreviation**  $BinOp\text{-}CondOr :: ('a, 'b, 'c) exp \Rightarrow ('a, 'b, 'c) exp \Rightarrow ('a, 'b, 'c) exp$   
 $(- \llbracket || \rrbracket - [80,81] 80)$

**where**  $e \llbracket || \rrbracket e' \equiv \text{if } (e) \text{ true else } e'$

**lemma**  $\text{inj-Val } [simp]: \text{inj Val}$

**by**(rule  $\text{inj-onI}$ )(simp)

**lemma**  $\text{expr-ineqs } [simp]: \text{Val } v ;; e \neq e \text{ if } (e1) \text{ } e \text{ else } e2 \neq e \text{ if } (e1) \text{ } e2 \text{ else } e \neq e$

**by**(induct  $e$ ) auto

## 4.2.2 Free Variables

**primrec**  $fv :: ('a, 'b, 'addr) exp \Rightarrow 'a \text{ set}$

**and**  $fvs :: ('a, 'b, 'addr) exp \text{ list} \Rightarrow 'a \text{ set}$

**where**

$fv(\text{new } C) = \{\}$   
 $| fv(\text{newA } T[e]) = fv \text{ } e$   
 $| fv(\text{Cast } C \text{ } e) = fv \text{ } e$   
 $| fv(e \text{ instanceof } T) = fv \text{ } e$   
 $| fv(\text{Val } v) = \{\}$   
 $| fv(e_1 \llbracket bop \rrbracket e_2) = fv \text{ } e_1 \cup fv \text{ } e_2$   
 $| fv(\text{Var } V) = \{V\}$   
 $| fv(a[i]) = fv \text{ } a \cup fv \text{ } i$   
 $| fv(AAss \text{ } a \text{ } i \text{ } e) = fv \text{ } a \cup fv \text{ } i \cup fv \text{ } e$

$| \text{fv}(a \cdot \text{length}) = \text{fv } a$   
 $| \text{fv}(\text{LAss } V \ e) = \{V\} \cup \text{fv } e$   
 $| \text{fv}(e \cdot F\{D\}) = \text{fv } e$   
 $| \text{fv}(\text{FAss } e_1 \ F \ D \ e_2) = \text{fv } e_1 \cup \text{fv } e_2$   
 $| \text{fv}(e_1 \cdot \text{compareAndSwap}(D \cdot F, \ e_2, \ e_3)) = \text{fv } e_1 \cup \text{fv } e_2 \cup \text{fv } e_3$   
 $| \text{fv}(e \cdot M(es)) = \text{fv } e \cup \text{fvs } es$   
 $| \text{fv}(\{V : T = \text{vo}; \ e\}) = \text{fv } e - \{V\}$   
 $| \text{fv}(\text{sync}_V(h) \ e) = \text{fv } h \cup \text{fv } e$   
 $| \text{fv}(\text{insync}_V(a) \ e) = \text{fv } e$   
 $| \text{fv}(e_1;;e_2) = \text{fv } e_1 \cup \text{fv } e_2$   
 $| \text{fv}(\text{if } (b) \ e_1 \ \text{else } e_2) = \text{fv } b \cup \text{fv } e_1 \cup \text{fv } e_2$   
 $| \text{fv}(\text{while } (b) \ e) = \text{fv } b \cup \text{fv } e$   
 $| \text{fv}(\text{throw } e) = \text{fv } e$   
 $| \text{fv}(\text{try } e_1 \ \text{catch}(C \ V) \ e_2) = \text{fv } e_1 \cup (\text{fv } e_2 - \{V\})$

$| \text{fvs}(\square) = \{\}$   
 $| \text{fvs}(e \# es) = \text{fv } e \cup \text{fvs } es$

**lemma** [simp]:  $\text{fvs}(es \ @ \ es') = \text{fvs } es \cup \text{fvs } es'$

**by** (induct es) auto

**lemma** [simp]:  $\text{fvs}(\text{map } \text{Val } vs) = \{\}$

**by** (induct vs) auto

### 4.2.3 Locks and addresses

**primrec**  $\text{expr-locks} :: ('a, 'b, 'addr) \text{exp} \Rightarrow 'addr \Rightarrow \text{nat}$

**and**  $\text{expr-lockss} :: ('a, 'b, 'addr) \text{exp list} \Rightarrow 'addr \Rightarrow \text{nat}$

**where**

$\text{expr-locks } (\text{new } C) = (\lambda ad. \ 0)$   
 $| \text{expr-locks } (\text{newA } T[e]) = \text{expr-locks } e$   
 $| \text{expr-locks } (\text{Cast } T \ e) = \text{expr-locks } e$   
 $| \text{expr-locks } (e \ \text{instanceof } T) = \text{expr-locks } e$   
 $| \text{expr-locks } (\text{Val } v) = (\lambda ad. \ 0)$   
 $| \text{expr-locks } (\text{Var } v) = (\lambda ad. \ 0)$   
 $| \text{expr-locks } (e \ \ll \text{bop} \gg \ e') = (\lambda ad. \ \text{expr-locks } e \ ad + \text{expr-locks } e' \ ad)$   
 $| \text{expr-locks } (V := e) = \text{expr-locks } e$   
 $| \text{expr-locks } (a[i]) = (\lambda ad. \ \text{expr-locks } a \ ad + \text{expr-locks } i \ ad)$   
 $| \text{expr-locks } (\text{AAss } a \ i \ e) = (\lambda ad. \ \text{expr-locks } a \ ad + \text{expr-locks } i \ ad + \text{expr-locks } e \ ad)$   
 $| \text{expr-locks } (a \cdot \text{length}) = \text{expr-locks } a$   
 $| \text{expr-locks } (e \cdot F\{D\}) = \text{expr-locks } e$   
 $| \text{expr-locks } (\text{FAss } e \ F \ D \ e') = (\lambda ad. \ \text{expr-locks } e \ ad + \text{expr-locks } e' \ ad)$   
 $| \text{expr-locks } (e \cdot \text{compareAndSwap}(D \cdot F, \ e', \ e'')) = (\lambda ad. \ \text{expr-locks } e \ ad + \text{expr-locks } e' \ ad + \text{expr-locks } e'' \ ad)$   
 $| \text{expr-locks } (e \cdot m(ps)) = (\lambda ad. \ \text{expr-locks } e \ ad + \text{expr-lockss } ps \ ad)$   
 $| \text{expr-locks } (\{V : T = \text{vo}; \ e\}) = \text{expr-locks } e$   
 $| \text{expr-locks } (\text{sync}_V(o') \ e) = (\lambda ad. \ \text{expr-locks } o' \ ad + \text{expr-locks } e \ ad)$   
 $| \text{expr-locks } (\text{insync}_V(a) \ e) = (\lambda ad. \ \text{if } (a = ad) \ \text{then } \text{Suc } (\text{expr-locks } e \ ad) \ \text{else } \text{expr-locks } e \ ad)$   
 $| \text{expr-locks } (e;;e') = (\lambda ad. \ \text{expr-locks } e \ ad + \text{expr-locks } e' \ ad)$   
 $| \text{expr-locks } (\text{if } (b) \ e \ \text{else } e') = (\lambda ad. \ \text{expr-locks } b \ ad + \text{expr-locks } e \ ad + \text{expr-locks } e' \ ad)$   
 $| \text{expr-locks } (\text{while } (b) \ e) = (\lambda ad. \ \text{expr-locks } b \ ad + \text{expr-locks } e \ ad)$   
 $| \text{expr-locks } (\text{throw } e) = \text{expr-locks } e$   
 $| \text{expr-locks } (\text{try } e \ \text{catch}(C \ v) \ e') = (\lambda ad. \ \text{expr-locks } e \ ad + \text{expr-locks } e' \ ad)$

|  $\text{expr-lockss } [] = (\lambda a. 0)$   
 |  $\text{expr-lockss } (x \# xs) = (\lambda ad. \text{expr-locks } x \text{ } ad + \text{expr-lockss } xs \text{ } ad)$

**lemma**  $\text{expr-lockss-append}$  [simp]:

$\text{expr-lockss } (es @ es') = (\lambda ad. \text{expr-lockss } es \text{ } ad + \text{expr-lockss } es' \text{ } ad)$

**by**(*induct es*) *auto*

**lemma**  $\text{expr-lockss-map-Val}$  [simp]:  $\text{expr-lockss } (\text{map Val } vs) = (\lambda ad. 0)$

**by**(*induct vs*) *auto*

**primrec**  $\text{contains-insync} :: ('a, 'b, 'addr) \text{exp} \Rightarrow \text{bool}$

**and**  $\text{contains-insyncs} :: ('a, 'b, 'addr) \text{exp list} \Rightarrow \text{bool}$

**where**

$\text{contains-insync } (\text{new } C) = \text{False}$

|  $\text{contains-insync } (\text{newA } T [i]) = \text{contains-insync } i$

|  $\text{contains-insync } (\text{Cast } T \text{ } e) = \text{contains-insync } e$

|  $\text{contains-insync } (e \text{ instanceof } T) = \text{contains-insync } e$

|  $\text{contains-insync } (\text{Val } v) = \text{False}$

|  $\text{contains-insync } (\text{Var } v) = \text{False}$

|  $\text{contains-insync } (e \llcorner e') = (\text{contains-insync } e \vee \text{contains-insync } e')$

|  $\text{contains-insync } (V := e) = \text{contains-insync } e$

|  $\text{contains-insync } (a[i]) = (\text{contains-insync } a \vee \text{contains-insync } i)$

|  $\text{contains-insync } (\text{AAss } a \text{ } i \text{ } e) = (\text{contains-insync } a \vee \text{contains-insync } i \vee \text{contains-insync } e)$

|  $\text{contains-insync } (a \cdot \text{length}) = \text{contains-insync } a$

|  $\text{contains-insync } (e \cdot F\{D\}) = \text{contains-insync } e$

|  $\text{contains-insync } (F\text{Ass } e \text{ } F \text{ } D \text{ } e') = (\text{contains-insync } e \vee \text{contains-insync } e')$

|  $\text{contains-insync } (e \cdot \text{compareAndSwap}(D \cdot F, e', e'')) = (\text{contains-insync } e \vee \text{contains-insync } e' \vee \text{contains-insync } e'')$

|  $\text{contains-insync } (e \cdot m(pns)) = (\text{contains-insync } e \vee \text{contains-insyncs } pns)$

|  $\text{contains-insync } (\{V : T = vo; e\}) = \text{contains-insync } e$

|  $\text{contains-insync } (\text{sync}_V(o') \text{ } e) = (\text{contains-insync } o' \vee \text{contains-insync } e)$

|  $\text{contains-insync } (\text{insync}_V(a) \text{ } e) = \text{True}$

|  $\text{contains-insync } (e;; e') = (\text{contains-insync } e \vee \text{contains-insync } e')$

|  $\text{contains-insync } (\text{if } (b) \text{ } e \text{ else } e') = (\text{contains-insync } b \vee \text{contains-insync } e \vee \text{contains-insync } e')$

|  $\text{contains-insync } (\text{while } (b) \text{ } e) = (\text{contains-insync } b \vee \text{contains-insync } e)$

|  $\text{contains-insync } (\text{throw } e) = \text{contains-insync } e$

|  $\text{contains-insync } (\text{try } e \text{ catch } (C \text{ } v) \text{ } e') = (\text{contains-insync } e \vee \text{contains-insync } e')$

|  $\text{contains-insyncs } [] = \text{False}$

|  $\text{contains-insyncs } (x \# xs) = (\text{contains-insync } x \vee \text{contains-insyncs } xs)$

**lemma**  $\text{contains-insyncs-append}$  [simp]:

$\text{contains-insyncs } (es @ es') \longleftrightarrow \text{contains-insyncs } es \vee \text{contains-insyncs } es'$

**by**(*induct es*, *auto*)

**lemma**  $\text{fixes } e :: ('a, 'b, 'addr) \text{exp}$

**and**  $es :: ('a, 'b, 'addr) \text{exp list}$

**shows**  $\text{contains-insync-conv}: (\text{contains-insync } e \longleftrightarrow (\exists ad. \text{expr-locks } e \text{ } ad > 0))$

**and**  $\text{contains-insyncs-conv}: (\text{contains-insyncs } es \longleftrightarrow (\exists ad. \text{expr-lockss } es \text{ } ad > 0))$

**by**(*induct e and es rule: expr-locks.induct expr-lockss.induct*)(*auto*)

**lemma**  $\text{contains-insyncs-map-Val}$  [simp]:  $\neg \text{contains-insyncs } (\text{map Val } vs)$

**by**(*induct vs*) *auto*

### 4.2.4 Value expressions

**inductive** *is-val* :: ('a, 'b, 'addr) exp  $\Rightarrow$  bool **where**  
*is-val* (Val v)

**declare** *is-val.intros* [simp]  
**declare** *is-val.cases* [elim!]

**lemma** *is-val-iff*: *is-val* e  $\longleftrightarrow$  ( $\exists$  v. e = Val v)  
**by**(auto)

**code-pred** *is-val* .

**fun** *is-vals* :: ('a, 'b, 'addr) exp list  $\Rightarrow$  bool **where**  
*is-vals* [] = True  
| *is-vals* (e#es) = (*is-val* e  $\wedge$  *is-vals* es)

**lemma** *is-vals-append* [simp]: *is-vals* (es @ es')  $\longleftrightarrow$  *is-vals* es  $\wedge$  *is-vals* es'  
**by**(induct es) auto

**lemma** *is-vals-conv*: *is-vals* es = ( $\exists$  vs. es = map Val vs)  
**by**(induct es)(auto simp add: Cons-eq-map-conv)

**lemma** *is-vals-map-Val*s [simp]: *is-vals* (map Val vs) = True  
**unfolding** *is-vals-conv* **by** auto

**inductive** *is-addr* :: ('a, 'b, 'addr) exp  $\Rightarrow$  bool  
**where** *is-addr* (addr a)

**declare** *is-addr.intros*[intro!]  
**declare** *is-addr.cases*[elim!]

**lemma** [simp]: (*is-addr* e)  $\longleftrightarrow$  ( $\exists$  a. e = addr a)  
**by** auto

**primrec** *the-Val* :: ('a, 'b, 'addr) exp  $\Rightarrow$  'addr val  
**where**  
*the-Val* (Val v) = v

**inductive** *is-Throws* :: ('a, 'b, 'addr) exp list  $\Rightarrow$  bool  
**where**  
*is-Throws* (Throw a # es)  
| *is-Throws* es  $\Longrightarrow$  *is-Throws* (Val v # es)

**inductive-simps** *is-Throws-simps*:  
*is-Throws* []  
*is-Throws* (e # es)

**code-pred** *is-Throws* .

**lemma** *is-Throws-conv*: *is-Throws* es  $\longleftrightarrow$  ( $\exists$  vs a es'. es = map Val vs @ Throw a # es')  
(**is** ?lhs  $\longleftrightarrow$  ?rhs)

**proof**  
**assume** ?lhs **thus** ?rhs

```

  by(induct)(fastforce simp add: Cons-eq-append-conv Cons-eq-map-conv)+
next
  assume ?rhs thus ?lhs
  by(induct es)(auto simp add: is-Throws-simps Cons-eq-map-conv Cons-eq-append-conv)
qed

```

#### 4.2.5 blocks

```

fun blocks :: 'a list  $\Rightarrow$  ty list  $\Rightarrow$  'addr val list  $\Rightarrow$  ('a,'b,'addr) exp  $\Rightarrow$  ('a,'b,'addr) exp
where
  blocks (V # Vs) (T # Ts) (v # vs) e = { V:T=[v]; blocks Vs Ts vs e }
| blocks [] [] [] e = e

```

**lemma** [simp]:

```

 $\llbracket \text{size } vs = \text{size } Vs; \text{size } Ts = \text{size } Vs \rrbracket \Longrightarrow \text{fv } (\text{blocks } Vs \ Ts \ vs \ e) = \text{fv } e - \text{set } Vs$ 
by(induct rule:blocks.induct)(simp-all, blast)

```

**lemma** expr-locks-blocks:

```

 $\llbracket \text{length } vs = \text{length } pns; \text{length } Ts = \text{length } pns \rrbracket$ 
 $\Longrightarrow \text{expr-locks } (\text{blocks } pns \ Ts \ vs \ e) = \text{expr-locks } e$ 
by(induct pns Ts vs e rule: blocks.induct)(auto)

```

#### 4.2.6 Final expressions

**inductive** final :: ('a,'b,'addr) exp  $\Rightarrow$  bool **where**

```

  final (Val v)
| final (Throw a)

```

**declare** final.cases [elim]

**declare** final.intros[simp]

**lemmas** finalE[consumes 1, case-names Val Throw] = final.cases

**lemma** final-iff: final e  $\longleftrightarrow (\exists v. e = \text{Val } v) \vee (\exists a. e = \text{Throw } a)$   
**by**(auto)

**lemma** final-locks: final e  $\Longrightarrow \text{expr-locks } e \ l = 0$   
**by**(auto elim: finalE)

**inductive** finals :: ('a,'b,'addr) exp list  $\Rightarrow$  bool

**where**

```

  finals []
| finals (Throw a # es)
| finals es  $\Longrightarrow$  finals (Val v # es)

```

**inductive-simps** finals-simps:

```

  finals (e # es)

```

**lemma** [iff]: finals []

**by**(rule finals.intros)

**lemma** [iff]: finals (Val v # es) = finals es

**by**(simp add: finals-simps)

**lemma** *finals-app-map* [iff]: *finals* (map Val vs @ es) = *finals* es  
**by**(*induct* vs) *simp-all*

**lemma** [iff]: *finals* (throw e # es) = ( $\exists a. e = \text{addr } a$ )  
**by**(*simp* add: *finals-simps*)

**lemma** *not-finals-ConsI*:  $\neg \text{final } e \implies \neg \text{finals } (e \# es)$   
**by**(*simp* add: *finals-simps* *final-iff*)

**lemma** *finals-iff*: *finals* es  $\longleftrightarrow (\exists \text{vs}. es = \text{map Val vs}) \vee (\exists \text{vs } a \text{ es}'. es = \text{map Val vs @ Throw } a \# es')$

(*is* ?lhs  $\longleftrightarrow$  ?rhs)

**proof**

assume ?lhs **thus** ?rhs

by *induct*(auto *simp* add: *Cons-eq-append-conv* *Cons-eq-map-conv*, *metis*)

**next**

assume ?rhs **thus** ?lhs **by**(*induct* es) *auto*

**qed**

**code-pred** *final* .

## 4.2.7 converting results from external calls

**primrec** *extRet2J* :: ('a, 'b, 'addr) exp  $\Rightarrow$  'addr *extCallRet*  $\Rightarrow$  ('a, 'b, 'addr) exp  
**where**

*extRet2J* e (*RetVal* v) = Val v  
| *extRet2J* e (*RetExc* a) = Throw a  
| *extRet2J* e *RetStaySame* = e

**lemma** *fv-extRet2J* [*simp*]: *fv* (*extRet2J* e va)  $\subseteq$  *fv* e  
**by**(*cases* va) *simp-all*

## 4.2.8 expressions at a call

**primrec** *call* :: ('a, 'b, 'addr) exp  $\Rightarrow$  ('addr  $\times$  mname  $\times$  'addr val list) option  
**and** *calls* :: ('a, 'b, 'addr) exp list  $\Rightarrow$  ('addr  $\times$  mname  $\times$  'addr val list) option  
**where**

*call* (new C) = None  
| *call* (newA T[e]) = *call* e  
| *call* (Cast C e) = *call* e  
| *call* (e instanceof T) = *call* e  
| *call* (Val v) = None  
| *call* (Var V) = None  
| *call* (V:=e) = *call* e  
| *call* (e «bop» e') = (if *is-val* e then *call* e' else *call* e)  
| *call* (a[i]) = (if *is-val* a then *call* i else *call* a)  
| *call* (AAss a i e) = (if *is-val* a then (if *is-val* i then *call* e else *call* i) else *call* a)  
| *call* (a.length) = *call* a  
| *call* (e.F{D}) = *call* e  
| *call* (FAss e F D e') = (if *is-val* e then *call* e' else *call* e)  
| *call* (e.compareAndSwap(D.F, e', e'')) = (if *is-val* e then if *is-val* e' then *call* e'' else *call* e' else *call* e)  
| *call* (e.M(es)) = (if *is-val* e then  
(if *is-vals* es  $\wedge$  *is-addr* e then  $\lfloor$ (THE a. e = *addr* a, M, THE vs. es = map Val vs) $\rfloor$ )

```

else calls es)
      else call e)
| call ({ V:T=vo; e }) = call e
| call (syncV (o') e) = call o'
| call (insyncV (a) e) = call e
| call (e;;e') = call e
| call (if (e) e1 else e2) = call e
| call (while(b) e) = None
| call (throw e) = call e
| call (try e1 catch(C V) e2) = call e1

| calls [] = None
| calls (e#es) = (if is-val e then calls es else call e)

```

**lemma** *calls-append* [simp]:

*calls (es @ es') = (if calls es = None ∧ is-vals es then calls es' else calls es)*

**by**(*induct es*) *auto*

**lemma** *call-callE* [consumes 1, case-names *CallObj CallParams Call*]:

$\llbracket \text{call } (\text{obj} \cdot M(\text{pns})) \rrbracket = \lfloor (a, M', \text{vs}) \rfloor;$   
 $\text{call obj} = \lfloor (a, M', \text{vs}) \rfloor \implies \text{thesis};$   
 $\bigwedge v. \llbracket \text{obj} = \text{Val } v; \text{calls pns} = \lfloor (a, M', \text{vs}) \rfloor \rrbracket \implies \text{thesis};$   
 $\llbracket \text{obj} = \text{addr } a; \text{pns} = \text{map Val vs}; M = M' \rrbracket \implies \text{thesis} \rrbracket \implies \text{thesis}$

**by**(*auto split: if-split-asm simp add: is-vals-conv*)

**lemma** *calls-map-Val* [simp]:

*calls (map Val vs) = None*

**by**(*induct vs*) *auto*

**lemma** *call-not-is-val* [dest]: *call e =  $\lfloor aMvs \rfloor \implies \neg \text{is-val } e$*

**by**(*cases e*) *auto*

**lemma** *is-calls-not-is-vals* [dest]: *calls es =  $\lfloor aMvs \rfloor \implies \neg \text{is-vals es}$*

**by**(*induct es*) *auto*

**end**

### 4.3 Abstract heap locales for source code programs

**theory** *JHeap*

**imports**

*../Common/Conform*

*Expr*

**begin**

**locale** *J-heap-base* = *heap-base* +

**constrains** *addr2thread-id* :: ('addr :: addr)  $\Rightarrow$  'thread-id

**and** *thread-id2addr* :: 'thread-id  $\Rightarrow$  'addr

**and** *spurious-wakeups* :: bool

**and** *empty-heap* :: 'heap

**and** *allocate* :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set

**and** *typeof-addr* :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype

**and** *heap-read* :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool



**and** *heap-write* :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool

**locale** *J-heap* = *heap* +  
**constrains** *addr2thread-id* :: ('addr :: addr)  $\Rightarrow$  'thread-id  
**and** *thread-id2addr* :: 'thread-id  $\Rightarrow$  'addr  
**and** *spurious-wakeups* :: bool  
**and** *empty-heap* :: 'heap  
**and** *allocate* :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set  
**and** *typeof-addr* :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype  
**and** *heap-read* :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool  
**and** *heap-write* :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool  
**and** *P* :: 'addr *J-prog*

**sublocale** *J-heap* < *J-heap-base* .

**locale** *J-heap-conf-base* = *heap-conf-base* +  
**constrains** *addr2thread-id* :: ('addr :: addr)  $\Rightarrow$  'thread-id  
**and** *thread-id2addr* :: 'thread-id  $\Rightarrow$  'addr  
**and** *spurious-wakeups* :: bool  
**and** *empty-heap* :: 'heap  
**and** *allocate* :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set  
**and** *typeof-addr* :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype  
**and** *heap-read* :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool  
**and** *heap-write* :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool  
**and** *hconf* :: 'heap  $\Rightarrow$  bool  
**and** *P* :: 'addr *J-prog*

**sublocale** *J-heap-conf-base* < *J-heap-base* .

**locale** *J-heap-conf* =  
*J-heap-conf-base* +  
*heap-conf* +  
**constrains** *addr2thread-id* :: ('addr :: addr)  $\Rightarrow$  'thread-id  
**and** *thread-id2addr* :: 'thread-id  $\Rightarrow$  'addr  
**and** *spurious-wakeups* :: bool  
**and** *empty-heap* :: 'heap  
**and** *allocate* :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set  
**and** *typeof-addr* :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype  
**and** *heap-read* :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool  
**and** *heap-write* :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool  
**and** *hconf* :: 'heap  $\Rightarrow$  bool  
**and** *P* :: 'addr *J-prog*

**sublocale** *J-heap-conf* < *J-heap*  
**by**(*unfold-locales*)

**locale** *J-progress* =  
*heap-progress* +  
*J-heap-conf-base* +  
**constrains** *addr2thread-id* :: ('addr :: addr)  $\Rightarrow$  'thread-id  
**and** *thread-id2addr* :: 'thread-id  $\Rightarrow$  'addr  
**and** *spurious-wakeups* :: bool  
**and** *empty-heap* :: 'heap  
**and** *allocate* :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set

```

and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype
and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
and hconf :: 'heap  $\Rightarrow$  bool
and P :: 'addr J-prog

```

```

sublocale J-progress < J-heap by(unfold-locales)

```

```

locale J-conf-read =
  heap-conf-read +
  J-heap-conf +
  constrains addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
  and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
  and spurious-wakeups :: bool
  and empty-heap :: 'heap
  and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
  and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype
  and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
  and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
  and hconf :: 'heap  $\Rightarrow$  bool
  and P :: 'addr J-prog

```

```

sublocale J-conf-read < J-heap by(unfold-locales)

```

```

locale J-typesafe =
  heap-typesafe +
  J-conf-read +
  J-progress +
  constrains addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
  and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
  and spurious-wakeups :: bool
  and empty-heap :: 'heap
  and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
  and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype
  and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
  and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
  and hconf :: 'heap  $\Rightarrow$  bool
  and P :: 'addr J-prog

```

```

end

```

## 4.4 Small Step Semantics

```

theory SmallStep

```

```

imports

```

```

  Expr

```

```

  State

```

```

  JHeap

```

```

begin

```

```

type-synonym

```

```

  ('addr, 'thread-id, 'heap) J-thread-action =

```

```

  ('addr, 'thread-id, 'addr expr  $\times$  'addr locals, 'heap) Jinja-thread-action

```

**type-synonym**

```

('addr, 'thread-id, 'heap) J-state =
('addr, 'thread-id, 'addr expr × 'addr locals, 'heap, 'addr) state

```

**print-translation** <

```

let
  fun tr'
    [a1, t
    , Const (@{type-syntax prod}, -) $
      (Const (@{type-syntax exp}, -) $
        Const (@{type-syntax String.literal}, -) $ Const (@{type-syntax unit}, -) $ a2) $
      (Const (@{type-syntax fun}, -) $
        Const (@{type-syntax String.literal}, -) $
        (Const (@{type-syntax option}, -) $
          (Const (@{type-syntax val}, -) $ a3))))
    , h] =
  if a1 = a2 andalso a2 = a3 then Syntax.const @{{type-syntax J-thread-action}} $ a1 $ t $ h
  else raise Match;
  in [(@{{type-syntax Jinja-thread-action}}, K tr')]
end
>
typ ('addr, 'thread-id, 'heap) J-thread-action

```

**print-translation** <

```

let
  fun tr'
    [a1, t
    , Const (@{type-syntax prod}, -) $
      (Const (@{type-syntax exp}, -) $
        Const (@{type-syntax String.literal}, -) $ Const (@{type-syntax unit}, -) $ a2) $
      (Const (@{type-syntax fun}, -) $
        Const (@{type-syntax String.literal}, -) $
        (Const (@{type-syntax option}, -) $
          (Const (@{type-syntax val}, -) $ a3))))
    , h, a4] =
  if a1 = a2 andalso a2 = a3 andalso a3 = a4 then Syntax.const @{{type-syntax J-state}} $ a1 $ t
  $ h
  else raise Match;
  in [(@{{type-syntax state}}, K tr')]
end
>
typ ('addr, 'thread-id, 'heap) J-state

```

**definition** *extNTA2J* :: 'addr J-prog ⇒ (cname × mname × 'addr) ⇒ 'addr expr × 'addr locals  
**where** *extNTA2J* P = (λ(C, M, a). let (D, Ts, T, meth) = method P C M; (pns, body) = the meth  
 in ({this:Class D=[Addr a]; body}, Map.empty))

**abbreviation** *J-local-start* ::

```

  cname ⇒ mname ⇒ ty list ⇒ ty ⇒ 'addr J-mb ⇒ 'addr val list
  ⇒ 'addr expr × 'addr locals
where

```

$J\text{-local-start} \equiv$   
 $\lambda C M Ts T (pns, body) \text{ vs.}$   
 $(blocks (this \# pns) (Class C \# Ts) (Null \# vs) body, Map.empty)$

**abbreviation** (in  $J\text{-heap-base}$ )

$J\text{-start-state} :: 'addr J\text{-prog} \Rightarrow cname \Rightarrow mname \Rightarrow 'addr \text{ val list} \Rightarrow ('addr, 'thread\text{-}id, 'heap) J\text{-state}$

**where**

$J\text{-start-state} \equiv start\text{-}state J\text{-local-start}$

**lemma**  $extNTA2J\text{-iff}$  [simp]:

$extNTA2J P (C, M, a) = (\{this:Class (fst (method P C M))=[Addr a]; snd (the (snd (snd (snd (method P C M))))\}, Map.empty)$

**by**(simp add:  $extNTA2J\text{-def}$  split-beta)

**abbreviation**  $extTA2J ::$

$'addr J\text{-prog} \Rightarrow ('addr, 'thread\text{-}id, 'heap) \text{ external-thread-action} \Rightarrow ('addr, 'thread\text{-}id, 'heap) J\text{-thread-action}$

**where**  $extTA2J P \equiv convert\text{-}extTA (extNTA2J P)$

**lemma**  $extTA2J\text{-}\varepsilon$ :  $extTA2J P \varepsilon = \varepsilon$

**by**(simp)

Locking mechanism: The expression on which the thread is synchronized is evaluated first to a value. If this expression evaluates to null, a null pointer expression is thrown. If this expression evaluates to an address, a lock must be obtained on this address, the sync expression is rewritten to insync. For insync expressions, the body expression may be evaluated. If the body expression is only a value or a thrown exception, the lock is released and the synchronized expression reduces to the body's expression. This is the normal Java semantics, not the one as presented in LNCS 1523, Cenciarelli/Knapp/Reus/Wirsing. There the expression on which the thread synchronized is evaluated except for the last step. If the thread can obtain the lock on the object immediately after the last evaluation step, the evaluation is done and the lock acquired. If the lock cannot be obtained, the evaluation step is discarded. If another thread changes the evaluation result of this last step, the thread then will try to synchronize on the new object.

**context**  $J\text{-heap-base}$  **begin**

**inductive**  $red ::$

$(('addr, 'thread\text{-}id, 'heap) \text{ external-thread-action} \Rightarrow ('addr, 'thread\text{-}id, 'x, 'heap) \text{ Jinja-thread-action})$

$\Rightarrow 'addr J\text{-prog} \Rightarrow 'thread\text{-}id$

$\Rightarrow 'addr \text{ expr} \Rightarrow ('addr, 'heap) J\text{state}$

$\Rightarrow ('addr, 'thread\text{-}id, 'x, 'heap) \text{ Jinja-thread-action}$

$\Rightarrow 'addr \text{ expr} \Rightarrow ('addr, 'heap) J\text{state} \Rightarrow bool$

$(-, -, \vdash ((1 \langle -, / - \rangle) \dashrightarrow / (1 \langle -, / - \rangle))) [51, 51, 0, 0, 0, 0, 0, 0] 81)$

**and**  $reds ::$

$(('addr, 'thread\text{-}id, 'heap) \text{ external-thread-action} \Rightarrow ('addr, 'thread\text{-}id, 'x, 'heap) \text{ Jinja-thread-action})$

$\Rightarrow 'addr J\text{-prog} \Rightarrow 'thread\text{-}id$

$\Rightarrow 'addr \text{ expr list} \Rightarrow ('addr, 'heap) J\text{state}$

$\Rightarrow ('addr, 'thread\text{-}id, 'x, 'heap) \text{ Jinja-thread-action}$

$\Rightarrow 'addr \text{ expr list} \Rightarrow ('addr, 'heap) J\text{state} \Rightarrow bool$

$(-, -, \vdash ((1 \langle -, / - \rangle) \dashrightarrow / (1 \langle -, / - \rangle))) [51, 51, 0, 0, 0, 0, 0, 0] 81)$

**for**  $extTA :: ('addr, 'thread\text{-}id, 'heap) \text{ external-thread-action} \Rightarrow ('addr, 'thread\text{-}id, 'x, 'heap) \text{ Jinja-thread-action}$

**and**  $P :: 'addr J\text{-prog}$  **and**  $t :: 'thread\text{-}id$

**where**

*RedNew:*

$(h', a) \in \text{allocate } h \text{ (Class-type } C)$

$\implies \text{extTA}, P, t \vdash \langle \text{new } C, (h, l) \rangle -\llbracket \text{NewHeapElem } a \text{ (Class-type } C) \rrbracket \rightarrow \langle \text{addr } a, (h', l) \rangle$

| *RedNewFail:*

$\text{allocate } h \text{ (Class-type } C) = \{\}$

$\implies \text{extTA}, P, t \vdash \langle \text{new } C, (h, l) \rangle -\varepsilon \rightarrow \langle \text{THROW OutOfMemory}, (h, l) \rangle$

| *NewArrayRed:*

$\text{extTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies \text{extTA}, P, t \vdash \langle \text{newA } T[e], s \rangle -ta \rightarrow \langle \text{newA } T[e'], s' \rangle$

| *RedNewArray:*

$\llbracket 0 \leq s \ i; (h', a) \in \text{allocate } h \text{ (Array-type } T \text{ (nat (sint } i))) \rrbracket$

$\implies \text{extTA}, P, t \vdash \langle \text{newA } T[\text{Val (Intg } i)], (h, l) \rangle -\llbracket \text{NewHeapElem } a \text{ (Array-type } T \text{ (nat (sint } i))) \rrbracket \rightarrow \langle \text{addr } a, (h', l) \rangle$

| *RedNewArrayNegative:*

$i < s \ 0 \implies \text{extTA}, P, t \vdash \langle \text{newA } T[\text{Val (Intg } i)], s \rangle -\varepsilon \rightarrow \langle \text{THROW NegativeArraySize}, s \rangle$

| *RedNewArrayFail:*

$\llbracket 0 \leq s \ i; \text{allocate } h \text{ (Array-type } T \text{ (nat (sint } i))) = \{\} \rrbracket$

$\implies \text{extTA}, P, t \vdash \langle \text{newA } T[\text{Val (Intg } i)], (h, l) \rangle -\varepsilon \rightarrow \langle \text{THROW OutOfMemory}, (h, l) \rangle$

| *CastRed:*

$\text{extTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies \text{extTA}, P, t \vdash \langle \text{Cast } C \ e, s \rangle -ta \rightarrow \langle \text{Cast } C \ e', s' \rangle$

| *RedCast:*

$\llbracket \text{typeof}_{hp} \ s \ v = \lfloor U \rfloor; P \vdash U \leq T \rrbracket$

$\implies \text{extTA}, P, t \vdash \langle \text{Cast } T \text{ (Val } v), s \rangle -\varepsilon \rightarrow \langle \text{Val } v, s \rangle$

| *RedCastFail:*

$\llbracket \text{typeof}_{hp} \ s \ v = \lfloor U \rfloor; \neg P \vdash U \leq T \rrbracket$

$\implies \text{extTA}, P, t \vdash \langle \text{Cast } T \text{ (Val } v), s \rangle -\varepsilon \rightarrow \langle \text{THROW ClassCast}, s \rangle$

| *InstanceOfRed:*

$\text{extTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies \text{extTA}, P, t \vdash \langle e \text{ instanceof } T, s \rangle -ta \rightarrow \langle e' \text{ instanceof } T, s' \rangle$

| *RedInstanceOf:*

$\llbracket \text{typeof}_{hp} \ s \ v = \lfloor U \rfloor; b \longleftrightarrow v \neq \text{Null} \wedge P \vdash U \leq T \rrbracket$

$\implies \text{extTA}, P, t \vdash \langle (\text{Val } v) \text{ instanceof } T, s \rangle -\varepsilon \rightarrow \langle \text{Val (Bool } b), s \rangle$

| *BinOpRed1:*

$\text{extTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies \text{extTA}, P, t \vdash \langle e \text{ «bop» } e2, s \rangle -ta \rightarrow \langle e' \text{ «bop» } e2, s' \rangle$

| *BinOpRed2:*

$\text{extTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies \text{extTA}, P, t \vdash \langle (\text{Val } v) \text{ «bop» } e, s \rangle -ta \rightarrow \langle (\text{Val } v) \text{ «bop» } e', s' \rangle$

| *RedBinOp:*

$\text{binop } bop \ v1 \ v2 = \text{Some (Inl } v) \implies$

$\text{extTA}, P, t \vdash \langle (\text{Val } v1) \text{ «bop» } (\text{Val } v2), s \rangle -\varepsilon \rightarrow \langle \text{Val } v, s \rangle$

| *RedBinOpFail:*

$\text{binop } bop \ v1 \ v2 = \text{Some (Inr } a) \implies$

$\text{extTA}, P, t \vdash \langle (\text{Val } v1) \text{ «bop» } (\text{Val } v2), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$

- | *RedVar*:  

$$lcl\ s\ V = Some\ v \implies$$

$$extTA, P, t \vdash \langle Var\ V, s \rangle -\varepsilon \rightarrow \langle Val\ v, s \rangle$$
- | *LAssRed*:  

$$extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies extTA, P, t \vdash \langle V := e, s \rangle -ta \rightarrow \langle V := e', s' \rangle$$
- | *RedLAss*:  

$$extTA, P, t \vdash \langle V := (Val\ v), (h, l) \rangle -\varepsilon \rightarrow \langle unit, (h, l(V \mapsto v)) \rangle$$
- | *AAccRed1*:  

$$extTA, P, t \vdash \langle a, s \rangle -ta \rightarrow \langle a', s' \rangle \implies extTA, P, t \vdash \langle a[i], s \rangle -ta \rightarrow \langle a'[i], s' \rangle$$
- | *AAccRed2*:  

$$extTA, P, t \vdash \langle i, s \rangle -ta \rightarrow \langle i', s' \rangle \implies extTA, P, t \vdash \langle (Val\ a)[i], s \rangle -ta \rightarrow \langle (Val\ a)[i'], s' \rangle$$
- | *RedAAccNull*:  

$$extTA, P, t \vdash \langle null[Val\ i], s \rangle -\varepsilon \rightarrow \langle THROW\ NullPointer, s \rangle$$
- | *RedAAccBounds*:  

$$\llbracket typeof\_addr\ (hp\ s)\ a = \lfloor Array\_type\ T\ n \rfloor; i < s\ 0 \vee sint\ i \geq int\ n \rrbracket$$

$$\implies extTA, P, t \vdash \langle (addr\ a)[Val\ (Intg\ i)], s \rangle -\varepsilon \rightarrow \langle THROW\ ArrayIndexOutOfBounds, s \rangle$$
- | *RedAAcc*:  

$$\llbracket typeof\_addr\ h\ a = \lfloor Array\_type\ T\ n \rfloor; 0 \leq s\ i; sint\ i < int\ n;$$

$$heap\_read\ h\ a\ (ACell\ (nat\ (sint\ i)))\ v \rrbracket$$

$$\implies extTA, P, t \vdash \langle (addr\ a)[Val\ (Intg\ i)], (h, l) \rangle -\llbracket ReadMem\ a\ (ACell\ (nat\ (sint\ i)))\ v \rrbracket \rightarrow \langle Val\ v, (h, l) \rangle$$
- | *AAssRed1*:  

$$extTA, P, t \vdash \langle a, s \rangle -ta \rightarrow \langle a', s' \rangle \implies extTA, P, t \vdash \langle a[i] := e, s \rangle -ta \rightarrow \langle a'[i] := e, s' \rangle$$
- | *AAssRed2*:  

$$extTA, P, t \vdash \langle i, s \rangle -ta \rightarrow \langle i', s' \rangle \implies extTA, P, t \vdash \langle (Val\ a)[i] := e, s \rangle -ta \rightarrow \langle (Val\ a)[i'] := e, s' \rangle$$
- | *AAssRed3*:  

$$extTA, P, t \vdash \langle (e::'addr\ expr), s \rangle -ta \rightarrow \langle e', s' \rangle \implies extTA, P, t \vdash \langle (Val\ a)[Val\ i] := e, s \rangle -ta \rightarrow \langle (Val\ a)[Val\ i] := e', s' \rangle$$
- | *RedAAssNull*:  

$$extTA, P, t \vdash \langle null[Val\ i] := (Val\ e::'addr\ expr), s \rangle -\varepsilon \rightarrow \langle THROW\ NullPointer, s \rangle$$
- | *RedAAssBounds*:  

$$\llbracket typeof\_addr\ (hp\ s)\ a = \lfloor Array\_type\ T\ n \rfloor; i < s\ 0 \vee sint\ i \geq int\ n \rrbracket$$

$$\implies extTA, P, t \vdash \langle (addr\ a)[Val\ (Intg\ i)] := (Val\ e::'addr\ expr), s \rangle -\varepsilon \rightarrow \langle THROW\ ArrayIndexOutOfBounds, s \rangle$$
- | *RedAAssStore*:  

$$\llbracket typeof\_addr\ (hp\ s)\ a = \lfloor Array\_type\ T\ n \rfloor; 0 \leq s\ i; sint\ i < int\ n;$$

$$typeof_{hp\ s}\ w = \lfloor U \rfloor; \neg (P \vdash U \leq T) \rrbracket$$

$$\implies extTA, P, t \vdash \langle (addr\ a)[Val\ (Intg\ i)] := (Val\ w::'addr\ expr), s \rangle -\varepsilon \rightarrow \langle THROW\ ArrayStore, s \rangle$$
- | *RedAAss*:

$\llbracket \text{typeof-addr } h \ a = \lfloor \text{Array-type } T \ n \rfloor; \ 0 \leq s \ i; \ \text{sint } i < \text{int } n; \ \text{typeof}_h \ w = \text{Some } U; \ P \vdash U \leq T; \\ \text{heap-write } h \ a \ (\text{ACell } (\text{nat } (\text{sint } i))) \ w \ h' \rrbracket \\ \implies \text{extTA}, P, t \vdash \langle (\text{addr } a) \lfloor \text{Val } (\text{Intg } i) \rfloor := \text{Val } w::'\text{addr } \text{expr}, (h, l) \rangle -\llbracket \text{WriteMem } a \ (\text{ACell } (\text{nat } (\text{sint } i))) \ w \rrbracket \rightarrow \langle \text{unit}, (h', l) \rangle$

| *ALengthRed*:

$\text{extTA}, P, t \vdash \langle a, s \rangle -ta \rightarrow \langle a', s' \rangle \implies \text{extTA}, P, t \vdash \langle a \cdot \text{length}, s \rangle -ta \rightarrow \langle a' \cdot \text{length}, s' \rangle$

| *RedALength*:

$\text{typeof-addr } h \ a = \lfloor \text{Array-type } T \ n \rfloor \\ \implies \text{extTA}, P, t \vdash \langle \text{addr } a \cdot \text{length}, (h, l) \rangle -\varepsilon \rightarrow \langle \text{Val } (\text{Intg } (\text{word-of-nat } n)), (h, l) \rangle$

| *RedALengthNull*:

$\text{extTA}, P, t \vdash \langle \text{null} \cdot \text{length}, s \rangle -\varepsilon \rightarrow \langle \text{THROW NullPointer}, s \rangle$

| *FAccRed*:

$\text{extTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies \text{extTA}, P, t \vdash \langle e \cdot F\{D\}, s \rangle -ta \rightarrow \langle e' \cdot F\{D\}, s' \rangle$

| *RedFAcc*:

$\text{heap-read } h \ a \ (\text{CField } D \ F) \ v \\ \implies \text{extTA}, P, t \vdash \langle (\text{addr } a) \cdot F\{D\}, (h, l) \rangle -\llbracket \text{ReadMem } a \ (\text{CField } D \ F) \ v \rrbracket \rightarrow \langle \text{Val } v, (h, l) \rangle$

| *RedFAccNull*:

$\text{extTA}, P, t \vdash \langle \text{null} \cdot F\{D\}, s \rangle -\varepsilon \rightarrow \langle \text{THROW NullPointer}, s \rangle$

| *FAssRed1*:

$\text{extTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies \text{extTA}, P, t \vdash \langle e \cdot F\{D\} := e2, s \rangle -ta \rightarrow \langle e' \cdot F\{D\} := e2, s' \rangle$

| *FAssRed2*:

$\text{extTA}, P, t \vdash \langle (e::'\text{addr } \text{expr}), s \rangle -ta \rightarrow \langle e', s' \rangle \implies \text{extTA}, P, t \vdash \langle \text{Val } v \cdot F\{D\} := e, s \rangle -ta \rightarrow \langle \text{Val } v \cdot F\{D\} := e', s' \rangle$

| *RedFAss*:

$\text{heap-write } h \ a \ (\text{CField } D \ F) \ v \ h' \implies \\ \text{extTA}, P, t \vdash \langle (\text{addr } a) \cdot F\{D\} := \text{Val } v, (h, l) \rangle -\llbracket \text{WriteMem } a \ (\text{CField } D \ F) \ v \rrbracket \rightarrow \langle \text{unit}, (h', l) \rangle$

| *RedFAssNull*:

$\text{extTA}, P, t \vdash \langle \text{null} \cdot F\{D\} := \text{Val } v::'\text{addr } \text{expr}, s \rangle -\varepsilon \rightarrow \langle \text{THROW NullPointer}, s \rangle$

| *CASRed1*:

$\text{extTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies \\ \text{extTA}, P, t \vdash \langle e \cdot \text{compareAndSwap}(D \cdot F, e2, e3), s \rangle -ta \rightarrow \langle e' \cdot \text{compareAndSwap}(D \cdot F, e2, e3), s' \rangle$

| *CASRed2*:

$\text{extTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies \\ \text{extTA}, P, t \vdash \langle \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, e, e3), s \rangle -ta \rightarrow \langle \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, e', e3), s' \rangle$

| *CASRed3*:

$\text{extTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies \\ \text{extTA}, P, t \vdash \langle \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', e), s \rangle -ta \rightarrow \langle \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', e'), s' \rangle$

| *CASNull*:

$extTA, P, t \vdash \langle null \cdot compareAndSwap(D \cdot F, Val\ v, Val\ v'), s \rangle -\varepsilon \rightarrow \langle THROW\ NullPointer, s \rangle$

| *RedCASSucceed*:

$\llbracket heap-read\ h\ a\ (CField\ D\ F)\ v; heap-write\ h\ a\ (CField\ D\ F)\ v'\ h' \rrbracket \implies$   
 $extTA, P, t \vdash \langle addr\ a \cdot compareAndSwap(D \cdot F, Val\ v, Val\ v'), (h, l) \rangle$   
 $-\llbracket ReadMem\ a\ (CField\ D\ F)\ v, WriteMem\ a\ (CField\ D\ F)\ v' \rrbracket \rightarrow$   
 $\langle true, (h', l) \rangle$

| *RedCASFail*:

$\llbracket heap-read\ h\ a\ (CField\ D\ F)\ v''; v \neq v'' \rrbracket \implies$   
 $extTA, P, t \vdash \langle addr\ a \cdot compareAndSwap(D \cdot F, Val\ v, Val\ v'), (h, l) \rangle$   
 $-\llbracket ReadMem\ a\ (CField\ D\ F)\ v'' \rrbracket \rightarrow$   
 $\langle false, (h, l) \rangle$

| *CallObj*:

$extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies extTA, P, t \vdash \langle e \cdot M(es), s \rangle -ta \rightarrow \langle e' \cdot M(es), s' \rangle$

| *CallParams*:

$extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies$   
 $extTA, P, t \vdash \langle (Val\ v) \cdot M(es), s \rangle -ta \rightarrow \langle (Val\ v) \cdot M(es'), s' \rangle$

| *RedCall*:

$\llbracket typeof-addr\ (hp\ s)\ a = \lfloor hU \rfloor; P \vdash class-type-of\ hU\ sees\ M:Ts \rightarrow T = \lfloor (pns, body) \rfloor\ in\ D;$   
 $size\ vs = size\ pns; size\ Ts = size\ pns \rrbracket$   
 $\implies extTA, P, t \vdash \langle (addr\ a) \cdot M(map\ Val\ vs), s \rangle -\varepsilon \rightarrow \langle blocks\ (this\ \# \ pns)\ (Class\ D\ \# \ Ts)\ (Addr\ a\ \# \ vs)\ body, s \rangle$

| *RedCallExternal*:

$\llbracket typeof-addr\ (hp\ s)\ a = \lfloor hU \rfloor; P \vdash class-type-of\ hU\ sees\ M:Ts \rightarrow T = Native\ in\ D;$   
 $P, t \vdash \langle a \cdot M(vs), hp\ s \rangle -ta \rightarrow ext\ \langle va, h' \rangle;$   
 $ta' = extTA\ ta; e' = extRet2J\ ((addr\ a) \cdot M(map\ Val\ vs))\ va; s' = (h', lcl\ s) \rrbracket$   
 $\implies extTA, P, t \vdash \langle (addr\ a) \cdot M(map\ Val\ vs), s \rangle -ta' \rightarrow \langle e', s' \rangle$

| *RedCallNull*:

$extTA, P, t \vdash \langle null \cdot M(map\ Val\ vs), s \rangle -\varepsilon \rightarrow \langle THROW\ NullPointer, s \rangle$

| *BlockRed*:

$extTA, P, t \vdash \langle e, (h, l(V := vo)) \rangle -ta \rightarrow \langle e', (h', l') \rangle$   
 $\implies extTA, P, t \vdash \langle \{V:T=vo; e\}, (h, l) \rangle -ta \rightarrow \langle \{V:T=l'\ V; e'\}, (h', l'(V := l\ V)) \rangle$

| *RedBlock*:

$extTA, P, t \vdash \langle \{V:T=vo; Val\ u\}, s \rangle -\varepsilon \rightarrow \langle Val\ u, s \rangle$

| *SynchronizedRed1*:

$extTA, P, t \vdash \langle o', s \rangle -ta \rightarrow \langle o'', s' \rangle \implies extTA, P, t \vdash \langle sync(o')\ e, s \rangle -ta \rightarrow \langle sync(o'')\ e, s' \rangle$

| *SynchronizedNull*:

$extTA, P, t \vdash \langle sync(null)\ e, s \rangle -\varepsilon \rightarrow \langle THROW\ NullPointer, s \rangle$

| *LockSynchronized*:

$extTA, P, t \vdash \langle sync(addr\ a)\ e, s \rangle -\llbracket Lock \rightarrow a, SyncLock\ a \rrbracket \rightarrow \langle insync(a)\ e, s \rangle$

| *SynchronizedRed2*:

$extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies extTA, P, t \vdash \langle insync(a)\ e, s \rangle -ta \rightarrow \langle insync(a)\ e', s' \rangle$



- | *UnlockSynchronized*:  
 $extTA, P, t \vdash \langle insync(a) (Val v), s \rangle - \llbracket Unlock \rightarrow a, SyncUnlock a \rrbracket \rightarrow \langle Val v, s \rangle$
- | *SeqRed*:  
 $extTA, P, t \vdash \langle e, s \rangle - ta \rightarrow \langle e', s' \rangle \implies extTA, P, t \vdash \langle e;;e2, s \rangle - ta \rightarrow \langle e';e2, s' \rangle$
- | *RedSeq*:  
 $extTA, P, t \vdash \langle (Val v);;e, s \rangle - \varepsilon \rightarrow \langle e, s \rangle$
- | *CondRed*:  
 $extTA, P, t \vdash \langle b, s \rangle - ta \rightarrow \langle b', s' \rangle \implies extTA, P, t \vdash \langle if (b) e1 else e2, s \rangle - ta \rightarrow \langle if (b') e1 else e2, s' \rangle$
- | *RedCondT*:  
 $extTA, P, t \vdash \langle if (true) e1 else e2, s \rangle - \varepsilon \rightarrow \langle e1, s \rangle$
- | *RedCondF*:  
 $extTA, P, t \vdash \langle if (false) e1 else e2, s \rangle - \varepsilon \rightarrow \langle e2, s \rangle$
- | *RedWhile*:  
 $extTA, P, t \vdash \langle while(b) c, s \rangle - \varepsilon \rightarrow \langle if (b) (c;;while(b) c) else unit, s \rangle$
- | *ThrowRed*:  
 $extTA, P, t \vdash \langle e, s \rangle - ta \rightarrow \langle e', s' \rangle \implies extTA, P, t \vdash \langle throw e, s \rangle - ta \rightarrow \langle throw e', s' \rangle$
- | *RedThrowNull*:  
 $extTA, P, t \vdash \langle throw null, s \rangle - \varepsilon \rightarrow \langle THROW NullPointer, s \rangle$
- | *TryRed*:  
 $extTA, P, t \vdash \langle e, s \rangle - ta \rightarrow \langle e', s' \rangle \implies extTA, P, t \vdash \langle try e catch(C V) e2, s \rangle - ta \rightarrow \langle try e' catch(C V) e2, s' \rangle$
- | *RedTry*:  
 $extTA, P, t \vdash \langle try (Val v) catch(C V) e2, s \rangle - \varepsilon \rightarrow \langle Val v, s \rangle$
- | *RedTryCatch*:  
 $\llbracket typeof\_addr (hp s) a = \lfloor Class\_type D \rfloor; P \vdash D \preceq^* C \rrbracket$   
 $\implies extTA, P, t \vdash \langle try (Throw a) catch(C V) e2, s \rangle - \varepsilon \rightarrow \langle \{ V:Class C = \lfloor Addr a \rfloor; e2 \}, s \rangle$
- | *RedTryFail*:  
 $\llbracket typeof\_addr (hp s) a = \lfloor Class\_type D \rfloor; \neg P \vdash D \preceq^* C \rrbracket$   
 $\implies extTA, P, t \vdash \langle try (Throw a) catch(C V) e2, s \rangle - \varepsilon \rightarrow \langle Throw a, s \rangle$
- | *ListRed1*:  
 $extTA, P, t \vdash \langle e, s \rangle - ta \rightarrow \langle e', s' \rangle \implies$   
 $extTA, P, t \vdash \langle e \# es, s \rangle [-ta \rightarrow] \langle e' \# es, s' \rangle$
- | *ListRed2*:  
 $extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies$   
 $extTA, P, t \vdash \langle Val v \# es, s \rangle [-ta \rightarrow] \langle Val v \# es', s' \rangle$
- Exception propagation
- | *NewArrayThrow*:  $extTA, P, t \vdash \langle newA T \lfloor Throw a \rfloor, s \rangle - \varepsilon \rightarrow \langle Throw a, s \rangle$

$\text{CastThrow}: \text{extTA}, P, t \vdash \langle \text{Cast } C \text{ (Throw } a), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
 $\text{InstanceOfThrow}: \text{extTA}, P, t \vdash \langle (\text{Throw } a) \text{ instanceof } T, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
 $\text{BinOpThrow1}: \text{extTA}, P, t \vdash \langle (\text{Throw } a) \llbracket \text{bop} \rrbracket e_2, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
 $\text{BinOpThrow2}: \text{extTA}, P, t \vdash \langle (\text{Val } v_1) \llbracket \text{bop} \rrbracket (\text{Throw } a), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
 $\text{LAssThrow}: \text{extTA}, P, t \vdash \langle V := (\text{Throw } a), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
 $\text{AAccThrow1}: \text{extTA}, P, t \vdash \langle (\text{Throw } a)[i], s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
 $\text{AAccThrow2}: \text{extTA}, P, t \vdash \langle (\text{Val } v)[\text{Throw } a], s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
 $\text{AAssThrow1}: \text{extTA}, P, t \vdash \langle (\text{Throw } a)[i] := e, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
 $\text{AAssThrow2}: \text{extTA}, P, t \vdash \langle (\text{Val } v)[\text{Throw } a] := e, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
 $\text{AAssThrow3}: \text{extTA}, P, t \vdash \langle (\text{Val } v)[\text{Val } i] := \text{Throw } a :: 'addr \text{ expr}, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
 $\text{ALengthThrow}: \text{extTA}, P, t \vdash \langle (\text{Throw } a) \cdot \text{length}, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
 $\text{FAccThrow}: \text{extTA}, P, t \vdash \langle (\text{Throw } a) \cdot F\{D\}, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
 $\text{FAssThrow1}: \text{extTA}, P, t \vdash \langle (\text{Throw } a) \cdot F\{D\} := e_2, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
 $\text{FAssThrow2}: \text{extTA}, P, t \vdash \langle \text{Val } v \cdot F\{D\} := (\text{Throw } a :: 'addr \text{ expr}), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
 $\text{CASThrow}: \text{extTA}, P, t \vdash \langle \text{Throw } a \cdot \text{compareAndSwap}(D \cdot F, e_2, e_3), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
 $\text{CASThrow2}: \text{extTA}, P, t \vdash \langle \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Throw } a, e_3), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
 $\text{CASThrow3}: \text{extTA}, P, t \vdash \langle \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', \text{Throw } a), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
 $\text{CallThrowObj}: \text{extTA}, P, t \vdash \langle (\text{Throw } a) \cdot M(es), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
 $\text{CallThrowParams}: \llbracket es = \text{map Val } vs @ \text{Throw } a \# es' \rrbracket \implies \text{extTA}, P, t \vdash \langle (\text{Val } v) \cdot M(es), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
 $\text{BlockThrow}: \text{extTA}, P, t \vdash \langle \{ V : T = vo; \text{Throw } a \}, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
 $\text{SynchronizedThrow1}: \text{extTA}, P, t \vdash \langle \text{sync}(\text{Throw } a) e, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
 $\text{SynchronizedThrow2}: \text{extTA}, P, t \vdash \langle \text{insync}(a) \text{Throw } ad, s \rangle -\llbracket \text{Unlock} \rightarrow a, \text{SyncUnlock } a \rrbracket \rightarrow \langle \text{Throw } ad, s \rangle$   
 $\text{SeqThrow}: \text{extTA}, P, t \vdash \langle (\text{Throw } a); e_2, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
 $\text{CondThrow}: \text{extTA}, P, t \vdash \langle \text{if } (\text{Throw } a) e_1 \text{ else } e_2, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
 $\text{ThrowThrow}: \text{extTA}, P, t \vdash \langle \text{throw}(\text{Throw } a), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$

**inductive-cases red-cases:**

$\text{extTA}, P, t \vdash \langle \text{new } C, s \rangle -ta \rightarrow \langle e', s' \rangle$   
 $\text{extTA}, P, t \vdash \langle \text{newA } T[e], s \rangle -ta \rightarrow \langle e', s' \rangle$   
 $\text{extTA}, P, t \vdash \langle \text{Cast } T e, s \rangle -ta \rightarrow \langle e', s' \rangle$   
 $\text{extTA}, P, t \vdash \langle e \text{ instanceof } T, s \rangle -ta \rightarrow \langle e', s' \rangle$   
 $\text{extTA}, P, t \vdash \langle e \llbracket \text{bop} \rrbracket e', s \rangle -ta \rightarrow \langle e'', s' \rangle$   
 $\text{extTA}, P, t \vdash \langle \text{Var } V, s \rangle -ta \rightarrow \langle e', s' \rangle$   
 $\text{extTA}, P, t \vdash \langle V := e, s \rangle -ta \rightarrow \langle e', s' \rangle$   
 $\text{extTA}, P, t \vdash \langle a[i], s \rangle -ta \rightarrow \langle e', s' \rangle$   
 $\text{extTA}, P, t \vdash \langle a[i] := e, s \rangle -ta \rightarrow \langle e', s' \rangle$   
 $\text{extTA}, P, t \vdash \langle a \cdot \text{length}, s \rangle -ta \rightarrow \langle e', s' \rangle$   
 $\text{extTA}, P, t \vdash \langle e \cdot F\{D\}, s \rangle -ta \rightarrow \langle e', s' \rangle$   
 $\text{extTA}, P, t \vdash \langle e \cdot F\{D\} := e', s \rangle -ta \rightarrow \langle e'', s' \rangle$   
 $\text{extTA}, P, t \vdash \langle e \cdot \text{compareAndSwap}(D \cdot F, e', e''), s \rangle -ta \rightarrow \langle e''', s' \rangle$   
 $\text{extTA}, P, t \vdash \langle e \cdot M(es), s \rangle -ta \rightarrow \langle e', s' \rangle$   
 $\text{extTA}, P, t \vdash \langle \{ V : T = vo; e \}, s \rangle -ta \rightarrow \langle e', s' \rangle$   
 $\text{extTA}, P, t \vdash \langle \text{sync}(o') e, s \rangle -ta \rightarrow \langle e', s' \rangle$   
 $\text{extTA}, P, t \vdash \langle \text{insync}(a) e, s \rangle -ta \rightarrow \langle e', s' \rangle$   
 $\text{extTA}, P, t \vdash \langle e; e', s \rangle -ta \rightarrow \langle e'', s' \rangle$   
 $\text{extTA}, P, t \vdash \langle \text{if } (b) e_1 \text{ else } e_2, s \rangle -ta \rightarrow \langle e', s' \rangle$   
 $\text{extTA}, P, t \vdash \langle \text{while } (b) e, s \rangle -ta \rightarrow \langle e', s' \rangle$   
 $\text{extTA}, P, t \vdash \langle \text{throw } e, s \rangle -ta \rightarrow \langle e', s' \rangle$   
 $\text{extTA}, P, t \vdash \langle \text{try } e \text{ catch } (C V) e', s \rangle -ta \rightarrow \langle e'', s' \rangle$

**inductive-cases reds-cases:**

$\text{extTA}, P, t \vdash \langle e \# es, s \rangle [-ta \rightarrow] \langle es', s' \rangle$

**abbreviation**  $\text{red}' ::$

$'\text{addr } J\text{-prog} \Rightarrow '\text{thread-id} \Rightarrow '\text{addr expr} \Rightarrow ('heap \times '\text{addr locals})$   
 $\Rightarrow ('addr, '\text{thread-id}, 'heap) J\text{-thread-action} \Rightarrow '\text{addr expr} \Rightarrow ('heap \times '\text{addr locals}) \Rightarrow \text{bool}$   
 $(-, - \vdash ((1 \langle -, / - \rangle) \dashrightarrow / (1 \langle -, / - \rangle))) [51, 0, 0, 0, 0, 0] 81)$   
**where**  $\text{red}' P \equiv \text{red } (\text{extTA2J } P) P$

**abbreviation**  $\text{reds}' ::$

$'\text{addr } J\text{-prog} \Rightarrow '\text{thread-id} \Rightarrow '\text{addr expr list} \Rightarrow ('heap \times '\text{addr locals})$   
 $\Rightarrow ('addr, '\text{thread-id}, 'heap) J\text{-thread-action} \Rightarrow '\text{addr expr list} \Rightarrow ('heap \times '\text{addr locals}) \Rightarrow \text{bool}$   
 $(-, - \vdash ((1 \langle -, / - \rangle) [\dashrightarrow / (1 \langle -, / - \rangle)]) [51, 0, 0, 0, 0, 0] 81)$   
**where**  $\text{reds}' P \equiv \text{reds } (\text{extTA2J } P) P$

#### 4.4.1 Some easy lemmas

**lemma**  $[\text{iff}]$ :

$\neg \text{extTA}, P, t \vdash \langle \text{Val } v, s \rangle -ta \rightarrow \langle e', s' \rangle$   
**by**  $(\text{fastforce elim: red.cases})$

**lemma**  $\text{red-no-val } [\text{dest}]$ :

$\llbracket \text{extTA}, P, t \vdash \langle e, s \rangle -tas \rightarrow \langle e', s' \rangle; \text{is-val } e \rrbracket \Longrightarrow \text{False}$   
**by**  $(\text{auto})$

**lemma**  $[\text{iff}]$ :  $\neg \text{extTA}, P, t \vdash \langle \text{Throw } a, s \rangle -ta \rightarrow \langle e', s' \rangle$

**by**  $(\text{fastforce elim: red-cases})$

**lemma**  $\text{reds-map-Val-Throw}$ :

$\text{extTA}, P, t \vdash \langle \text{map Val vs } @ \text{ Throw } a \# es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \longleftrightarrow \text{False}$   
**by**  $(\text{induct vs arbitrary: } es')(\text{auto elim!: reds-cases})$

**lemma**  $\text{reds-preserves-len}$ :

$\text{extTA}, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \Longrightarrow \text{length } es' = \text{length } es$   
**by**  $(\text{induct es arbitrary: } es')(\text{auto elim: reds.cases})$

**lemma**  $\text{red-lcl-incr}$ :  $\text{extTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \Longrightarrow \text{dom } (\text{lcl } s) \subseteq \text{dom } (\text{lcl } s')$

**and**  $\text{reds-lcl-incr}$ :  $\text{extTA}, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \Longrightarrow \text{dom } (\text{lcl } s) \subseteq \text{dom } (\text{lcl } s')$

**apply**  $(\text{induct rule: red-reds.inducts})$

**apply**  $(\text{auto simp del: fun-upd-apply split: if-split-asm})$

**done**

**lemma**  $\text{red-lcl-add-aux}$ :

$\text{extTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \Longrightarrow \text{extTA}, P, t \vdash \langle e, (\text{hp } s, l0 ++ \text{lcl } s) \rangle -ta \rightarrow \langle e', (\text{hp } s', l0 ++ \text{lcl } s') \rangle$

**and**  $\text{reds-lcl-add-aux}$ :

$\text{extTA}, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \Longrightarrow \text{extTA}, P, t \vdash \langle es, (\text{hp } s, l0 ++ \text{lcl } s) \rangle [-ta \rightarrow] \langle es', (\text{hp } s', l0 ++ \text{lcl } s') \rangle$

**proof**  $(\text{induct arbitrary: } l0 \text{ and } l0 \text{ rule: red-reds.inducts})$

**case**  $(\text{BlockRed } e \ h \ x \ V \ vo \ ta \ e' \ h' \ x' \ T)$

**note**  $IH = \langle \bigwedge l0. \text{extTA}, P, t \vdash \langle e, (\text{hp } (h, x(V := vo)), l0 ++ \text{lcl } (h, x(V := vo))) \rangle -ta \rightarrow \langle e', (\text{hp } (h', x'), l0 ++ \text{lcl } (h', x')) \rangle \rangle [\text{simplified}]$

**have**  $\text{lew}$ :  $\bigwedge x \ x'. x(V := vo) ++ x'(V := vo) = (x ++ x')(V := vo)$

**by**  $(\text{simp add: fun-eq-iff map-add-def})$

**have**  $\text{lew1}$ :  $\bigwedge X \ X' \ X'' \ vo. (X(V := vo) ++ X')(V := (X ++ X'') V) = X ++ X'(V := X'' V)$

**by**  $(\text{simp add: fun-eq-iff map-add-def})$

```

have lrew2:  $\bigwedge X X'. (X(V := \text{None}) ++ X') \ V = X' \ V$ 
  by(simp add: map-add-def)
show ?case
proof(cases vo)
  case None
  from IH[of l0(V := vo)]
  show ?thesis
    apply(simp del: fun-upd-apply add: lrew)
    apply(drule red-reds.BlockRed)
    by(simp only: lrew1 None lrew2)
next
  case (Some v)
  with  $\langle \text{extTA}, P, t \vdash \langle e, (h, x(V := vo)) \rangle -ta \rightarrow \langle e', (h', x') \rangle \rangle$ 
  have  $x' \ V \neq \text{None}$ 
    by  $-(\text{drule red-lcl-incr}, \text{auto split: if-split-asm})$ 
  with IH[of l0(V := vo)]
  show ?thesis
    apply(clarsimp simp del: fun-upd-apply simp add: lrew)
    apply(drule red-reds.BlockRed)
    by(simp add: lrew1 Some del: fun-upd-apply)
qed
next
  case RedTryFail thus ?case
    by(auto intro: red-reds.RedTryFail)
qed(fastforce intro:red-reds.intros simp del: fun-upd-apply)+

lemma red-lcl-add:  $\text{extTA}, P, t \vdash \langle e, (h, l) \rangle -ta \rightarrow \langle e', (h', l') \rangle \implies \text{extTA}, P, t \vdash \langle e, (h, l0 ++ l) \rangle -ta \rightarrow$ 
 $\langle e', (h', l0 ++ l') \rangle$ 
  and reds-lcl-add:  $\text{extTA}, P, t \vdash \langle es, (h, l) \rangle [-ta \rightarrow] \langle es', (h', l') \rangle \implies \text{extTA}, P, t \vdash \langle es, (h, l0 ++ l) \rangle$ 
 $[-ta \rightarrow] \langle es', (h', l0 ++ l') \rangle$ 
by(auto dest:red-lcl-add-aux reds-lcl-add-aux)

lemma reds-no-val [dest]:
   $\llbracket \text{extTA}, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{is-vals } es \rrbracket \implies \text{False}$ 
apply(induct es arbitrary: s ta es' s')
apply(blast elim: reds.cases)
apply(erule reds.cases)
apply(auto, blast)
done

lemma red-no-Throw [dest!]:
   $\text{extTA}, P, t \vdash \langle \text{Throw } a, s \rangle -ta \rightarrow \langle e', s' \rangle \implies \text{False}$ 
by(auto elim!: red-cases)

lemma red-lcl-sub:
   $\llbracket \text{extTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{fv } e \subseteq W \rrbracket$ 
 $\implies \text{extTA}, P, t \vdash \langle e, (hp \ s, (lcl \ s) | 'W) \rangle -ta \rightarrow \langle e', (hp \ s', (lcl \ s') | 'W) \rangle$ 

and reds-lcl-sub:
   $\llbracket \text{extTA}, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{fvs } es \subseteq W \rrbracket$ 
 $\implies \text{extTA}, P, t \vdash \langle es, (hp \ s, (lcl \ s) | 'W) \rangle [-ta \rightarrow] \langle es', (hp \ s', (lcl \ s') | 'W) \rangle$ 
proof(induct arbitrary: W and W rule: red-reds.inducts)
  case (RedLAss V v h l W)
  have  $\text{extTA}, P, t \vdash \langle V := \text{Val } v, (h, l | 'W) \rangle -\varepsilon \rightarrow \langle \text{unit}, (h, (l | 'W)(V \mapsto v)) \rangle$ 

```

```

  by(rule red-reds.RedLAss)
  with RedLAss show ?case by(simp del: fun-upd-apply)
next
  case (BlockRed e h x V vo ta e' h' x' T)
  have IH:  $\bigwedge W. \text{fv } e \subseteq W \implies \text{extTA}, P, t \vdash \langle e, (\text{hp } (h, x(V := vo)), \text{lcl } (h, x(V := vo))) \mid ' W \rangle -ta\rightarrow \langle e', (\text{hp } (h', x'), \text{lcl } (h', x')) \mid ' W \rangle$  by fact
  from  $\langle \text{fv } \{V:T=vo; e\} \subseteq W \rangle$  have fve:  $\text{fv } e \subseteq \text{insert } V W$  by auto
  show ?case
  proof(cases  $V \in W$ )
    case True
    with fve have  $\text{fv } e \subseteq W$  by auto
    from True IH[OF this] have  $\text{extTA}, P, t \vdash \langle e, (h, (x \mid ' W)(V := vo)) \rangle -ta\rightarrow \langle e', (h', x' \mid ' W) \rangle$ 
  by(simp)
    with True have  $\text{extTA}, P, t \vdash \langle \{V:T=vo; e\}, (h, x \mid ' W) \rangle -ta\rightarrow \langle \{V:T=x' V; e'\}, (h', (x' \mid ' W)(V := x V)) \rangle$ 
    by  $-(\text{drule red-reds.BlockRed}[\text{where } T=T], \text{simp})$ 
    with True show ?thesis by(simp del: fun-upd-apply)
  next
    case False
    with IH[OF fve] have  $\text{extTA}, P, t \vdash \langle e, (h, (x \mid ' W)(V := vo)) \rangle -ta\rightarrow \langle e', (h', x' \mid ' \text{insert } V W) \rangle$ 
  by(simp)
    with False have  $\text{extTA}, P, t \vdash \langle \{V:T=vo; e\}, (h, x \mid ' W) \rangle -ta\rightarrow \langle \{V:T=x' V; e'\}, (h', (x' \mid ' W)) \rangle$ 
    by  $-(\text{drule red-reds.BlockRed}[\text{where } T=T], \text{simp})$ 
    with False show ?thesis by(simp del: fun-upd-apply)
  qed
next
  case RedTryFail thus ?case by(auto intro: red-reds.RedTryFail)
qed(fastforce intro: red-reds.intros)+

lemma red-notfree-unchanged:  $\llbracket \text{extTA}, P, t \vdash \langle e, s \rangle -ta\rightarrow \langle e', s' \rangle; V \notin \text{fv } e \rrbracket \implies \text{lcl } s' V = \text{lcl } s V$ 
  and reds-notfree-unchanged:  $\llbracket \text{extTA}, P, t \vdash \langle es, s \rangle [-ta\rightarrow] \langle es', s' \rangle; V \notin \text{fvs } es \rrbracket \implies \text{lcl } s' V = \text{lcl } s V$ 
  apply(induct rule: red-reds.inducts)
  apply(fastforce)+
done

lemma red-dom-lcl:  $\text{extTA}, P, t \vdash \langle e, s \rangle -ta\rightarrow \langle e', s' \rangle \implies \text{dom } (\text{lcl } s') \subseteq \text{dom } (\text{lcl } s) \cup \text{fv } e$ 
  and reds-dom-lcl:  $\text{extTA}, P, t \vdash \langle es, s \rangle [-ta\rightarrow] \langle es', s' \rangle \implies \text{dom } (\text{lcl } s') \subseteq \text{dom } (\text{lcl } s) \cup \text{fvs } es$ 
proof (induct rule:red-reds.inducts)
  case (BlockRed e h x V vo ta e' h' x' T)
  thus ?case by(clarsimp)(fastforce split:if-split-asm)
qed auto

lemma red-Suspend-is-call:
 $\llbracket \text{convert-extTA extNTA}, P, t \vdash \langle e, s \rangle -ta\rightarrow \langle e', s' \rangle; \text{Suspend } w \in \text{set } \{ta\}_w \rrbracket$ 
 $\implies \exists a \text{ vs } hT Ts Tr D. \text{call } e' = \lfloor (a, \text{wait}, \text{vs}) \rfloor \wedge \text{typeof-addr } (\text{hp } s) a = \lfloor hT \rfloor \wedge P \vdash \text{class-type-of}$ 
 $hT \text{ sees wait: } Ts \rightarrow Tr = \text{Native in } D$ 
  and reds-Suspend-is-calls:
 $\llbracket \text{convert-extTA extNTA}, P, t \vdash \langle es, s \rangle [-ta\rightarrow] \langle es', s' \rangle; \text{Suspend } w \in \text{set } \{ta\}_w \rrbracket$ 
 $\implies \exists a \text{ vs } hT Ts Tr D. \text{calls } es' = \lfloor (a, \text{wait}, \text{vs}) \rfloor \wedge \text{typeof-addr } (\text{hp } s) a = \lfloor hT \rfloor \wedge P \vdash \text{class-type-of}$ 
 $hT \text{ sees wait: } Ts \rightarrow Tr = \text{Native in } D$ 
proof(induct rule: red-reds.inducts)
  case RedCallExternal
  thus ?case

```

```

    apply clarsimp
    apply (frule red-external-Suspend-StaySame, simp)
    apply (drule red-external-Suspend-waitD, fastforce+)
  done
qed auto

```

```
end
```

```
context J-heap begin
```

```

lemma red-heap-incr: extTA, P, t ⊢ ⟨e, s⟩ -ta→ ⟨e', s'⟩ ⟹ hp s ≤ hp s'
  and reds-heap-incr: extTA, P, t ⊢ ⟨es, s⟩ [-ta→] ⟨es', s'⟩ ⟹ hp s ≤ hp s'
by (induct rule:red-reds.inducts) (auto intro: heap-ops red-external-heap)

```

```

lemma red-preserves-tconf: [ extTA, P, t ⊢ ⟨e, s⟩ -ta→ ⟨e', s'⟩; P, hp s ⊢ t √t ] ⟹ P, hp s' ⊢ t √t
by (drule red-heap-incr) (rule tconf-heap-mono)

```

```

lemma reds-preserves-tconf: [ extTA, P, t ⊢ ⟨es, s⟩ [-ta→] ⟨es', s'⟩; P, hp s ⊢ t √t ] ⟹ P, hp s' ⊢ t √t
by (drule reds-heap-incr) (rule tconf-heap-mono)

```

```
end
```

#### 4.4.2 Code generation

```
context J-heap-base begin
```

```
lemma RedCall-code:
```

```

[ is-vals es; typeof-addr (hp s) a = [hU]; P ⊢ class-type-of hU sees M:Ts→T = [(pns,body)] in D;
  size es = size pns; size Ts = size pns ]
  ⟹ extTA, P, t ⊢ ⟨(addr a)•M(es), s⟩ -ε→ ⟨blocks (this # pns) (Class D # Ts) (Addr a # map
the-Val es) body, s⟩

```

```
and RedCallExternal-code:
```

```

[ is-vals es; typeof-addr (hp s) a = [hU]; P ⊢ class-type-of hU sees M:Ts→T = Native in D;
  P, t ⊢ ⟨a•M(map the-Val es), hp s⟩ -ta→ ext ⟨va, h'⟩ ]
  ⟹ extTA, P, t ⊢ ⟨(addr a)•M(es), s⟩ -extTA ta→ ⟨extRet2J ((addr a)•M(es)) va, (h', lcl s)⟩

```

```
and RedCallNull-code:
```

```
is-vals es ⟹ extTA, P, t ⊢ ⟨null•M(es), s⟩ -ε→ ⟨THROW NullPointer, s⟩
```

```
and CallThrowParams-code:
```

```
is-Throws es ⟹ extTA, P, t ⊢ ⟨(Val v)•M(es), s⟩ -ε→ ⟨hd (dropWhile is-val es), s⟩
```

```

apply (auto simp add: is-vals-conv is-Throws-conv o-def intro: RedCall RedCallExternal RedCallNull
simp del: blocks.simps)
apply (subst dropWhile-append2)
apply (auto intro: CallThrowParams)
done

```

```
end
```

```
lemmas [code-pred-intro] =
```

```
J-heap-base.RedNew[folded Predicate-Compile.contains-def] J-heap-base.RedNewFail J-heap-base.NewArrayRed
```

*J-heap-base.RedNewArray*[folded *Predicate-Compile.contains-def*]  
*J-heap-base.RedNewArrayNegative* *J-heap-base.RedNewArrayFail*  
*J-heap-base.CastRed* *J-heap-base.RedCast* *J-heap-base.RedCastFail* *J-heap-base.InstanceOfRed*  
*J-heap-base.RedInstanceOf* *J-heap-base.BinOpRed1* *J-heap-base.BinOpRed2* *J-heap-base.RedBinOp*  
*J-heap-base.RedBinOpFail*  
*J-heap-base.RedVar* *J-heap-base.LAssRed* *J-heap-base.RedLAss*  
*J-heap-base.AAccRed1* *J-heap-base.AAccRed2* *J-heap-base.RedAAccNull*  
*J-heap-base.RedAAccBounds* *J-heap-base.RedAAcc* *J-heap-base.AAssRed1* *J-heap-base.AAssRed2* *J-heap-base.AAssRed3*  
*J-heap-base.RedAAssNull* *J-heap-base.RedAAssBounds* *J-heap-base.RedAAssStore* *J-heap-base.RedAAss*  
*J-heap-base.ALengthRed*  
*J-heap-base.RedALength* *J-heap-base.RedALengthNull* *J-heap-base.FAccRed* *J-heap-base.RedFAcc* *J-heap-base.RedFAccNull*  
*J-heap-base.FAssRed1* *J-heap-base.FAssRed2* *J-heap-base.RedFAss* *J-heap-base.RedFAssNull*  
*J-heap-base.CASRed1* *J-heap-base.CASRed2* *J-heap-base.CASRed3* *J-heap-base.CASNull* *J-heap-base.RedCASSucceed*  
*J-heap-base.RedCASFail*  
*J-heap-base.CallObj* *J-heap-base.CallParams*

# declare

*J-heap-base.RedCall-code*[code-pred-intro *RedCall-code*]  
*J-heap-base.RedCallExternal-code*[code-pred-intro *RedCallExternal-code*]  
*J-heap-base.RedCallNull-code*[code-pred-intro *RedCallNull-code*]

# lemmas [code-pred-intro] =

*J-heap-base.BlockRed* *J-heap-base.RedBlock* *J-heap-base.SynchronizedRed1* *J-heap-base.SynchronizedNull*  
*J-heap-base.LockSynchronized* *J-heap-base.SynchronizedRed2* *J-heap-base.UnlockSynchronized*  
*J-heap-base.SeqRed* *J-heap-base.RedSeq* *J-heap-base.CondRed* *J-heap-base.RedCondT* *J-heap-base.RedCondF*  
*J-heap-base.RedWhile*  
*J-heap-base.ThrowRed*

# declare

*J-heap-base.RedThrowNull*[code-pred-intro *RedThrowNull*']

# lemmas [code-pred-intro] =

*J-heap-base.TryRed* *J-heap-base.RedTry* *J-heap-base.RedTryCatch*  
*J-heap-base.RedTryFail* *J-heap-base.ListRed1* *J-heap-base.ListRed2*  
*J-heap-base.NewArrayThrow* *J-heap-base.CastThrow* *J-heap-base.InstanceOfThrow* *J-heap-base.BinOpThrow1*  
*J-heap-base.BinOpThrow2*  
*J-heap-base.LAssThrow* *J-heap-base.AAccThrow1* *J-heap-base.AAccThrow2* *J-heap-base.AAssThrow1*  
*J-heap-base.AAssThrow2*  
*J-heap-base.AAssThrow3* *J-heap-base.ALengthThrow* *J-heap-base.FAccThrow* *J-heap-base.FAssThrow1*  
*J-heap-base.FAssThrow2*  
*J-heap-base.CASThrow* *J-heap-base.CASThrow2* *J-heap-base.CASThrow3*  
*J-heap-base.CallThrowObj*

# declare

*J-heap-base.CallThrowParams-code*[code-pred-intro *CallThrowParams-code*]

# lemmas [code-pred-intro] =

*J-heap-base.BlockThrow* *J-heap-base.SynchronizedThrow1* *J-heap-base.SynchronizedThrow2* *J-heap-base.SeqThrow*  
*J-heap-base.CondThrow*

# declare

*J-heap-base.ThrowThrow*[code-pred-intro *ThrowThrow*']

**code-pred**

(modes:

$$J\text{-heap-base.red: } i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow (i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow (i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow o \Rightarrow o \Rightarrow \text{bool}$$
**and**

$$J\text{-heap-base.reds: } i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow (i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow (i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}) \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow o \Rightarrow o \Rightarrow \text{bool}$$

[detect-switches, skip-proof] — proofs are possible, but take veeerry long

*J-heap-base.red***proof** –**case** *red***from** *red.premis* **show** *thesis***proof**(cases rule: *J-heap-base.red.cases*[consumes 1, case-names
*RedNew RedNewFail NewArrayRed RedNewArray RedNewArrayNegative RedNewArrayFail CastRed RedCast RedCastFail InstanceOfRed*
*RedInstanceOf BinOpRed1 BinOpRed2 RedBinOp RedBinOpFail RedVar LAssRed RedLAss*
*AAccRed1 AAccRed2 RedAAccNull RedAAccBounds RedAAcc*
*AAssRed1 AAssRed2 AAssRed3 RedAAssNull RedAAssBounds RedAAssStore RedAAss ALengthRed RedALength RedALengthNull FAccRed*
*RedFAcc RedFAccNull FAssRed1 FAssRed2 RedFAss RedFAssNull CASRed1 CASRed2 CASRed3 RedCASNull RedCASSucceed RedCASFail*
*CallObj CallParams RedCall RedCallExternal RedCallNull*
*BlockRed RedBlock SynchronizedRed1 SynchronizedNull LockSynchronized SynchronizedRed2 UnlockSynchronized SeqRed*
*RedSeq CondRed RedCondT RedCondF RedWhile ThrowRed RedThrowNull TryRed RedTry RedTryCatch RedTryFail*
*NewArrayThrow CastThrow InstanceOfThrow BinOpThrow1 BinOpThrow2 LAssThrow AAccThrow1 AAccThrow2 AAssThrow1 AAssThrow2*
*AAssThrow3 ALengthThrow FAccThrow FAssThrow1 FAssThrow2 CASThrow CASThrow2 CASThrow3*
*CallThrowObj CallThrowParams BlockThrow SynchronizedThrow1*
*SynchronizedThrow2 SeqThrow CondThrow ThrowThrow))*
**case** (*RedCall s a U M Ts T pns body D vs*)**with** *red.RedCall-code*[*OF refl refl refl refl refl refl refl refl refl refl refl, of a M map Val vs s pns D Ts body U T*]**show** *?thesis by*(*simp add: o-def*)**next****case** (*RedCallExternal s a U M Ts T D vs ta va h' ta' e' s'*)**with** *red.RedCallExternal-code*[*OF refl refl refl refl refl refl refl refl refl refl refl, of a M map Val vs s ta va h' U Ts T D*]**show** *?thesis by*(*simp add: o-def*)**next****case** (*RedCallNull M vs s*)**with** *red.RedCallNull-code*[*OF refl refl refl refl refl refl refl refl refl refl refl, of M map Val vs s*]**show** *?thesis by*(*simp add: o-def*)**next****case** (*CallThrowParams es vs a es' v M s*)**with** *red.CallThrowParams-code*[*OF refl refl refl refl refl refl refl refl refl refl refl, of v M map Val vs @ Throw a # es' s*]**show** *?thesis***apply**(*auto simp add: is-Throws-conv*)**apply**(*erule meta-impE*)**apply**(*subst dropWhile-append2*)



```

    apply auto
  done
next
  case RedThrowNull thus ?thesis
    by—(erule (4) red.RedThrowNull'[OF refl refl refl refl refl refl refl refl refl refl])
next
  case ThrowThrow thus ?thesis
    by—(erule (4) red.ThrowThrow'[OF refl refl refl refl refl refl refl refl refl refl])
qed(assumption|erule (4) red.that[unfolded Predicate-Compile.contains-def, OF refl refl refl refl refl refl refl refl refl refl])+
next
  case reds
  from reds.premis show thesis
    by(rule J-heap-base.reds.cases)(assumption|erule (4) reds.that[OF refl refl refl refl refl refl refl refl refl refl])
qed
end

```

## 4.5 Weak well-formedness of Jinja programs

```

theory WWellForm
imports
  ../Common/WellForm
  Expr
begin

definition
  wwf-J-mdecl :: 'addr J-prog  $\Rightarrow$  cname  $\Rightarrow$  'addr J-mb mdecl  $\Rightarrow$  bool
where
  wwf-J-mdecl P C  $\equiv$   $\lambda(M, Ts, T, (pns, body)).$ 
    length Ts = length pns  $\wedge$  distinct pns  $\wedge$  this  $\notin$  set pns  $\wedge$  fv body  $\subseteq$  {this}  $\cup$  set pns

lemma wwf-J-mdecl[simp]:
  wwf-J-mdecl P C (M, Ts, T, pns, body) =
    (length Ts = length pns  $\wedge$  distinct pns  $\wedge$  this  $\notin$  set pns  $\wedge$  fv body  $\subseteq$  {this}  $\cup$  set pns)
abbreviation wwf-J-prog :: 'addr J-prog  $\Rightarrow$  bool
where wwf-J-prog == wf-prog wwf-J-mdecl

end

```

## 4.6 Well-typedness of Jinja expressions

```

theory WellType
imports
  Expr
  State
  ../Common/ExternalCallWF
  ../Common/WellForm
  ../Common/SemiType
begin

declare Listn.lesub-list-impl-same-size[simp del]

```

**declare** *listE-length* [*simp del*]

**type-synonym**

*env* = *vname*  $\rightarrow$  *ty*

**inductive**

*WT* :: (*ty*  $\Rightarrow$  *ty*  $\Rightarrow$  *ty*  $\Rightarrow$  *bool*)  $\Rightarrow$  '*addr J-prog*  $\Rightarrow$  *env*  $\Rightarrow$  '*addr expr*  $\Rightarrow$  *ty*  $\Rightarrow$  *bool* (*-*,*-*,*-*  $\vdash$  *-* :: *-* [51,51,51,51]50)

**and** *WTs* :: (*ty*  $\Rightarrow$  *ty*  $\Rightarrow$  *ty*  $\Rightarrow$  *bool*)  $\Rightarrow$  '*addr J-prog*  $\Rightarrow$  *env*  $\Rightarrow$  '*addr expr list*  $\Rightarrow$  *ty list*  $\Rightarrow$  *bool* (*-*,*-*,*-*  $\vdash$  *-* :: *-* [51,51,51,51]50)

**for** *is-lub* :: *ty*  $\Rightarrow$  *ty*  $\Rightarrow$  *ty*  $\Rightarrow$  *bool* ( $\vdash$  *lub'*((*-*,*-*)') = *-* [51,51,51] 50)

**and** *P* :: '*addr J-prog*

**where**

*WTNew*:

*is-class* *P C*  $\Longrightarrow$

*is-lub*,*P,E*  $\vdash$  *new C* :: *Class C*

| *WTNewArray*:

$\llbracket$  *is-lub*,*P,E*  $\vdash$  *e* :: *Integer*; *is-type* *P* (*T*[])  $\rrbracket \Longrightarrow$

*is-lub*,*P,E*  $\vdash$  *newA T[e]* :: *T*[]

| *WTCast*:

$\llbracket$  *is-lub*,*P,E*  $\vdash$  *e* :: *T*; *P*  $\vdash$  *U*  $\leq$  *T*  $\vee$  *P*  $\vdash$  *T*  $\leq$  *U*; *is-type* *P U*  $\rrbracket$

$\Longrightarrow$  *is-lub*,*P,E*  $\vdash$  *Cast U e* :: *U*

| *WTInstanceOf*:

$\llbracket$  *is-lub*,*P,E*  $\vdash$  *e* :: *T*; *P*  $\vdash$  *U*  $\leq$  *T*  $\vee$  *P*  $\vdash$  *T*  $\leq$  *U*; *is-type* *P U*; *is-refT U*  $\rrbracket$

$\Longrightarrow$  *is-lub*,*P,E*  $\vdash$  *e instanceof U* :: *Boolean*

| *WTVal*:

*typeof v* = *Some T*  $\Longrightarrow$

*is-lub*,*P,E*  $\vdash$  *Val v* :: *T*

| *WTVar*:

*E V* = *Some T*  $\Longrightarrow$

*is-lub*,*P,E*  $\vdash$  *Var V* :: *T*

| *WTBinOp*:

$\llbracket$  *is-lub*,*P,E*  $\vdash$  *e1* :: *T1*; *is-lub*,*P,E*  $\vdash$  *e2* :: *T2*; *P*  $\vdash$  *T1*  $\llbracket$  *bop*  $\rrbracket$  *T2* :: *T*  $\rrbracket$

$\Longrightarrow$  *is-lub*,*P,E*  $\vdash$  *e1*  $\llbracket$  *bop*  $\rrbracket$  *e2* :: *T*

| *WTLAss*:

$\llbracket$  *E V* = *Some T*; *is-lub*,*P,E*  $\vdash$  *e* :: *T'*; *P*  $\vdash$  *T'*  $\leq$  *T*; *V*  $\neq$  *this*  $\rrbracket$

$\Longrightarrow$  *is-lub*,*P,E*  $\vdash$  *V:=e* :: *Void*

| *WTAAcc*:

$\llbracket$  *is-lub*,*P,E*  $\vdash$  *a* :: *T*[]; *is-lub*,*P,E*  $\vdash$  *i* :: *Integer*  $\rrbracket$

$\Longrightarrow$  *is-lub*,*P,E*  $\vdash$  *a[i]* :: *T*

| *WTAAss*:

$\llbracket$  *is-lub*,*P,E*  $\vdash$  *a* :: *T*[]; *is-lub*,*P,E*  $\vdash$  *i* :: *Integer*; *is-lub*,*P,E*  $\vdash$  *e* :: *T'*; *P*  $\vdash$  *T'*  $\leq$  *T*  $\rrbracket$

$\Longrightarrow$  *is-lub*,*P,E*  $\vdash$  *a[i] := e* :: *Void*

- | *WTALength*:  
 $is-lub, P, E \vdash a :: T \mid \implies is-lub, P, E \vdash a.length :: Integer$
- | *WTFAcc*:  
 $\llbracket is-lub, P, E \vdash e :: U; class-type-of' U = \lfloor C \rfloor; P \vdash C \text{ sees } F:T (fm) \text{ in } D \rrbracket$   
 $\implies is-lub, P, E \vdash e.F\{D\} :: T$
- | *WTFAss*:  
 $\llbracket is-lub, P, E \vdash e_1 :: U; class-type-of' U = \lfloor C \rfloor; P \vdash C \text{ sees } F:T (fm) \text{ in } D; is-lub, P, E \vdash e_2 :: T'; P \vdash T' \leq T \rrbracket$   
 $\implies is-lub, P, E \vdash e_1.F\{D\} := e_2 :: Void$
- | *WTCAS*:  
 $\llbracket is-lub, P, E \vdash e1 :: U; class-type-of' U = \lfloor C \rfloor; P \vdash C \text{ sees } F:T (fm) \text{ in } D; volatile fm; is-lub, P, E \vdash e2 :: T'; P \vdash T' \leq T; is-lub, P, E \vdash e3 :: T''; P \vdash T'' \leq T \rrbracket$   
 $\implies is-lub, P, E \vdash e1.compareAndSwap(D.F, e2, e3) :: Boolean$
- | *WTCall*:  
 $\llbracket is-lub, P, E \vdash e :: U; class-type-of' U = \lfloor C \rfloor; P \vdash C \text{ sees } M:Ts \rightarrow T = meth \text{ in } D; is-lub, P, E \vdash es \llbracket :: Ts'; P \vdash Ts' \leq Ts \rrbracket$   
 $\implies is-lub, P, E \vdash e.M(es) :: T$
- | *WTBlock*:  
 $\llbracket is-type P T; is-lub, P, E(V \mapsto T) \vdash e :: T'; case vo of None \Rightarrow True \mid \lfloor v \rfloor \Rightarrow \exists T'. typeof v = \lfloor T' \rfloor \wedge P \vdash T' \leq T \rrbracket$   
 $\implies is-lub, P, E \vdash \{V:T=vo; e\} :: T'$
- | *WTSynchronized*:  
 $\llbracket is-lub, P, E \vdash o' :: T; is-refT T; T \neq NT; is-lub, P, E \vdash e :: T' \rrbracket$   
 $\implies is-lub, P, E \vdash sync(o') e :: T'$
- Note that insync is not statically typable.
- | *WTSeq*:  
 $\llbracket is-lub, P, E \vdash e_1 :: T_1; is-lub, P, E \vdash e_2 :: T_2 \rrbracket$   
 $\implies is-lub, P, E \vdash e_1;;e_2 :: T_2$
- | *WTCond*:  
 $\llbracket is-lub, P, E \vdash e :: Boolean; is-lub, P, E \vdash e_1 :: T_1; is-lub, P, E \vdash e_2 :: T_2; lub(T_1, T_2) = T \rrbracket$   
 $\implies is-lub, P, E \vdash if (e) e_1 else e_2 :: T$
- | *WTWhile*:  
 $\llbracket is-lub, P, E \vdash e :: Boolean; is-lub, P, E \vdash c :: T \rrbracket$   
 $\implies is-lub, P, E \vdash while (e) c :: Void$
- | *WTThrow*:  
 $\llbracket is-lub, P, E \vdash e :: Class C; P \vdash C \preceq^* Throwable \rrbracket \implies$   
 $is-lub, P, E \vdash throw e :: Void$
- | *WTTry*:  
 $\llbracket is-lub, P, E \vdash e_1 :: T; is-lub, P, E(V \mapsto Class C) \vdash e_2 :: T; P \vdash C \preceq^* Throwable \rrbracket$   
 $\implies is-lub, P, E \vdash try e_1 catch(C V) e_2 :: T$
- | *WTNil*:  $is-lub, P, E \vdash [] \llbracket :: \rrbracket$

|  $WTCons: \llbracket is-lub, P, E \vdash e :: T; is-lub, P, E \vdash es \llbracket :: Ts \rrbracket \implies is-lub, P, E \vdash e \# es \llbracket :: T \# Ts \rrbracket$

**abbreviation**  $WT' :: 'addr J-prog \Rightarrow env \Rightarrow 'addr expr \Rightarrow ty \Rightarrow bool \ (-, - \vdash - :: - [51, 51, 51] 50)$   
**where**  $WT' P \equiv WT (TypeRel.is-lub P) P$

**abbreviation**  $WTs' :: 'addr J-prog \Rightarrow env \Rightarrow 'addr expr list \Rightarrow ty list \Rightarrow bool \ (-, - \vdash - \llbracket :: - [51, 51, 51] 50)$

**where**  $WTs' P \equiv WTs (TypeRel.is-lub P) P$

**declare**  $WT-WTs.intros[intro!]$

**inductive-simps**  $WTs-iffs [iff]:$

$is-lub', P, E \vdash [] \llbracket :: Ts$   
 $is-lub', P, E \vdash e \# es \llbracket :: T \# Ts$   
 $is-lub', P, E \vdash e \# es \llbracket :: Ts$

**lemma**  $WTs-conv-list-all2:$

**fixes**  $is-lub$

**shows**  $is-lub, P, E \vdash es \llbracket :: Ts = list-all2 (WT is-lub P E) es Ts$

**by**( $induct es arbitrary: Ts$ )( $auto simp add: list-all2-Cons1 elim: WTs.cases$ )

**lemma**  $WTs-append [iff]: \bigwedge is-lub Ts. (is-lub, P, E \vdash es_1 @ es_2 \llbracket :: Ts) =$   
 $(\exists Ts_1 Ts_2. Ts = Ts_1 @ Ts_2 \wedge is-lub, P, E \vdash es_1 \llbracket :: Ts_1 \wedge is-lub, P, E \vdash es_2 \llbracket :: Ts_2)$

**by**( $auto simp add: WTs-conv-list-all2 list-all2-append1 dest: list-all2-lengthD[symmetric]$ )

**inductive-simps**  $WT-iffs [iff]:$

$is-lub', P, E \vdash Val v :: T$   
 $is-lub', P, E \vdash Var V :: T$   
 $is-lub', P, E \vdash e_1;;e_2 :: T_2$   
 $is-lub', P, E \vdash \{V:T=vo; e\} :: T'$

**inductive-cases**  $WT-elim-cases[elim!]:$

$is-lub', P, E \vdash V := e :: T$   
 $is-lub', P, E \vdash sync(o') e :: T$   
 $is-lub', P, E \vdash if (e) e_1 else e_2 :: T$   
 $is-lub', P, E \vdash while (e) c :: T$   
 $is-lub', P, E \vdash throw e :: T$   
 $is-lub', P, E \vdash try e_1 catch (C V) e_2 :: T$   
 $is-lub', P, E \vdash Cast D e :: T$   
 $is-lub', P, E \vdash e instanceof U :: T$   
 $is-lub', P, E \vdash a \cdot F\{D\} :: T$   
 $is-lub', P, E \vdash a \cdot F\{D\} := v :: T$   
 $is-lub', P, E \vdash e \cdot compareAndSwap(D \cdot F, e', e'') :: T$   
 $is-lub', P, E \vdash e_1 \llcorner bop \rrcorner e_2 :: T$   
 $is-lub', P, E \vdash new C :: T$   
 $is-lub', P, E \vdash newA T[e] :: T'$   
 $is-lub', P, E \vdash a[i] := e :: T$   
 $is-lub', P, E \vdash a[i] :: T$   
 $is-lub', P, E \vdash a \cdot length :: T$   
 $is-lub', P, E \vdash e \cdot M(ps) :: T$   
 $is-lub', P, E \vdash sync(o') e :: T$   
 $is-lub', P, E \vdash insync(a) e :: T$

```

lemma fixes is-lub :: ty ⇒ ty ⇒ ty ⇒ bool (⊢ lub'((-,/ -)') = - [51,51,51] 50)
  assumes is-lub-unique:  $\bigwedge T1\ T2\ T3\ T4. \llbracket \vdash \text{lub}(T1, T2) = T3; \vdash \text{lub}(T1, T2) = T4 \rrbracket \implies T3 = T4$ 
  shows WT-unique:  $\llbracket \text{is-lub}, P, E \vdash e :: T; \text{is-lub}, P, E \vdash e :: T' \rrbracket \implies T = T'$ 
  and WTs-unique:  $\llbracket \text{is-lub}, P, E \vdash es \llbracket :: \rrbracket Ts; \text{is-lub}, P, E \vdash es \llbracket :: \rrbracket Ts' \rrbracket \implies Ts = Ts'$ 
apply(induct arbitrary: T' and Ts' rule: WT-WTs.inducts)
apply blast
apply blast
apply blast
apply blast
apply fastforce
apply fastforce
apply(fastforce dest: WT-binop-fun)
apply fastforce
apply fastforce
apply fastforce
apply fastforce
apply(fastforce dest: sees-field-fun)
apply(fastforce dest: sees-field-fun)
apply blast
apply(fastforce dest: sees-method-fun)
apply fastforce
apply fastforce
apply fastforce
apply(blast dest: is-lub-unique)
apply fastforce
apply fastforce
apply blast
apply fastforce
apply fastforce
done

```

```

lemma fixes is-lub
  shows wt-env-mono:  $\text{is-lub}, P, E \vdash e :: T \implies (\bigwedge E'. E \subseteq_m E' \implies \text{is-lub}, P, E' \vdash e :: T)$ 
  and wts-env-mono:  $\text{is-lub}, P, E \vdash es \llbracket :: \rrbracket Ts \implies (\bigwedge E'. E \subseteq_m E' \implies \text{is-lub}, P, E' \vdash es \llbracket :: \rrbracket Ts)$ 
apply(induct rule: WT-WTs.inducts)
apply(simp add: WTNew)
apply(simp add: WTNewArray)
apply(fastforce simp: WTCast)
apply(fastforce simp: WTInstanceOf)
apply(fastforce simp: WTVal)
apply(simp add: WTVar map-le-def dom-def)
apply(fastforce simp: WTBinOp)
apply(force simp:map-le-def)
apply(simp add: WTAAcc)
apply(simp add: WTAAss, fastforce)
apply(simp add: WTALength, fastforce)
apply(fastforce simp: WTFAcc)
apply(fastforce simp: WTFAss del: WT-WTs.intros WT-elim-cases)
apply blast
apply(fastforce)
apply(fastforce simp: map-le-def WTBlock)
apply(fastforce simp: WTSynchronized)
apply(fastforce simp: WTSeq)

```

```

apply(fastforce simp: WTCond)
apply(fastforce simp: WTWhile)
apply(fastforce simp: WTThrow)
apply(fastforce simp: WTTry map-le-def dom-def)
apply(fastforce)+
done

```

```

lemma fixes is-lub
  shows WT-fv: is-lub,P,E ⊢ e :: T ⇒ fv e ⊆ dom E
  and WT-fvs: is-lub,P,E ⊢ es [::] Ts ⇒ fvs es ⊆ dom E
apply(induct rule: WT-WTs.inducts)
apply(simp-all del: fun-upd-apply)
apply fast+
done

```

```

lemma fixes is-lub
  shows WT-expr-locks: is-lub,P,E ⊢ e :: T ⇒ expr-locks e = (λad. 0)
  and WTs-expr-lockss: is-lub,P,E ⊢ es [::] Ts ⇒ expr-lockss es = (λad. 0)
by(induct rule: WT-WTs.inducts)(auto)

```

```

lemma
  fixes is-lub :: ty ⇒ ty ⇒ ty ⇒ bool (⊢ lub'((-,/ -)') = - [51,51,51] 50)
  assumes is-lub-is-type: ∧ T1 T2 T3. [⊢ lub(T1, T2) = T3; is-type P T1; is-type P T2] ⇒ is-type P T3
  and wf: wf-prog wf-md P
  shows WT-is-type: [ is-lub,P,E ⊢ e :: T; ran E ⊆ types P ] ⇒ is-type P T
  and WTs-is-type: [ is-lub,P,E ⊢ es [::] Ts; ran E ⊆ types P ] ⇒ set Ts ⊆ types P
apply(induct rule: WT-WTs.inducts)
apply simp
apply simp
apply simp
apply simp
apply (simp add: typeof-lit-is-type)
apply (fastforce intro: nth-mem simp add: ran-def)
apply(simp add: WT-binop-is-type)
apply(simp)
apply(simp del: is-type-array add: is-type-ArrayD)
apply(simp)
apply(simp)
apply(simp add: sees-field-is-type[OF wf])
apply(simp)
apply simp
apply(fastforce dest: sees-wf-mdecl[OF wf] simp: wf-mdecl-def)
apply(fastforce simp add: ran-def split: if-split-asm)
apply(simp add: is-class-Object[OF wf])
apply(simp)
apply(simp)
apply(fastforce intro: is-lub-is-type)
apply(simp)
apply(simp)
apply simp
apply simp
apply simp
done

```

**lemma**

**fixes** *is-lub1* :: *ty*  $\Rightarrow$  *ty*  $\Rightarrow$  *ty*  $\Rightarrow$  *bool* ( $\vdash_1 \text{lub}'((- / -)') = - [51, 51, 51] 50$ )  
**and** *is-lub2* :: *ty*  $\Rightarrow$  *ty*  $\Rightarrow$  *ty*  $\Rightarrow$  *bool* ( $\vdash_2 \text{lub}'((- / -)') = - [51, 51, 51] 50$ )  
**assumes** *wf*: *wf-prog wf-md P*  
**and** *is-lub1-into-is-lub2*:  $\bigwedge T1\ T2\ T3. \llbracket \vdash_1 \text{lub}(T1, T2) = T3; \text{is-type } P\ T1; \text{is-type } P\ T2 \rrbracket \Longrightarrow \vdash_2 \text{lub}(T1, T2) = T3$   
**and** *is-lub2-is-type*:  $\bigwedge T1\ T2\ T3. \llbracket \vdash_2 \text{lub}(T1, T2) = T3; \text{is-type } P\ T1; \text{is-type } P\ T2 \rrbracket \Longrightarrow \text{is-type } P\ T3$   
**shows** *WT-change-is-lub*:  $\llbracket \text{is-lub1}, P, E \vdash e :: T; \text{ran } E \subseteq \text{types } P \rrbracket \Longrightarrow \text{is-lub2}, P, E \vdash e :: T$   
**and** *WTs-change-is-lub*:  $\llbracket \text{is-lub1}, P, E \vdash es \llbracket :: \rrbracket Ts; \text{ran } E \subseteq \text{types } P \rrbracket \Longrightarrow \text{is-lub2}, P, E \vdash es \llbracket :: \rrbracket Ts$   
**proof**(*induct rule: WT-WTs.inducts*)  
**case** (*WTBlock U E V e' T vo*)  
**from**  $\langle \text{is-type } P\ U \rangle \langle \text{ran } E \subseteq \text{types } P \rangle$   
**have**  $\text{ran } (E(V \mapsto U)) \subseteq \text{types } P$  **by**(*auto simp add: ran-def*)  
**hence**  $\text{is-lub2}, P, E(V \mapsto U) \vdash e' :: T$  **by**(*rule WTBlock*)  
**with**  $\langle \text{is-type } P\ U \rangle$  **show** ?*case*  
**using**  $\langle \text{case } vo \text{ of } None \Rightarrow \text{True} \mid [v] \Rightarrow \exists T'. \text{typeof } v = [T'] \wedge P \vdash T' \leq U \rangle$  **by** *auto*  
**next**  
**case** (*WTCond E e e1 T1 e2 T2 T*)  
**from**  $\langle \text{ran } E \subseteq \text{types } P \rangle$  **have**  $\text{is-lub2}, P, E \vdash e :: \text{Boolean}$   $\text{is-lub2}, P, E \vdash e1 :: T1$   $\text{is-lub2}, P, E \vdash e2$   
 $:: T2$   
**by**(*rule WTCond*)  
**moreover from** *is-lub2-is-type wf*  $\langle \text{is-lub2}, P, E \vdash e1 :: T1 \rangle \langle \text{ran } E \subseteq \text{types } P \rangle$   
**have** *is-type* *P T1* **by**(*rule WT-is-type*)  
**from** *is-lub2-is-type wf*  $\langle \text{is-lub2}, P, E \vdash e2 :: T2 \rangle \langle \text{ran } E \subseteq \text{types } P \rangle$   
**have** *is-type* *P T2* **by**(*rule WT-is-type*)  
**with**  $\langle \vdash_1 \text{lub}(T1, T2) = T \rangle \langle \text{is-type } P\ T1 \rangle$   
**have**  $\vdash_2 \text{lub}(T1, T2) = T$  **by**(*rule is-lub1-into-is-lub2*)  
**ultimately show** ?*case* ..  
**next**  
**case** (*WTTry E e1 T V C e2*)  
**from**  $\langle \text{ran } E \subseteq \text{types } P \rangle$  **have**  $\text{is-lub2}, P, E \vdash e1 :: T$  **by**(*rule WTTry*)  
**moreover from**  $\langle P \vdash C \preceq^* \text{Throwable} \rangle$  **have** *is-class* *P C*  
**by**(*rule is-class-sub-Throwable[OF wf]*)  
**with**  $\langle \text{ran } E \subseteq \text{types } P \rangle$  **have**  $\text{ran } (E(V \mapsto \text{Class } C)) \subseteq \text{types } P$   
**by**(*auto simp add: ran-def*)  
**hence**  $\text{is-lub2}, P, E(V \mapsto \text{Class } C) \vdash e2 :: T$  **by**(*rule WTTry*)  
**ultimately show** ?*case* **using**  $\langle P \vdash C \preceq^* \text{Throwable} \rangle$  ..  
**qed** *auto*

#### 4.6.1 Code generator setup

**lemma** *WTBlock-code*:

$\bigwedge \text{is-lub}. \llbracket \text{is-type } P\ T; \text{is-lub}, P, E(V \mapsto T) \vdash e :: T' \rrbracket$   
 $\text{case } vo \text{ of } None \Rightarrow \text{True} \mid [v] \Rightarrow \text{case } \text{typeof } v \text{ of } None \Rightarrow \text{False} \mid \text{Some } T' \Rightarrow P \vdash T' \leq T \rrbracket$   
 $\Longrightarrow \text{is-lub}, P, E \vdash \{V:T=vo; e\} :: T'$   
**by**(*auto*)

**lemmas** [*code-pred-intro*] =

*WTNew WTNewArray WTCast WTInstanceOf WTVal WTVar WTBinOp WTLAss WTAAcc WTAAss*  
*WTALength WTFAcc WTFAss WTCAS WTCall*

**declare**

*WTBlock-code* [*code-pred-intro WTBlock*]

```

lemmas [code-pred-intro] =
  WTSynchronized WTSeq WTCond WTWhile WTThrow WTTry
  WTNil WTCons

```

```

code-pred

```

```

  (modes:
    (i ⇒ i ⇒ o ⇒ bool) ⇒ i ⇒ i ⇒ i ⇒ o ⇒ bool,
    (i ⇒ i ⇒ o ⇒ bool) ⇒ i ⇒ i ⇒ i ⇒ i ⇒ bool)
  [detect-switches, skip-proof]
  WT

```

```

proof -

```

```

  case WT

```

```

  from WT.premis show thesis

```

```

  proof cases

```

```

    case (WTBlock T V e vo)

```

```

    thus thesis using WT.WTBlock'[OF refl refl refl, of V T vo e] by(auto)

```

```

  qed(assumption|erule WT.that[OF refl refl refl]|rule refl)+

```

```

next

```

```

  case WTs

```

```

  from WTs.premis WTs.that show thesis by cases blast+

```

```

qed

```

```

inductive is-lub-sup :: 'm prog ⇒ ty ⇒ ty ⇒ ty ⇒ bool

```

```

for P T1 T2 T3

```

```

where

```

```

  sup P T1 T2 = OK T3 ⇒ is-lub-sup P T1 T2 T3

```

```

code-pred

```

```

  (modes: i ⇒ i ⇒ i ⇒ o ⇒ bool, i ⇒ i ⇒ i ⇒ i ⇒ bool)
  is-lub-sup

```

```

  .

```

```

definition WT-code :: 'addr J-prog ⇒ env ⇒ 'addr expr ⇒ ty ⇒ bool (-, - ⊢ - ::'' - [51,51,51] 50)

```

```

where WT-code P ≡ WT (is-lub-sup P) P

```

```

definition WTs-code :: 'addr J-prog ⇒ env ⇒ 'addr expr list ⇒ ty list ⇒ bool (-, - ⊢ - [::'] - [51,51,51] 50)

```

```

where WTs-code P ≡ WTs (is-lub-sup P) P

```

```

lemma assumes wf: wf-prog wf-md P

```

```

  shows WT-code-into-WT:

```

```

  [ P, E ⊢ e ::' T; ran E ⊆ types P ] ⇒ P, E ⊢ e :: T

```

```

  and WTs-code-into-WTs:

```

```

  [ P, E ⊢ es [::'] Ts; ran E ⊆ types P ] ⇒ P, E ⊢ es [::] Ts

```

```

proof -

```

```

  assume ran: ran E ⊆ types P

```

```

  { assume wt: P, E ⊢ e ::' T

```

```

    show P, E ⊢ e :: T

```

```

    by(rule WT-change-is-lub[OF wf - - wt[unfolded WT-code-def] ran])(blast elim!: is-lub-sup.cases
intro: sup-is-lubI[OF wf] is-lub-is-type[OF wf])+ }

```

```

  { assume wts: P, E ⊢ es [::'] Ts

```

```

    show P, E ⊢ es [::] Ts

```

```

    by(rule WTs-change-is-lub[OF wf - - wts[unfolded WTs-code-def] ran])(blast elim!: is-lub-sup.cases

```



```

intro: sup-is-lubI[OF wf] is-lub-is-type[OF wf])+ }
qed

lemma assumes wf: wf-prog wf-md P
  shows WT-into-WT-code:
     $\llbracket P, E \vdash e :: T; \text{ran } E \subseteq \text{types } P \rrbracket \implies P, E \vdash e ::' T$ 

  and WT-into-WTs-code-OK:
     $\llbracket P, E \vdash es [\::] Ts; \text{ran } E \subseteq \text{types } P \rrbracket \implies P, E \vdash es [\::'] Ts$ 
proof -
  assume ran:  $\text{ran } E \subseteq \text{types } P$ 
  { assume wt:  $P, E \vdash e :: T$ 
    show  $P, E \vdash e ::' T$  unfolding WT-code-def
      by(rule WT-change-is-lub[OF wf - - wt ran])(blast intro!: is-lub-sup.intros intro: is-lub-subD[OF
wf] sup-is-type[OF wf] elim!: is-lub-sup.cases)+ }
    { assume wts:  $P, E \vdash es [\::] Ts$ 
      show  $P, E \vdash es [\::'] Ts$  unfolding WTs-code-def
        by(rule WTs-change-is-lub[OF wf - - wts ran])(blast intro!: is-lub-sup.intros intro: is-lub-subD[OF
wf] sup-is-type[OF wf] elim!: is-lub-sup.cases)+ }
  }
qed

theorem WT-eq-WT-code:
  assumes wf-prog wf-md P
  and  $\text{ran } E \subseteq \text{types } P$ 
  shows  $P, E \vdash e :: T \longleftrightarrow P, E \vdash e ::' T$ 
using assms by(blast intro: WT-code-into-WT WT-into-WT-code)

code-pred
  (modes:  $i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow \text{bool}, i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}$ )
  [inductify]
  WT-code
.

code-pred
  (modes:  $i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow \text{bool}, i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}$ )
  [inductify]
  WTs-code
.

end

```

## 4.7 Definite assignment

```

theory DefAss
imports
  Expr
begin

```

### 4.7.1 Hypersets

```

type-synonym 'a hyperset = 'a set option

```

```

definition hyperUn :: 'a hyperset  $\Rightarrow$  'a hyperset  $\Rightarrow$  'a hyperset  (infixl  $\sqcup$  65)
where

```

$A \sqcup B \equiv \text{case } A \text{ of } \text{None} \Rightarrow \text{None} \mid \lfloor A \rfloor \Rightarrow (\text{case } B \text{ of } \text{None} \Rightarrow \text{None} \mid \lfloor B \rfloor \Rightarrow \lfloor A \sqcup B \rfloor)$

**definition** *hyperInt* :: 'a hyperset  $\Rightarrow$  'a hyperset  $\Rightarrow$  'a hyperset (infixl  $\sqcap$  70)  
**where**

$A \sqcap B \equiv \text{case } A \text{ of } \text{None} \Rightarrow B \mid \lfloor A \rfloor \Rightarrow (\text{case } B \text{ of } \text{None} \Rightarrow \lfloor A \rfloor \mid \lfloor B \rfloor \Rightarrow \lfloor A \sqcap B \rfloor)$

**definition** *hyperDiff1* :: 'a hyperset  $\Rightarrow$  'a  $\Rightarrow$  'a hyperset (infixl  $\ominus$  65)  
**where**

$A \ominus a \equiv \text{case } A \text{ of } \text{None} \Rightarrow \text{None} \mid \lfloor A \rfloor \Rightarrow \lfloor A - \{a\} \rfloor$

**definition** *hyper-isin* :: 'a  $\Rightarrow$  'a hyperset  $\Rightarrow$  bool (infix  $\in\in$  50)  
**where**

$a \in\in A \equiv \text{case } A \text{ of } \text{None} \Rightarrow \text{True} \mid \lfloor A \rfloor \Rightarrow a \in A$

**definition** *hyper-subset* :: 'a hyperset  $\Rightarrow$  'a hyperset  $\Rightarrow$  bool (infix  $\sqsubseteq$  50)  
**where**

$A \sqsubseteq B \equiv \text{case } B \text{ of } \text{None} \Rightarrow \text{True} \mid \lfloor B \rfloor \Rightarrow (\text{case } A \text{ of } \text{None} \Rightarrow \text{False} \mid \lfloor A \rfloor \Rightarrow A \sqsubseteq B)$

**lemmas** *hyperset-defs* =

*hyperUn-def hyperInt-def hyperDiff1-def hyper-isin-def hyper-subset-def*

**lemma** [simp]:  $\lfloor \{\} \rfloor \sqcup A = A \wedge A \sqcup \lfloor \{\} \rfloor = A$

**lemma** [simp]:  $\lfloor A \rfloor \sqcup \lfloor B \rfloor = \lfloor A \sqcup B \rfloor \wedge \lfloor A \rfloor \ominus a = \lfloor A - \{a\} \rfloor$

**lemma** [simp]:  $\text{None} \sqcup A = \text{None} \wedge A \sqcup \text{None} = \text{None}$

**lemma** [simp]:  $a \in\in \text{None} \wedge \text{None} \ominus a = \text{None}$

**lemma** *hyperUn-assoc*:  $(A \sqcup B) \sqcup C = A \sqcup (B \sqcup C)$

**lemma** *hyper-insert-comm*:  $A \sqcup \lfloor \{a\} \rfloor = \lfloor \{a\} \rfloor \sqcup A \wedge A \sqcup (\lfloor \{a\} \rfloor \sqcup B) = \lfloor \{a\} \rfloor \sqcup (A \sqcup B)$

**lemma** *sqSub-mem-lem* [elim]:  $\llbracket A \sqsubseteq A'; a \in\in A \rrbracket \Longrightarrow a \in\in A'$

**by**(*auto simp add: hyperset-defs*)

**lemma** [iff]:  $A \sqsubseteq \text{None}$

**by**(*auto simp add: hyperset-defs*)

**lemma** [simp]:  $A \sqsubseteq A$

**by**(*auto simp add: hyperset-defs*)

**lemma** [iff]:  $\lfloor A \rfloor \sqsubseteq \lfloor B \rfloor \longleftrightarrow A \subseteq B$

**by**(*auto simp add: hyperset-defs*)

**lemma** *sqUn-lem2*:  $A \sqsubseteq A' \Longrightarrow B \sqcup A \sqsubseteq B \sqcup A'$

**by**(*simp add: hyperset-defs*) *blast*

**lemma** *sqSub-trans* [trans, intro]:  $\llbracket A \sqsubseteq B; B \sqsubseteq C \rrbracket \Longrightarrow A \sqsubseteq C$

**by**(*auto simp add: hyperset-defs*)

**lemma** *hyperUn-comm*:  $A \sqcup B = B \sqcup A$

**by**(*auto simp add: hyperset-defs*)

**lemma** *hyperUn-leftComm*:  $A \sqcup (B \sqcup C) = B \sqcup (A \sqcup C)$

**by**(*auto simp add: hyperset-defs*)

**lemmas** *hyperUn-ac = hyperUn-comm hyperUn-leftComm hyperUn-assoc*

**lemma** [*simp*]:  $\lfloor \{\} \rfloor \sqcup B = B$   
**by**(*auto*)

**lemma** [*simp*]:  $\lfloor \{\} \rfloor \sqsubseteq A$   
**by**(*auto simp add: hyperset-defs*)

**lemma** *sqInt-lem*:  $A \sqsubseteq A' \implies A \sqcap B \sqsubseteq A' \sqcap B$   
**by**(*auto simp add: hyperset-defs*)

### 4.7.2 Definite assignment

**primrec**  $\mathcal{A} :: ('a, 'b, 'addr) \exp \Rightarrow 'a \text{ hyperset}$   
**and**  $\mathcal{A}s :: ('a, 'b, 'addr) \exp \text{ list} \Rightarrow 'a \text{ hyperset}$   
**where**

$\mathcal{A} (\text{new } C) = \lfloor \{\} \rfloor$   
 $\mathcal{A} (\text{newA } T[e]) = \mathcal{A} e$   
 $\mathcal{A} (\text{Cast } C e) = \mathcal{A} e$   
 $\mathcal{A} (e \text{ instanceof } T) = \mathcal{A} e$   
 $\mathcal{A} (\text{Val } v) = \lfloor \{\} \rfloor$   
 $\mathcal{A} (e_1 \llcorner \text{bop} \rceil e_2) = \mathcal{A} e_1 \sqcup \mathcal{A} e_2$   
 $\mathcal{A} (\text{Var } V) = \lfloor \{\} \rfloor$   
 $\mathcal{A} (\text{LAss } V e) = \lfloor \{V\} \rfloor \sqcup \mathcal{A} e$   
 $\mathcal{A} (a[i]) = \mathcal{A} a \sqcup \mathcal{A} i$   
 $\mathcal{A} (a[i] := e) = \mathcal{A} a \sqcup \mathcal{A} i \sqcup \mathcal{A} e$   
 $\mathcal{A} (a \cdot \text{length}) = \mathcal{A} a$   
 $\mathcal{A} (e \cdot F\{D\}) = \mathcal{A} e$   
 $\mathcal{A} (e_1 \cdot F\{D\} := e_2) = \mathcal{A} e_1 \sqcup \mathcal{A} e_2$   
 $\mathcal{A} (e_1 \cdot \text{compareAndSwap}(D \cdot F, e_2, e_3)) = \mathcal{A} e_1 \sqcup \mathcal{A} e_2 \sqcup \mathcal{A} e_3$   
 $\mathcal{A} (e \cdot M(es)) = \mathcal{A} e \sqcup \mathcal{A}s es$   
 $\mathcal{A} (\{V : T = v_0; e\}) = \mathcal{A} e \ominus V$   
 $\mathcal{A} (\text{sync}_V(o') e) = \mathcal{A} o' \sqcup \mathcal{A} e$   
 $\mathcal{A} (\text{insync}_V(a) e) = \mathcal{A} e$   
 $\mathcal{A} (e_1 ;; e_2) = \mathcal{A} e_1 \sqcup \mathcal{A} e_2$   
 $\mathcal{A} (\text{if } (e) e_1 \text{ else } e_2) = \mathcal{A} e \sqcup (\mathcal{A} e_1 \sqcap \mathcal{A} e_2)$   
 $\mathcal{A} (\text{while } (b) e) = \mathcal{A} b$   
 $\mathcal{A} (\text{throw } e) = \text{None}$   
 $\mathcal{A} (\text{try } e_1 \text{ catch } (C V) e_2) = \mathcal{A} e_1 \sqcap (\mathcal{A} e_2 \ominus V)$

$\mathcal{A}s (\[]) = \lfloor \{\} \rfloor$   
 $\mathcal{A}s (e \# es) = \mathcal{A} e \sqcup \mathcal{A}s es$

**primrec**  $\mathcal{D} :: ('a, 'b, 'addr) \exp \Rightarrow 'a \text{ hyperset} \Rightarrow \text{bool}$   
**and**  $\mathcal{D}s :: ('a, 'b, 'addr) \exp \text{ list} \Rightarrow 'a \text{ hyperset} \Rightarrow \text{bool}$   
**where**

$\mathcal{D} (\text{new } C) A = \text{True}$   
 $\mathcal{D} (\text{newA } T[e]) A = \mathcal{D} e A$   
 $\mathcal{D} (\text{Cast } C e) A = \mathcal{D} e A$   
 $\mathcal{D} (e \text{ instanceof } T) = \mathcal{D} e$   
 $\mathcal{D} (\text{Val } v) A = \text{True}$   
 $\mathcal{D} (e_1 \llcorner \text{bop} \rceil e_2) A = (\mathcal{D} e_1 A \wedge \mathcal{D} e_2 (A \sqcup \mathcal{A} e_1))$   
 $\mathcal{D} (\text{Var } V) A = (V \in A)$   
 $\mathcal{D} (\text{LAss } V e) A = \mathcal{D} e A$

```

|  $\mathcal{D} (a[i]) A = (\mathcal{D} a A \wedge \mathcal{D} i (A \sqcup \mathcal{A} a))$ 
|  $\mathcal{D} (a[i] := e) A = (\mathcal{D} a A \wedge \mathcal{D} i (A \sqcup \mathcal{A} a) \wedge \mathcal{D} e (A \sqcup \mathcal{A} a \sqcup \mathcal{A} i))$ 
|  $\mathcal{D} (a.length) A = \mathcal{D} a A$ 
|  $\mathcal{D} (e.F\{D\}) A = \mathcal{D} e A$ 
|  $\mathcal{D} (e_1.F\{D\} := e_2) A = (\mathcal{D} e_1 A \wedge \mathcal{D} e_2 (A \sqcup \mathcal{A} e_1))$ 
|  $\mathcal{D} (e_1.compareAndSwap(D.F, e_2, e_3)) A = (\mathcal{D} e_1 A \wedge \mathcal{D} e_2 (A \sqcup \mathcal{A} e_1) \wedge \mathcal{D} e_3 (A \sqcup \mathcal{A} e_1 \sqcup \mathcal{A} e_2))$ 
|  $\mathcal{D} (e.M(es)) A = (\mathcal{D} e A \wedge \mathcal{D} s es (A \sqcup \mathcal{A} e))$ 
|  $\mathcal{D} (\{V:T=vo; e\}) A = (if\ vo = None\ then\ \mathcal{D} e (A \ominus V)\ else\ \mathcal{D} e (A \sqcup \lfloor \{V\} \rfloor))$ 
|  $\mathcal{D} (sync_V (o') e) A = (\mathcal{D} o' A \wedge \mathcal{D} e (A \sqcup \mathcal{A} o'))$ 
|  $\mathcal{D} (insync_V (a) e) A = \mathcal{D} e A$ 
|  $\mathcal{D} (e_1;;e_2) A = (\mathcal{D} e_1 A \wedge \mathcal{D} e_2 (A \sqcup \mathcal{A} e_1))$ 
|  $\mathcal{D} (if (e) e_1 else e_2) A = (\mathcal{D} e A \wedge \mathcal{D} e_1 (A \sqcup \mathcal{A} e) \wedge \mathcal{D} e_2 (A \sqcup \mathcal{A} e))$ 
|  $\mathcal{D} (while (e) c) A = (\mathcal{D} e A \wedge \mathcal{D} c (A \sqcup \mathcal{A} e))$ 
|  $\mathcal{D} (throw e) A = \mathcal{D} e A$ 
|  $\mathcal{D} (try e_1 catch(C V) e_2) A = (\mathcal{D} e_1 A \wedge \mathcal{D} e_2 (A \sqcup \lfloor \{V\} \rfloor))$ 

```

```

|  $\mathcal{D} s (\lfloor \rfloor) A = True$ 
|  $\mathcal{D} s (e\#es) A = (\mathcal{D} e A \wedge \mathcal{D} s es (A \sqcup \mathcal{A} e))$ 

```

**lemma** *As-map-Val[simp]*:  $\mathcal{A} s (map\ Val\ vs) = \lfloor \{ \} \rfloor$

**lemma** *As-append [simp]*:  $\mathcal{A} s (xs @ ys) = (\mathcal{A} s xs) \sqcup (\mathcal{A} s ys)$

**by**(*induct xs, auto simp add: hyperset-defs*)

**lemma** *Ds-map-Val[simp]*:  $\mathcal{D} s (map\ Val\ vs) A$

**lemma** *D-append[iff]*:  $\bigwedge A. \mathcal{D} s (es @ es') A = (\mathcal{D} s es A \wedge \mathcal{D} s es' (A \sqcup \mathcal{A} s es))$

**lemma fixes**  $e :: ('a, 'b, 'addr)\ exp$  **and**  $es :: ('a, 'b, 'addr)\ exp\ list$

**shows** *A-fv*:  $\bigwedge A. \mathcal{A} e = \lfloor A \rfloor \implies A \subseteq fv\ e$

**and**  $\bigwedge A. \mathcal{A} s es = \lfloor A \rfloor \implies A \subseteq fvs\ es$

**apply**(*induct e and es rule: A.induct As.induct*)

**apply** (*simp-all add: hyperset-defs*)

**apply** *fast+*

**done**

**lemma** *sqUn-lem*:  $A \sqsubseteq A' \implies A \sqcup B \sqsubseteq A' \sqcup B$

**lemma** *diff-lem*:  $A \sqsubseteq A' \implies A \ominus b \sqsubseteq A' \ominus b$

**lemma fixes**  $e :: ('a, 'b, 'addr)\ exp$  **and**  $es :: ('a, 'b, 'addr)\ exp\ list$

**shows** *D-mono*:  $\bigwedge A A'. A \sqsubseteq A' \implies \mathcal{D} e A \implies \mathcal{D} e A'$

**and** *Ds-mono*:  $\bigwedge A A'. A \sqsubseteq A' \implies \mathcal{D} s es A \implies \mathcal{D} s es A'$

**lemma** *D-mono'*:  $\mathcal{D} e A \implies A \sqsubseteq A' \implies \mathcal{D} e A'$

**and** *Ds-mono'*:  $\mathcal{D} s es A \implies A \sqsubseteq A' \implies \mathcal{D} s es A'$

**declare** *hyperUn-comm [simp]*

**declare** *hyperUn-leftComm [simp]*

**end**

## 4.8 Well-formedness Constraints

**theory** *JWellForm*

**imports**

*WWellForm*

*WellType*

*DefAss*

**begin**

**definition** *wf-J-mdecl* :: 'addr J-prog  $\Rightarrow$  cname  $\Rightarrow$  'addr J-mb mdecl  $\Rightarrow$  bool

**where**

*wf-J-mdecl* *P C*  $\equiv \lambda(M, Ts, T, (pns, body)).$

*length* *Ts* = *length* *pns*  $\wedge$

*distinct* *pns*  $\wedge$

*this*  $\notin$  *set* *pns*  $\wedge$

$(\exists T'. P, [this \mapsto \text{Class } C, pns \mapsto Ts] \vdash body :: T' \wedge P \vdash T' \leq T) \wedge$

$\mathcal{D} \text{ body } [\{this\} \cup \text{set } pns]$

**lemma** *wf-J-mdecl[simp]*:

*wf-J-mdecl* *P C* (*M*, *Ts*, *T*, *pns*, *body*)  $\equiv$

(*length* *Ts* = *length* *pns*  $\wedge$

*distinct* *pns*  $\wedge$

*this*  $\notin$  *set* *pns*  $\wedge$

$(\exists T'. P, [this \mapsto \text{Class } C, pns \mapsto Ts] \vdash body :: T' \wedge P \vdash T' \leq T) \wedge$

$\mathcal{D} \text{ body } [\{this\} \cup \text{set } pns])$

**abbreviation** *wf-J-prog* :: 'addr J-prog  $\Rightarrow$  bool

**where** *wf-J-prog* == *wf-prog* *wf-J-mdecl*

**lemma** *wf-mdecl-wwf-mdecl*: *wf-J-mdecl* *P C Md*  $\Longrightarrow$  *wwf-J-mdecl* *P C Md*

## 4.9 The source language as an instance of the framework

**theory** *Threaded*

**imports**

*SmallStep*

*JWellForm*

*../Common/ConformThreaded*

*../Common/ExternalCallWF*

*../Framework/FWLiftingSem*

*../Framework/FWProgressAux*

**begin**

**context** *heap-base* **begin** — Move to ?? - also used in BV

**lemma** *wset-Suspend-ok-start-state*:

**fixes** *final r convert-RA*

**assumes** *start-state* *f P C M vs*  $\in I$

**shows** *start-state* *f P C M vs*  $\in \text{multithreaded-base.wset-Suspend-ok final r convert-RA } I$

**using** *assms*

**by**(*rule* *multithreaded-base.wset-Suspend-okI*)(*simp* *add: start-state-def split-beta*)

**end**

**abbreviation** *final-expr* :: 'addr expr  $\times$  'addr locals  $\Rightarrow$  bool **where**

*final-expr*  $\equiv \lambda(e, x). \text{final } e$

**lemma** *final-locks*:  $\text{final } e \implies \text{expr-locks } e \text{ } l = 0$   
**by**(*auto elim*: *finalE*)

**context** *J-heap-base* **begin**

**abbreviation** *mred*

$:: 'addr \text{ } J\text{-prog} \Rightarrow ('addr, 'thread\text{-}id, 'addr \text{ } expr \times 'addr \text{ } locals, 'heap, 'addr, ('addr, 'thread\text{-}id) \text{ } obs\text{-}event) \text{ } semantics$

**where**

$mred \text{ } P \text{ } t \equiv (\lambda((e, l), h) \text{ } ta \text{ } ((e', l'), h'). P, t \vdash \langle e, (h, l) \rangle -ta \rightarrow \langle e', (h', l') \rangle)$

**lemma** *red-new-thread-heap*:

$\llbracket \text{convert-extTA extNTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{NewThread } t'' \text{ } ex'' \text{ } h'' \in \text{set } \llbracket ta \rrbracket_t \rrbracket \implies h'' =_{hp} s'$

**and** *reds-new-thread-heap*:

$\llbracket \text{convert-extTA extNTA}, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{NewThread } t'' \text{ } ex'' \text{ } h'' \in \text{set } \llbracket ta \rrbracket_t \rrbracket \implies h'' =_{hp} s'$

**apply**(*induct rule*: *red-reds.inducts*)

**apply**(*fastforce dest*: *red-ext-new-thread-heap simp add*: *ta-upd-simps*) +

**done**

**lemma** *red-ta-Wakeup-no-Join-no-Lock-no-Interrupt*:

$\llbracket \text{convert-extTA extNTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{Notified} \in \text{set } \llbracket ta \rrbracket_w \vee \text{WokenUp} \in \text{set } \llbracket ta \rrbracket_w \rrbracket \implies \text{collect-locks } \llbracket ta \rrbracket_l = \{\} \wedge \text{collect-cond-actions } \llbracket ta \rrbracket_c = \{\} \wedge \text{collect-interrupts } \llbracket ta \rrbracket_i = \{\}$

**and** *reds-ta-Wakeup-no-Join-no-Lock-no-Interrupt*:

$\llbracket \text{convert-extTA extNTA}, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{Notified} \in \text{set } \llbracket ta \rrbracket_w \vee \text{WokenUp} \in \text{set } \llbracket ta \rrbracket_w \rrbracket \implies \text{collect-locks } \llbracket ta \rrbracket_l = \{\} \wedge \text{collect-cond-actions } \llbracket ta \rrbracket_c = \{\} \wedge \text{collect-interrupts } \llbracket ta \rrbracket_i = \{\}$

**apply**(*induct rule*: *red-reds.inducts*)

**apply**(*auto simp add*: *ta-upd-simps dest*: *red-external-Wakeup-no-Join-no-Lock-no-Interrupt del*: *conjI*)

**done**

**lemma** *final-no-red*:

$\text{final } e \implies \neg P, t \vdash \langle e, (h, l) \rangle -ta \rightarrow \langle e', (h', l') \rangle$

**by**(*auto elim*: *red.cases finalE*)

**lemma** *red-mthr*: *multithreaded final-expr* (*mred* *P*)

**by**(*unfold-locales*)(*auto dest*: *red-new-thread-heap*)

**end**

**sublocale** *J-heap-base* < *red-mthr*: *multithreaded*

*final-expr*

*mred* *P*

*convert-RA*

**for** *P*

**by**(*rule red-mthr*)

**context** *J-heap-base* **begin**

**abbreviation**

*mredT*  $::$

$'addr \text{ } J\text{-prog} \Rightarrow ('addr, 'thread\text{-}id, 'addr \text{ } expr \times 'addr \text{ } locals, 'heap, 'addr) \text{ } state$

$\Rightarrow ('thread-id \times ('addr, 'thread-id, 'addr\ expr \times 'addr\ locals, 'heap)\ Jinja-thread-action)$   
 $\Rightarrow ('addr, 'thread-id, 'addr\ expr \times 'addr\ locals, 'heap, 'addr)\ state \Rightarrow bool$

**where**

$mredT\ P \equiv red-mthr.redT\ P$

**abbreviation**

$mredT-syntax1 :: 'addr\ J-prog \Rightarrow ('addr, 'thread-id, 'addr\ expr \times 'addr\ locals, 'heap, 'addr)\ state$   
 $\Rightarrow 'thread-id \Rightarrow ('addr, 'thread-id, 'addr\ expr \times 'addr\ locals, 'heap)\ Jinja-thread-action$   
 $\Rightarrow ('addr, 'thread-id, 'addr\ expr \times 'addr\ locals, 'heap, 'addr)\ state \Rightarrow bool$   
 $(- \vdash - \multimap - \rightarrow - [50, 0, 0, 0, 50] 80)$

**where**

$mredT-syntax1\ P\ s\ t\ ta\ s' \equiv mredT\ P\ s\ (t, ta)\ s'$

**abbreviation**

$mRedT-syntax1 ::$   
 $'addr\ J-prog$   
 $\Rightarrow ('addr, 'thread-id, 'addr\ expr \times 'addr\ locals, 'heap, 'addr)\ state$   
 $\Rightarrow ('thread-id \times ('addr, 'thread-id, 'addr\ expr \times 'addr\ locals, 'heap)\ Jinja-thread-action)\ list$   
 $\Rightarrow ('addr, 'thread-id, 'addr\ expr \times 'addr\ locals, 'heap, 'addr)\ state \Rightarrow bool$   
 $(- \vdash - \multimap - \rightarrow * - [50, 0, 0, 50] 80)$

**where**

$P \vdash s \multimap tta \rightarrow * s' \equiv red-mthr.RedT\ P\ s\ tta\ s'$

**end**

**context** *J-heap* **begin**

**lemma** *redT-hext-incr*:

$P \vdash s \multimap ta \rightarrow s' \Longrightarrow shr\ s \sqsubseteq shr\ s'$

**by**(erule *red-mthr.redT.cases*)(auto dest!: *red-hext-incr intro: hext-trans*)

**lemma** *RedT-hext-incr*:

**assumes**  $P \vdash s \multimap tta \rightarrow * s'$

**shows**  $shr\ s \sqsubseteq shr\ s'$

**using** *assms* **unfolding** *red-mthr.RedT-def*

**by**(*induct*)(auto dest: *redT-hext-incr intro: hext-trans*)

**end**

### 4.9.1 Lifting *tconf* to multithreaded states

**context** *J-heap* **begin**

**lemma** *red-NewThread-Thread-Object*:

$\llbracket convert-extTA\ extNTA, P, t \vdash \langle e, s \rangle \multimap ta \rightarrow \langle e', s' \rangle; NewThread\ t'\ x\ m \in set\ \{ta\}_t \rrbracket$   
 $\Longrightarrow \exists C. typeof\_addr\ (hp\ s')\ (thread-id2addr\ t') = \lfloor Class-type\ C \rfloor \wedge P \vdash C \preceq^* Thread$

**and** *reds-NewThread-Thread-Objects*:

$\llbracket convert-extTA\ extNTA, P, t \vdash \langle es, s \rangle \multimap [-ta \rightarrow] \langle es', s' \rangle; NewThread\ t'\ x\ m \in set\ \{ta\}_t \rrbracket$   
 $\Longrightarrow \exists C. typeof\_addr\ (hp\ s')\ (thread-id2addr\ t') = \lfloor Class-type\ C \rfloor \wedge P \vdash C \preceq^* Thread$

**apply**(*induct rule: red-reds.inducts*)

**apply**(*fastforce dest: red-external-new-thread-exists-thread-object simp add: ta-upd-simps*) +

**done**

**lemma** *lifting-wf-tconf*:

*lifting-wf final-expr* (*mred P*) ( $\lambda t \text{ ex } h. P, h \vdash t \sqrt{t}$ )  
**by**(*unfold-locales*)(*fastforce dest: red-hext-incr red-NewThread-Thread-Object elim!: tconf-hext-mono intro: tconfI*)+

**end**

**sublocale** *J-heap* < *red-tconf*: *lifting-wf final-expr mred P convert-RA*  $\lambda t \text{ ex } h. P, h \vdash t \sqrt{t}$   
**by**(*rule lifting-wf-tconf*)

#### 4.9.2 Towards agreement between the framework semantics' lock state and the locks stored in the expressions

**primrec** *sync-ok* :: ('a,'b,'addr) *exp*  $\Rightarrow$  *bool*  
**and** *sync-oks* :: ('a,'b,'addr) *exp list*  $\Rightarrow$  *bool*  
**where**  
*sync-ok* (*new C*) = *True*  
*sync-ok* (*newA T* [*i*]) = *sync-ok i*  
*sync-ok* (*Cast T e*) = *sync-ok e*  
*sync-ok* (*e instanceof T*) = *sync-ok e*  
*sync-ok* (*Val v*) = *True*  
*sync-ok* (*Var v*) = *True*  
*sync-ok* (*e «bop» e'*) = (*sync-ok e*  $\wedge$  *sync-ok e'*  $\wedge$  (*contains-insync e'  $\longrightarrow$  is-val e*))  
*sync-ok* (*V := e*) = *sync-ok e*  
*sync-ok* (*a* [*i*]) = (*sync-ok a*  $\wedge$  *sync-ok i*  $\wedge$  (*contains-insync i  $\longrightarrow$  is-val a*))  
*sync-ok* (*AAss a i e*) = (*sync-ok a*  $\wedge$  *sync-ok i*  $\wedge$  *sync-ok e*  $\wedge$  (*contains-insync i  $\longrightarrow$  is-val a*)  $\wedge$  (*contains-insync e  $\longrightarrow$  is-val a*  $\wedge$  *is-val i*))  
*sync-ok* (*a.length*) = *sync-ok a*  
*sync-ok* (*e.F{D}*) = *sync-ok e*  
*sync-ok* (*FAss e F D e'*) = (*sync-ok e*  $\wedge$  *sync-ok e'*  $\wedge$  (*contains-insync e'  $\longrightarrow$  is-val e*))  
*sync-ok* (*e.compareAndSwap(D.F, e', e'')*) = (*sync-ok e*  $\wedge$  *sync-ok e'*  $\wedge$  *sync-ok e''*  $\wedge$  (*contains-insync e'  $\longrightarrow$  is-val e*)  $\wedge$  (*contains-insync e''  $\longrightarrow$  is-val e*  $\wedge$  *is-val e'*))  
*sync-ok* (*e.m(pns)*) = (*sync-ok e*  $\wedge$  *sync-oks pns*  $\wedge$  (*contains-insyncs pns  $\longrightarrow$  is-val e*))  
*sync-ok* (*{V : T=vo; e}*) = *sync-ok e*  
*sync-ok* (*sync<sub>V</sub> (o') e*) = (*sync-ok o'*  $\wedge$   $\neg$  *contains-insync e*)  
*sync-ok* (*insync<sub>V</sub> (a) e*) = *sync-ok e*  
*sync-ok* (*e;;e'*) = (*sync-ok e*  $\wedge$   $\neg$  *contains-insync e'*)  
*sync-ok* (*if (b) e else e'*) = (*sync-ok b*  $\wedge$   $\neg$  *contains-insync e*  $\wedge$   $\neg$  *contains-insync e'*)  
*sync-ok* (*while (b) e*) = ( $\neg$  *contains-insync b*  $\wedge$   $\neg$  *contains-insync e*)  
*sync-ok* (*throw e*) = *sync-ok e*  
*sync-ok* (*try e catch (C v) e'*) = (*sync-ok e*  $\wedge$   $\neg$  *contains-insync e'*)  
*sync-oks* [] = *True*  
*sync-oks* (*x # xs*) = (*sync-ok x*  $\wedge$  *sync-oks xs*  $\wedge$  (*contains-insyncs xs  $\longrightarrow$  is-val x*))

**lemma** *sync-oks-append* [*simp*]:

*sync-oks* (*xs @ ys*)  $\longleftrightarrow$  *sync-oks xs*  $\wedge$  *sync-oks ys*  $\wedge$  (*contains-insyncs ys  $\longrightarrow$  ( $\exists$  vs. xs = map Val vs)*)

**by**(*induct xs*)(*auto simp add: Cons-eq-map-conv*)

**lemma** *fixes e* :: ('a,'b,'addr) *exp* **and** *es* :: ('a,'b,'addr) *exp list*

**shows** *not-contains-insync-sync-ok*:  $\neg$  *contains-insync e*  $\Longrightarrow$  *sync-ok e*

**and** *not-contains-insyncs-sync-oks*:  $\neg$  *contains-insyncs es*  $\Longrightarrow$  *sync-oks es*

**by**(*induct e and es rule: sync-ok.induct sync-oks.induct*)(*auto*)

**lemma** *expr-locks-sync-ok*: ( $\bigwedge ad. \text{expr-locks } e \text{ ad} = 0$ )  $\Longrightarrow$  *sync-ok e*



**and** *expr-lockss-sync-oks*:  $(\bigwedge ad. \text{expr-lockss } es \text{ } ad = 0) \implies \text{sync-oks } es$   
**by**(*auto intro!*: *not-contains-insync-sync-ok not-contains-insyncs-sync-oks*  
*simp add: contains-insync-conv contains-insyncs-conv*)

**lemma** *sync-ok-extRet2J* [*simp, intro!*]: *sync-ok e*  $\implies$  *sync-ok (extRet2J e va)*  
**by**(*cases va*) *auto*

### abbreviation

*sync-es-ok* ::  $(\text{'addr}, \text{'thread-id}, (\text{'a}, \text{'b}, \text{'addr}) \text{exp} \times \text{'c}) \text{thread-info} \Rightarrow \text{'heap} \Rightarrow \text{bool}$   
**where**  
*sync-es-ok*  $\equiv$  *ts-ok*  $(\lambda t (e, x) m. \text{sync-ok } e)$

**lemma** *sync-es-ok-blocks* [*simp*]:

$\llbracket \text{length } pns = \text{length } Ts; \text{length } Ts = \text{length } vs \rrbracket \implies \text{sync-ok } (\text{blocks } pns \text{ } Ts \text{ } vs \text{ } e) = \text{sync-ok } e$   
**by**(*induct pns Ts vs e rule: blocks.induct*) *auto*

**context** *J-heap-base* **begin**

**lemma** *assumes wf: wf-J-prog P*

**shows** *red-preserve-sync-ok*:  $\llbracket \text{extTA}, P, t \vdash \langle e, s \rangle \text{ } -ta \rightarrow \langle e', s' \rangle; \text{sync-ok } e \rrbracket \implies \text{sync-ok } e'$   
**and** *reds-preserve-sync-oks*:  $\llbracket \text{extTA}, P, t \vdash \langle es, s \rangle \text{ } [-ta \rightarrow] \langle es', s' \rangle; \text{sync-oks } es \rrbracket \implies \text{sync-oks } es'$

**proof**(*induct rule: red-reds.inducts*)

**case** (*RedCall s a U M Ts T pns body D vs*)  
**from** *wf*  $\langle P \vdash \text{class-type-of } U \text{ sees } M: Ts \rightarrow T = \llbracket (pns, \text{body}) \rrbracket \text{ in } D \rangle$   
**have** *wf-mdecl wf-J-mdecl P D (M, Ts, T,  $\llbracket (pns, \text{body}) \rrbracket$ )*  
**by**(*rule sees-wf-mdecl*)  
**then obtain** *T* **where** *P,  $\llbracket \text{this} \mapsto \text{Class } D, pns \mapsto \rrbracket Ts \vdash \text{body} :: T$*   
**by**(*auto simp add: wf-mdecl-def*)  
**hence** *expr-locks body* =  $(\lambda ad. 0)$  **by**(*rule WT-expr-locks*)  
**with**  $\langle \text{length } vs = \text{length } pns \rangle \langle \text{length } Ts = \text{length } pns \rangle$   
**have** *expr-locks (blocks pns Ts vs body)* =  $(\lambda ad. 0)$   
**by**(*simp add: expr-locks-blocks*)  
**thus** *?case* **by**(*auto intro: expr-locks-sync-ok*)  
**qed**(*fastforce intro: not-contains-insync-sync-ok*) +

**lemma** *assumes wf: wf-J-prog P*

**shows** *expr-locks-new-thread*:  
 $\llbracket P, t \vdash \langle e, s \rangle \text{ } -ta \rightarrow \langle e', s' \rangle; \text{NewThread } t'' (e'', x'') h \in \text{set } \llbracket ta \rrbracket_t \rrbracket \implies \text{expr-locks } e'' = (\lambda ad. 0)$

**and** *expr-locks-new-thread'*:  
 $\llbracket P, t \vdash \langle es, s \rangle \text{ } [-ta \rightarrow] \langle es', s' \rangle; \text{NewThread } t'' (e'', x'') h \in \text{set } \llbracket ta \rrbracket_t \rrbracket \implies \text{expr-locks } e'' = (\lambda ad. 0)$

**proof**(*induct rule: red-reds.inducts*)

**case** (*RedCallExternal s a U M Ts T D vs ta va h' ta' e' s'*)  
**then obtain** *C fs a* **where** *subThread: P  $\vdash C \preceq^* \text{Thread}$  and ext: extNTA2J P (C, run, a) = (e'', x'')*  
**by**(*fastforce dest: red-external-new-thread-sub-thread*)  
**from** *sub-Thread-sees-run*[*OF wf subThread*] **obtain** *D pns body*  
**where** *sees: P  $\vdash C$  sees run:  $\square \rightarrow \text{Void} = \llbracket (pns, \text{body}) \rrbracket \text{ in } D$*  **by** *auto*  
**from** *sees-wf-mdecl*[*OF wf this*] **obtain** *T* **where** *P,  $\llbracket \text{this} \mapsto \text{Class } D \rrbracket \vdash \text{body} :: T$*   
**by**(*auto simp add: wf-mdecl-def*)  
**hence** *expr-locks body* =  $(\lambda ad. 0)$  **by**(*rule WT-expr-locks*)  
**with** *sees ext* **show** *?case* **by**(*auto simp add: extNTA2J-def*)  
**qed**(*auto simp add: ta-upd-simps*)

**lemma assumes** *wf*: *wf-J-prog P*

**shows** *red-new-thread-sync-ok*:  $\llbracket P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{NewThread } t'' (e'', x'') h'' \in \text{set } \{ta\}_t \rrbracket \implies \text{sync-ok } e''$

**and** *reds-new-thread-sync-ok*:  $\llbracket P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{NewThread } t'' (e'', x'') h'' \in \text{set } \{ta\}_t \rrbracket \implies \text{sync-ok } e''$

**by**(*auto dest!*: *expr-locks-new-thread*[*OF wf*] *expr-locks-new-thread'*[*OF wf*] *intro*: *expr-locks-sync-ok expr-lockss-sync-oks*)

**lemma** *lifting-wf-sync-ok*: *wf-J-prog P*  $\implies$  *lifting-wf final-expr* (*mred P*) ( $\lambda t (e, x) m. \text{sync-ok } e$ )

**by**(*unfold-locales*)(*auto intro*: *red-preserve-sync-ok red-new-thread-sync-ok*)

**lemma** *redT-preserve-sync-ok*:

**assumes** *red*:  $P \vdash s -t \triangleright ta \rightarrow s'$

**shows**  $\llbracket \text{wf-J-prog } P; \text{sync-es-ok } (thr\ s) (shr\ s) \rrbracket \implies \text{sync-es-ok } (thr\ s') (shr\ s')$

**by**(*rule lifting-wf.redT-preserves*[*OF lifting-wf-sync-ok red*])

**lemma** *RedT-preserves-sync-ok*:

$\llbracket \text{wf-J-prog } P; P \vdash s -\triangleright ttas \rightarrow^* s'; \text{sync-es-ok } (thr\ s) (shr\ s) \rrbracket$

$\implies \text{sync-es-ok } (thr\ s') (shr\ s')$

**by**(*rule lifting-wf.RedT-preserves*[*OF lifting-wf-sync-ok*])

**lemma** *sync-es-ok-J-start-state*:

$\llbracket \text{wf-J-prog } P; P \vdash C \text{ sees } M:Ts \rightarrow T = \lfloor (pns, body) \rfloor \text{ in } D; \text{length } Ts = \text{length } vs \rrbracket$

$\implies \text{sync-es-ok } (thr\ (J\text{-start-state } P\ C\ M\ vs))\ m$

**apply**(*rule ts-okI*)

**apply**(*clarsimp simp add*: *start-state-def split-beta split*: *if-split-asm*)

**apply**(*drule* (1) *sees-wf-mdecl*)

**apply**(*clarsimp simp add*: *wf-mdecl-def*)

**apply**(*drule WT-expr-locks*)

**apply**(*rule expr-locks-sync-ok*)

**apply** *simp*

**done**

**end**

Framework lock state agrees with locks stored in the expression

**definition** *lock-ok* ::  $(\text{'addr}, \text{'thread-id}) \text{ locks} \Rightarrow (\text{'addr}, \text{'thread-id}, (\text{'a}, \text{'b}, \text{'addr}) \text{ exp} \times \text{'x}) \text{ thread-info} \Rightarrow \text{bool}$  **where**

$\bigwedge ln. \text{lock-ok } ls\ ts \equiv \forall t. (\text{case } (ts\ t) \text{ of } \text{None} \Rightarrow (\forall l. \text{has-locks } (ls\ \$\ l)\ t = 0) \mid \lfloor ((e, x), ln) \rfloor \Rightarrow (\forall l. \text{has-locks } (ls\ \$\ l)\ t + ln\ \$\ l = \text{expr-locks } e\ l))$

**lemma** *lock-okI*:

$\llbracket \bigwedge t\ l. ts\ t = \text{None} \implies \text{has-locks } (ls\ \$\ l)\ t = 0; \bigwedge t\ e\ x\ ln\ l. ts\ t = \lfloor ((e, x), ln) \rfloor \implies \text{has-locks } (ls\ \$\ l)\ t + ln\ \$\ l = \text{expr-locks } e\ l \rrbracket \implies \text{lock-ok } ls\ ts$

**apply**(*fastforce simp add*: *lock-ok-def*)

**done**

**lemma** *lock-okE*:

$\llbracket \text{lock-ok } ls\ ts;$

$\forall t. ts\ t = \text{None} \longrightarrow (\forall l. \text{has-locks } (ls\ \$\ l)\ t = 0) \implies Q;$

$\forall t\ e\ x\ ln. ts\ t = \lfloor ((e, x), ln) \rfloor \longrightarrow (\forall l. \text{has-locks } (ls\ \$\ l)\ t + ln\ \$\ l = \text{expr-locks } e\ l) \implies Q \rrbracket$

$\implies Q$

**by**(*fastforce simp add*: *lock-ok-def*)

**lemma** *lock-okD1*:

```

  [[ lock-ok ls ts; ts t = None ]] ==> ∀ l. has-locks (ls $ l) t = 0
apply(simp add: lock-ok-def)
apply(erule-tac x=t in allE)
apply(auto)
done

```

**lemma** *lock-okD2*:

```

  ∧ ln. [[ lock-ok ls ts; ts t = [(e, x), ln] ]] ==> ∀ l. has-locks (ls $ l) t + ln $ l = expr-locks e l
apply(fastforce simp add: lock-ok-def)
done

```

**lemma** *lock-ok-lock-thread-ok*:

```

  assumes lock: lock-ok ls ts
  shows lock-thread-ok ls ts
proof(rule lock-thread-okI)
  fix l t
  assume lsl: has-lock (ls $ l) t
  show ∃ xw. ts t = [xw]
  proof(cases ts t)
    case None
    with lock have has-locks (ls $ l) t = 0
    by(auto dest: lock-okD1)
    with lsl show ?thesis by simp
  next
    case (Some a) thus ?thesis by blast
  qed
qed

```

**lemma** (in *J-heap-base*) *lock-ok-J-start-state*:

```

  [[ wf-J-prog P; P ⊢ C sees M:Ts→T=[(pns, body)] in D; length Ts = length vs ]]
  ==> lock-ok (locks (J-start-state P C M vs)) (thr (J-start-state P C M vs))
apply(rule lock-okI)
apply(auto simp add: start-state-def split: if-split-asm)
apply(drule (1) sees-wf-mdecl)
apply(clarsimp simp add: wf-mdecl-def)
apply(drule WT-expr-locks)
apply(simp add: expr-locks-blocks)
done

```

### 4.9.3 Preservation of lock state agreement

**fun** *upd-expr-lock-action* :: int ⇒ lock-action ⇒ int  
**where**

```

  upd-expr-lock-action i Lock = i + 1
| upd-expr-lock-action i Unlock = i - 1
| upd-expr-lock-action i UnlockFail = i
| upd-expr-lock-action i ReleaseAcquire = i

```

**fun** *upd-expr-lock-actions* :: int ⇒ lock-action list ⇒ int **where**

```

  upd-expr-lock-actions n [] = n
| upd-expr-lock-actions n (L # Ls) = upd-expr-lock-actions (upd-expr-lock-action n L) Ls

```

**lemma** *upd-expr-lock-actions-append* [simp]:

$\text{upd-expr-lock-actions } n \ (Ls @ Ls') = \text{upd-expr-lock-actions } (\text{upd-expr-lock-actions } n \ Ls) \ Ls'$   
**by**(*induct Ls arbitrary: n, auto*)

**definition**  $\text{upd-expr-locks} :: ('l \Rightarrow \text{int}) \Rightarrow 'l \text{ lock-actions} \Rightarrow 'l \Rightarrow \text{int}$   
**where**  $\text{upd-expr-locks } \text{els } \text{las} \equiv \lambda l. \text{upd-expr-lock-actions } (\text{els } l) \ (\text{las } \$ l)$

**lemma**  $\text{upd-expr-locks-iff}$  [*simp*]:  
 $\text{upd-expr-locks } \text{els } \text{las } l = \text{upd-expr-lock-actions } (\text{els } l) \ (\text{las } \$ l)$   
**by**(*simp add: upd-expr-locks-def*)

**lemma**  $\text{upd-expr-lock-action-add}$  [*simp*]:  
 $\text{upd-expr-lock-action } (l + l') \ L = \text{upd-expr-lock-action } l \ L + l'$   
**by**(*cases L, auto*)

**lemma**  $\text{upd-expr-lock-actions-add}$  [*simp*]:  
 $\text{upd-expr-lock-actions } (l + l') \ Ls = \text{upd-expr-lock-actions } l \ Ls + l'$   
**by**(*induct Ls arbitrary: l, auto*)

**lemma**  $\text{upd-expr-locks-add}$  [*simp*]:  
 $\text{upd-expr-locks } (\lambda a. x \ a + y \ a) \ \text{las} = (\lambda a. \text{upd-expr-locks } x \ \text{las } a + y \ a)$   
**by**(*auto intro: ext*)

**lemma**  $\text{expr-locks-extRet2J}$  [*simp, intro!*]:  $\text{expr-locks } e = (\lambda ad. 0) \implies \text{expr-locks } (\text{extRet2J } e \ va) = (\lambda ad. 0)$   
**by**(*cases va*) *auto*

**lemma** (**in** *J-heap-base*)  
**assumes** *wf: wf-J-prog P*  
**shows** *red-update-expr-locks*:  
 $\llbracket \text{convert-extTA } \text{extNTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{sync-ok } e \rrbracket$   
 $\implies \text{upd-expr-locks } (\text{int } o \ \text{expr-locks } e) \ \{\!|ta|\!\}_l = \text{int } o \ \text{expr-locks } e'$   
**and** *reds-update-expr-locks*:  
 $\llbracket \text{convert-extTA } \text{extNTA}, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{sync-oks } es \rrbracket$   
 $\implies \text{upd-expr-locks } (\text{int } o \ \text{expr-lockss } es) \ \{\!|ta|\!\}_l = \text{int } o \ \text{expr-lockss } es'$   
**proof** –  
**have**  $\llbracket \text{convert-extTA } \text{extNTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{sync-ok } e \rrbracket$   
 $\implies \text{upd-expr-locks } (\lambda ad. 0) \ \{\!|ta|\!\}_l = (\lambda ad. (\text{int } o \ \text{expr-locks } e') \ ad - (\text{int } o \ \text{expr-locks } e) \ ad)$   
**and**  $\llbracket \text{convert-extTA } \text{extNTA}, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{sync-oks } es \rrbracket$   
 $\implies \text{upd-expr-locks } (\lambda ad. 0) \ \{\!|ta|\!\}_l = (\lambda ad. (\text{int } o \ \text{expr-lockss } es') \ ad - (\text{int } o \ \text{expr-lockss } es) \ ad)$   
**proof**(*induct rule: red-reds.inducts*)  
**case** (*RedCall s a U M Ts T pns body D vs*)  
**from** *wf*  $\langle P \vdash \text{class-type-of } U \text{ sees } M: Ts \rightarrow T = \llbracket (pns, \text{body}) \rrbracket \text{ in } D \rangle$   
**have** *wf-mdecl wf-J-mdecl P D (M, Ts, T, \llbracket (pns, body) \rrbracket)*  
**by**(*rule sees-wf-mdecl*)  
**then obtain T where**  $P, [\text{this} \mapsto \text{Class } D, pns[\mapsto] Ts] \vdash \text{body} :: T$   
**by**(*auto simp add: wf-mdecl-def*)  
**hence**  $\text{expr-locks } \text{body} = (\lambda ad. 0)$  **by**(*rule WT-expr-locks*)  
**with**  $\langle \text{length } vs = \text{length } pns \rangle \langle \text{length } Ts = \text{length } pns \rangle$   
**have**  $\text{expr-locks } (\text{blocks } pns \ Ts \ vs \ \text{body}) = (\lambda ad. 0)$   
**by**(*simp add: expr-locks-blocks*)  
**thus** *?case* **by**(*auto intro: expr-locks-sync-ok*)  
**next**  
**case** *RedCallExternal* **thus** *?case*  
**by**(*auto simp add: fun-eq-iff contains-insync-conv contains-insyncs-conv finfun-upd-apply ta-upd-simps*)

*elim!*: *red-external.cases*)

**qed**(*fastforce simp add: fun-eq-iff contains-insync-conv contains-insyncs-conv finfun-upd-apply ta-upd-simps*) +  
**hence**  $\llbracket \text{convert-extTA extNTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{sync-ok } e \rrbracket$   
 $\implies \text{upd-expr-locks } (\lambda ad. 0 + (int \circ \text{expr-locks } e) \text{ ad}) \llbracket ta \rrbracket_l = int \circ \text{expr-locks } e'$   
**and**  $\llbracket \text{convert-extTA extNTA}, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{sync-oks } es \rrbracket$   
 $\implies \text{upd-expr-locks } (\lambda ad. 0 + (int \circ \text{expr-lockss } es) \text{ ad}) \llbracket ta \rrbracket_l = int \circ \text{expr-lockss } es'$   
**by**(*auto intro: ext simp only: upd-expr-locks-add*)  
**thus**  $\llbracket \text{convert-extTA extNTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{sync-ok } e \rrbracket$   
 $\implies \text{upd-expr-locks } (int \circ \text{expr-locks } e) \llbracket ta \rrbracket_l = int \circ \text{expr-locks } e'$   
**and**  $\llbracket \text{convert-extTA extNTA}, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{sync-oks } es \rrbracket$   
 $\implies \text{upd-expr-locks } (int \circ \text{expr-lockss } es) \llbracket ta \rrbracket_l = int \circ \text{expr-lockss } es'$   
**by**(*auto simp add: o-def*)  
**qed**

**definition** *lock-expr-locks-ok* :: '*t* FWState.lock  $\Rightarrow$  '*t*  $\Rightarrow$  nat  $\Rightarrow$  int  $\Rightarrow$  bool **where**  
*lock-expr-locks-ok* *l t n i*  $\equiv (i = int (has-locks \text{ l } t) + int \text{ n}) \wedge i \geq 0$

**lemma** *upd-lock-upd-expr-lock-action-preserve-lock-expr-locks-ok*:

**assumes** *lao*: *lock-action-ok* *l t L*  
**and** *lelo*: *lock-expr-locks-ok* *l t n i*  
**shows** *lock-expr-locks-ok* (*upd-lock* *l t L*) *t* (*upd-threadR* *n l t L*) (*upd-expr-lock-action* *i L*)

**proof** –

**from** *lelo* **have** *i*:  $i \geq 0$   
**and** *hl*:  $i = int (has-locks \text{ l } t) + int \text{ n}$   
**by**(*auto simp add: lock-expr-locks-ok-def*)  
**from** *lao*  
**show** ?thesis  
**proof**(*cases L*)  
**case** *Lock*  
**with** *lao* **have** *may-lock* *l t* **by**(*simp*)  
**with** *hl* **have** *has-locks* (*lock-lock* *l t*)  $t = (Suc (has-locks \text{ l } t))$  **by**(*auto*)  
**with** *Lock i hl* **show** ?thesis  
**by**(*simp add: lock-expr-locks-ok-def*)  
**next**  
**case** *Unlock*  
**with** *lao* **have** *has-lock* *l t* **by** *simp*  
**then obtain** *n'*  
**where** *hl'*: *has-locks* *l t* = *Suc n'*  
**by**(*auto dest: has-lock-has-locks-Suc*)  
**hence** *has-locks* (*unlock-lock* *l*)  $t = n'$  **by** *simp*  
**with** *Unlock i hl hl'* **show** ?thesis  
**by**(*simp add: lock-expr-locks-ok-def*)  
**qed**(*auto simp add: lock-expr-locks-ok-def*)  
**qed**

**lemma** *upd-locks-upd-expr-lock-preserve-lock-expr-locks-ok*:

$\llbracket \text{lock-actions-ok } l \text{ t } Ls; \text{lock-expr-locks-ok } l \text{ t } n \text{ i} \rrbracket$   
 $\implies \text{lock-expr-locks-ok } (\text{upd-locks } l \text{ t } Ls) \text{ t } (\text{upd-threadRs } n \text{ l t } Ls) (\text{upd-expr-lock-actions } i \text{ Ls})$   
**by**(*induct Ls arbitrary: l i n*)(*auto intro: upd-lock-upd-expr-lock-action-preserve-lock-expr-locks-ok*)

**definition** *ls-els-ok* :: ('addr, 'thread-id) locks  $\Rightarrow$  'thread-id  $\Rightarrow$  ('addr  $\Rightarrow$  nat)  $\Rightarrow$  ('addr  $\Rightarrow$  int)  $\Rightarrow$  bool  
**where**

$\bigwedge ln. \text{ls-els-ok } ls \text{ t } ln \text{ els} \equiv \forall l. \text{lock-expr-locks-ok } (ls \text{ \$ } l) \text{ t } (ln \text{ \$ } l) (\text{els } l)$

**lemma** *ls-els-okI*:

$\bigwedge l n. (\bigwedge l. \text{lock-expr-locks-ok } (ls \ \$ \ l) \ t \ (ln \ \$ \ l) \ (els \ l)) \implies \text{ls-els-ok } ls \ t \ ln \ els$   
**by**(*auto simp add: ls-els-ok-def*)

**lemma** *ls-els-okE*:

$\bigwedge l n. [\text{ls-els-ok } ls \ t \ ln \ els; \forall l. \text{lock-expr-locks-ok } (ls \ \$ \ l) \ t \ (ln \ \$ \ l) \ (els \ l) \implies P] \implies P$   
**by**(*auto simp add: ls-els-ok-def*)

**lemma** *ls-els-okD*:

$\bigwedge l n. \text{ls-els-ok } ls \ t \ ln \ els \implies \text{lock-expr-locks-ok } (ls \ \$ \ l) \ t \ (ln \ \$ \ l) \ (els \ l)$   
**by**(*auto simp add: ls-els-ok-def*)

**lemma** *redT-updLs-upd-expr-locks-preserves-ls-els-ok*:

$\bigwedge l n. [\text{ls-els-ok } ls \ t \ ln \ els; \text{lock-ok-las } ls \ t \ las] \implies \text{ls-els-ok } (\text{redT-updLs } ls \ t \ las) \ t \ (\text{redT-updLns } ls \ t \ ln \ las) \ (\text{upd-expr-locks } els \ las)$   
**by**(*auto intro!: ls-els-okI upd-locks-upd-expr-lock-preserve-lock-expr-locks-ok elim!: ls-els-okE simp add: redT-updLs-def lock-ok-las-def*)

**lemma** *sync-ok-redT-updT*:

**assumes** *sync-es-ok ts h*  
**and** *nt*:  $\bigwedge t \ e \ x \ h''. ta = \text{NewThread } t \ (e, x) \ h'' \implies \text{sync-ok } e$   
**shows** *sync-es-ok (redT-updT ts ta) h'*  
**using** *assms*  
**proof**(*cases ta*)  
**case** (*NewThread T x m*)  
**obtain** *E X* **where** [*simp*]:  $x = (E, X)$  **by** (*cases x, auto*)  
**with** *NewThread* **have** *sync-ok E* **by**(*simp*)(*rule nt*)  
**with** *NewThread*  $\langle \text{sync-es-ok } ts \ h \rangle$  **show** *?thesis*  
**apply** –  
**apply**(*rule ts-okI*)  
**apply**(*case-tac t=T*)  
**by**(*auto dest: ts-okD*)  
**qed**(*auto intro: ts-okI dest: ts-okD*)

**lemma** *sync-ok-redT-updT*:

$[\text{sync-es-ok } ts \ h; \bigwedge t \ e \ x \ h. \text{NewThread } t \ (e, x) \ h \in \text{set } tas \implies \text{sync-ok } e] \implies \text{sync-es-ok } (\text{redT-updT } ts \ tas) \ h'$   
**proof**(*induct tas arbitrary: ts*)  
**case** *Nil* **thus** *?case* **by**(*auto intro: ts-okI dest: ts-okD*)  
**next**  
**case** (*Cons TA TAS TS*)  
**note** *IH* =  $\langle \bigwedge ts. [\text{sync-es-ok } ts \ h; \bigwedge t \ e \ x \ h''. \text{NewThread } t \ (e, x) \ h'' \in \text{set } TAS \implies \text{sync-ok } e] \implies \text{sync-es-ok } (\text{redT-updT } ts \ TAS) \ h' \rangle$   
**note** *nt* =  $\langle \bigwedge t \ e \ x \ h. \text{NewThread } t \ (e, x) \ h \in \text{set } (TA \ \# \ TAS) \implies \text{sync-ok } e \rangle$   
**from**  $\langle \text{sync-es-ok } TS \ h \rangle$  *nt*  
**have** *sync-es-ok (redT-updT TS TA) h*  
**by**(*auto elim!: sync-ok-redT-updT*)  
**hence** *sync-es-ok (redT-updT (redT-updT TS TA) TAS) h'*  
**by**(*rule IH*)(*auto intro: nt*)  
**thus** *?case* **by** *simp*  
**qed**

**lemma** *lock-ok-thr-updI*:

$\bigwedge ln. \llbracket \text{lock-ok } ls \text{ } ts; \text{ } ts \text{ } t = \llbracket ((e, xs), ln) \rrbracket; \text{expr-locks } e = \text{expr-locks } e' \rrbracket$   
 $\implies \text{lock-ok } ls \text{ } (ts(t \mapsto ((e', xs'), ln)))$

**by**(rule *lock-okI*)(auto *split*: if-split-asm dest: *lock-okD2 lock-okD1*)

**context** *J-heap-base* **begin**

**lemma** *redT-preserves-lock-ok*:

**assumes** *wf*: *wf-J-prog P*

**and**  $P \vdash s \multimap ta \rightarrow s'$

**and** *lock-ok* (*locks s*) (*thr s*)

**and** *sync-es-ok* (*thr s*) (*shr s*)

**shows** *lock-ok* (*locks s'*) (*thr s'*)

**proof** –

**obtain** *ls ts h ws is* **where**  $s \text{ [simp] } : s = (ls, (ts, h), ws, is)$  **by**(cases *s*) *fastforce*

**obtain** *ls' ts' h' ws' is'* **where**  $s' \text{ [simp] } : s' = (ls', (ts', h'), ws', is')$  **by**(cases *s'*) *fastforce*

**from** *assms* **have** *redT*:  $P \vdash (ls, (ts, h), ws, is) \multimap ta \rightarrow (ls', (ts', h'), ws', is')$

**and** *loes*: *lock-ok ls ts*

**and** *aoes*: *sync-es-ok ts h* **by** *auto*

**from** *redT* **have** *lock-ok ls' ts'*

**proof**(cases rule: *red-mthr.redT-elim*s)

**case** (*normal a a' m'*)

**moreover obtain** *e x* **where**  $a = (e, x)$  **by** (cases *a*, *auto*)

**moreover obtain** *e' x'* **where**  $a' = (e', x')$  **by** (cases *a'*, *auto*)

**ultimately have** *P*:  $P, t \vdash \langle e, (h, x) \rangle \multimap ta \rightarrow \langle e', (m', x') \rangle$

**and** *est*:  $ts \text{ } t = \llbracket ((e, x), \text{no-wait-locks}) \rrbracket$

**and** *lota*: *lock-ok-las ls t*  $\llbracket ta \rrbracket_l$

**and** *cctta*: *thread-oks ts*  $\llbracket ta \rrbracket_t$

**and** *ls'*:  $ls' = \text{redT-updLs } ls \text{ } t \llbracket ta \rrbracket_l$

**and** *s'*:  $ts' = (\text{redT-updTss } ts \llbracket ta \rrbracket_t)(t \mapsto ((e', x'), \text{redT-updLns } ls \text{ } t \text{ no-wait-locks } \llbracket ta \rrbracket_l))$

**by** *auto*

**let** *?ts'* =  $(\text{redT-updTss } ts \llbracket ta \rrbracket_t)(t \mapsto ((e', x'), \text{redT-updLns } ls \text{ } t \text{ no-wait-locks } \llbracket ta \rrbracket_l))$

**from** *est aoes* **have** *aoe*: *sync-ok e* **by**(auto dest: *ts-okD*)

**from** *aoe P* **have** *aoe'*: *sync-ok e'* **by**(auto dest: *red-preserve-sync-ok[OF wf]*)

**from** *aoes red-new-thread-sync-ok[OF wf P]*

**have** *sync-es-ok* (*redT-updTss ts*  $\llbracket ta \rrbracket_t$ ) *h'*

**by**(rule *sync-ok-redT-updTss*)

**with** *aoe'* **have** *aoes'*: *sync-es-ok ?ts' m'*

**by**(auto intro!: *ts-okI* dest: *ts-okD split*: if-split-asm)

**have** *lock-ok ls' ?ts'*

**proof**(rule *lock-okI*)

**fix** *t'' l*

**assume** *?ts' t'' = None*

**hence** *ts t'' = None*

**by**(auto *split*: if-split-asm intro: *redT-updTss-None*)

**with** *loes* **have** *has-locks (ls \$ l) t'' = 0*

**by**(auto dest: *lock-okD1*)

**moreover from**  $\langle ?ts' t'' = None \rangle$

**have**  $t \neq t''$  **by**(simp *split*: if-split-asm)

**ultimately show** *has-locks (ls' \$ l) t'' = 0*

**by**(simp add: *red-mthr.redT-has-locks-inv[OF redT]*)

**next**

**fix** *t'' e'' x'' l ln''*

**assume** *ts't''*: *?ts' t'' =*  $\llbracket ((e'', x''), ln'') \rrbracket$

```

with aoes' have aoe'': sync-ok e'' by(auto dest: ts-okD)
show has-locks (ls' $ l) t'' + ln'' $ l = expr-locks e'' l
proof(cases t = t'')
  case True
    note tt'' = ⟨t = t''⟩
    with ts't'' have e'': e'' = e' and x'': x'' = x'
      and ln'': ln'' = redT-updLns ls t no-wait-locks {ta}_l by auto
    have ls-els-ok ls t no-wait-locks (int o expr-locks e)
    proof(rule ls-els-okI)
      fix l
      note lock-okD2[OF loes, OF est]
      thus lock-expr-locks-ok (ls $ l) t (no-wait-locks $ l) ((int o expr-locks e) l)
        by(simp add: lock-expr-locks-ok-def)
      qed
    hence ls-els-ok (redT-updLns ls t {ta}_l) t (redT-updLns ls t no-wait-locks {ta}_l) (upd-expr-locks
      (int o expr-locks e) {ta}_l)
      by(rule redT-updLns-upd-expr-locks-preserves-ls-els-ok[OF - lota])
    hence ls-els-ok (redT-updLns ls t {ta}_l) t (redT-updLns ls t no-wait-locks {ta}_l) (int o expr-locks
      e')
      by(simp only: red-update-expr-locks[OF wf P aoe])
    thus ?thesis using ls' e'' tt'' ln''
      by(auto dest: ls-els-okD[where l = l] simp: lock-expr-locks-ok-def)
  next
    case False
      note tt'' = ⟨t ≠ t''⟩
      from lota have lao: lock-actions-ok (ls $ l) t ({ta}_l $ l)
        by(simp add: lock-ok-las-def)
      show ?thesis
      proof(cases ts t'')
        case None
          with est ts't'' tt'' cctta
          obtain m where NewThread t'' (e'', x'') m ∈ set {ta}_t and ln'': ln'' = no-wait-locks
            by(auto dest: redT-updTs-new-thread)
          moreover with P have m' = m by(auto dest: red-new-thread-heap)
          ultimately have NewThread t'' (e'', x'') m' ∈ set {ta}_t by simp
          with wf P ln'' have expr-locks e'' = (λad. 0)
            by -(rule expr-locks-new-thread)
          hence elcl: expr-locks e'' l = 0 by simp
          from loes None have has-locks (ls $ l) t'' = 0
            by(auto dest: lock-okD1)
          moreover note lock-actions-ok-has-locks-upd-locks-eq-has-locks[OF lao tt''[symmetric]]
          ultimately have has-locks (redT-updLns ls t {ta}_l $ l) t'' = 0
            by(auto simp add: fun-eq-iff)
          with elcl ls' ln'' show ?thesis by(auto)
        next
          case (Some a)
          then obtain E X LN where est': ts t'' = [((E, X), LN)] by(cases a, auto)
          with loes have IH: has-locks (ls $ l) t'' + LN $ l = expr-locks E l
            by(auto dest: lock-okD2)
          from est est' tt'' cctta have ?ts' t'' = [((E, X), LN)]
            by(simp)(rule redT-updTs-Some, simp-all)
          with ts't'' have e'': E = e'' and x'': X = x''
            and ln'': ln'' = LN by(simp-all)
          with lock-actions-ok-has-locks-upd-locks-eq-has-locks[OF lao tt''[symmetric]] IH ls'

```



```

    show ?thesis by (clarsimp simp add: redT-updLs-def fun-eq-iff)
  qed
  qed
  qed
  with s' show ?thesis by simp
next
case (acquire a ln n)
hence [simp]: ta = (K$ [], [], [], [], [], convert-RA ln) ws' = ws h' = h
  and ls': ls' = acquire-all ls t ln
  and ts': ts' = ts(t ↦ (a, no-wait-locks))
  and ts t = [(a, ln)]
  and may-acquire-all ls t ln
  by auto
obtain e x where [simp]: a = (e, x) by (cases a, auto)
from ts' have ts': ts' = ts(t ↦ ((e, x), no-wait-locks)) by simp
from ⟨ts t = [(a, ln)]⟩ have tst: ts t = [((e, x), ln)] by simp
show ?thesis
proof(rule lock-okI)
  fix t'' l
  assume rtutes: ts' t'' = None
  with ts' have tst'': ts t'' = None
  by (simp split: if-split-asm)
  with tst have tt'': t ≠ t'' by auto
  from tst'' loes have has-locks (ls $ l) t'' = 0
  by (auto dest: lock-okD1)
  thus has-locks (ls' $ l) t'' = 0
  by (simp add: red-mthr.redT-has-locks-inv[OF redT tt''])
next
fix t'' e'' x'' ln'' l
assume ts't'': ts' t'' = [((e'', x''), ln'')]
show has-locks (ls' $ l) t'' + ln'' $ l = expr-locks e'' l
proof(cases t = t'')
  case True
  note [simp] = this
  with ts't'' ts' tst
  have [simp]: ln'' = no-wait-locks e = e'' by auto
  from tst loes have has-locks (ls $ l) t + ln $ l = expr-locks e l
  by (auto dest: lock-okD2)
  show ?thesis
  proof(cases ln $ l > 0)
    case True
    with ⟨may-acquire-all ls t ln⟩ ls' have may-lock (ls $ l) t
    by (auto elim: may-acquire-allE)
    with ls'
    have has-locks (ls' $ l) t = has-locks (ls $ l) t + ln $ l
    by (simp add: has-locks-acquire-locks-conv)
    with ⟨has-locks (ls $ l) t + ln $ l = expr-locks e l⟩
    show ?thesis by (simp)
  next
  case False
  hence ln $ l = 0 by simp
  with ls' have has-locks (ls' $ l) t = has-locks (ls $ l) t
  by (simp)
  with ⟨has-locks (ls $ l) t + ln $ l = expr-locks e l⟩ ⟨ln $ l = 0⟩

```

```

    show ?thesis by(simp)
  qed
next
case False
with  $ts' ts't''$  have  $tst'': ts\ t'' = \lfloor (e'', x''), ln'' \rfloor$  by(simp)
with  $loes$  have  $has-locks\ (ls\ \$\ l)\ t'' + ln''\ \$\ l = expr-locks\ e''\ l$ 
  by(auto dest: lock-okD2)
show ?thesis
proof(cases  $ln\ \$\ l > 0$ )
  case False
  with  $\langle t \neq t'' \rangle\ ls'$ 
  have  $has-locks\ (ls'\ \$\ l)\ t'' = has-locks\ (ls\ \$\ l)\ t''$  by(simp)
  with  $\langle has-locks\ (ls\ \$\ l)\ t'' + ln''\ \$\ l = expr-locks\ e''\ l \rangle$ 
  show ?thesis by(simp)
next
case True
with  $\langle may-acquire-all\ ls\ t\ ln \rangle$  have  $may-lock\ (ls\ \$\ l)\ t$ 
  by(auto elim: may-acquire-allE)
with  $ls'\ \langle t \neq t'' \rangle$  have  $has-locks\ (ls'\ \$\ l)\ t'' = has-locks\ (ls\ \$\ l)\ t''$ 
  by(simp add: has-locks-acquire-locks-conv')
with  $ls'\ \langle has-locks\ (ls\ \$\ l)\ t'' + ln''\ \$\ l = expr-locks\ e''\ l \rangle$ 
  show ?thesis by(simp)
qed
qed
qed
qed
thus ?thesis by simp
qed

lemma invariant3p-sync-es-ok-lock-ok:
  assumes  $wf: wf\text{-}J\text{-prog}\ P$ 
  shows  $invariant3p\ (mredT\ P)\ \{s.\ sync-es-ok\ (thr\ s)\ (shr\ s) \wedge lock-ok\ (locks\ s)\ (thr\ s)\}$ 
apply(rule invariant3pI)
apply clarify
apply(rule conjI)
apply(rule lifting-wf.redT-preserves[OF lifting-wf-sync-ok[OF wf]], blast)
apply(assumption)
apply(erule (2) redT-preserves-lock-ok[OF wf])
done

lemma RedT-preserves-lock-ok:
  assumes  $wf: wf\text{-}J\text{-prog}\ P$ 
  and  $Red: P \vdash s \multimap ttas \rightarrow^* s'$ 
  and  $ae: sync-es-ok\ (thr\ s)\ (shr\ s)$ 
  and  $loes: lock-ok\ (locks\ s)\ (thr\ s)$ 
  shows  $lock-ok\ (locks\ s')\ (thr\ s')$ 
using invariant3p-rtrancl3p[OF invariant3p-sync-es-ok-lock-ok[OF wf] Red[unfolded red-mthr.RedT-def]]
ae loes
by simp

end

```

#### 4.9.4 Determinism

**context** *J-heap-base* **begin**

**lemma**

**fixes** *final*

**assumes** *det*: *deterministic-heap-ops*

**shows** *red-deterministic*:

$\llbracket \text{convert-extTA } \text{extTA}, P, t \vdash \langle e, (\text{shr } s, xs) \rangle \text{--}ta \rightarrow \langle e', s' \rangle;$   
 $\text{convert-extTA } \text{extTA}, P, t \vdash \langle e, (\text{shr } s, xs) \rangle \text{--}ta' \rightarrow \langle e'', s'' \rangle;$   
 $\text{final-thread.actions-ok final } s \ t \ ta; \text{final-thread.actions-ok final } s \ t \ ta' \rrbracket$   
 $\implies ta = ta' \wedge e' = e'' \wedge s' = s''$

**and** *reds-deterministic*:

$\llbracket \text{convert-extTA } \text{extTA}, P, t \vdash \langle es, (\text{shr } s, xs) \rangle \text{--}[-ta \rightarrow] \langle es', s' \rangle;$   
 $\text{convert-extTA } \text{extTA}, P, t \vdash \langle es, (\text{shr } s, xs) \rangle \text{--}[-ta' \rightarrow] \langle es'', s'' \rangle;$   
 $\text{final-thread.actions-ok final } s \ t \ ta; \text{final-thread.actions-ok final } s \ t \ ta' \rrbracket$   
 $\implies ta = ta' \wedge es' = es'' \wedge s' = s''$

**proof**(*induct* *e* (*shr* *s*, *xs*) *ta* *e'* *s'* **and** *es* (*shr* *s*, *xs*) *ta* *es'* *s'* *arbitrary*: *e''* *s''* *xs* **and** *es''* *s''* *xs* *rule*: *red-reds.inducts*)

**case** *RedNew*

**thus** ?*case* **by**(*auto elim!*: *red-cases dest*: *deterministic-heap-ops-allocateD*[*OF det*])

**next**

**case** *RedNewArray*

**thus** ?*case* **by**(*auto elim!*: *red-cases dest*: *deterministic-heap-ops-allocateD*[*OF det*])

**next**

**case** *RedCall* **thus** ?*case*

**by**(*auto elim!*: *red-cases dest*: *sees-method-fun simp add*: *map-eq-append-conv*)

**next**

**case** *RedCallExternal* **thus** ?*case*

**by**(*auto elim!*: *red-cases dest*: *red-external-deterministic*[*OF det*] *simp add*: *final-thread.actions-ok-iff* *map-eq-append-conv dest*: *sees-method-fun*)

**next**

**case** *RedCallNull* **thus** ?*case* **by**(*auto elim!*: *red-cases dest*: *sees-method-fun simp add*: *map-eq-append-conv*)

**next**

**case** *CallThrowParams* **thus** ?*case*

**by**(*auto elim!*: *red-cases dest*: *sees-method-fun simp add*: *map-eq-append-conv append-eq-map-conv* *append-eq-append-conv2* *reds-map-Val-Throw Cons-eq-append-conv append-eq-Cons-conv*)

**qed**(*fastforce elim!*: *red-cases reds-cases dest*: *deterministic-heap-ops-readD*[*OF det*] *deterministic-heap-ops-writeD*[*OF det*] *iff*: *reds-map-Val-Throw*)**+**

**lemma** *red-mthr-deterministic*:

**assumes** *det*: *deterministic-heap-ops*

**shows** *red-mthr.deterministic* *P UNIV*

**proof**(*rule red-mthr.deterministicI*)

**fix** *s t x ta' x' m' ta'' x'' m''*

**assume** *thr* *s t* =  $\lfloor (x, \text{no-wait-locks}) \rfloor$

**and** *red*: *mred* *P t* (*x*, *shr* *s*) *ta'* (*x'*, *m'*) *mred* *P t* (*x*, *shr* *s*) *ta''* (*x''*, *m''*)

**and** *aok*: *red-mthr.actions-ok* *s t ta'* *red-mthr.actions-ok* *s t ta''*

**moreover obtain** *e xs* **where** [*simp*]: *x* = (*e*, *xs*) **by**(*cases* *x*)

**moreover obtain** *e' xs'* **where** [*simp*]: *x'* = (*e'*, *xs'*) **by**(*cases* *x'*)

**moreover obtain** *e'' xs''* **where** [*simp*]: *x''* = (*e''*, *xs''*) **by**(*cases* *x''*)

**ultimately have** *extTA2J* *P, P, t*  $\vdash \langle e, (\text{shr } s, xs) \rangle \text{--}ta' \rightarrow \langle e', (m', xs') \rangle$

**and** *extTA2J* *P, P, t*  $\vdash \langle e, (\text{shr } s, xs) \rangle \text{--}ta'' \rightarrow \langle e'', (m'', xs'') \rangle$

**by** *simp-all*

```

from red-deterministic[OF det this aok]
show ta' = ta'' ∧ x' = x'' ∧ m' = m'' by simp
qed simp

end

end

```

## 4.10 Runtime Well-typedness

```

theory WellTypeRT
imports
  WellType
  JHeap
begin

context J-heap-base begin

inductive WTrt :: 'addr J-prog ⇒ 'heap ⇒ env ⇒ 'addr expr ⇒ ty ⇒ bool
and WTrts :: 'addr J-prog ⇒ 'heap ⇒ env ⇒ 'addr expr list ⇒ ty list ⇒ bool
for P :: 'addr J-prog and h :: 'heap
where

  WTrtNew:
    is-class P C ⇒ WTrt P h E (new C) (Class C)

  | WTrtNewArray:
    [| WTrt P h E e Integer; is-type P (T[]) |]
    ⇒ WTrt P h E (newA T[e]) (T[])

  | WTrtCast:
    [| WTrt P h E e T; is-type P U |] ⇒ WTrt P h E (Cast U e) U

  | WTrtInstanceOf:
    [| WTrt P h E e T; is-type P U |] ⇒ WTrt P h E (e instanceof U) Boolean

  | WTrtVal:
    typeofh v = Some T ⇒ WTrt P h E (Val v) T

  | WTrtVar:
    E V = Some T ⇒ WTrt P h E (Var V) T

  | WTrtBinOp:
    [| WTrt P h E e1 T1; WTrt P h E e2 T2; P ⊢ T1 «bop» T2 : T |]
    ⇒ WTrt P h E (e1 «bop» e2) T

  | WTrtLAss:
    [| E V = Some T; WTrt P h E e T'; P ⊢ T' ≤ T |]
    ⇒ WTrt P h E (V:=e) Void

  | WTrtAAcc:
    [| WTrt P h E a (T[]); WTrt P h E i Integer |]
    ⇒ WTrt P h E (a[i]) T

```

- |  $WTrtAAccNT$ :  
 $\llbracket WTrt\ P\ h\ E\ a\ NT; WTrt\ P\ h\ E\ i\ Integer \rrbracket$   
 $\implies WTrt\ P\ h\ E\ (a[i])\ T$
- |  $WTrtAAss$ :  
 $\llbracket WTrt\ P\ h\ E\ a\ (T[]); WTrt\ P\ h\ E\ i\ Integer; WTrt\ P\ h\ E\ e\ T' \rrbracket$   
 $\implies WTrt\ P\ h\ E\ (a[i] := e)\ Void$
- |  $WTrtAAssNT$ :  
 $\llbracket WTrt\ P\ h\ E\ a\ NT; WTrt\ P\ h\ E\ i\ Integer; WTrt\ P\ h\ E\ e\ T' \rrbracket$   
 $\implies WTrt\ P\ h\ E\ (a[i] := e)\ Void$
- |  $WTrtALength$ :  
 $WTrt\ P\ h\ E\ a\ (T[]) \implies WTrt\ P\ h\ E\ (a.length)\ Integer$
- |  $WTrtALengthNT$ :  
 $WTrt\ P\ h\ E\ a\ NT \implies WTrt\ P\ h\ E\ (a.length)\ T$
- |  $WTrtFAcc$ :  
 $\llbracket WTrt\ P\ h\ E\ e\ U; class\text{-}type\text{-}of'\ U = \lfloor C \rfloor; P \vdash C\ has\ F:T\ (fm)\ in\ D \rrbracket \implies$   
 $WTrt\ P\ h\ E\ (e \cdot F\{D\})\ T$
- |  $WTrtFAccNT$ :  
 $WTrt\ P\ h\ E\ e\ NT \implies WTrt\ P\ h\ E\ (e \cdot F\{D\})\ T$
- |  $WTrtFAss$ :  
 $\llbracket WTrt\ P\ h\ E\ e1\ U; class\text{-}type\text{-}of'\ U = \lfloor C \rfloor; P \vdash C\ has\ F:T\ (fm)\ in\ D; WTrt\ P\ h\ E\ e2\ T2; P$   
 $\vdash T2 \leq T \rrbracket$   
 $\implies WTrt\ P\ h\ E\ (e1 \cdot F\{D\} := e2)\ Void$
- |  $WTrtFAssNT$ :  
 $\llbracket WTrt\ P\ h\ E\ e1\ NT; WTrt\ P\ h\ E\ e2\ T2 \rrbracket$   
 $\implies WTrt\ P\ h\ E\ (e1 \cdot F\{D\} := e2)\ Void$
- |  $WTrtCAS$ :  
 $\llbracket WTrt\ P\ h\ E\ e1\ U; class\text{-}type\text{-}of'\ U = \lfloor C \rfloor; P \vdash C\ has\ F:T\ (fm)\ in\ D; volatile\ fm;$   
 $WTrt\ P\ h\ E\ e2\ T2; P \vdash T2 \leq T; WTrt\ P\ h\ E\ e3\ T3; P \vdash T3 \leq T \rrbracket$   
 $\implies WTrt\ P\ h\ E\ (e1 \cdot compareAndSwap(D \cdot F, e2, e3))\ Boolean$
- |  $WTrtCASNT$ :  
 $\llbracket WTrt\ P\ h\ E\ e1\ NT; WTrt\ P\ h\ E\ e2\ T2; WTrt\ P\ h\ E\ e3\ T3 \rrbracket$   
 $\implies WTrt\ P\ h\ E\ (e1 \cdot compareAndSwap(D \cdot F, e2, e3))\ Boolean$
- |  $WTrtCall$ :  
 $\llbracket WTrt\ P\ h\ E\ e\ U; class\text{-}type\text{-}of'\ U = \lfloor C \rfloor; P \vdash C\ sees\ M:Ts \rightarrow T = meth\ in\ D;$   
 $WTrts\ P\ h\ E\ es\ Ts'; P \vdash Ts' [\leq] Ts \rrbracket$   
 $\implies WTrt\ P\ h\ E\ (e \cdot M(es))\ T$
- |  $WTrtCallNT$ :  
 $\llbracket WTrt\ P\ h\ E\ e\ NT; WTrts\ P\ h\ E\ es\ Ts \rrbracket$   
 $\implies WTrt\ P\ h\ E\ (e \cdot M(es))\ T$
- |  $WTrtBlock$ :

$$\begin{aligned} & \llbracket WTrt\ P\ h\ (E(V \mapsto T))\ e\ T';\ case\ vo\ of\ None \Rightarrow True \mid \lfloor v \rfloor \Rightarrow \exists T'.\ typeof_h\ v = \lfloor T' \rfloor \wedge P \vdash T' \\ & \leq T \rrbracket \\ & \implies WTrt\ P\ h\ E\ \{V:T=vo;\ e\}\ T' \end{aligned}$$

$$\begin{aligned} & | WTrtSynchronized: \\ & \llbracket WTrt\ P\ h\ E\ o'\ T;\ is-refT\ T;\ WTrt\ P\ h\ E\ e\ T' \rrbracket \\ & \implies WTrt\ P\ h\ E\ (sync(o')\ e)\ T' \end{aligned}$$

$$\begin{aligned} & | WTrtInSynchronized: \\ & \llbracket WTrt\ P\ h\ E\ (addr\ a)\ T;\ WTrt\ P\ h\ E\ e\ T' \rrbracket \\ & \implies WTrt\ P\ h\ E\ (insync(a)\ e)\ T' \end{aligned}$$

$$\begin{aligned} & | WTrtSeq: \\ & \llbracket WTrt\ P\ h\ E\ e1\ T1;\ WTrt\ P\ h\ E\ e2\ T2 \rrbracket \\ & \implies WTrt\ P\ h\ E\ (e1;;e2)\ T2 \end{aligned}$$

$$\begin{aligned} & | WTrtCond: \\ & \llbracket WTrt\ P\ h\ E\ e\ Boolean;\ WTrt\ P\ h\ E\ e1\ T1;\ WTrt\ P\ h\ E\ e2\ T2;\ P \vdash lub(T1,\ T2) = T \rrbracket \\ & \implies WTrt\ P\ h\ E\ (if\ (e)\ e1\ else\ e2)\ T \end{aligned}$$

$$\begin{aligned} & | WTrtWhile: \\ & \llbracket WTrt\ P\ h\ E\ e\ Boolean;\ WTrt\ P\ h\ E\ c\ T \rrbracket \\ & \implies WTrt\ P\ h\ E\ (while(e)\ c)\ Void \end{aligned}$$

$$\begin{aligned} & | WTrtThrow: \\ & \llbracket WTrt\ P\ h\ E\ e\ T;\ P \vdash T \leq Class\ Throwable \rrbracket \\ & \implies WTrt\ P\ h\ E\ (throw\ e)\ T' \end{aligned}$$

$$\begin{aligned} & | WTrtTry: \\ & \llbracket WTrt\ P\ h\ E\ e1\ T1;\ WTrt\ P\ h\ (E(V \mapsto Class\ C))\ e2\ T2;\ P \vdash T1 \leq T2 \rrbracket \\ & \implies WTrt\ P\ h\ E\ (try\ e1\ catch(C\ V)\ e2)\ T2 \end{aligned}$$

$$| WTrtNil: WTrts\ P\ h\ E\ []\ []$$

$$| WTrtCons: \llbracket WTrt\ P\ h\ E\ e\ T;\ WTrts\ P\ h\ E\ es\ Ts \rrbracket \implies WTrts\ P\ h\ E\ (e\ \# \ es)\ (T\ \# \ Ts)$$

### abbreviation

$WTrt-syntax :: 'addr\ J-prog \Rightarrow env \Rightarrow 'heap \Rightarrow 'addr\ expr \Rightarrow ty \Rightarrow bool\ (-,-,- \vdash - : - \quad [51,51,51]50)$

### where

$P,E,h \vdash e : T \equiv WTrt\ P\ h\ E\ e\ T$

### abbreviation

$WTrts-syntax :: 'addr\ J-prog \Rightarrow env \Rightarrow 'heap \Rightarrow 'addr\ expr\ list \Rightarrow ty\ list \Rightarrow bool\ (-,-,- \vdash - [:] - \quad [51,51,51]50)$

### where

$P,E,h \vdash es\ [:]\ Ts \equiv WTrts\ P\ h\ E\ es\ Ts$

### lemmas [intro] =

$WTrtNew\ WTrtNewArray\ WTrtCast\ WTrtInstanceOf\ WTrtVal\ WTrtVar\ WTrtBinOp\ WTrtLAss$   
 $WTrtBlock\ WTrtSynchronized\ WTrtInSynchronized\ WTrtSeq\ WTrtCond\ WTrtWhile$   
 $WTrtThrow\ WTrtTry\ WTrtNil\ WTrtCons$

### lemmas [intro] =

$WTrtFAcc\ WTrtFAccNT\ WTrtFAss\ WTrtFAssNT\ WTrtCall\ WTrtCallNT$

*WTrtAAcc WTrtAAccNT WTrtAAss WTrtAAssNT WTrtALength WTrtALengthNT*

#### 4.10.1 Easy consequences

**inductive-simps** *WTrts-iffs* [iff]:

$P, E, h \vdash [] \text{ } [:] \text{ } Ts$   
 $P, E, h \vdash e \# es \text{ } [:] \text{ } T \# Ts$   
 $P, E, h \vdash (e \# es) \text{ } [:] \text{ } Ts$

**lemma** *WTrts-conv-list-all2*:  $P, E, h \vdash es \text{ } [:] \text{ } Ts = \text{list-all2 } (WTrt \text{ } P \text{ } h \text{ } E) \text{ } es \text{ } Ts$   
**by**(*induct es arbitrary: Ts*)(*auto simp add: list-all2-Cons1 elim: WTrts.cases*)

**lemma** [*simp*]:  $(P, E, h \vdash es_1 \text{ } @ \text{ } es_2 \text{ } [:] \text{ } Ts) =$

$(\exists Ts_1 \text{ } Ts_2. Ts = Ts_1 \text{ } @ \text{ } Ts_2 \wedge P, E, h \vdash es_1 \text{ } [:] \text{ } Ts_1 \ \& \ P, E, h \vdash es_2 \text{ } [:] \text{ } Ts_2)$

**by**(*auto simp add: WTrts-conv-list-all2 list-all2-append1 dest: list-all2-lengthD[symmetric]*)

**inductive-simps** *WTrt-iffs* [iff]:

$P, E, h \vdash \text{Val } v : T$   
 $P, E, h \vdash \text{Var } v : T$   
 $P, E, h \vdash e_1 ;; e_2 : T_2$   
 $P, E, h \vdash \{ V : T = vo; e \} : T'$

**inductive-cases** *WTrt-elim-cases*[*elim!*]:

$P, E, h \vdash \text{newA } T[i] : U$   
 $P, E, h \vdash v := e : T$   
 $P, E, h \vdash \text{if } (e) \text{ } e_1 \text{ } \text{else } e_2 : T$   
 $P, E, h \vdash \text{while}(e) \text{ } c : T$   
 $P, E, h \vdash \text{throw } e : T$   
 $P, E, h \vdash \text{try } e_1 \text{ } \text{catch}(C \text{ } V) \text{ } e_2 : T$   
 $P, E, h \vdash \text{Cast } D \text{ } e : T$   
 $P, E, h \vdash e \text{ } \text{instanceof } U : T$   
 $P, E, h \vdash a[i] : T$   
 $P, E, h \vdash a[i] := e : T$   
 $P, E, h \vdash a \cdot \text{length} : T$   
 $P, E, h \vdash e \cdot F\{D\} : T$   
 $P, E, h \vdash e \cdot F\{D\} := v : T$   
 $P, E, h \vdash e \cdot \text{compareAndSwap}(D \cdot F, e2, e3) : T$   
 $P, E, h \vdash e_1 \ll \text{bop} \gg e_2 : T$   
 $P, E, h \vdash \text{new } C : T$   
 $P, E, h \vdash e \cdot M(es) : T$   
 $P, E, h \vdash \text{sync}(o') \text{ } e : T$   
 $P, E, h \vdash \text{insync}(a) \text{ } e : T$

#### 4.10.2 Some interesting lemmas

**lemma** *WTrts-Val*[*simp*]:

$P, E, h \vdash \text{map Val } vs \text{ } [:] \text{ } Ts \longleftrightarrow \text{map } (\text{typeof}_h) \text{ } vs = \text{map Some } Ts$

**by**(*induct vs arbitrary: Ts*) *auto*

**lemma** *WTrt-env-mono*:  $P, E, h \vdash e : T \implies (\bigwedge E'. E \subseteq_m E' \implies P, E', h \vdash e : T)$

**and** *WTrts-env-mono*:  $P, E, h \vdash es \text{ } [:] \text{ } Ts \implies (\bigwedge E'. E \subseteq_m E' \implies P, E', h \vdash es \text{ } [:] \text{ } Ts)$

**apply**(*induct rule: WTrt-WTrts.inducts*)

**apply**(*simp add: WTrtNew*)

**apply**(*fastforce simp: WTrtNewArray*)

```

apply(fastforce simp: WTrtCast)
apply(fastforce simp: WTrtInstanceOf)
apply(fastforce simp: WTrtVal)
apply(simp add: WTrtVar map-le-def dom-def)
apply(fastforce simp add: WTrtBinOp)
apply(force simp: map-le-def)
apply(force simp: WTrtAAcc)
apply(force simp: WTrtAAccNT)
apply(rule WTrtAAss, fastforce, blast, blast)
apply(fastforce)
apply(rule WTrtALength, blast)
apply(blast)
apply(fastforce simp: WTrtFAcc)
apply(simp add: WTrtFAccNT)
apply(fastforce simp: WTrtFAss)
apply(fastforce simp: WTrtFAssNT)
apply(fastforce simp: WTrtCAS)
apply(fastforce simp: WTrtCASNT)
apply(fastforce simp: WTrtCall)
apply(fastforce simp: WTrtCallNT)
apply(fastforce simp: map-le-def)
apply(fastforce)
apply(fastforce)
apply(fastforce)
apply(fastforce)
apply(fastforce simp: WTrtSeq)
apply(fastforce simp: WTrtCond)
apply(fastforce simp: WTrtWhile)
apply(fastforce simp: WTrtThrow)
apply(auto simp: WTrtTry map-le-def dom-def)
done

```

```

lemma WT-implies-WTrt:  $P, E \vdash e :: T \implies P, E, h \vdash e : T$ 
  and WTs-implies-WTrts:  $P, E \vdash es [::] Ts \implies P, E, h \vdash es [:] Ts$ 
apply(induct rule: WT-WTs.inducts)
apply fast
apply fast
apply fast
apply fast
apply(fastforce dest:typeof-lit-typeof)
apply(simp)
apply(fastforce intro: WT-binop-WTrt-binop)
apply(fastforce)
apply(erule WTrtAAcc)
apply(assumption)
apply(erule WTrtAAss)
apply(assumption)+
apply(erule WTrtALength)
apply(fastforce intro: has-visible-field)
apply(fastforce simp: WTrtFAss dest: has-visible-field)
apply(fastforce simp: WTrtCAS dest: has-visible-field)
apply(fastforce simp: WTrtCall)
apply(clarsimp simp del: fun-upd-apply, blast intro: typeof-lit-typeof)
apply(fastforce)+

```



done

lemma *wt-blocks*:

$\bigwedge E. \llbracket \text{length } Vs = \text{length } Ts; \text{length } vs = \text{length } Ts \rrbracket \implies$   
 $(P, E, h \vdash \text{blocks } Vs \ Ts \ vs \ e : T) =$   
 $(P, E(Vs[\mapsto] Ts), h \vdash e : T \wedge (\exists Ts'. \text{map } (\text{typeof}_h) \ vs = \text{map } \text{Some } Ts' \wedge P \vdash Ts' [\leq] Ts))$   
 apply(induct *Vs Ts vs e rule:blocks.induct*)  
 apply(force)  
 apply simp-all  
 done

end

context *J-heap* begin

lemma *WTrt-hext-mono*:  $P, E, h \vdash e : T \implies h \sqsubseteq h' \implies P, E, h' \vdash e : T$   
 and *WTrts-hext-mono*:  $P, E, h \vdash es [:] Ts \implies h \sqsubseteq h' \implies P, E, h' \vdash es [:] Ts$   
 apply(induct rule: *WTrt-WTrts.inducts*)  
 apply(simp add: *WTrtNew*)  
 apply(fastforce simp: *WTrtNewArray*)  
 apply(fastforce simp: *WTrtCast*)  
 apply(fastforce simp: *WTrtInstanceOf*)  
 apply(fastforce simp: *WTrtVal dest:hext-typeof-mono*)  
 apply(simp add: *WTrtVar*)  
 apply(fastforce simp add: *WTrtBinOp*)  
 apply(fastforce simp add: *WTrtLAss*)  
 apply fastforce  
 apply fastforce  
 apply fastforce  
 apply fastforce  
 apply fastforce  
 apply fastforce  
 apply fastforce  
 apply(fast)  
 apply(simp add: *WTrtFAccNT*)  
 apply(fastforce simp: *WTrtFAss del:WTrt-WTrts.intros WTrt-elim-cases*)  
 apply(fastforce simp: *WTrtFAssNT*)  
 apply(fastforce simp: *WTrtCAS*)  
 apply(fastforce simp: *WTrtCASNT*)  
 apply(fastforce simp: *WTrtCall*)  
 apply(fastforce simp: *WTrtCallNT*)  
 apply(fastforce intro: *hext-typeof-mono*)  
 apply fastforce+  
 done

end

end

## 4.11 Progress of Small Step Semantics

theory *Progress*

imports

*WellTypeRT*

```

DefAss
SmallStep
../Common/ExternalCallWF
WWellForm
begin

context J-heap begin

lemma final-addrE [consumes 3, case-names addr Throw]:
  [| P,E,h ⊢ e : T; class-type-of' T = [U]; final e;
    ∧ a. e = addr a ⇒ R;
    ∧ a. e = Throw a ⇒ R |] ⇒ R
apply(auto elim!: final.cases)
apply(case-tac v)
apply auto
done

lemma finalRefE [consumes 3, case-names null Class Array Throw]:
  [| P,E,h ⊢ e : T; is-refT T; final e;
    e = null ⇒ R;
    ∧ a C. [| e = addr a; T = Class C |] ⇒ R;
    ∧ a U. [| e = addr a; T = U[] |] ⇒ R;
    ∧ a. e = Throw a ⇒ R |] ⇒ R
apply(auto simp:final-iff)
apply(case-tac v)
apply(auto elim!: is-refT.cases)
done

end

theorem (in J-progress) red-progress:
  assumes wf: wwf-J-prog P and hconf: hconf h
  shows progress: [| P,E,h ⊢ e : T; D e [dom l]; ¬ final e |] ⇒ ∃ e' s' ta. extTA,P,t ⊢ ⟨e,(h,l)⟩ -ta→
  ⟨e',s'⟩
  and progresss: [| P,E,h ⊢ es [:] Ts; Ds es [dom l]; ¬ finals es |] ⇒ ∃ es' s' ta. extTA,P,t ⊢ ⟨es,(h,l)⟩
  [-ta→] ⟨es',s'⟩
proof (induct arbitrary: l and l rule: WTrt-WTrts.inducts)
  case (WTrtNew C)
  thus ?case using WTrtNew
  by(cases allocate h (Class-type C) = {})(fastforce intro: RedNewFail RedNew)+
next
  case (WTrtNewArray E e T l)
  have IH: ∧ l. [| D e [dom l]; ¬ final e |] ⇒ ∃ e' s' tas. extTA,P,t ⊢ ⟨e,(h,l)⟩ -tas→ ⟨e', s'⟩
  and D: D (newA T[e]) [dom l]
  and ei: P,E,h ⊢ e : Integer by fact+
  from D have De: D e [dom l] by auto
  show ?case
proof cases
  assume final e
  thus ?thesis
proof (rule finalE)
  fix v
  assume e [simp]: e = Val v
  with ei have typeofh v = Some Integer by fastforce

```

```

hence exei:  $\exists i. v = \text{Intg } i$  by fastforce
then obtain i where  $v : v = \text{Intg } i$  by blast
thus ?thesis
proof (cases  $0 \leq s \ i$ )
  case True
    thus ?thesis using True  $\langle v = \text{Intg } i \rangle$  WTrtNewArray.prems
    by (cases allocate h (Array-type T (nat (sint i))) =  $\{\}$ ) (auto simp del: split-paired-Ex intro: RedNewArrayFail RedNewArray)
  next
    assume  $\neg 0 \leq s \ i$ 
    hence  $i < s \ 0$  by simp
    then have  $\text{extTA}, P, t \vdash \langle \text{newA } T [\text{Val}(\text{Intg } i)], (h, l) \rangle -\varepsilon \rightarrow \langle \text{THROW NegativeArraySize}, (h, l) \rangle$ 
      by - (rule RedNewArrayNegative, auto)
    with e v show ?thesis by blast
  qed
next
fix exa
assume e:  $e = \text{Throw } \text{exa}$ 
then have  $\text{extTA}, P, t \vdash \langle \text{newA } T [\text{Throw } \text{exa}], (h, l) \rangle -\varepsilon \rightarrow \langle \text{Throw } \text{exa}, (h, l) \rangle$ 
  by - (rule NewArrayThrow)
with e show ?thesis by blast
qed
next
assume  $\neg \text{final } e$ 
with IH De have exes:  $\exists e' s' \text{ ta. } \text{extTA}, P, t \vdash \langle e, (h, l) \rangle -\text{ta} \rightarrow \langle e', s' \rangle$  by simp
then obtain  $e' s' \text{ ta}$  where  $\text{extTA}, P, t \vdash \langle e, (h, l) \rangle -\text{ta} \rightarrow \langle e', s' \rangle$  by blast
hence  $\text{extTA}, P, t \vdash \langle \text{newA } T [e], (h, l) \rangle -\text{ta} \rightarrow \langle \text{newA } T [e'], s' \rangle$  by - (rule NewArrayRed)
thus ?thesis by blast
qed
next
case (WTrtCast E e T U l)
have wte:  $P, E, h \vdash e : T$ 
and IH:  $\bigwedge l. \llbracket \mathcal{D} \ e \ [dom \ l]; \neg \text{final } e \rrbracket$ 
   $\implies \exists e' s' \text{ tas. } \text{extTA}, P, t \vdash \langle e, (h, l) \rangle -\text{tas} \rightarrow \langle e', s' \rangle$ 
and D:  $\mathcal{D} \ (\text{Cast } U \ e) \ [dom \ l]$  by fact+
from D have De:  $\mathcal{D} \ e \ [dom \ l]$  by auto
show ?case
proof (cases final e)
  assume final e
  thus ?thesis
  proof (rule finalE)
    fix v
    assume ev:  $e = \text{Val } v$ 
    with WTrtCast obtain V where  $\text{thvU}: \text{typeof}_h \ v = \lfloor V \rfloor$  by fastforce
    thus ?thesis
  proof (cases  $P \vdash V \leq U$ )
    assume  $P \vdash V \leq U$ 
    with thvU have  $\text{extTA}, P, t \vdash \langle \text{Cast } U \ (\text{Val } v), (h, l) \rangle -\varepsilon \rightarrow \langle \text{Val } v, (h, l) \rangle$ 
      by - (rule RedCast, auto)
    with ev show ?thesis by blast
  next
    assume  $\neg P \vdash V \leq U$ 
    with thvU have  $\text{extTA}, P, t \vdash \langle \text{Cast } U \ (\text{Val } v), (h, l) \rangle -\varepsilon \rightarrow \langle \text{THROW ClassCast}, (h, l) \rangle$ 
      by - (rule RedCastFail, auto)
  qed
qed

```

```

    with ev show ?thesis by blast
  qed
next
  fix a
  assume e = Throw a
  thus ?thesis by (blast intro!: CastThrow)
qed
next
  assume nf:  $\neg \text{final } e$ 
  from IH[OF De nf] show ?thesis by (blast intro: CastRed)
qed
next
  case (WTrtInstanceOf E e T U l)
  have wte:  $P, E, h \vdash e : T$ 
  and IH:  $\bigwedge l. \llbracket \mathcal{D} \ e \ \lfloor \text{dom } l \rfloor; \neg \text{final } e \rrbracket$ 
     $\implies \exists e' s' \text{tas. } \text{extTA}, P, t \vdash \langle e, (h, l) \rangle -\text{tas} \rightarrow \langle e', s' \rangle$ 
  and D:  $\mathcal{D} \ (e \text{ instanceof } U) \ \lfloor \text{dom } l \rfloor$  by fact+
  from D have De:  $\mathcal{D} \ e \ \lfloor \text{dom } l \rfloor$  by auto
  show ?case
  proof (cases final e)
    assume final e
    thus ?thesis
    proof (rule finalE)
      fix v
      assume ev: e = Val v
      with WTrtInstanceOf obtain V where thvU:  $\text{typeof}_h \ v = \lfloor V \rfloor$  by fastforce
      hence  $\text{extTA}, P, t \vdash \langle (\text{Val } v) \text{ instanceof } U, (h, l) \rangle -\varepsilon \rightarrow \langle \text{Val } (\text{Bool } (v \neq \text{Null} \wedge P \vdash V \leq U)), (h, l) \rangle$ 
        by  $\neg(\text{rule RedInstanceOf, auto})$ 
      with ev show ?thesis by blast
    next
      fix a
      assume e = Throw a
      thus ?thesis by (blast intro!: InstanceOfThrow)
    qed
  next
    assume nf:  $\neg \text{final } e$ 
    from IH[OF De nf] show ?thesis by (blast intro: InstanceOfRed)
  qed
next
  case WTrtVal thus ?case by (simp add: final-iff)
next
  case WTrtVar thus ?case by (fastforce intro: RedVar simp: hyper-isin-def)
next
  case (WTrtBinOp E e1 T1 e2 T2 bop T)
  show ?case
  proof cases
    assume final e1
    thus ?thesis
    proof (rule finalE)
      fix v1 assume [simp]: e1 = Val v1
      show ?thesis
      proof cases
        assume final e2
        thus ?thesis

```

```

proof (rule finalE)
  fix v2 assume [simp]: e2 = Val v2
  with WTrtBinOp have type: typeofh v1 = [T1] typeofh v2 = [T2] by auto
  from binop-progress[OF this ⟨P ⊢ T1 «bop» T2 : T⟩] obtain va
    where binop bop v1 v2 = [va] by blast
  thus ?thesis by (cases va)(fastforce intro: RedBinOp RedBinOpFail)+
next
  fix a assume e2 = Throw a
  thus ?thesis by (fastforce intro: BinOpThrow2)
qed
next
  assume ¬ final e2 with WTrtBinOp show ?thesis
    by simp (fast intro!: BinOpRed2)
qed
next
  fix a assume e1 = Throw a
  thus ?thesis by simp (fast intro: BinOpThrow1)
qed
next
  assume ¬ final e1 with WTrtBinOp show ?thesis
    by simp (fast intro: BinOpRed1)
qed
next
  case (WTrtLAss E V T e T')
  show ?case
  proof cases
    assume final e with WTrtLAss show ?thesis
      by (fastforce simp: final-iff intro!: RedLAss LAssThrow)
  next
    assume ¬ final e with WTrtLAss show ?thesis
      by simp (fast intro: LAssRed)
  qed
next
  case (WTrtAAcc E a T i l)
  have wte: P, E, h ⊢ a : T[]
  and wtei: P, E, h ⊢ i : Integer
  and IHa: ∧l. [D a [dom l]; ¬ final a]
    ⇒ ∃ e' s' tas. extTA, P, t ⊢ ⟨a, (h, l)⟩ -tas→ ⟨e', s'⟩
  and IHl: ∧l. [D i [dom l]; ¬ final i]
    ⇒ ∃ e' s' tas. extTA, P, t ⊢ ⟨i, (h, l)⟩ -tas→ ⟨e', s'⟩
  and D: D (a[i]) [dom l] by fact+
  have ref: is-refT (T[]) by simp
  from D have Da: D a [dom l] by simp
  show ?case
  proof (cases final a)
    assume final a
    with wte ref show ?case
    proof (cases rule: finalRefE)
      case null
      thus ?thesis
    proof (cases final i)
      assume final i
      thus ?thesis
    proof (rule finalE)

```

```

    fix v
    assume i: i = Val v
    have extTA,P,t ⊢ ⟨null[Val v], (h, l)⟩ −ε→ ⟨THROW NullPointer, (h,l)⟩
      by(rule RedAAccNull)
    with i null show ?thesis by blast
  next
    fix ex
    assume i: i = Throw ex
    have extTA,P,t ⊢ ⟨null[Throw ex], (h, l)⟩ −ε→ ⟨Throw ex, (h,l)⟩
      by(rule AAccThrow2)
    with i null show ?thesis by blast
  qed
next
  assume ¬ final i
  from WTrtAAcc null show ?thesis
    by simp
  qed
next
  case (Array ad U)
  with wte obtain n where ty: typeof-addr h ad = [Array-type U n] by auto
  thus ?thesis
  proof (cases final i)
    assume final i
    thus ?thesis
    proof(rule finalE)
      fix v
      assume [simp]: i = Val v
      with wtei have typeofh v = Some Integer by fastforce
      hence ∃ i. v = Intg i by fastforce
      then obtain i where [simp]: v = Intg i by blast
      thus ?thesis
      proof (cases i < s 0 ∨ sint i ≥ int n)
        case True
        with WTrtAAcc Array ty show ?thesis by (fastforce intro: RedAAccBounds)
      next
        case False
        then have nat (sint i) < n
          by (simp add: not-le word-sless-alt nat-less-iff)
        with ty have P,h ⊢ ad@ACell (nat (sint i)) : U by(auto intro!: addr-loc-type.intros)
        from heap-read-total[OF hconf this]
        obtain v where heap-read h ad (ACell (nat (sint i))) v by blast
        with False Array ty show ?thesis by(fastforce intro: RedAAcc)
      qed
    next
      fix ex
      assume i = Throw ex
      with WTrtAAcc Array show ?thesis by (fastforce intro: AAccThrow2)
    qed
  next
    assume ¬ final i
    with WTrtAAcc Array show ?thesis by (fastforce intro: AAccRed2)
  qed
next
  fix ex

```

```

    assume a = Throw ex
    with WTrtAAcc show ?thesis by (fastforce intro: AAccThrow1)
  qed simp
next
  assume ¬ final a
  with WTrtAAcc show ?thesis by (fastforce intro: AAccRed1)
  qed
next
  case (WTrtAAccNT E a i T l)
  have wte: P,E,h ⊢ a : NT
  and wtei: P,E,h ⊢ i : Integer
  and IHa: ∧l. [D a [dom l]; ¬ final a]
    ⇒ ∃ e' s' tas. extTA,P,t ⊢ ⟨a,(h,l)⟩ −tas→ ⟨e',s'⟩
  and IHi: ∧l. [D i [dom l]; ¬ final i]
    ⇒ ∃ e' s' tas. extTA,P,t ⊢ ⟨i,(h,l)⟩ −tas→ ⟨e',s'⟩ by fact+
  have ref: is-refT NT by simp
  with WTrtAAccNT have Da: D a [dom l] by simp
  thus ?case
proof (cases final a)
  case True
  with wte ref show ?thesis
proof (cases rule: finalRefE)
  case null
  thus ?thesis
proof (cases final i)
  assume final i
  thus ?thesis
proof (rule finalE)
  fix v
  assume i: i = Val v
  have extTA,P,t ⊢ ⟨null[Val v], (h, l)⟩ −ε→ ⟨THROW NullPointer, (h,l)⟩
    by (rule RedAAccNull)
  with WTrtAAccNT ⟨final a⟩ null ⟨final i⟩ i show ?thesis by blast
next
  fix ex
  assume i: i = Throw ex
  have extTA,P,t ⊢ ⟨null[Throw ex], (h, l)⟩ −ε→ ⟨Throw ex, (h,l)⟩
    by (rule AAccThrow2)
  with WTrtAAccNT ⟨final a⟩ null ⟨final i⟩ i show ?thesis by blast
  qed
next
  assume ¬ final i
  with WTrtAAccNT null show ?thesis
    by (fastforce intro: AAccRed2)
  qed
next
  case Throw thus ?thesis by (fastforce intro: AAccThrow1)
  qed simp-all
next
  case False
  with WTrtAAccNT Da show ?thesis by (fastforce intro: AAccRed1)
  qed
next
  case (WTrtAAss E a T i e T' l)

```

```

have wta:  $P, E, h \vdash a : T[]$ 
and wti:  $P, E, h \vdash i : \text{Integer}$ 
and wte:  $P, E, h \vdash e : T'$ 
and D:  $\mathcal{D} (a[i] := e) [dom\ l]$ 
and IH1:  $\bigwedge l. [\mathcal{D} a [dom\ l]; \neg final\ a] \implies \exists e' s' tas. extTA, P, t \vdash \langle a, (h, l) \rangle -tas \rightarrow \langle e', s' \rangle$ 
and IH2:  $\bigwedge l. [\mathcal{D} i [dom\ l]; \neg final\ i] \implies \exists e' s' tas. extTA, P, t \vdash \langle i, (h, l) \rangle -tas \rightarrow \langle e', s' \rangle$ 
and IH3:  $\bigwedge l. [\mathcal{D} e [dom\ l]; \neg final\ e] \implies \exists e' s' tas. extTA, P, t \vdash \langle e, (h, l) \rangle -tas \rightarrow \langle e', s' \rangle$  by
fact+
have ref: is-refT (T[]) by simp
show ?case
proof (cases final a)
  assume fa: final a
  with wta ref show ?thesis
  proof (cases rule: finalRefE)
    case null
    show ?thesis
    proof (cases final i)
      assume final i
      thus ?thesis
      proof (rule finalE)
        fix v
        assume i: i = Val v
        with wti have typeofh v = Some Integer by fastforce
        then obtain idx where v = Intg idx by fastforce
        thus ?thesis
        proof (cases final e)
          assume final e
          thus ?thesis
          proof (rule finalE)
            fix w
            assume e = Val w
            with WTrtAAss null show ?thesis by (fastforce intro: RedAAssNull)
          next
            fix ex
            assume e = Throw ex
            with WTrtAAss null show ?thesis by (fastforce intro: AAsthrow3)
          qed
        next
          assume  $\neg final\ e$ 
          with WTrtAAss null show ?thesis by (fastforce intro: AAsthrow3)
        qed
      next
        fix ex
        assume i = Throw ex
        with WTrtAAss null show ?thesis by (fastforce intro: AAsthrow2)
      qed
    next
      assume  $\neg final\ i$ 
      with WTrtAAss null show ?thesis by (fastforce intro: AAsthrow2)
    qed
  next
    case (Array ad U)
    with wta obtain n where ty: typeof-addr h ad = [Array-type U n] by auto
    thus ?thesis

```



```

proof (cases final i)
  assume fi: final i
  thus ?thesis
proof (rule finalE)
  fix v
  assume ivalv: i = Val v
  with wti have typeofh v = Some Integer by fastforce
  then obtain idx where vidx: v = Intg idx by fastforce
  thus ?thesis
proof (cases final e)
  assume fe: final e
  thus ?thesis
proof(rule finalE)
  fix w
  assume evalw: e = Val w
  show ?thesis
proof(cases idx < s 0 ∨ sint idx ≥ int n)
  case True
    with ty evalw Array ivalv vidx show ?thesis by(fastforce intro: RedAAssBounds)
  next
  case False
    then have nat (sint idx) < n
      by (simp add: not-le word-sless-alt nat-less-iff)
    with ty have adal: P, h ⊢ ad@ACell (nat (sint idx)) : U
      by(auto intro!: addr-loc-type.intros)
    show ?thesis
    proof(cases P ⊢ T' ≤ U)
      case True
        with wte evalw have P, h ⊢ w :≤ U
          by(auto simp add: conf-def)
        from heap-write-total[OF hconf adal this]
        obtain h' where h': heap-write h ad (ACell (nat (sint idx))) w h' ..
        with ty False vidx ivalv evalw Array wte True
        show ?thesis by(fastforce intro: RedAAss)
      next
      case False
        with ty vidx ivalv evalw Array wte <¬ (idx < s 0 ∨ sint idx ≥ int n)>
        show ?thesis by(fastforce intro: RedAAssStore)
    qed
  qed
next
  fix ex
  assume e = Throw ex
  with Array ivalv show ?thesis by (fastforce intro: AAssThrow3)
  qed
next
  assume ¬ final e
  with WTrtAAss Array fi ivalv vidx show ?thesis by (fastforce intro: AAssRed3)
  qed
next
  fix ex
  assume i = Throw ex
  with WTrtAAss Array show ?thesis by (fastforce intro: AAssThrow2)
  qed

```

```

    next
      assume  $\neg \text{final } i$ 
      with  $WTrtAAss \text{ Array show ?thesis by (fastforce intro: AAssRed2)}$ 
    qed
  next
    fix  $ex$ 
    assume  $a = \text{Throw } ex$ 
    with  $WTrtAAss \text{ show ?thesis by (fastforce intro: AAssThrow1)}$ 
  qed simp-all
next
  assume  $\neg \text{final } a$ 
  with  $WTrtAAss \text{ show ?thesis by (fastforce intro: AAssRed1)}$ 
qed
next
  case ( $WTrtAAssNT \ E \ a \ i \ e \ T' \ l$ )
  have  $wta: P, E, h \vdash a : NT$ 
  and  $wti: P, E, h \vdash i : Integer$ 
  and  $wte: P, E, h \vdash e : T'$ 
  and  $D: \mathcal{D} \ (a[i] := e) \ [dom \ l]$ 
  and  $IH1: \bigwedge l. [\mathcal{D} \ a \ [dom \ l]; \neg \text{final } a] \implies \exists e' \ s' \ \text{tas. } extTA, P, t \vdash \langle a, (h, l) \rangle -tas \rightarrow \langle e', s' \rangle$ 
  and  $IH2: \bigwedge l. [\mathcal{D} \ i \ [dom \ l]; \neg \text{final } i] \implies \exists e' \ s' \ \text{tas. } extTA, P, t \vdash \langle i, (h, l) \rangle -tas \rightarrow \langle e', s' \rangle$ 
  and  $IH3: \bigwedge l. [\mathcal{D} \ e \ [dom \ l]; \neg \text{final } e] \implies \exists e' \ s' \ \text{tas. } extTA, P, t \vdash \langle e, (h, l) \rangle -tas \rightarrow \langle e', s' \rangle$  by
fact+
  have  $ref: is-refT \ NT$  by simp
  show ?case
  proof (cases  $\text{final } a$ )
    assume  $fa: \text{final } a$ 
    show ?case
    proof (cases  $\text{final } i$ )
      assume  $fi: \text{final } i$ 
      show ?case
      proof (cases  $\text{final } e$ )
        assume  $fe: \text{final } e$ 
        with  $WTrtAAssNT \ fa \ fi \ \text{show ?thesis}$ 
        by (fastforce simp:  $\text{final-iff}$  intro:  $RedAAssNull \ AAssThrow1 \ AAssThrow2 \ AAssThrow3$ )
      next
        assume  $\neg \text{final } e$ 
        with  $WTrtAAssNT \ fa \ fi \ \text{show ?thesis}$ 
        by (fastforce simp:  $\text{final-iff}$  intro!:  $AAssRed3 \ AAssThrow1 \ AAssThrow2$ )
      qed
    next
      assume  $\neg \text{final } i$ 
      with  $WTrtAAssNT \ fa \ \text{show ?thesis}$ 
      by (fastforce simp:  $\text{final-iff}$  intro!:  $AAssRed2 \ AAssThrow1$ )
    qed
  next
    assume  $\neg \text{final } a$ 
    with  $WTrtAAssNT \ \text{show ?thesis by (fastforce simp: final-iff intro!: AAssRed1)}$ 
  qed
next
  case ( $WTrtALength \ E \ a \ T \ l$ )
  show ?case
  proof (cases  $\text{final } a$ )
    case True

```

```

note  $wta = \langle P, E, h \vdash a : T[] \rangle$ 
thus ?thesis
proof(rule finalRefE[OF - - True])
  show is-refT (T[]) by simp
next
  assume  $a = \text{null}$ 
  thus ?thesis by(fastforce intro: RedALengthNull)
next
  fix ad U
  assume  $a = \text{addr } ad$  and  $T[] = U[]$ 
  with  $wta$  show ?thesis by(fastforce intro: RedALength)
next
  fix ad
  assume  $a = \text{Throw } ad$ 
  thus ?thesis by (fastforce intro: ALengthThrow)
qed simp
next
  case False
  from  $\langle \mathcal{D} (a \cdot \text{length}) \lfloor \text{dom } l \rfloor \rangle$  have  $\mathcal{D} a \lfloor \text{dom } l \rfloor$  by simp
  with  $\text{False } \langle \llbracket \mathcal{D} a \lfloor \text{dom } l \rfloor; \neg \text{final } a \rrbracket \implies \exists e' s' ta. \text{extTA}, P, t \vdash \langle a, (h, l) \rangle -ta \rightarrow \langle e', s' \rangle$ 
  obtain  $e' s' ta$  where  $\text{extTA}, P, t \vdash \langle a, (h, l) \rangle -ta \rightarrow \langle e', s' \rangle$  by blast
  thus ?thesis by(blast intro: ALengthRed)
qed
next
  case (WTrtALengthNT E a T l)
  show ?case
  proof(cases final a)
    case True
    note  $wta = \langle P, E, h \vdash a : NT \rangle$ 
    thus ?thesis
    proof(rule finalRefE[OF - - True])
      show is-refT NT by simp
    next
      assume  $a = \text{null}$ 
      thus ?thesis by(blast intro: RedALengthNull)
    next
      fix ad
      assume  $a = \text{Throw } ad$ 
      thus ?thesis by(blast intro: ALengthThrow)
    qed simp-all
  next
    case False
    from  $\langle \mathcal{D} (a \cdot \text{length}) \lfloor \text{dom } l \rfloor \rangle$  have  $\mathcal{D} a \lfloor \text{dom } l \rfloor$  by simp
    with  $\text{False } \langle \llbracket \mathcal{D} a \lfloor \text{dom } l \rfloor; \neg \text{final } a \rrbracket \implies \exists e' s' ta. \text{extTA}, P, t \vdash \langle a, (h, l) \rangle -ta \rightarrow \langle e', s' \rangle$ 
    obtain  $e' s' ta$  where  $\text{extTA}, P, t \vdash \langle a, (h, l) \rangle -ta \rightarrow \langle e', s' \rangle$  by blast
    thus ?thesis by(blast intro: ALengthRed)
  qed
next
  case (WTrtFAcc E e U C F T fm D l)
  have  $wte: P, E, h \vdash e : U$ 
  and icto: class-type-of'  $U = \lfloor C \rfloor$ 
  and field:  $P \vdash C$  has  $F:T$  (fm) in D by fact+
  show ?case
  proof cases

```

```

assume final e
with wte icto show ?thesis
proof (cases rule: final-addrE)
  case (addr a)
    with wte obtain hU where ty: typeof-addr h a = [hU] U = ty-of-htype hU by auto
    with icto field have P, h ⊢ a@CField D F : T by(auto intro: addr-loc-type.intros)
    from heap-read-total[OF hconf this]
    obtain v where heap-read h a (CField D F) v by blast
    with addr ty show ?thesis by(fastforce intro: RedFAcc)
  next
    fix a assume e = Throw a
    thus ?thesis by(fastforce intro: FAccThrow)
  qed
next
  assume  $\neg$  final e with WTrtFAcc show ?thesis
    by(fastforce intro!: FAccRed)
  qed
next
  case (WTrtFAccNT E e F D T l)
  show ?case
  proof cases
    assume final e — e is null or throw
    with WTrtFAccNT show ?thesis
      by(fastforce simp: final-iff intro: RedFAccNull FAccThrow)
  next
    assume  $\neg$  final e — e reduces by IH
    with WTrtFAccNT show ?thesis by simp (fast intro: FAccRed)
  qed
next
  case (WTrtFAss E e1 U C F T fm D e2 T2 l)
  have wte1: P, E, h ⊢ e1 : U
    and icto: class-type-of' U = [C]
    and field: P ⊢ C has F:T (fm) in D by fact+
  show ?case
  proof cases
    assume final e1
    with wte1 icto show ?thesis
    proof (rule final-addrE)
      fix a assume e1: e1 = addr a
      show ?thesis
    proof cases
      assume final e2
      thus ?thesis
    proof (rule finalE)
      fix v assume e2: e2 = Val v
      from wte1 field icto e1 have adal: P, h ⊢ a@CField D F : T
        by(auto intro: addr-loc-type.intros)
      from e2  $\langle P \vdash T2 \leq T \rangle$   $\langle P, E, h \vdash e2 : T2 \rangle$ 
      have P, h ⊢ v : ≤ T by(auto simp add: conf-def)
      from heap-write-total[OF hconf adal this] obtain h'
        where heap-write h a (CField D F) v h' ..
      with wte1 field e1 e2 show ?thesis
        by(fastforce intro: RedFAss)
  next

```

```

    fix a assume e2 = Throw a
    thus ?thesis using e1 by(fastforce intro:FAssThrow2)
  qed
next
  assume  $\neg$  final e2 with WTrtFAss  $\langle$ final e1 $\rangle$  e1 show ?thesis
    by simp (fast intro!:FAssRed2)
  qed
next
  fix a assume e1 = Throw a
  thus ?thesis by(fastforce intro:FAssThrow1)
  qed
next
  assume  $\neg$  final e1 with WTrtFAss show ?thesis
    by(simp del: split-paired-Ex)(blast intro!:FAssRed1)
  qed
next
  case (WTrtFAssNT E e1 e2 T2 F D l)
  show ?case
  proof cases
    assume final e1 — e1 is null or throw
    show ?thesis
    proof cases
      assume final e2 — e2 is Val or throw
      with WTrtFAssNT  $\langle$ final e1 $\rangle$  show ?thesis
        by(fastforce simp:final-iff intro: RedFAssNull FAssThrow1 FAssThrow2)
    next
      assume  $\neg$  final e2 — e2 reduces by IH
      with WTrtFAssNT  $\langle$ final e1 $\rangle$  show ?thesis
        by (fastforce simp:final-iff intro!:FAssRed2 FAssThrow1)
    qed
  next
    assume  $\neg$  final e1 — e1 reduces by IH
    with WTrtFAssNT show ?thesis by (fastforce intro:FAssRed1)
  qed
next
  case (WTrtCAS E e1 U C F T fm D e2 T2 e3 T3)
  show ?case
  proof(cases final e1)
    case e1: True
    with WTrtCAS.hyps(1,3) show ?thesis
    proof(rule final-addrE)
      fix a
      assume e1: e1 = addr a
      with WTrtCAS.hyps(1) obtain hU
        where ty: typeof-addr h a =  $\lfloor$ hU $\rfloor$  U = ty-of-htype hU by auto
      with WTrtCAS.hyps(3,4) have adal: P, h  $\vdash$  a @ CField D F : T by(auto intro: addr-loc-type.intros)
      from heap-read-total[OF hconf this]
      obtain v where v: heap-read h a (CField D F) v by blast
      show ?thesis
    proof(cases final e2)
      case e2: True
      show ?thesis
    proof(cases final e3)
      case e3: True

```

```

consider (Val2) v2 where e2 = Val v2 | (Throw2) a2 where e2 = Throw a2
  using e2 by(auto simp add: final-iff)
then show ?thesis
proof(cases)
  case Val2
  consider (Succeed) v3 where e3 = Val v3 v2 = v
    | (Fail) v3 where e3 = Val v3 v2 ≠ v
    | (Throw3) a3 where e3 = Throw a3
  using e3 by(auto simp add: final-iff)
  then show ?thesis
  proof cases
    case Succeed
    with WTrtCAS.hyps(9,11) adal have  $P, h \vdash v3 \leq T$  by(auto simp add: conf-def)
    from heap-write-total[OF hconf adal this] obtain h'
      where heap-write h a (CField D F) v3 h' ..
    with Val2 e1 v Succeed show ?thesis
      by(auto intro: RedCASSucceed simp del: split-paired-Ex)
    next
    case Fail
    with Val2 e1 v show ?thesis
      by(auto intro: RedCASFail simp del: split-paired-Ex)
    next
    case Throw3
    then show ?thesis using e1 Val2 by(auto intro: CASThrow3 simp del: split-paired-Ex)
  qed
next
  case Throw2
  then show ?thesis using e1 by(auto intro: CASThrow2 simp del: split-paired-Ex)
  qed
next
  case False
  with WTrtCAS e1 e2 show ?thesis
    by(fastforce simp del: split-paired-Ex simp add: final-iff intro: CASRed3 CASThrow2)
  qed
next
  case False
  with WTrtCAS e1 show ?thesis
    by(fastforce intro: CASRed2 CASThrow2 simp del: split-paired-Ex)
  qed
qed(fastforce intro: CASThrow)
next
  case False
  then show ?thesis using WTrtCAS by(fastforce intro: CASRed1)
  qed
next
  case (WTrtCASNT E e1 e2 T2 e3 T3 D F)
  note [simp del] = split-paired-Ex
  show ?case
  proof(cases final e1)
    case e1: True
    show ?thesis
  proof(cases final e2)
    case e2: True
    show ?thesis
  
```

```

proof(cases final e3)
  case True
  with e1 e2 WTrtCASNT show ?thesis
    by(fastforce simp add: final-iff intro: CASNull CASThrow CASThrow2 CASThrow3)
next
  case False
  with e1 e2 WTrtCASNT show ?thesis
    by(fastforce simp add: final-iff intro: CASRed3 CASThrow CASThrow2)
qed
next
  case False
  with e1 WTrtCASNT show ?thesis
    by(fastforce simp add: final-iff intro: CASRed2 CASThrow)
qed
next
  case False
  with WTrtCASNT show ?thesis
    by(fastforce simp add: final-iff intro: CASRed1)
qed
next
case (WTrtCall E e U C M Ts T meth D es Ts' l)
have wte:  $P, E, h \vdash e : U$ 
  and icto: class-type-of'  $U = \lfloor C \rfloor$  by fact+
have IHe:  $\bigwedge l. \llbracket \mathcal{D} \ e \ \lfloor \text{dom } l \rfloor; \neg \text{final } e \rrbracket$ 
   $\implies \exists e' s' \text{ tas. } \text{extTA}, P, t \vdash \langle e, (h, l) \rangle \text{ --tas--} \langle e', s' \rangle$  by fact
have sees:  $P \vdash C \text{ sees } M: Ts \rightarrow T = \text{meth in } D$  by fact
have wtes:  $P, E, h \vdash \text{es} \text{ [:] } Ts'$  by fact
have IHes:  $\bigwedge l. \llbracket \mathcal{D} \text{ es } \lfloor \text{dom } l \rfloor; \neg \text{finals es} \rrbracket \implies \exists es' s' \text{ ta. } \text{extTA}, P, t \vdash \langle \text{es}, (h, l) \rangle \text{ [-ta--]} \langle es', s' \rangle$ 
by fact
have subtype:  $P \vdash Ts' \lfloor \leq \rfloor Ts$  by fact
have dae:  $\mathcal{D} \ (e \cdot M(\text{es})) \ \lfloor \text{dom } l \rfloor$  by fact
show ?case
proof(cases final e)
  assume fine: final e
  with wte icto show ?thesis
  proof (rule final-addrE)
    fix a assume e-addr:  $e = \text{addr } a$ 
    show ?thesis
    proof(cases  $\exists \text{vs. es} = \text{map Val vs}$ )
      assume es:  $\exists \text{vs. es} = \text{map Val vs}$ 
      from wte e-addr obtain hU where ha: typeof-addr  $h \ a = \lfloor hU \rfloor$   $U = \text{ty-of-htype } hU$  by(auto)
      have length es = length Ts' using wtes by(auto simp add: WTrts-conv-list-all2 dest: list-all2-lengthD)
      moreover from subtype have length Ts' = length Ts by(auto dest: list-all2-lengthD)
      ultimately have esTs: length es = length Ts by(auto)
      show ?thesis
    proof(cases meth)
      case (Some pnsbody)
      with esTs e-addr ha sees subtype es sees-wf-mdecl[OF wf sees] icto
      show ?thesis by(cases pnsbody) (fastforce intro!: RedCall simp:list-all2-iff wf-mdecl-def)
    next
      case None
      with sees wf have  $D \cdot M(Ts) :: T$  by(auto intro: sees-wf-native)
      moreover from es obtain vs where vs:  $\text{es} = \text{map Val vs}$  ..
      with wtes have tyes:  $\text{map typeof}_h \text{vs} = \text{map Some } Ts'$  by simp

```

```

with ha  $\langle D \cdot M(Ts) :: T \rangle$  icto sees None
have  $P, h \vdash a \cdot M(vs) : T$  using subtype by(auto simp add: external-WT'-iff)
from external-call-progress[OF wf this hconf, of t] obtain ta va h'
  where  $P, t \vdash \langle a \cdot M(vs), h \rangle \rightarrow_{ta} \langle va, h' \rangle$  by blast
thus ?thesis using ha icto None sees vs e-addr
  by(fastforce intro: RedCallExternal simp del: split-paired-Ex)
qed
next
assume  $\neg(\exists vs. es = \text{map Val } vs)$ 
hence not-all-Val:  $\neg(\forall e \in \text{set } es. \exists v. e = \text{Val } v)$ 
  by(simp add:ex-map-conv)
let ?ves = takeWhile  $(\lambda e. \exists v. e = \text{Val } v)$  es
let ?rest = dropWhile  $(\lambda e. \exists v. e = \text{Val } v)$  es
let ?ex = hd ?rest let ?rst = tl ?rest
from not-all-Val have nonempty: ?rest  $\neq []$  by auto
hence es:  $es = ?ves @ ?ex \# ?rst$  by simp
have  $\forall e \in \text{set } ?ves. \exists v. e = \text{Val } v$  by(fastforce dest:set-takeWhileD)
then obtain vs where ves:  $?ves = \text{map Val } vs$ 
  using ex-map-conv by blast
show ?thesis
proof cases
  assume final ?ex
  moreover from nonempty have  $\neg(\exists v. ?ex = \text{Val } v)$ 
    by(auto simp:neq-Nil-conv simp del:dropWhile-eq-Nil-conv)
    (simp add:dropWhile-eq-Cons-conv)
  ultimately obtain b where ex-Throw:  $?ex = \text{Throw } b$ 
    by(fast elim!:finalE)
  show ?thesis using e-addr es ex-Throw ves
    by(fastforce intro:CallThrowParams)
next
assume not-fin:  $\neg \text{final } ?ex$ 
have finals es = finals( $?ves @ ?ex \# ?rst$ ) using es
  by(rule arg-cong)
also have  $\dots = \text{finals}(?ex \# ?rst)$  using ves by simp
finally have finals es = finals( $?ex \# ?rst$ ) .
hence  $\neg \text{finals } es$  using not-finals-ConsI[OF not-fin] by blast
thus ?thesis using e-addr dae IHes by(fastforce intro!:CallParams)
qed
qed
next
fix a assume  $e = \text{Throw } a$ 
thus ?thesis by(fast intro!:CallThrowObj)
qed
next
assume  $\neg \text{final } e$ 
with WTrtCall show ?thesis by(simp del: split-paired-Ex)(blast intro!:CallObj)
qed
next
case (WTrtCallNT E e es Ts M T l)
have wte:  $P, E, h \vdash e : NT$  by fact
have IHes:  $\bigwedge l. [\![ \mathcal{D} \ e \ \lfloor \text{dom } l \rfloor; \neg \text{final } e \ ]\!] \implies \exists e' s' \text{ tas. } \text{extTA}, P, t \vdash \langle e, (h, l) \rangle \rightarrow_{\text{tas}} \langle e', s' \rangle$  by fact
have IHes:  $\bigwedge l. [\![ \mathcal{D} \ s \ \lfloor \text{dom } l \rfloor; \neg \text{finals } es \ ]\!] \implies \exists es' s' \text{ ta. } \text{extTA}, P, t \vdash \langle es, (h, l) \rangle \rightarrow_{[-\text{ta} \rightarrow]} \langle es', s' \rangle$ 
by fact

```



```

have wtes:  $P, E, h \vdash es$  [:]  $Ts$  by fact
have dae:  $\mathcal{D} (e \cdot M(es)) \lfloor \text{dom } l \rfloor$  by fact
show ?case
proof (cases final e)
  assume final e
  moreover
  { fix v assume  $e = \text{Val } v$ 
    hence  $e = \text{null}$  using WTrtCallNT by simp
    have ?case
    proof cases
      assume finals es
      moreover
      { fix vs assume  $es = \text{map } \text{Val } vs$ 
        with WTrtCallNT  $\langle e = \text{null} \rangle \langle \text{finals } es \rangle$  have ?thesis by (fastforce intro: RedCallNull) }
      moreover
      { fix vs a es' assume  $es = \text{map } \text{Val } vs @ \text{Throw } a \# es'$ 
        with WTrtCallNT  $\langle e = \text{null} \rangle \langle \text{finals } es \rangle$  have ?thesis by (fastforce intro: CallThrowParams)
      }
    }
  ultimately show ?thesis by (fastforce simp:finals-iff)
next
  assume  $\neg \text{finals } es$  —  $es$  reduces by IH
  with WTrtCallNT  $\langle e = \text{null} \rangle$  show ?thesis by (fastforce intro: CallParams)
qed
}
moreover
{ fix a assume  $e = \text{Throw } a$ 
  with WTrtCallNT have ?case by (fastforce intro: CallThrowObj) }
ultimately show ?thesis by (fastforce simp:final-iff)
next
  assume  $\neg \text{final } e$  —  $e$  reduces by IH
  with WTrtCallNT show ?thesis by (fastforce intro: CallObj)
qed
next
case (WTrtBlock E V T e T' vo l)
have IH:  $\bigwedge l. \llbracket \mathcal{D} e \lfloor \text{dom } l \rfloor; \neg \text{final } e \rrbracket$ 
   $\implies \exists e' s' \text{ tas. } \text{extTA}, P, t \vdash \langle e, (h, l) \rangle -\text{tas} \rightarrow \langle e', s' \rangle$ 
and D:  $\mathcal{D} \{ V:T=vo; e \} \lfloor \text{dom } l \rfloor$  by fact+
show ?case
proof cases
  assume final e
  thus ?thesis
  proof (rule finalE)
    fix v assume  $e = \text{Val } v$  thus ?thesis by (fast intro: RedBlock)
  next
    fix a assume  $e = \text{Throw } a$ 
    thus ?thesis by (fast intro: BlockThrow)
  qed
next
  assume not-fin:  $\neg \text{final } e$ 
  from D have De:  $\mathcal{D} e \lfloor \text{dom}(l(V:=vo)) \rfloor$  by (auto simp add: hyperset-defs)
  from IH[OF De not-fin]
  obtain  $h' l' e' \text{ tas}$  where red:  $\text{extTA}, P, t \vdash \langle e, (h, l(V:=vo)) \rangle -\text{tas} \rightarrow \langle e', (h', l') \rangle$ 
  by auto
  thus ?thesis by (blast intro: BlockRed)

```

```

qed
next
case (WTrtSynchronized E o' T e T' l)
note wto = ⟨P, E, h ⊢ o' : T⟩
note IHe = ⟨∧ l. [D e [dom l]; ¬ final e] ⇒ ∃ e' s' tas. extTA, P, t ⊢ ⟨e, (h, l)⟩ -tas→ ⟨e', s'⟩⟩
note wte = ⟨P, E, h ⊢ e : T'⟩
note IHo = ⟨∧ l. [D o' [dom l]; ¬ final o'] ⇒ ∃ e' s' tas. extTA, P, t ⊢ ⟨o', (h, l)⟩ -tas→ ⟨e', s'⟩⟩
note refT = ⟨is-refT T⟩
note dae = ⟨D (sync(o') e) [dom l]⟩
show ?case
proof(cases final o')
  assume fino: final o'
  thus ?thesis
proof (rule finalE)
  fix v
  assume oval: o' = Val v
  with wto refT show ?thesis
proof(cases v)
  assume vnull: v = Null
  with oval vnull show ?thesis
    by(fastforce intro: SynchronizedNull)
next
  fix ad
  assume vaddr: v = Addr ad
  thus ?thesis using oval
    by(fastforce intro: LockSynchronized)
qed(auto elim: refTE)
next
  fix a
  assume othrow: o' = Throw a
  thus ?thesis by(fastforce intro: SynchronizedThrow1)
qed
next
  assume nfino: ¬ final o'
  with dae IHo show ?case by(fastforce intro: SynchronizedRed1)
qed
next
case (WTrtInSynchronized E a T e T' l)
show ?case
proof(cases final e)
  case True thus ?thesis
    by(fastforce elim!: finalE intro: UnlockSynchronized SynchronizedThrow2)
next
  case False
  moreover from ⟨D (insync(a) e) [dom l]⟩ have D e [dom l] by simp
  moreover note IHe = ⟨∧ l. [D e [dom l]; ¬ final e] ⇒ ∃ e' s' tas. extTA, P, t ⊢ ⟨e, (h, l)⟩ -tas→
  ⟨e', s'⟩⟩
  ultimately show ?thesis by(fastforce intro: SynchronizedRed2)
qed
next
case (WTrtSeq E e1 T1 e2 T2 l)
show ?case
proof cases
  assume final e1

```

```

    thus ?thesis
      by(fast elim:finalE intro:intro:RedSeq SeqThrow)
next
  assume  $\neg$  final e1 with WTrtSeq show ?thesis
    by(simp del: split-paired-Ex)(blast intro!:SeqRed)
qed
next
  case (WTrtCond E e e1 T1 e2 T2 T l)
  have wt:  $P, E, h \vdash e : \text{Boolean}$  by fact
  show ?case
  proof cases
    assume final e
    thus ?thesis
    proof (rule finalE)
      fix v assume val:  $e = \text{Val } v$ 
      then obtain b where  $v: v = \text{Bool } b$  using wt by auto
      show ?thesis
      proof (cases b)
        case True with val v show ?thesis by(fastforce intro:RedCondT)
      next
        case False with val v show ?thesis by(fastforce intro:RedCondF)
      qed
    next
      fix a assume  $e = \text{Throw } a$ 
      thus ?thesis by(fast intro:CondThrow)
    qed
  next
    assume  $\neg$  final e with WTrtCond show ?thesis
    by simp (fast intro:CondRed)
  qed
next
  case WTrtWhile show ?case by(fast intro:RedWhile)
next
  case (WTrtThrow E e T T' l)
  show ?case
  proof cases
    assume final e — Then e must be throw or null
    thus ?thesis
    proof(induct rule: finalE)
      case (Val v)
      with  $\langle P \vdash T \leq \text{Class Throwable} \rangle \langle \neg \text{final } (\text{throw } e) \rangle \langle P, E, h \vdash e : T \rangle$ 
      have  $v = \text{Null}$  by(cases v)(auto simp add: final-iff widen-Class)
      thus ?thesis using Val by(fastforce intro: RedThrowNull)
    next
      case (Throw a)
      thus ?thesis by(fastforce intro: ThrowThrow)
    qed
  next
    assume  $\neg$  final e — Then e must reduce
    with WTrtThrow show ?thesis by simp (blast intro:ThrowRed)
  qed
next
  case (WTrtTry E e1 T1 V C e2 T2 l)
  have wt1:  $P, E, h \vdash e1 : T1$  by fact

```

```

show ?case
proof cases
  assume final e1
  thus ?thesis
proof (rule finalE)
  fix v assume e1 = Val v
  thus ?thesis by (fast intro: RedTry)
next
  fix a
  assume e1-Throw: e1 = Throw a
  with wt1 obtain D where ha: typeof-addr h a = [Class-type D]
  by (auto simp add: widen-Class)
  thus ?thesis using e1-Throw
  by (cases P ⊢ D ≤* C) (fastforce intro: RedTryCatch RedTryFail)+
qed
next
  assume ¬ final e1
  with WTrtTry show ?thesis by simp (fast intro: TryRed)
qed
next
  case WTrtNil thus ?case by simp
next
  case (WTrtCons E e T es Ts)
  have IHe:  $\bigwedge l. \llbracket \mathcal{D} \ e \ \lfloor \text{dom } l \rfloor; \neg \text{final } e \rrbracket$ 
     $\implies \exists e' \ s' \ ta. \text{extTA}, P, t \vdash \langle e, (h, l) \rangle \dashv ta \rightarrow \langle e', s' \rangle$ 
  and IHes:  $\bigwedge l. \llbracket \mathcal{D} s \ es \ \lfloor \text{dom } l \rfloor; \neg \text{finals } es \rrbracket$ 
     $\implies \exists es' \ s' \ ta. \text{extTA}, P, t \vdash \langle es, (h, l) \rangle \dashv ta \rightarrow \langle es', s' \rangle$ 
  and D:  $\mathcal{D} s \ (e \# es) \ \lfloor \text{dom } l \rfloor$  and not-fins:  $\neg \text{finals}(e \# es)$  by fact+
  have De:  $\mathcal{D} \ e \ \lfloor \text{dom } l \rfloor$  and Des:  $\mathcal{D} s \ es \ (\lfloor \text{dom } l \rfloor \sqcup \mathcal{A} \ e)$ 
    using D by auto
  show ?case
proof cases
  assume final e
  thus ?thesis
proof (rule finalE)
  fix v assume e: e = Val v
  hence Des':  $\mathcal{D} s \ es \ \lfloor \text{dom } l \rfloor$  using De Des by auto
  have not-fins-tl:  $\neg \text{finals } es$  using not-fins e by simp
  show ?thesis using e IHes[OF Des' not-fins-tl]
  by (blast intro!: ListRed2)
next
  fix a assume e = Throw a
  hence False using not-fins by simp
  thus ?thesis ..
qed
next
  assume ¬ final e
  with IHe[OF De] show ?thesis by (fast intro!: ListRed1)
qed
qed
end

```

## 4.12 Preservation of definite assignment

**theory** *DefAssPreservation*

**imports**

*DefAss*

*JWellForm*

*SmallStep*

**begin**

Preservation of definite assignment more complex and requires a few lemmas first.

**lemma** *D-extRetJ* [intro!]:  $\mathcal{D} \ e \ A \implies \mathcal{D} \ (\text{extRet2J } e \ va) \ A$

**by** (cases va) simp-all

**lemma** *blocks-defass* [iff]:  $\bigwedge A. \llbracket \text{length } Vs = \text{length } Ts; \text{length } vs = \text{length } Ts \rrbracket \implies$

$\mathcal{D} \ (\text{blocks } Vs \ Ts \ vs \ e) \ A = \mathcal{D} \ e \ (A \sqcup \llbracket \text{set } Vs \rrbracket)$

**context** *J-heap-base* **begin**

**lemma** *red-lA-incr*:  $\text{extTA}, P, t \vdash \langle e, s \rangle \rightarrow \langle e', s' \rangle \implies \llbracket \text{dom } (lcl \ s) \rrbracket \sqcup \mathcal{A} \ e \sqsubseteq \llbracket \text{dom } (lcl \ s') \rrbracket \sqcup \mathcal{A} \ e'$

**and** *reds-lA-incr*:  $\text{extTA}, P, t \vdash \langle es, s \rangle \rightarrow \langle es', s' \rangle \implies \llbracket \text{dom } (lcl \ s) \rrbracket \sqcup \mathcal{A} s \ es \sqsubseteq \llbracket \text{dom } (lcl \ s') \rrbracket \sqcup \mathcal{A} s \ es'$

**apply** (induct rule:red-reds.inducts)

**apply** (simp-all del:fun-upd-apply add:hyperset-defs)

**apply** blast

**apply** blast

**apply** blast

**apply** blast

**apply** blast

**apply** blast

**apply** blast

**apply** blast

**apply** blast

**apply** blast

**apply** blast

**apply** blast

**apply** blast

**apply** blast

**apply** (force split: if-split-asm)

**apply** blast

**apply** blast

**apply** blast

**apply** blast

**apply** blast

**apply** (blast dest: red-lcl-incr)

**apply** (blast dest: red-lcl-incr)

**by** blast+

**end**

Now preservation of definite assignment.

**declare** *hyperUn-comm* [simp del]

**declare** *hyperUn-leftComm* [simp del]

**context** *J-heap-base* **begin**

```

lemma assumes wf: wf-J-prog P
shows red-preserves-defass: extTA,P,t ⊢ ⟨e,s⟩ -ta→ ⟨e',s'⟩ ⇒  $\mathcal{D} \ e \ [dom \ (lcl \ s)] \Rightarrow \mathcal{D} \ e' \ [dom \ (lcl \ s')]$ 
and reds-preserves-defass: extTA,P,t ⊢ ⟨es,s⟩ [-ta→] ⟨es',s'⟩ ⇒  $\mathcal{D}s \ es \ [dom \ (lcl \ s)] \Rightarrow \mathcal{D}s \ es' \ [dom \ (lcl \ s')]$ 
proof (induction rule:red-reds.inducts)
  case BinOpRed1 thus ?case by (auto elim!: D-mono[OF red-lA-incr])
next
  case AAccRed1 thus ?case by (auto elim!: D-mono[OF red-lA-incr])
next
  case AAssRed1 thus ?case by(auto intro: red-lA-incr sqUn-lem D-mono)
next
  case AAssRed2 thus ?case by (auto elim!: D-mono[OF red-lA-incr])
next
  case FAssRed1 thus ?case by (auto elim!: D-mono[OF red-lA-incr])
next
  case CASRed1 thus ?case by(auto intro: red-lA-incr sqUn-lem D-mono)
next
  case CASRed2 thus ?case by (auto elim!: D-mono[OF red-lA-incr])
next
  case CallObj thus ?case by (auto elim!: Ds-mono[OF red-lA-incr])
next
  case CallParams thus ?case by(auto elim!: Ds-mono[OF red-lA-incr])
next
  case RedCall thus ?case by(auto dest!: sees-wf-mdecl[OF wf] simp:wf-mdecl-def elim!:D-mono')
next
  case BlockRed thus ?case
    by(auto simp:hyperset-defs elim!:D-mono' simp del:fun-upd-apply split: if-split-asm)
next
  case SynchronizedRed1 thus ?case by(auto elim!: D-mono[OF red-lA-incr])
next
  case SeqRed thus ?case by (auto elim!: D-mono[OF red-lA-incr])
next
  case CondRed thus ?case by (auto elim!: D-mono[OF red-lA-incr])
next
  case TryRed thus ?case
    by (fastforce dest:red-lcl-incr intro:D-mono' simp:hyperset-defs)
next
  case RedWhile thus ?case by(auto simp:hyperset-defs elim!:D-mono')
next
  case ListRed1 thus ?case by (auto elim!: Ds-mono[OF red-lA-incr])
qed (auto simp:hyperset-defs)

end

end

```

## 4.13 Type Safety Proof

```

theory TypeSafe
imports
  Progress

```

*DefAssPreservation*  
**begin**

### 4.13.1 Basic preservation lemmas

First two easy preservation lemmas.

**theorem** (in *J-conf-read*)  
**shows** *red-preserves-hconf*:  
 $\llbracket \text{extTA}, P, t \vdash \langle e, s \rangle \rightarrow \langle e', s' \rangle; P, E, hp\ s \vdash e : T; hconf\ (hp\ s) \rrbracket \implies hconf\ (hp\ s')$   
**and** *reds-preserves-hconf*:  
 $\llbracket \text{extTA}, P, t \vdash \langle es, s \rangle \rightarrow \langle es', s' \rangle; P, E, hp\ s \vdash es\ [:]\ Ts; hconf\ (hp\ s) \rrbracket \implies hconf\ (hp\ s')$   
**proof** (*induct arbitrary: T E and Ts E rule: red-reds.inducts*)  
**case** *RedNew* **thus** ?case  
**by**(*auto intro: hconf-heap-ops-mono*)  
**next**  
**case** *RedNewFail* **thus** ?case  
**by**(*auto intro: hconf-heap-ops-mono*)  
**next**  
**case** *RedNewArray* **thus** ?case  
**by**(*auto intro: hconf-heap-ops-mono*)  
**next**  
**case** *RedNewArrayFail* **thus** ?case  
**by**(*auto intro: hconf-heap-ops-mono*)  
**next**  
**case** (*RedAAss* *h a U n i v U' h' l*)  
**from**  $\langle sint\ i < int\ n \rangle \langle 0 \leq i \rangle$   
**have**  $nat\ (sint\ i) < n$   
**by** (*simp add: word-sle-eq nat-less-iff*)  
**thus** ?case **using** *RedAAss*  
**by**(*fastforce elim: hconf-heap-write-mono intro: addr-loc-type.intros simp add: conf-def*)  
**next**  
**case** *RedFAss* **thus** ?case  
**by**(*fastforce elim: hconf-heap-write-mono intro: addr-loc-type.intros simp add: conf-def*)  
**next**  
**case** *RedCASSucceed* **thus** ?case  
**by**(*fastforce elim: hconf-heap-write-mono intro: addr-loc-type.intros simp add: conf-def*)  
**next**  
**case** (*RedCallExternal* *s a U M Ts T' D vs ta va h' ta' e' s'*)  
**hence**  $P, hp\ s \vdash a \cdot M\ (vs) : T$   
**by**(*fastforce simp add: external-WT'-iff dest: sees-method-fun*)  
**with** *RedCallExternal* **show** ?case **by**(*auto dest: external-call-hconf*)  
**qed** *auto*

**theorem** (in *J-heap*) *red-preserves-lconf*:  
 $\llbracket \text{extTA}, P, t \vdash \langle e, s \rangle \rightarrow \langle e', s' \rangle; P, E, hp\ s \vdash e : T; P, hp\ s \vdash lcl\ s\ (: \leq)\ E \rrbracket \implies P, hp\ s' \vdash lcl\ s'\ (: \leq)\ E$   
**and** *reds-preserves-lconf*:  
 $\llbracket \text{extTA}, P, t \vdash \langle es, s \rangle \rightarrow \langle es', s' \rangle; P, E, hp\ s \vdash es\ [:]\ Ts; P, hp\ s \vdash lcl\ s\ (: \leq)\ E \rrbracket \implies P, hp\ s' \vdash lcl\ s'\ (: \leq)\ E$   
**proof**(*induct arbitrary: T E and Ts E rule: red-reds.inducts*)  
**case** *RedNew* **thus** ?case  
**by**(*fastforce intro: lconf-hext hext-heap-ops simp del: fun-upd-apply*)  
**next**  
**case** *RedNewFail* **thus** ?case  
**by**(*auto intro: lconf-hext hext-heap-ops simp del: fun-upd-apply*)

```

next
  case RedNewArray thus ?case
    by(fastforce intro:lconf-hext hext-heap-ops simp del: fun-upd-apply)
next
  case RedNewArrayFail thus ?case
    by(fastforce intro:lconf-hext hext-heap-ops simp del: fun-upd-apply)
next
  case RedLAss thus ?case
    by(fastforce elim: lconf-upd simp add: conf-def simp del: fun-upd-apply)
next
  case RedAAss thus ?case
    by(fastforce intro:lconf-hext hext-heap-ops simp del: fun-upd-apply)
next
  case RedFAss thus ?case
    by(fastforce intro:lconf-hext hext-heap-ops simp del: fun-upd-apply)
next
  case RedCASSucceed thus ?case
    by(fastforce intro:lconf-hext hext-heap-ops simp del: fun-upd-apply)
next
  case (BlockRed e h x V vo ta e' h' x' T T' E)
    note red = ⟨extTA,P,t ⊢ ⟨e,(h, x(V := vo))⟩ -ta→ ⟨e',(h', x')⟩⟩
    note IH = ⟨∧ T E. ⟦P,E,hp (h, x(V := vo)) ⊢ e : T;
      P,hp (h, x(V := vo)) ⊢ lcl (h, x(V := vo)) (⋮≤) E⟧
      ⇒ P,hp (h', x') ⊢ lcl (h', x') (⋮≤) E⟩
    note wt = ⟨P,E,hp (h, x) ⊢ {V:T=vo; e} : T'⟩
    note lconf = ⟨P,hp (h, x) ⊢ lcl (h, x) (⋮≤) E⟩
    from lconf-hext[OF lconf[simplified] red-hext-incr[OF red, simplified]]
    have P,h' ⊢ x (⋮≤) E .
    moreover from wt have P,E(V↦T),h ⊢ e : T' by(cases vo, auto)
    moreover from lconf wt have P,h ⊢ x(V := vo) (⋮≤) E(V ↦ T)
    by(cases vo)(simp add: lconf-def,auto intro: lconf-upd2 simp add: conf-def)
    ultimately have P,h' ⊢ x' (⋮≤) E(V↦T)
    by(auto intro: IH[simplified])
    with ⟨P,h' ⊢ x (⋮≤) E⟩ show ?case
    by(auto simp add: lconf-def split: if-split-asm)
next
  case (RedCallExternal s a U M Ts T' D vs ta va h' ta' e' s')
    from ⟨P,t ⊢ ⟨a•M(vs),hp s⟩ -ta→ext ⟨va,h'⟩⟩ have hp s ⊑ h' by(rule red-external-hext)
    with ⟨s' = (h', lcl s)⟩ ⟨P,hp s ⊢ lcl s (⋮≤) E⟩ show ?case by(auto intro: lconf-hext)
qed auto

```

Combining conformance of heap and local variables:

**definition** (in *J-heap-conf-base*) *sconf* :: *env* ⇒ (*'addr*, *'heap*) *Jstate* ⇒ *bool* (*-* ⊢ *-* √ [51,51]50)  
 where  $E \vdash s \sqrt{\quad} \equiv \text{let } (h,l) = s \text{ in } h\text{conf } h \wedge P,h \vdash l (\cdot \leq) E \wedge \text{preallocated } h$

**context** *J-conf-read* **begin**

**lemma** *red-preserves-sconf*:

⟦ extTA,P,t ⊢ ⟨e,s⟩ -tas→ ⟨e',s'⟩; P,E,hp s ⊢ e : T; E ⊢ s √ ⟧ ⇒ E ⊢ s' √  
**apply**(auto dest: red-preserves-hconf red-preserves-lconf simp add:sconf-def)  
**apply**(fastforce dest: red-hext-incr intro: preallocated-hext)  
**done**

**lemma** *reds-preserves-sconf*:



$\llbracket \text{extTA}, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; P, E, hp \ s \vdash es [:] Ts; E \vdash s \checkmark \rrbracket \implies E \vdash s' \checkmark$   
**apply**(*auto dest: reds-preserves-hconf reds-preserves-lconf simp add: sconf-def*)  
**apply**(*fastforce dest: reds-heap-incr intro: preallocated-heap*)  
**done**

**end**

**lemma** (*in J-heap-base*) *wt-external-call*:

$\llbracket \text{conf-extRet } P \ h \ va \ T; P, E, h \vdash e : T \rrbracket \implies \exists T'. P, E, h \vdash \text{extRet2J } e \ va : T' \wedge P \vdash T' \leq T$   
**by**(*cases va*)(*auto simp add: conf-def*)

### 4.13.2 Subject reduction

**theorem** (*in J-conf-read*) **assumes** *wf: wf-J-prog P*

**shows** *subject-reduction*:

$\llbracket \text{extTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; E \vdash s \checkmark; P, E, hp \ s \vdash e : T; P, hp \ s \vdash t \checkmark \rrbracket$   
 $\implies \exists T'. P, E, hp \ s' \vdash e' : T' \wedge P \vdash T' \leq T$

**and** *subjects-reduction*:

$\llbracket \text{extTA}, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; E \vdash s \checkmark; P, E, hp \ s \vdash es [:] Ts; P, hp \ s \vdash t \checkmark \rrbracket$   
 $\implies \exists Ts'. P, E, hp \ s' \vdash es' [:] Ts' \wedge P \vdash Ts' [\leq] Ts$

**proof** (*induct arbitrary: T E and Ts E rule:red-reds.inducts*)

**case** *RedNew*

**thus** *?case by*(*auto dest: allocate-SomeD*)

**next**

**case** *RedNewFail* **thus** *?case unfolding sconf-def*

**by**(*fastforce intro:typeof-OutOfMemory preallocated-heap-ops simp add: xcpt-subcls-Throwable[OF - wf]*)

**next**

**case** *NewArrayRed*

**thus** *?case by fastforce*

**next**

**case** *RedNewArray*

**thus** *?case by*(*auto dest: allocate-SomeD*)

**next**

**case** *RedNewArrayNegative* **thus** *?case unfolding sconf-def*

**by**(*fastforce intro: preallocated-heap-ops simp add: xcpt-subcls-Throwable[OF - wf]*)

**next**

**case** *RedNewArrayFail* **thus** *?case unfolding sconf-def*

**by**(*fastforce intro:typeof-OutOfMemory preallocated-heap-ops simp add: xcpt-subcls-Throwable[OF - wf]*)

**next**

**case** (*CastRed e s ta e' s' C T E*)

**have** *esse: extTA, P, t*  $\vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle$

**and** *IH:  $\bigwedge T E. \llbracket E \vdash s \checkmark; P, E, hp \ s \vdash e : T; P, hp \ s \vdash t \checkmark \rrbracket \implies \exists T'. P, E, hp \ s' \vdash e' : T' \wedge P \vdash T' \leq T$*

**and** *hconf: E*  $\vdash s \checkmark$

**and** *wtc: P, E, hp*  $s \vdash \text{Cast } C \ e : T$  **by** *fact+*

**thus** *?case*

**proof**(*clarsimp*)

**fix** *T'*

**assume** *wte: P, E, hp*  $s \vdash e : T'$  *is-type P C*

**from** *wte* **and** *hconf* **and** *IH* **and**  $\langle P, hp \ s \vdash t \checkmark \rangle$  **have**  $\exists U. P, E, hp \ s' \vdash e' : U \wedge P \vdash U \leq T'$

**by** *simp*

**then obtain** *U* **where** *wtee: P, E, hp*  $s' \vdash e' : U$  **and** *UsTT: P*  $\vdash U \leq T'$  **by** *blast*

```

    from wtee ⟨is-type P C⟩ have P,E,hp s' ⊢ Cast C e' : C by(rule WTrtCast)
    thus ∃ T'. P,E,hp s' ⊢ Cast C e' : T' ∧ P ⊢ T' ≤ C by blast
qed
next
  case RedCast thus ?case
    by(clarsimp simp add: is-refT-def)
next
  case RedCastFail thus ?case unfolding sconf-def
    by(fastforce simp add: xcpt-subcls-Throwable[OF - wf])
next
  case (InstanceOfRed e s ta e' s' U T E)
  have IH: ∧ T E. [E ⊢ s √; P,E,hp s ⊢ e : T; P,hp s ⊢ t √t] ⇒ ∃ T'. P,E,hp s' ⊢ e' : T' ∧ P ⊢
    T' ≤ T
    and hconf: E ⊢ s √
    and wtc: P,E,hp s ⊢ e instanceof U : T
    and tconf: P,hp s ⊢ t √t by fact+
  from wtc obtain T' where P,E,hp s ⊢ e : T' by auto
  from IH[OF hconf this tconf] obtain T'' where P,E,hp s' ⊢ e' : T'' by auto
  with wtc show ?case by auto
next
  case RedInstanceOf thus ?case
    by(clarsimp)
next
  case (BinOpRed1 e1 s ta e1' s' bop e2 T E)
  have red: extTA,P,t ⊢ ⟨e1, s⟩ -ta→ ⟨e1', s'⟩
  and IH: ∧ T E. [E ⊢ s √; P,E,hp s ⊢ e1:T; P,hp s ⊢ t √t]
    ⇒ ∃ U. P,E,hp s' ⊢ e1' : U ∧ P ⊢ U ≤ T
  and conf: E ⊢ s √ and wt: P,E,hp s ⊢ e1 «bop» e2 : T
  and tconf: P,hp s ⊢ t √t by fact+
  from wt obtain T1 T2 where wt1: P,E,hp s ⊢ e1 : T1
    and wt2: P,E,hp s ⊢ e2 : T2 and wtbp: P ⊢ T1 «bop» T2 : T by auto
  from IH[OF conf wt1 tconf] obtain T1' where wt1': P,E,hp s' ⊢ e1' : T1'
    and sub: P ⊢ T1' ≤ T1 by blast
  from WTrt-binop-widen-mono[OF wtbp sub widen-refl]
  obtain T' where wtbp': P ⊢ T1' «bop» T2 : T' and sub': P ⊢ T' ≤ T by blast
  from wt1' WTrt-hest-mono[OF wt2 red-hest-incr[OF red]] wtbp'
  have P,E,hp s' ⊢ e1' «bop» e2 : T' by(rule WTrtBinOp)
  with sub' show ?case by blast
next
  case (BinOpRed2 e2 s ta e2' s' v1 bop T E)
  have red: extTA,P,t ⊢ ⟨e2, s⟩ -ta→ ⟨e2', s'⟩ by fact
  have IH: ∧ E T. [E ⊢ s √; P,E,hp s ⊢ e2:T; P,hp s ⊢ t √t]
    ⇒ ∃ U. P,E,hp s' ⊢ e2' : U ∧ P ⊢ U ≤ T
  and tconf: P,hp s ⊢ t √t by fact+
  have conf: E ⊢ s √ and wt: P,E,hp s ⊢ (Val v1) «bop» e2 : T by fact+
  from wt obtain T1 T2 where wt1: P,E,hp s ⊢ Val v1 : T1
    and wt2: P,E,hp s ⊢ e2 : T2 and wtbp: P ⊢ T1 «bop» T2 : T by auto
  from IH[OF conf wt2 tconf] obtain T2' where wt2': P,E,hp s' ⊢ e2' : T2'
    and sub: P ⊢ T2' ≤ T2 by blast
  from WTrt-binop-widen-mono[OF wtbp widen-refl sub]
  obtain T' where wtbp': P ⊢ T1 «bop» T2' : T' and sub': P ⊢ T' ≤ T by blast
  from WTrt-hest-mono[OF wt1 red-hest-incr[OF red]] wt2' wtbp'
  have P,E,hp s' ⊢ Val v1 «bop» e2' : T' by(rule WTrtBinOp)
  with sub' show ?case by blast

```

```

next
  case (RedBinOp bop v1 v2 v s)
  from  $\langle E \vdash s \sqrt{\phantom{x}} \rangle$  have preh: preallocated (hp s) by (cases s) (simp add: sconf-def)
  from  $\langle P, E, hp \ s \vdash \text{Val } v1 \ll bop \gg \text{Val } v2 : T \rangle$  obtain T1 T2
    where typeofhp s v1 =  $\lfloor T1 \rfloor$  typeofhp s v2 =  $\lfloor T2 \rfloor$   $P \vdash T1 \ll bop \gg T2 : T$  by auto
  with wf preh have  $P, hp \ s \vdash v : \leq T$  using  $\langle binop \ bop \ v1 \ v2 = \lfloor Inl \ v \rfloor \rangle$ 
    by (rule binop-type)
  thus ?case by (auto simp add: conf-def)
next
  case (RedBinOpFail bop v1 v2 a s)
  from  $\langle E \vdash s \sqrt{\phantom{x}} \rangle$  have preh: preallocated (hp s) by (cases s) (simp add: sconf-def)
  from  $\langle P, E, hp \ s \vdash \text{Val } v1 \ll bop \gg \text{Val } v2 : T \rangle$  obtain T1 T2
    where typeofhp s v1 =  $\lfloor T1 \rfloor$  typeofhp s v2 =  $\lfloor T2 \rfloor$   $P \vdash T1 \ll bop \gg T2 : T$  by auto
  with wf preh have  $P, hp \ s \vdash \text{Addr } a : \leq \text{Class Throwable}$  using  $\langle binop \ bop \ v1 \ v2 = \lfloor Inr \ a \rfloor \rangle$ 
    by (rule binop-type)
  thus ?case by (auto simp add: conf-def)
next
  case RedVar thus ?case by (fastforce simp:sconf-def lconf-def conf-def)
next
  case LAssRed thus ?case by (blast intro:widen-trans)
next
  case RedLAss thus ?case by fastforce
next
  case (AAccRed1 a s ta a' s' i T E)
  have IH:  $\bigwedge E \ T. \ll E \vdash s \sqrt{\phantom{x}}; P, E, hp \ s \vdash a : T; P, hp \ s \vdash t \sqrt{\phantom{x}} \gg \implies \exists T'. P, E, hp \ s' \vdash a' : T' \wedge P \vdash T' \leq T$ 
    and assa:  $\text{extTA}, P, t \vdash \langle a, s \rangle -ta \rightarrow \langle a', s' \rangle$ 
    and wt:  $P, E, hp \ s \vdash a[i] : T$ 
    and hconf:  $E \vdash s \sqrt{\phantom{x}}$ 
    and tconf:  $P, hp \ s \vdash t \sqrt{\phantom{x}}$  by fact+
  from wt have wti:  $P, E, hp \ s \vdash i : \text{Integer}$  by auto
  from wti red-heat-incr[OF assa] have wti':  $P, E, hp \ s' \vdash i : \text{Integer}$  by - (rule WTrt-heat-mono)
  { assume wta:  $P, E, hp \ s \vdash a : T[]$ 
    from IH[OF hconf wta tconf]
    obtain U where wta':  $P, E, hp \ s' \vdash a' : U$  and UsubT:  $P \vdash U \leq T[]$  by fastforce
    with wta' wti' have ?case by (cases U, auto simp add: widen-Array) }
  moreover
  { assume wta:  $P, E, hp \ s \vdash a : NT$ 
    from IH[OF hconf wta tconf] have  $P, E, hp \ s' \vdash a' : NT$  by fastforce
    from this wti' have ?case
      by (fastforce intro:WTrtAAccNT) }
  ultimately show ?case using wt by auto
next
  case (AAccRed2 i s ta i' s' a T E)
  have IH:  $\bigwedge E \ T. \ll E \vdash s \sqrt{\phantom{x}}; P, E, hp \ s \vdash i : T; P, hp \ s \vdash t \sqrt{\phantom{x}} \gg \implies \exists T'. P, E, hp \ s' \vdash i' : T' \wedge P \vdash T' \leq T$ 
    and issi:  $\text{extTA}, P, t \vdash \langle i, s \rangle -ta \rightarrow \langle i', s' \rangle$ 
    and wt:  $P, E, hp \ s \vdash \text{Val } a[i] : T$ 
    and sconf:  $E \vdash s \sqrt{\phantom{x}}$ 
    and tconf:  $P, hp \ s \vdash t \sqrt{\phantom{x}}$  by fact+
  from wt have wti:  $P, E, hp \ s \vdash i : \text{Integer}$  by auto
  from wti IH sconf tconf have wti':  $P, E, hp \ s' \vdash i' : \text{Integer}$  by blast
  from wt show ?case
  proof (rule WTrt-elim-cases)

```

```

    assume wta:  $P, E, hp \ s \vdash \text{Val } a : T[]$ 
    from wta red-heat-incr[OF issi] have wta':  $P, E, hp \ s' \vdash \text{Val } a : T[]$  by (rule WTrt-heat-mono)
    from wta' wti' show ?case by(fastforce)
next
    assume wta:  $P, E, hp \ s \vdash \text{Val } a : NT$ 
    from wta red-heat-incr[OF issi] have wta':  $P, E, hp \ s' \vdash \text{Val } a : NT$  by (rule WTrt-heat-mono)
    from wta' wti' show ?case
      by(fastforce elim: WTrtAAccNT)
qed
next
  case RedAAccNull thus ?case unfolding sconf-def
    by(fastforce simp add: xcpt-subcls-Throwable[OF - wf])
next
  case RedAAccBounds thus ?case unfolding sconf-def
    by(fastforce simp add: xcpt-subcls-Throwable[OF - wf])
next
  case (RedAAcc h a T n i v l T' E)
  from  $\langle E \vdash (h, l) \checkmark \rangle$  have hconf h by(clarsimp simp add: sconf-def)
  from  $\langle 0 \leq s \ i \rangle \langle \text{sint } i < \text{int } n \rangle$ 
  have nat (sint i) < n
    by (simp add: word-sle-eq nat-less-iff)
  with  $\langle \text{typeof-addr } h \ a = \lfloor \text{Array-type } T \ n \rfloor \rangle$  have  $P, h \vdash a @ \text{ACell } (\text{nat } (\text{sint } i)) : T$ 
    by(auto intro: addr-loc-type.intros)
  from heap-read-conf[OF  $\langle \text{heap-read } h \ a \ (\text{ACell } (\text{nat } (\text{sint } i))) \ v \rangle \text{ this} \rangle \langle hconf \ h \rangle$ ]
  have  $P, h \vdash v : \leq T$  by simp
  thus ?case using RedAAcc by(auto simp add: sconf-def)
next
  case (AAssRed1 a s ta a' s' i e T E)
  have IH:  $\bigwedge E \ T. \llbracket E \vdash s \checkmark; P, E, hp \ s \vdash a : T; P, hp \ s \vdash t \checkmark \rrbracket \implies \exists T'. P, E, hp \ s' \vdash a' : T' \wedge P \vdash T' \leq T$ 
    and assa:  $\text{extTA}, P, t \vdash \langle a, s \rangle - \text{ta} \rightarrow \langle a', s' \rangle$ 
    and wt:  $P, E, hp \ s \vdash a[i] := e : T$ 
    and sconf:  $E \vdash s \checkmark$ 
    and tconf:  $P, hp \ s \vdash t \checkmark$  by fact+
  from wt have void:  $T = \text{Void}$  by blast
  from wt have wti:  $P, E, hp \ s \vdash i : \text{Integer}$  by auto
  from wti red-heat-incr[OF assa] have wti':  $P, E, hp \ s' \vdash i : \text{Integer}$  by - (rule WTrt-heat-mono)
  { assume wta:  $P, E, hp \ s \vdash a : NT$ 
    from IH[OF sconf wta tconf] have wta':  $P, E, hp \ s' \vdash a' : NT$  by fastforce
    from wt wta obtain V where wte:  $P, E, hp \ s \vdash e : V$  by(auto)
    from wte red-heat-incr[OF assa] have wte':  $P, E, hp \ s' \vdash e : V$  by - (rule WTrt-heat-mono)
    from wta' wti' wte' void have ?case
      by(fastforce elim: WTrtAAssNT) }
  moreover
  { fix U
    assume wta:  $P, E, hp \ s \vdash a : U[]$ 
    from IH[OF sconf wta tconf]
    obtain U' where wta':  $P, E, hp \ s' \vdash a' : U'$  and UsubT:  $P \vdash U' \leq U[]$  by fastforce
    with wta' have ?case
    proof(cases U')
      case NT
      assume UNT:  $U' = NT$ 
      from UNT wt wta obtain V where wte:  $P, E, hp \ s \vdash e : V$  by(auto)
      from wte red-heat-incr[OF assa] have wte':  $P, E, hp \ s' \vdash e : V$  by - (rule WTrt-heat-mono)

```

```

    from wta' UNT wti' wte' void show ?thesis
    by(fastforce elim: WTrtAAssNT)
  next
    case (Array A)
    have UA:  $U' = A[]$  by fact
    with UA UsubT wt wta obtain V where wte:  $P, E, hp\ s \vdash e : V$  by auto
    from wte red-hest-incr[OF assa] have wte':  $P, E, hp\ s' \vdash e : V$  by - (rule WTrt-hest-mono)
    with wta' wte' UA wti' void show ?thesis by (fast elim: WTrtAAss)
  qed(simp-all add: widen-Array) }
ultimately show ?case using wt by blast
next
  case (AAssRed2 i s ta i' s' a e T E)
  have IH:  $\bigwedge E\ T. \llbracket E \vdash s \sqrt{\phantom{x}}; P, E, hp\ s \vdash i : T; P, hp\ s \vdash t \sqrt{t} \rrbracket \implies \exists T'. P, E, hp\ s' \vdash i' : T' \wedge P \vdash T' \leq T$ 
  and issi:  $extTA, P, t \vdash \langle i, s \rangle -ta \rightarrow \langle i', s' \rangle$ 
  and wt:  $P, E, hp\ s \vdash Val\ a[i] := e : T$ 
  and sconf:  $E \vdash s \sqrt{\phantom{x}}$  and tconf:  $P, hp\ s \vdash t \sqrt{t}$  by fact+
  from wt have void:  $T = Void$  by blast
  from wt have wti:  $P, E, hp\ s \vdash i : Integer$  by auto
  from IH[OF sconf wti tconf] have wti':  $P, E, hp\ s' \vdash i' : Integer$  by fastforce
  from wt show ?case
proof(rule WTrt-elim-cases)
  fix U T'
  assume wta:  $P, E, hp\ s \vdash Val\ a : U[]$ 
  and wte:  $P, E, hp\ s \vdash e : T'$ 
  from wte red-hest-incr[OF issi] have wte':  $P, E, hp\ s' \vdash e : T'$  by - (rule WTrt-hest-mono)
  from wta red-hest-incr[OF issi] have wta':  $P, E, hp\ s' \vdash Val\ a : U[]$  by - (rule WTrt-hest-mono)
  from wta' wti' wte' void show ?case by (fastforce elim: WTrtAAss)
next
  fix T'
  assume wta:  $P, E, hp\ s \vdash Val\ a : NT$ 
  and wte:  $P, E, hp\ s \vdash e : T'$ 
  from wte red-hest-incr[OF issi] have wte':  $P, E, hp\ s' \vdash e : T'$  by - (rule WTrt-hest-mono)
  from wta red-hest-incr[OF issi] have wta':  $P, E, hp\ s' \vdash Val\ a : NT$  by - (rule WTrt-hest-mono)
  from wta' wti' wte' void show ?case by (fastforce elim: WTrtAAss)
qed
next
  case (AAssRed3 e s ta e' s' a i T E)
  have IH:  $\bigwedge E\ T. \llbracket E \vdash s \sqrt{\phantom{x}}; P, E, hp\ s \vdash e : T; P, hp\ s \vdash t \sqrt{t} \rrbracket \implies \exists T'. P, E, hp\ s' \vdash e' : T' \wedge P \vdash T' \leq T$ 
  and issi:  $extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle$ 
  and wt:  $P, E, hp\ s \vdash Val\ a[Val\ i] := e : T$ 
  and sconf:  $E \vdash s \sqrt{\phantom{x}}$  and tconf:  $P, hp\ s \vdash t \sqrt{t}$  by fact+
  from wt have void:  $T = Void$  by blast
  from wt have wti:  $P, E, hp\ s \vdash Val\ i : Integer$  by auto
  from wti red-hest-incr[OF issi] have wti':  $P, E, hp\ s' \vdash Val\ i : Integer$  by - (rule WTrt-hest-mono)
  from wt show ?case
proof(rule WTrt-elim-cases)
  fix U T'
  assume wta:  $P, E, hp\ s \vdash Val\ a : U[]$ 
  and wte:  $P, E, hp\ s \vdash e : T'$ 
  from wta red-hest-incr[OF issi] have wta':  $P, E, hp\ s' \vdash Val\ a : U[]$  by - (rule WTrt-hest-mono)
  from IH[OF sconf wte tconf]
  obtain V where wte':  $P, E, hp\ s' \vdash e' : V$  by fastforce

```

```

    from wta' wti' wte' void show ?case by (fastforce elim:WTrtAAss)
  next
    fix T'
    assume wta: P,E,hp s ⊢ Val a : NT
    and wte: P,E,hp s ⊢ e : T'
    from wta red-heat-incr[OF issi] have wta': P,E,hp s' ⊢ Val a : NT by - (rule WTrt-heat-mono)
    from IH[OF sconf wte tconf]
    obtain V where wte': P,E,hp s' ⊢ e' : V by fastforce
    from wta' wti' wte' void show ?case by (fastforce elim:WTrtAAss)
  qed
next
  case RedAAssNull thus ?case unfolding sconf-def
    by(fastforce simp add: xcpt-subcls-Throwable[OF - wf])
next
  case RedAAssBounds thus ?case unfolding sconf-def
    by(fastforce simp add: xcpt-subcls-Throwable[OF - wf])
next
  case RedAAssStore thus ?case unfolding sconf-def
    by(fastforce simp add: xcpt-subcls-Throwable[OF - wf])
next
  case RedAAss thus ?case
    by(auto simp del:fun-upd-apply)
next
  case (ALengthRed a s ta a' s' T E)
  note IH = ⟨ $\bigwedge T'. \llbracket E \vdash s \sqrt{\phantom{x}} \rrbracket; P,E,hp s \vdash a : T'; P,hp s \vdash t \sqrt{t} \rrbracket$ 
     $\Rightarrow \exists T''. P,E,hp s' \vdash a' : T'' \wedge P \vdash T'' \leq T' \rangle$ 
  from ⟨P,E,hp s ⊢ a.length : T⟩
  show ?case
  proof(rule WTrt-elim-cases)
    fix T'
    assume [simp]: T = Integer
    and wta: P,E,hp s ⊢ a : T'[]
    from wta ⟨E ⊢ s  $\sqrt{\phantom{x}}$ ⟩ IH ⟨P,hp s ⊢ t  $\sqrt{t}$ ⟩
    obtain T'' where wta': P,E,hp s' ⊢ a' : T''
    and sub: P ⊢ T'' ≤ T'[] by blast
    from sub have P,E,hp s' ⊢ a'.length : Integer
    unfolding widen-Array
  proof(rule disjE)
    assume T'' = NT
    with wta' show ?thesis by(auto)
  next
    assume  $\exists V. T'' = V[] \wedge P \vdash V \leq T'$ 
    then obtain V where T'' = V[] P ⊢ V ≤ T' by blast
    with wta' show ?thesis by -(rule WTrtALength, simp)
  qed
  thus ?thesis by(simp)
next
  assume P,E,hp s ⊢ a : NT
  with ⟨E ⊢ s  $\sqrt{\phantom{x}}$ ⟩ IH ⟨P,hp s ⊢ t  $\sqrt{t}$ ⟩
  obtain T'' where wta': P,E,hp s' ⊢ a' : T''
  and sub: P ⊢ T'' ≤ NT by blast
  from sub have T'' = NT by auto
  with wta' show ?thesis by(auto)
qed

```

```

next
  case (RedALength h a T n l T' E)
  from  $\langle P, E, hp \ (h, l) \vdash \text{addr } a \cdot \text{length} : T' \rangle \langle \text{typeof-addr } h \ a = \lfloor \text{Array-type } T \ n \rfloor \rangle$ 
  have [simp]:  $T' = \text{Integer}$  by (auto)
  thus ?case by (auto)
next
  case RedALengthNull thus ?case unfolding sconf-def
    by (fastforce simp add: xcpt-subcls-Throwable[OF - wf])
next
  case (FAccRed e s ta e' s' F D T E)
  have IH:  $\bigwedge E \ T. \llbracket E \vdash s \ \checkmark; P, E, hp \ s \vdash e : T; P, hp \ s \vdash t \ \checkmark t \rrbracket$ 
     $\implies \exists U. P, E, hp \ s' \vdash e' : U \wedge P \vdash U \leq T$ 
  and conf:  $E \vdash s \ \checkmark$  and wt:  $P, E, hp \ s \vdash e \cdot F\{D\} : T$ 
  and tconf:  $P, hp \ s \vdash t \ \checkmark t$  by fact+
  — Now distinguish the two cases how wt can have arisen.
  { fix T' C fm
    assume wte:  $P, E, hp \ s \vdash e : T'$ 
    and icto:  $\text{class-type-of}' \ T' = \lfloor C \rfloor$ 
    and has:  $P \vdash C \text{ has } F:T \ (fm) \text{ in } D$ 
    from IH[OF conf wte tconf]
    obtain U where wte':  $P, E, hp \ s' \vdash e' : U$  and UsubC:  $P \vdash U \leq T'$  by auto
    — Now distinguish what U can be.
    with UsubC have ?case
    proof (cases U = NT)
      case True
      thus ?thesis using wte' by (blast intro: WTrtFAccNT widen-refl)
    next
      case False
      with icto UsubC obtain C' where icto':  $\text{class-type-of}' \ U = \lfloor C' \rfloor$ 
      and C'subC:  $P \vdash C' \preceq^* C$ 
      by (rule widen-is-class-type-of)
      from has-field-mono[OF has C'subC] wte' icto'
      show ?thesis by (auto intro!: WTrtFAcc)
    qed }
  moreover
  { assume  $P, E, hp \ s \vdash e : NT$ 
    hence  $P, E, hp \ s' \vdash e' : NT$  using IH[OF conf - tconf] by fastforce
    hence ?case by (fastforce intro: WTrtFAccNT widen-refl) }
  ultimately show ?case using wt by blast
next
  case RedFAcc thus ?case unfolding sconf-def
    by (fastforce dest: heap-read-conf intro: addr-loc-type.intros simp add: conf-def)
next
  case RedFAccNull thus ?case unfolding sconf-def
    by (fastforce simp add: xcpt-subcls-Throwable[OF - wf])
next
  case (FAssRed1 e s ta e' s' F D e2)
  have red:  $\text{extTA}, P, t \vdash \langle e, s \rangle -ta\rightarrow \langle e', s' \rangle$ 
  and IH:  $\bigwedge E \ T. \llbracket E \vdash s \ \checkmark; P, E, hp \ s \vdash e : T; P, hp \ s \vdash t \ \checkmark t \rrbracket$ 
     $\implies \exists U. P, E, hp \ s' \vdash e' : U \wedge P \vdash U \leq T$ 
  and conf:  $E \vdash s \ \checkmark$  and wt:  $P, E, hp \ s \vdash e \cdot F\{D\} := e_2 : T$ 
  and tconf:  $P, hp \ s \vdash t \ \checkmark t$  by fact+
  from wt have void:  $T = \text{Void}$  by blast
  — We distinguish if e has type NT or a Class type

```

```

{ assume  $P, E, hp \ s \vdash e : NT$ 
  hence  $P, E, hp \ s' \vdash e' : NT$  using  $IH[OF \ conf - tconf]$  by fastforce
  moreover obtain  $T_2$  where  $P, E, hp \ s \vdash e_2 : T_2$  using  $wt$  by auto
  from this red-heat-incr $[OF \ red]$  have  $P, E, hp \ s' \vdash e_2 : T_2$ 
    by(rule WTrt-heat-mono)
  ultimately have  $?case$  using void by(blast intro! : WTrtFAssNT)
}
moreover
{ fix  $T' \ C \ TF \ T_2 \ fm$ 
  assume  $wt_1 : P, E, hp \ s \vdash e : T'$  and  $icto : class\text{-}type\text{-}of' \ T' = \lfloor C \rfloor$  and  $wt_2 : P, E, hp \ s \vdash e_2 : T_2$ 
    and  $has : P \vdash C \text{ has } F : TF \ (fm) \text{ in } D$  and  $sub : P \vdash T_2 \leq TF$ 
  obtain  $U$  where  $wt_1' : P, E, hp \ s' \vdash e' : U$  and  $UsubC : P \vdash U \leq T'$ 
    using  $IH[OF \ conf \ wt_1 \ tconf]$  by blast
  have  $wt_2' : P, E, hp \ s' \vdash e_2 : T_2$ 
    by(rule WTrt-heat-mono $[OF \ wt_2 \ red\text{-}heat\text{-}incr[OF \ red]]$ )
  — Is  $U$  the null type or a class type?
  have  $?case$ 
  proof(cases  $U = NT$ )
    case True
      with  $wt_1' \ wt_2'$  void show  $?thesis$  by(blast intro! : WTrtFAssNT)
    next
      case False
        with  $icto \ UsubC$  obtain  $C'$  where  $icto' : class\text{-}type\text{-}of' \ U = \lfloor C' \rfloor$ 
          and  $subclass : P \vdash C' \preceq^* C$  by(rule widen-is-class-type-of)
        have  $P \vdash C' \text{ has } F : TF \ (fm) \text{ in } D$  by(rule has-field-mono $[OF \ has \ subclass]$ )
        with  $wt_1'$  show  $?thesis$  using  $wt_2' \ sub$  void  $icto'$  by(blast intro : WTrtFAss)
      qed }
  ultimately show  $?case$  using  $wt$  by blast
}
next
case (FAssRed2  $e_2 \ s \ ta \ e_2' \ s' \ v \ F \ D \ T \ E$ )
have  $red : extTA, P, t \vdash \langle e_2, s \rangle - ta \rightarrow \langle e_2', s' \rangle$ 
and  $IH : \bigwedge E \ T. \llbracket E \vdash s \sqrt{\phantom{x}}; P, E, hp \ s \vdash e_2 : T; P, hp \ s \vdash t \sqrt{\phantom{x}} \rrbracket$ 
   $\implies \exists U. P, E, hp \ s' \vdash e_2' : U \wedge P \vdash U \leq T$ 
and  $conf : E \vdash s \sqrt{\phantom{x}}$  and  $wt : P, E, hp \ s \vdash Val \ v \cdot F\{D\} := e_2 : T$ 
and  $tconf : P, hp \ s \vdash t \sqrt{\phantom{x}}$  by fact+
from  $wt$  have  $[simp] : T = Void$  by auto
from  $wt$  show  $?case$ 
proof (rule WTrt-elim-cases)
  fix  $U \ C \ TF \ T_2 \ fm$ 
  assume  $wt_1 : P, E, hp \ s \vdash Val \ v : U$ 
    and  $icto : class\text{-}type\text{-}of' \ U = \lfloor C \rfloor$ 
    and  $has : P \vdash C \text{ has } F : TF \ (fm) \text{ in } D$ 
    and  $wt_2 : P, E, hp \ s \vdash e_2 : T_2$  and  $TsubTF : P \vdash T_2 \leq TF$ 
  have  $wt_1' : P, E, hp \ s' \vdash Val \ v : U$ 
    by(rule WTrt-heat-mono $[OF \ wt_1 \ red\text{-}heat\text{-}incr[OF \ red]]$ )
  obtain  $T_2'$  where  $wt_2' : P, E, hp \ s' \vdash e_2' : T_2'$  and  $T'subT : P \vdash T_2' \leq T_2$ 
    using  $IH[OF \ conf \ wt_2 \ tconf]$  by blast
  have  $P, E, hp \ s' \vdash Val \ v \cdot F\{D\} := e_2' : Void$ 
    by(rule WTrtFAss $[OF \ wt_1' \ icto \ has \ wt_2' \ widen\text{-}trans[OF \ T'subT \ TsubTF]]$ )
  thus  $?case$  by auto
}
next
fix  $T_2$  assume  $null : P, E, hp \ s \vdash Val \ v : NT$  and  $wt_2 : P, E, hp \ s \vdash e_2 : T_2$ 
from  $null$  have  $v = Null$  by simp
moreover

```



```

    obtain  $T_2'$  where  $P, E, hp \ s' \vdash e_2' : T_2' \wedge P \vdash T_2' \leq T_2$ 
      using  $IH[OF \ conf \ wt_2 \ tconf]$  by blast
    ultimately show ?thesis by (fastforce intro: WTrtFAssNT)
qed
next
  case RedFAss thus ?case by (auto simp del: fun-upd-apply)
next
  case RedFAssNull thus ?case unfolding sconf-def
    by (fastforce simp add: xcpt-subcls-Throwable[OF - wf])
next
  case (CASRed1 e s ta e' s' D F e2 e3)
  from CASRed1.prem1(2) consider (NT)  $T_2 \ T_3$  where
     $P, E, hp \ s \vdash e : NT \ T = Boolean \ P, E, hp \ s \vdash e_2 : T_2 \ P, E, hp \ s \vdash e_3 : T_3$ 
  | (RefT)  $U \ T' \ C \ fm \ T_2 \ T_3$  where
     $P, E, hp \ s \vdash e : U \ T = Boolean \ class\text{-}type\text{-}of' \ U = \lfloor C \rfloor \ P \vdash C \text{ has } F:T' \ (fm) \text{ in } D$ 
     $P, E, hp \ s \vdash e_2 : T_2 \ P \vdash T_2 \leq T' \ P, E, hp \ s \vdash e_3 : T_3 \ P \vdash T_3 \leq T' \ volatile \ fm$  by fastforce
  thus ?case
  proof cases
    case NT
    have  $P, E, hp \ s' \vdash e' : NT$  using CASRed1.hyps(2)[OF CASRed1.prem1(1) NT(1) CASRed1.prem1(3)]
  by auto
    moreover from NT CASRed1.hyps(1)[THEN red-heap-incr]
    have  $P, E, hp \ s' \vdash e_2 : T_2 \ P, E, hp \ s' \vdash e_3 : T_3$  by (auto intro: WTrt-heap-mono)
    ultimately show ?thesis using NT by (auto intro: WTrtCASNT)
  next
    case RefT
    from CASRed1.hyps(2)[OF CASRed1.prem1(1) RefT(1) CASRed1.prem1(3)]
    obtain  $U'$  where  $wt1: P, E, hp \ s' \vdash e' : U' \ P \vdash U' \leq U$  by blast
    from RefT CASRed1.hyps(1)[THEN red-heap-incr]
    have  $wt2: P, E, hp \ s' \vdash e_2 : T_2$  and  $wt3: P, E, hp \ s' \vdash e_3 : T_3$  by (auto intro: WTrt-heap-mono)
    show ?thesis
    proof (cases  $U' = NT$ )
      case True
      with RefT wt1 wt2 wt3 show ?thesis by (auto intro: WTrtCASNT)
    next
      case False
      with RefT(3) wt1 obtain  $C'$  where  $icto': class\text{-}type\text{-}of' \ U' = \lfloor C' \rfloor$ 
        and subclass:  $P \vdash C' \preceq^* C$  by (blast intro: widen-is-class-type-of)
      have  $P \vdash C' \text{ has } F:T' \ (fm) \text{ in } D$  by (rule has-field-mono[OF RefT(4) subclass])
      with RefT wt1 wt2 wt3 icto' show ?thesis by (auto intro!: WTrtCAS)
    qed
  qed
next
  case (CASRed2 e s ta e' s' v D F e3)
  consider (Null)  $v = Null$  | (Val)  $U \ C \ T' \ fm \ T_2 \ T_3$  where
     $class\text{-}type\text{-}of' \ U = \lfloor C \rfloor \ P \vdash C \text{ has } F:T' \ (fm) \text{ in } D \ volatile \ fm$ 
     $P, E, hp \ s \vdash e : T_2 \ P \vdash T_2 \leq T' \ P, E, hp \ s \vdash e_3 : T_3 \ P \vdash T_3 \leq T' \ T = Boolean$ 
     $typeof_{hp} \ s \ v = \lfloor U \rfloor$  using CASRed2.prem1(2) by auto
  then show ?case
  proof cases
    case Null
    then show ?thesis using CASRed2
      by (force dest: red-heap-incr intro: WTrt-heap-mono WTrtCASNT)
  next

```

```

    case Val
    from CASRed2.hyps(1) have hext:  $hp\ s \leq hp\ s'$  by(auto dest: red-hext-incr)
    with Val(9) have typeof $_{hp\ s'}\ v = \lfloor U \rfloor$  by(rule type-of-hext-type-of)
    moreover from CASRed2.hyps(2)[OF CASRed2.prem(1) Val(4) CASRed2.prem(3)] Val(5)
    obtain  $T2'$  where  $P, E, hp\ s' \vdash e' : T2'$   $P \vdash T2' \leq T'$  by(auto intro: widen-trans)
    moreover from Val(6) hext have  $P, E, hp\ s' \vdash e3 : T3$  by(rule WTrt-hext-mono)
    ultimately show ?thesis using Val by(auto intro: WTrtCAS)
  qed
next
  case (CASRed3 e s ta e' s' v D F v')
  consider (Null)  $v = Null$  | (Val)  $U\ C\ T'\ fm\ T2\ T3$  where
     $T = Boolean\ class\ type\ of'\ U = \lfloor C \rfloor$   $P \vdash C\ has\ F:T'$  (fm) in D volatile fm
     $P \vdash T2 \leq T'\ P, E, hp\ s \vdash e : T3\ P \vdash T3 \leq T'$ 
    typeof $_{hp\ s}\ v = \lfloor U \rfloor$  typeof $_{hp\ s'}\ v' = \lfloor T2 \rfloor$ 
    using CASRed3.prem(2) by auto
  then show ?case
  proof cases
    case Null
    then show ?thesis using CASRed3
    by(force dest: red-hext-incr intro: type-of-hext-type-of WTrtCASNT)
  next
    case Val
    from CASRed3.hyps(1) have hext:  $hp\ s \leq hp\ s'$  by(auto dest: red-hext-incr)
    with Val(8,9) have typeof $_{hp\ s'}\ v = \lfloor U \rfloor$  typeof $_{hp\ s'}\ v' = \lfloor T2 \rfloor$ 
    by(blast intro: type-of-hext-type-of)+
    moreover from CASRed3.hyps(2)[OF CASRed3.prem(1) Val(6) CASRed3.prem(3)] Val(7)
    obtain  $T3'$  where  $P, E, hp\ s' \vdash e' : T3'$   $P \vdash T3' \leq T'$  by(auto intro: widen-trans)
    ultimately show ?thesis using Val by(auto intro: WTrtCAS)
  qed
next
  case CASNull thus ?case unfolding sconf-def
  by(fastforce simp add: xcpt-subcls-Throwable[OF - wf])
next
  case (CallObj e s ta e' s' M es T E)
  have red:  $extTA, P, t \vdash \langle e, s \rangle \rightarrow \langle e', s' \rangle$ 
  and IH:  $\bigwedge E\ T. \llbracket E \vdash s \sqrt{\phantom{x}}; P, E, hp\ s \vdash e : T; P, hp\ s \vdash t \sqrt{t} \rrbracket$ 
     $\implies \exists U. P, E, hp\ s' \vdash e' : U \wedge P \vdash U \leq T$ 
  and conf:  $E \vdash s \sqrt{\phantom{x}}$  and wt:  $P, E, hp\ s \vdash e \cdot M(es) : T$ 
  and tconf:  $P, hp\ s \vdash t \sqrt{t}$  by fact+
  — We distinguish if e has type NT or a Class type
  from wt show ?case
  proof (rule WTrt-elim-cases)
    fix  $T'\ C\ Ts\ meth\ D\ Us$ 
    assume wte:  $P, E, hp\ s \vdash e : T'$  and icto:  $class\ type\ of'\ T' = \lfloor C \rfloor$ 
    and method:  $P \vdash C\ sees\ M:Ts \rightarrow T = meth\ in\ D$ 
    and wtes:  $P, E, hp\ s \vdash es\ [:]\ Us$  and subs:  $P \vdash Us\ [\leq]\ Ts$ 
    obtain U where wte':  $P, E, hp\ s' \vdash e' : U$  and UsubC:  $P \vdash U \leq T'$ 
    using IH[OF conf wte tconf] by blast
  show ?thesis
  proof (cases  $U = NT$ )
    case True
    moreover have  $P, E, hp\ s' \vdash es\ [:]\ Us$ 
    by(rule WTrts-hext-mono[OF wtes red-hext-incr[OF red]])
    ultimately show ?thesis using wte' by(blast intro!: WTrtCallNT)
  
```

```

next
  case False
  with icto UsubC obtain C'
    where icto': class-type-of' U =  $\lfloor C' \rfloor$  and subclass:  $P \vdash C' \preceq^* C$ 
    by(rule widen-is-class-type-of)

  obtain Ts' T' meth' D'
    where method':  $P \vdash C' \text{ sees } M:Ts' \rightarrow T' = \text{meth}' \text{ in } D'$ 
    and subs':  $P \vdash Ts \leq Ts'$  and sub':  $P \vdash T' \leq T$ 
    using Call-lemma[OF method subclass wf] by fast
  have wtes':  $P, E, hp \ s' \vdash es \ [\:] \ Us$ 
    by(rule WTrts-heat-mono[OF wtes red-heat-incr[OF red]])
  show ?thesis using wtes' wte' icto' subs method' subs' sub' by(blast intro:widens-trans)
qed
next
  fix Ts
  assume  $P, E, hp \ s \vdash e:NT$ 
  hence  $P, E, hp \ s' \vdash e' : NT$  using IH[OF conf - tconf] by fastforce
  moreover
  fix Ts assume wtes:  $P, E, hp \ s \vdash es \ [\:] \ Ts$ 
  have  $P, E, hp \ s' \vdash es \ [\:] \ Ts$ 
    by(rule WTrts-heat-mono[OF wtes red-heat-incr[OF red]])
  ultimately show ?thesis by(blast intro!: WTrtCallNT)
qed
next
  case (CallParams es s ta es' s' v M T E)
  have reds:  $extTA, P, t \vdash \langle es, s \rangle \ [-ta \rightarrow] \ \langle es', s' \rangle$ 
  and IH:  $\bigwedge Ts \ E. \llbracket E \vdash s \ \checkmark; P, E, hp \ s \vdash es \ [\:] \ Ts; P, hp \ s \vdash t \ \checkmark t \rrbracket$ 
     $\implies \exists Ts'. P, E, hp \ s' \vdash es' \ [\:] \ Ts' \wedge P \vdash Ts' \leq Ts$ 
  and conf:  $E \vdash s \ \checkmark$  and wt:  $P, E, hp \ s \vdash Val \ v.M(es) : T$ 
  and tconf:  $P, hp \ s \vdash t \ \checkmark t$  by fact+
  from wt show ?case
proof (rule WTrt-elim-cases)
  fix U C Ts meth D Us
  assume wte:  $P, E, hp \ s \vdash Val \ v : U$  and icto: class-type-of' U =  $\lfloor C \rfloor$ 
    and  $P \vdash C \text{ sees } M:Ts \rightarrow T = \text{meth} \text{ in } D$ 
    and wtes:  $P, E, hp \ s \vdash es \ [\:] \ Us$  and  $P \vdash Us \leq Ts$ 
  moreover have  $P, E, hp \ s' \vdash Val \ v : U$ 
    by(rule WTrt-heat-mono[OF wte reds-heat-incr[OF reds]])
  moreover obtain Us' where  $P, E, hp \ s' \vdash es' \ [\:] \ Us'$   $P \vdash Us' \leq Us$ 
    using IH[OF conf wtes tconf] by blast
  ultimately show ?thesis by(fastforce intro:WTrtCall widens-trans)
qed
next
  fix Us
  assume null:  $P, E, hp \ s \vdash Val \ v : NT$  and wtes:  $P, E, hp \ s \vdash es \ [\:] \ Us$ 
  from null have  $v = Null$  by simp
  moreover
  obtain Us' where  $P, E, hp \ s' \vdash es' \ [\:] \ Us' \wedge P \vdash Us' \leq Us$ 
    using IH[OF conf wtes tconf] by blast
  ultimately show ?thesis by(fastforce intro:WTrtCallNT)
qed
next
  case (RedCall s a U M Ts T pns body D vs T' E)
  have hp: typeof-addr (hp s) a =  $\lfloor U \rfloor$ 

```

**and** *method*:  $P \vdash \text{class-type-of } U \text{ sees } M: Ts \rightarrow T = \lfloor (pns, \text{body}) \rfloor \text{ in } D$   
**and** *wt*:  $P, E, hp \ s \vdash \text{addr } a \cdot M(\text{map Val } vs) : T' \text{ by } \text{fact+}$   
**obtain**  $Ts'$  **where** *wtes*:  $P, E, hp \ s \vdash \text{map Val } vs \ [:] Ts'$   
**and** *subs*:  $P \vdash Ts' \leq Ts$  **and**  $T' \text{ is } T: T' = T$   
**using** *wt method hp wf* **by** (*auto 4 3 dest: sees-method-fun*)  
**from** *wtes subs* **have** *length-vs*:  $\text{length } vs = \text{length } Ts$   
**by** (*auto simp add: WTrts-conv-list-all2 dest!: list-all2-lengthD*)  
**have** *UsubD*:  $P \vdash \text{ty-of-htype } U \leq \text{Class } (\text{class-type-of } U)$   
**by** (*cases U*) (*simp-all add: widen-array-object*)  
**from** *sees-wf-mdecl* [*OF wf method*] **obtain**  $T''$   
**where** *wtbody*:  $P, [this \# pns \mapsto] \text{Class } D \# Ts \vdash \text{body} :: T''$   
**and**  $T'' \text{ sub } T: P \vdash T'' \leq T$  **and** *length-pns*:  $\text{length } pns = \text{length } Ts$   
**by** (*fastforce simp: wf-mdecl-def simp del: map-upds-twist*)  
**from** *wtbody* **have**  $P, \text{Map.empty}(this \# pns \mapsto] \text{Class } D \# Ts), hp \ s \vdash \text{body} : T''$   
**by** (*rule WT-implies-WTrt*)  
**hence**  $P, E(this \# pns \mapsto] \text{Class } D \# Ts), hp \ s \vdash \text{body} : T''$   
**by** (*rule WTrt-env-mono*) *simp*  
**hence**  $P, E, hp \ s \vdash \text{blocks } (this \# pns) (\text{Class } D \# Ts) (\text{Addr } a \# vs) \text{ body} : T''$   
**using** *wtes subs hp sees-method-decl-above* [*OF method*] *length-vs length-pns UsubD*  
**by** (*auto simp add: wt-blocks rel-list-all2-Cons2 intro: widen-trans*)  
**with**  $T'' \text{ sub } T \ T' \text{ is } T$  **show** *?case* **by** *blast*  
**next**  
**case** (*RedCallExternal* *s a U M Ts T' D vs ta va h' ta' e' s'*)  
**from**  $\langle P, t \vdash \langle a \cdot M(vs), hp \ s \rangle -ta \rightarrow \text{ext} \langle va, h' \rangle \rangle$  **have**  $hp \ s \leq h'$  **by** (*rule red-external-hext*)  
**with**  $\langle P, E, hp \ s \vdash \text{addr } a \cdot M(\text{map Val } vs) : T \rangle$   
**have**  $P, E, h' \vdash \text{addr } a \cdot M(\text{map Val } vs) : T$  **by** (*rule WTrt-hext-mono*)  
**moreover from**  $\langle \text{typeof-addr } (hp \ s) \ a = \lfloor U \rfloor \rangle \langle P \vdash \text{class-type-of } U \text{ sees } M: Ts \rightarrow T' = \text{Native in } D \rangle$   
 $\langle P, E, hp \ s \vdash \text{addr } a \cdot M(\text{map Val } vs) : T \rangle$   
**have**  $P, hp \ s \vdash a \cdot M(vs) : T'$   
**by** (*fastforce simp add: external-WT'-iff dest: sees-method-fun*)  
**ultimately show** *?case* **using** *RedCallExternal*  
**by** (*auto 4 3 intro: red-external-conf-extRet* [*OF wf*] *intro!: wt-external-call simp add: sconf-def dest: sees-method-fun* [**where**  $C = \text{class-type-of } U$ ])  
**next**  
**case** *RedCallNull* **thus** *?case* **unfolding** *sconf-def*  
**by** (*fastforce simp add: xcpt-subcls-Throwable* [*OF - wf*])  
**next**  
**case** (*BlockRed* *e h x V vo ta e' h' x' T T' E*)  
**note**  $IH = \langle \bigwedge T \ E. \llbracket E \vdash (h, x(V := vo)) \rrbracket \checkmark; P, E, hp \ (h, x(V := vo)) \vdash e : T; P, hp \ (h, x(V := vo)) \vdash t \checkmark \rrbracket$   
 $\implies \exists T'. P, E, hp \ (h', x') \vdash e' : T' \wedge P \vdash T' \leq T \rrbracket [\text{simpified}]$   
**from**  $\langle P, E, hp \ (h, x) \vdash \{V : T = vo; e\} : T' \rangle$  **have**  $P, E(V \mapsto T), h \vdash e : T'$  **by** (*cases vo, auto*)  
**moreover from**  $\langle E \vdash (h, x) \checkmark \rangle \langle P, E, hp \ (h, x) \vdash \{V : T = vo; e\} : T' \rangle$   
**have**  $(E(V \mapsto T)) \vdash (h, x(V := vo)) \checkmark$   
**by** (*cases vo*) (*simp add: lconf-def sconf-def, auto simp add: sconf-def conf-def intro: lconf-upd2*)  
**ultimately obtain**  $T''$  **where** *wt'*:  $P, E(V \mapsto T), h' \vdash e' : T'' \ P \vdash T'' \leq T'$  **using**  $\langle P, hp \ (h, x) \vdash t \checkmark \rangle$   
**by** (*auto dest: IH*)  
**{ fix** *v*  
**assume**  $vo : x' \ V = \lfloor v \rfloor$   
**from**  $\langle (E(V \mapsto T)) \vdash (h, x(V := vo)) \checkmark \rangle \langle \text{extTA}, P, t \vdash \langle e, (h, x(V := vo)) \rangle -ta \rightarrow \langle e', (h', x') \rangle \rangle$   
 $\langle P, E(V \mapsto T), h \vdash e : T' \rangle$   
**have**  $P, h' \vdash x' (\leq) (E(V \mapsto T))$  **by** (*auto simp add: sconf-def dest: red-preserves-lconf*)  
**with** *vo* **have**  $\exists T'. \text{typeof}_{h'} \ v = \lfloor T' \rfloor \wedge P \vdash T' \leq T$  **by** (*fastforce simp add: sconf-def lconf-def*)

$\text{conf-def}$ )  
 then obtain  $T'$  where  $\text{typeof}_{h'} v = \lfloor T' \rfloor$   $P \vdash T' \leq T$  by *blast*  
 hence  $?case$  using  $wt' \text{ vo } \text{by}(auto)$  }  
 moreover  
 { assume  $x' V = \text{None}$  with  $wt'$  have  $?case$  by  $(auto)$  }  
 ultimately show  $?case$  by *blast*  
 next  
 case *RedBlock* thus  $?case$  by *auto*  
 next  
 case (*SynchronizedRed1*  $o' s \text{ ta } o'' s' e T E$ )  
 have  $red: \text{extTA}, P, t \vdash \langle o', s \rangle -ta \rightarrow \langle o'', s' \rangle$  by *fact*  
 have  $IH: \bigwedge T E. \llbracket E \vdash s \sqrt{\phantom{x}}; P, E, hp \ s \vdash o' : T; P, hp \ s \vdash t \sqrt{t} \rrbracket \implies \exists T'. P, E, hp \ s' \vdash o'' : T' \wedge P \vdash T' \leq T$  by *fact*  
 have  $conf: E \vdash s \sqrt{\phantom{x}}$  by *fact*  
 have  $wt: P, E, hp \ s \vdash \text{sync}(o') \ e : T$  by *fact* +  
 thus  $?case$   
 proof(rule *WTrt-elim-cases*)  
 fix  $To$   
 assume  $wto: P, E, hp \ s \vdash o' : To$   
 and  $refT: \text{is-refT } To$   
 and  $wte: P, E, hp \ s \vdash e : T$   
 from  $IH[OF \text{ conf } wto \langle P, hp \ s \vdash t \sqrt{t} \rangle]$  obtain  $To'$  where  $P, E, hp \ s' \vdash o'' : To'$  and  $sub: P \vdash To' \leq To$  by *auto*  
 moreover have  $P, E, hp \ s' \vdash e : T$   
 by(rule *WTrt-heat-mono* [*OF wte red-heat-incr* [*OF red*]])  
 moreover have  $\text{is-refT } To'$  using  $refT \ sub$  by(*auto intro: widen-refT*)  
 ultimately show  $?thesis$  by(*auto*)  
 qed  
 next  
 case *SynchronizedNull* thus  $?case$  unfolding *sconf-def*  
 by(*fastforce simp add: xcpt-subcls-Throwable* [*OF - wf*])  
 next  
 case *LockSynchronized* thus  $?case$  by(*auto*)  
 next  
 case (*SynchronizedRed2*  $e s \text{ ta } e' s' a T E$ )  
 have  $red: \text{extTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle$  by *fact*  
 have  $IH: \bigwedge T E. \llbracket E \vdash s \sqrt{\phantom{x}}; P, E, hp \ s \vdash e : T; P, hp \ s \vdash t \sqrt{t} \rrbracket \implies \exists T'. P, E, hp \ s' \vdash e' : T' \wedge P \vdash T' \leq T$  by *fact*  
 have  $conf: E \vdash s \sqrt{\phantom{x}}$  by *fact*  
 have  $wt: P, E, hp \ s \vdash \text{insync}(a) \ e : T$  by *fact*  
 thus  $?case$   
 proof(rule *WTrt-elim-cases*)  
 fix  $Ta$   
 assume  $P, E, hp \ s \vdash e : T$   
 and  $hpa: \text{typeof-addr } (hp \ s) \ a = \lfloor Ta \rfloor$   
 from  $\langle P, E, hp \ s \vdash e : T \rangle \text{ conf } \langle P, hp \ s \vdash t \sqrt{t} \rangle$  obtain  $T'$   
 where  $P, E, hp \ s' \vdash e' : T' \ P \vdash T' \leq T$  by(*blast dest: IH*)  
 moreover from  $red$  have  $heat: hp \ s \leq hp \ s'$  by(*auto dest: red-heat-incr*)  
 with  $hpa$  have  $P, E, hp \ s' \vdash \text{addr } a : \text{ty-of-htype } Ta$   
 by(*auto intro: typeof-addr-heat-mono*)  
 ultimately show  $?thesis$  by *auto*  
 qed  
 next  
 case *UnlockSynchronized* thus  $?case$  by(*auto*)

```

next
  case SeqRed thus ?case
    apply(auto)
    apply(drule WTrt-heat-mono[OF - red-heat-incr], assumption)
    by auto
next
  case (CondRed b s ta b' s' e1 e2 T E)
  have red: extTA,P,t ⊢ ⟨b,s⟩ -ta→ ⟨b',s'⟩ by fact
  have IH: ∧ T E. [E ⊢ s √; P,E,hp s ⊢ b : T; P,hp s ⊢ t √t] ⇒ ∃ T'. P,E,hp s' ⊢ b' : T' ∧ P ⊢
  T' ≤ T by fact
  have conf: E ⊢ s √ by fact
  have wt: P,E,hp s ⊢ if (b) e1 else e2 : T by fact
  thus ?case
proof(rule WTrt-elim-cases)
  fix T1 T2
  assume wtb: P,E,hp s ⊢ b : Boolean
  and wte1: P,E,hp s ⊢ e1 : T1
  and wte2: P,E,hp s ⊢ e2 : T2
  and lub: P ⊢ lub(T1, T2) = T
  from IH[OF conf wtb ⟨P,hp s ⊢ t √t⟩] have P,E,hp s' ⊢ b' : Boolean by(auto)
  moreover have P,E,hp s' ⊢ e1 : T1
    by(rule WTrt-heat-mono[OF wte1 red-heat-incr[OF red]])
  moreover have P,E,hp s' ⊢ e2 : T2
    by(rule WTrt-heat-mono[OF wte2 red-heat-incr[OF red]])
  ultimately show ?thesis using lub by auto
qed
next
  case (ThrowRed e s ta e' s' T E)
  have IH: ∧ T E. [E ⊢ s √; P,E,hp s ⊢ e : T; P,hp s ⊢ t √t] ⇒ ∃ T'. P,E,hp s' ⊢ e' : T' ∧ P ⊢
  T' ≤ T by fact
  have conf: E ⊢ s √ by fact
  have wt: P,E,hp s ⊢ throw e : T by fact
  then obtain T'
    where wte: P,E,hp s ⊢ e : T'
    and nobject: P ⊢ T' ≤ Class Throwable by auto
  from IH[OF conf wte ⟨P,hp s ⊢ t √t⟩] obtain T''
    where wte': P,E,hp s' ⊢ e' : T''
    and PT'T'': P ⊢ T'' ≤ T' by blast
  from nobject PT'T'' have P ⊢ T'' ≤ Class Throwable
    by(auto simp add: widen-Class)(erule notE, rule rtranclp-trans)
  hence P,E,hp s' ⊢ throw e' : T using wte' PT'T''
    by -(erule WTrtThrow)
  thus ?case by(auto)
next
  case RedThrowNull thus ?case unfolding sconf-def
    by(fastforce simp add: xcpt-subcls-Throwable[OF - wf])
next
  case (TryRed e s ta e' s' C V e2 T E)
  have red: extTA,P,t ⊢ ⟨e,s⟩ -ta→ ⟨e',s'⟩ by fact
  have IH: ∧ T E. [E ⊢ s √; P,E,hp s ⊢ e : T; P,hp s ⊢ t √t] ⇒ ∃ T'. P,E,hp s' ⊢ e' : T' ∧ P ⊢
  T' ≤ T by fact
  have conf: E ⊢ s √ by fact
  have wt: P,E,hp s ⊢ try e catch(C V) e2 : T by fact
  thus ?case

```

```

proof(rule WTrt-elim-cases)
  fix T1
  assume wte:  $P, E, hp\ s \vdash e : T1$ 
    and wte2:  $P, E(V \mapsto \text{Class } C), hp\ s \vdash e2 : T$ 
    and sub:  $P \vdash T1 \leq T$ 
  from IH[OF conf wte  $\langle P, hp\ s \vdash t \sqrt{t} \rangle$ ] obtain T1' where  $P, E, hp\ s' \vdash e' : T1'$  and  $P \vdash T1' \leq$ 
  T1 by(auto)
  moreover have  $P, E(V \mapsto \text{Class } C), hp\ s' \vdash e2 : T$ 
    by(rule WTrt-hext-mono[OF wte2 red-hext-incr[OF red]])
  ultimately show ?thesis using sub by(auto elim: widen-trans)
qed
next
  case RedTryFail thus ?case unfolding sconf-def
    by(fastforce simp add: xcpt-subcls-Throwable[OF - wf])
next
  case RedSeq thus ?case by auto
next
  case RedCondT thus ?case by(auto dest: is-lub-upper)
next
  case RedCondF thus ?case by(auto dest: is-lub-upper)
next
  case RedWhile thus ?case by(fastforce)
next
  case RedTry thus ?case by auto
next
  case RedTryCatch thus ?case by(fastforce)
next
  case (ListRed1  $e\ s\ ta\ e'\ s'\ es\ Ts\ E$ )
    note IH =  $\langle \bigwedge T\ E. \llbracket E \vdash s \sqrt{} \rrbracket; P, E, hp\ s \vdash e : T; P, hp\ s \vdash t \sqrt{t} \rrbracket \implies \exists T'. P, E, hp\ s' \vdash e' : T' \wedge P \vdash T' \leq T \rangle$ 
    from  $\langle P, E, hp\ s \vdash e \# es [:] Ts \rangle$  obtain  $T\ Ts'$  where  $Ts = T \# Ts'$   $P, E, hp\ s \vdash e : T$   $P, E, hp\ s \vdash$ 
     $es [:] Ts'$  by auto
    with IH[of E T]  $\langle E \vdash s \sqrt{} \rangle$  WTrts-hext-mono[OF  $\langle P, E, hp\ s \vdash es [:] Ts' \rangle$  red-hext-incr[OF  $\langle extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \rangle$ ]]
    show ?case using  $\langle P, hp\ s \vdash t \sqrt{t} \rangle$  by(auto simp add: list-all2-Cons2 intro: widens-refl)
next
  case ListRed2 thus ?case
    by(fastforce dest: hext-typeof-mono[OF reds-hext-incr])
qed(fastforce)+
end

```

## 4.14 Progress and type safety theorem for the multithreaded system

```

theory ProgressThreaded
imports
  Threaded
  TypeSafe
  ../Framework/FWProgress
begin

```

**lemma** lock-ok-ls-Some-ex-ts-not-final:

```

assumes lock: lock-ok ls ts
and hl: has-lock (ls $ l) t
shows  $\exists e\ x\ ln. ts\ t = \lfloor((e, x), ln)\rfloor \wedge \neg\ final\ e$ 
proof –
  from lock have lock-thread-ok ls ts
    by(rule lock-ok-lock-thread-ok)
  with hl obtain e x ln
    where tst: ts t =  $\lfloor((e, x), ln)\rfloor$ 
    by(auto dest!: lock-thread-okD)
  { assume final e
    hence expr-locks e l = 0 by(rule final-locks)
    with lock tst have has-locks (ls $ l) t = 0
      by(auto dest: lock-okD2[rule-format, where l=l])
    with hl have False by simp }
  with tst show ?thesis by auto
qed

```

#### 4.14.1 Preservation lemmata

#### 4.14.2 Definite assignment

##### abbreviation

$def-ass-ts-ok :: ('addr, 'thread-id, 'addr\ expr \times 'addr\ locals)\ thread-info \Rightarrow 'heap \Rightarrow bool$

##### where

$def-ass-ts-ok \equiv ts-ok\ (\lambda t\ (e, x)\ h.\ \mathcal{D}\ e\ \lfloor dom\ x \rfloor)$

##### context $J$ -heap-base **begin**

**lemma** **assumes** wf: wf-J-prog P

**shows** red-def-ass-new-thread:

$\llbracket P, t \vdash \langle e, s \rangle \rightarrow ta \rightarrow \langle e', s' \rangle; NewThread\ t''\ (e'', x'')\ c'' \in set\ \{ta\}_t \rrbracket \Longrightarrow \mathcal{D}\ e''\ \lfloor dom\ x'' \rrbracket$

**and** reds-def-ass-new-thread:

$\llbracket P, t \vdash \langle es, s \rangle \rightarrow ta \rightarrow \langle es', s' \rangle; NewThread\ t''\ (e'', x'')\ c'' \in set\ \{ta\}_t \rrbracket \Longrightarrow \mathcal{D}\ e''\ \lfloor dom\ x'' \rrbracket$

**proof**(induct rule: red-reds.inducts)

**case** (RedCallExternal s a T M vs ta va h' ta' e' s')

**then obtain** C fs a **where** subThread:  $P \vdash C \preceq^* Thread$  **and** ext: extNTA2J P (C, run, a) = (e'', x'')

**by**(fastforce dest: red-external-new-thread-sub-thread)

**from** sub-Thread-sees-run[OF wf subThread] **obtain** D pns body

**where** sees:  $P \vdash C\ sees\ run: \square \rightarrow Void = \lfloor(pns, body)\rfloor$  **in** D **by** auto

**from** sees-wf-mdecl[OF wf this] **have**  $\mathcal{D}\ body\ \lfloor\{this\}\rfloor$

**by**(auto simp add: wf-mdecl-def)

**with** sees ext **show** ?case **by**(clarsimp simp del: fun-upd-apply)

**qed**(auto simp add: ta-upd-simps)

**lemma** lifting-wf-def-ass: wf-J-prog P  $\Longrightarrow$  lifting-wf final-expr (mred P)  $(\lambda t\ (e, x)\ m.\ \mathcal{D}\ e\ \lfloor dom\ x \rfloor)$

**apply**(unfold-locales)

**apply**(auto dest: red-preserves-defass red-def-ass-new-thread)

**done**

**lemma** def-ass-ts-ok-J-start-state:

$\llbracket wf-J-prog\ P; P \vdash C\ sees\ M: Ts \rightarrow T = \lfloor(pns, body)\rfloor\ in\ D; length\ vs = length\ Ts \rrbracket \Longrightarrow$

$def-ass-ts-ok\ (thr\ (J-start-state\ P\ C\ M\ vs))\ h$



```

apply(rule ts-okI)
apply(drule (1) sees-wf-mdecl)
apply(clarsimp simp add: wf-mdecl-def start-state-def split: if-split-asm)
done

```

**end**

### 4.14.3 typeability

**context** *J-heap-base* **begin**

**definition** *type-ok* :: '*addr J-prog*  $\Rightarrow$  *env*  $\times$  *ty*  $\Rightarrow$  '*addr expr*  $\Rightarrow$  '*heap*  $\Rightarrow$  *bool*  
**where** *type-ok* *P*  $\equiv$  ( $\lambda(E, T) e$  c. ( $\exists T'. (P, E, c \vdash e : T' \wedge P \vdash T' \leq T)$ ))

**definition** *J-sconf-type-ET-start* :: '*m prog*  $\Rightarrow$  *cname*  $\Rightarrow$  *mname*  $\Rightarrow$  ('*thread-id*  $\rightarrow$  (*env*  $\times$  *ty*))  
**where**  
*J-sconf-type-ET-start* *P C M*  $\equiv$   
 let ( $\cdot$ ,  $\cdot$ , *T*,  $\cdot$ ) = *method* *P C M*  
 in ([*start-tid*  $\mapsto$  (*Map.empty*, *T*)])

**lemma** *fixes* *E* :: *env*  
**assumes** *wf*: *wf-J-prog* *P*  
**shows** *red-type-newthread*:  
 $\llbracket P, t \vdash \langle e, s \rangle \rightarrow ta \rightarrow \langle e', s' \rangle; P, E, hp \ s \vdash e : T; NewThread \ t'' (e'', x'') (hp \ s') \in set \ \{ta\}_t \rrbracket$   
 $\implies \exists E \ T. P, E, hp \ s' \vdash e'' : T \wedge P, hp \ s' \vdash x'' (\leq) E$   
**and** *reds-type-newthread*:  
 $\llbracket P, t \vdash \langle es, s \rangle \rightarrow ta \rightarrow \langle es', s' \rangle; NewThread \ t'' (e'', x'') (hp \ s') \in set \ \{ta\}_t; P, E, hp \ s \vdash es [:] Ts \rrbracket$   
 $\implies \exists E \ T. P, E, hp \ s' \vdash e'' : T \wedge P, hp \ s' \vdash x'' (\leq) E$   
**proof**(*induct arbitrary: E T and E Ts rule: red-reds.inducts*)  
**case** (*RedCallExternal* *s a U M Ts T' D vs ta va h' ta' e' s'*)  
**from**  $\langle NewThread \ t'' (e'', x'') (hp \ s') \in set \ \{ta'\}_t \rangle \langle ta' = extTA2J \ P \ ta \rangle$   
**obtain** *C' M' a'* **where** *nt*:  $NewThread \ t'' (C', M', a') (hp \ s') \in set \ \{ta\}_t$   
**and**  $extNTA2J \ P (C', M', a') = (e'', x'')$  **by** *fastforce*  
**from** *red-external-new-thread-sees*[*OF wf*  $\langle P, t \vdash \langle a \cdot M(vs), hp \ s \rangle \rightarrow ta \rightarrow ext \ \langle va, h' \rangle \ nt \rangle \langle typeof-addr$   
 $(hp \ s) \ a = \lfloor U \rfloor$   
**obtain** *T* *pns* *body* *D* **where**  $h'a' : typeof-addr \ h' \ a' = \lfloor Class-type \ C' \rfloor$   
**and** *sees*:  $P \vdash C' \ sees \ M' : \lfloor \rightarrow T = \lfloor (pns, body) \rfloor$  **in** *D* **by** *auto*  
**from** *sees-wf-mdecl*[*OF wf sees*] **obtain** *T* **where**  $P, [this \mapsto Class \ D] \vdash body :: T$   
**by**(*auto simp add: wf-mdecl-def*)  
**hence** *WTrt* *P*  $(hp \ s') [this \mapsto Class \ D] \ body \ T$  **by**(rule *WT-implies-WTrt*)  
**moreover from** *sees* **have**  $P \vdash C' \preceq^* D$  **by**(rule *sees-method-decl-above*)  
**with**  $h'a' \text{ have } P, h' \vdash [this \mapsto Addr \ a'] (\leq) [this \mapsto Class \ D]$  **by**(*auto simp add: lconf-def conf-def*)  
**ultimately show** *?case* **using**  $h'a' \ sees \ \langle s' = (h', lcl \ s) \rangle$   
 $\langle extNTA2J \ P (C', M', a') = (e'', x'') \rangle$  **by**(*fastforce intro: sees-method-decl-above*)  
**qed**(*fastforce simp add: ta-upd-simps*) +

**end**

**context** *J-heap-conf-base* **begin**

**definition** *sconf-type-ok* :: (*env*  $\times$  *ty*)  $\Rightarrow$  '*thread-id*  $\Rightarrow$  '*addr expr*  $\times$  '*addr locals*  $\Rightarrow$  '*heap*  $\Rightarrow$  *bool*  
**where**  
*sconf-type-ok* *ET t ex h*  $\equiv$  *fst* *ET*  $\vdash (h, snd \ ex) \surd \wedge type-ok \ P \ ET \ (fst \ ex) \ h \wedge P, h \vdash t \surd t$

**abbreviation** *sconf-type-ts-ok* ::

$(\text{'thread-id} \rightarrow (\text{env} \times \text{ty})) \Rightarrow (\text{'addr}, \text{'thread-id}, \text{'addr expr} \times \text{'addr locals}) \text{ thread-info} \Rightarrow \text{'heap} \Rightarrow \text{bool}$

**where**

$\text{sconf-type-ts-ok} \equiv \text{ts-inv sconf-type-ok}$

**lemma** *ts-inv-ok-J-sconf-type-ET-start*:

$\text{ts-inv-ok} (\text{thr } (J\text{-start-state } P \ C \ M \ \text{vs})) (J\text{-sconf-type-ET-start } P \ C \ M)$

**by**(rule *ts-inv-okI*)(simp add: *start-state-def J-sconf-type-ET-start-def split-beta*)

**end**

**lemma** (in *J-heap*) *red-preserve-welltype*:

$\llbracket \text{extTA}, P, t \vdash \langle e, (h, x) \rangle -ta \rightarrow \langle e', (h', x') \rangle; P, E, h \vdash e'' : T \rrbracket \Longrightarrow P, E, h' \vdash e'' : T$

**by**(auto elim: *WTrt-heap-mono dest!*: *red-heap-incr*)

**context** *J-heap-conf* **begin**

**lemma** *sconf-type-ts-ok-J-start-state*:

$\llbracket \text{wf-J-prog } P; \text{wf-start-state } P \ C \ M \ \text{vs} \rrbracket$

$\Longrightarrow \text{sconf-type-ts-ok} (J\text{-sconf-type-ET-start } P \ C \ M) (\text{thr } (J\text{-start-state } P \ C \ M \ \text{vs})) (\text{shr } (J\text{-start-state } P \ C \ M \ \text{vs}))$

**apply**(erule *wf-start-state.cases*)

**apply**(rule *ts-invI*)

**apply**(simp add: *start-state-def split: if-split-asm*)

**apply**(frule (1) *sees-wf-mdecl*)

**apply**(auto simp add: *wf-mdecl-def J-sconf-type-ET-start-def sconf-type-ok-def sconf-def type-ok-def*)

**apply**(erule *hconf-start-heap*)

**apply**(erule *preallocated-start-heap*)

**apply**(erule *wf-prog-wf-syscls*)

**apply**(frule *list-all2-lengthD*)

**apply**(auto simp add: *wt-blocks confs-conv-map intro: WT-implies-WTrt*)[1]

**apply**(erule *tconf-start-heap-start-tid*)

**apply**(erule *wf-prog-wf-syscls*)

**done**

**lemma** *J-start-state-sconf-type-ok*:

**assumes** *wf: wf-J-prog P*

**and** *ok: wf-start-state P C M vs*

**shows** *ts-ok*  $(\lambda t \ x \ h. \exists ET. \text{sconf-type-ok } ET \ t \ x \ h) (\text{thr } (J\text{-start-state } P \ C \ M \ \text{vs})) \text{ start-heap}$

**using** *sconf-type-ts-ok-J-start-state*[*OF assms*]

**unfolding** *shr-start-state* **by**(rule *ts-inv-into-ts-ok-Ex*)

**end**

**context** *J-conf-read* **begin**

**lemma** *red-preserves-type-ok*:

$\llbracket \text{extTA}, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{wf-J-prog } P; E \vdash s \ \checkmark; \text{type-ok } P \ (E, \ T) \ e \ (hp \ s); P, hp \ s \vdash t \ \checkmark/t \rrbracket$

$\Longrightarrow \text{type-ok } P \ (E, \ T) \ e' \ (hp \ s')$

**apply**(*clarsimp simp add: type-ok-def*)

**apply**(*subgoal-tac*  $\exists T''. P, E, hp \ s' \vdash e' : T'' \wedge P \vdash T'' \leq T'$ )

**apply**(*fast elim: widen-trans*)

**by**(rule *subject-reduction*)

**lemma** *lifting-inv-sconf-subject-ok*:

**assumes** *wf*: *wf-J-prog P*

**shows** *lifting-inv final-expr (mred P) sconf-type-ok*

**proof**(*unfold-locales*)

**fix** *t x m ta x' m' i*

**assume** *mred*: *mred P t (x, m) ta (x', m')*

**and** *sconf-type-ok i t x m*

**moreover obtain** *e l* **where** *x [simp]*: *x = (e, l)* **by**(*cases x, auto*)

**moreover obtain** *e' l'* **where** *x' [simp]*: *x' = (e', l')* **by**(*cases x', auto*)

**moreover obtain** *E T* **where** *i [simp]*: *i = (E, T)* **by**(*cases i, auto*)

**ultimately have** *sconf-type: sconf-type-ok (E, T) t (e, l) m*

**and** *red*: *P, t ⊢ ⟨e, (m, l)⟩ -ta→ ⟨e', (m', l')⟩* **by** *auto*

**from** *sconf-type* **have** *sconf*: *E ⊢ (m, l) √* **and** *type-ok P (E, T) e m* **and** *tconf*: *P, m ⊢ t √*  
**by**(*auto simp add: sconf-type-ok-def*)

**then obtain** *T'* **where** *P, E, m ⊢ e : T' P ⊢ T' ≤ T* **by**(*auto simp add: type-ok-def*)

**from** *⟨E ⊢ (m, l) √⟩ ⟨P, E, m ⊢ e : T'⟩ red tconf*

**have** *E ⊢ (m', l') √* **by**(*auto elim: red-preserves-sconf*)

**moreover**

**from** *red ⟨P, E, m ⊢ e : T'⟩ wf ⟨E ⊢ (m, l) √⟩ tconf*

**obtain** *T''* **where** *P, E, m' ⊢ e' : T'' P ⊢ T'' ≤ T'*

**by**(*auto dest: subject-reduction*)

**note** *⟨P, E, m' ⊢ e' : T''⟩*

**moreover**

**from** *⟨P ⊢ T'' ≤ T'⟩ ⟨P ⊢ T' ≤ T⟩*

**have** *P ⊢ T'' ≤ T* **by**(*rule widen-trans*)

**moreover from** *mred tconf* **have** *P, m' ⊢ t √* **by**(*rule red-tconf.preserves-red*)

**ultimately have** *sconf-type-ok (E, T) t (e', l') m'*

**by**(*auto simp add: sconf-type-ok-def type-ok-def*)

**thus** *sconf-type-ok i t x' m' by simp*

**next**

**fix** *t x m ta x' m' i t'' x''*

**assume** *mred*: *mred P t (x, m) ta (x', m')*

**and** *sconf-type-ok i t x m*

**and** *NewThread t'' x'' m' ∈ set {ta}\_t*

**moreover obtain** *e l* **where** *x [simp]*: *x = (e, l)* **by**(*cases x, auto*)

**moreover obtain** *e' l'* **where** *x' [simp]*: *x' = (e', l')* **by**(*cases x', auto*)

**moreover obtain** *E T* **where** *i [simp]*: *i = (E, T)* **by**(*cases i, auto*)

**moreover obtain** *e'' l''* **where** *x'' [simp]*: *x'' = (e'', l'')* **by**(*cases x'', auto*)

**ultimately have** *sconf-type: sconf-type-ok (E, T) t (e, l) m*

**and** *red*: *P, t ⊢ ⟨e, (m, l)⟩ -ta→ ⟨e', (m', l')⟩*

**and** *nt*: *NewThread t'' (e'', l'') m' ∈ set {ta}\_t* **by** *auto*

**from** *sconf-type* **have** *sconf*: *E ⊢ (m, l) √* **and** *type-ok P (E, T) e m* **and** *tconf*: *P, m ⊢ t √*  
**by**(*auto simp add: sconf-type-ok-def*)

**then obtain** *T'* **where** *P, E, m ⊢ e : T' P ⊢ T' ≤ T* **by**(*auto simp add: type-ok-def*)

**from** *nt ⟨P, E, m ⊢ e : T'⟩ red* **have**  $\exists E T. P, E, m' \vdash e'' : T \wedge P, m' \vdash l'' (\leq) E$

**by**(*fastforce dest: red-type-newthread[OF wf]*)

**then obtain** *E'' T''* **where** *P, E'', m' ⊢ e'' : T'' P, m' ⊢ l'' (\leq) E''* **by** *blast*

**moreover**

**from** *sconf red ⟨P, E, m ⊢ e : T'⟩ tconf* **have** *E ⊢ (m', l') √*

**by**(*auto intro: red-preserves-sconf*)

**moreover from** *mred tconf ⟨NewThread t'' x'' m' ∈ set {ta}\_t⟩* **have** *P, m' ⊢ t'' √*

**by**(*rule red-tconf.preserves-NewThread*)

**ultimately show**  $\exists i''. sconf-type-ok i'' t'' x'' m'$

**by**(*auto simp add: sconf-type-ok-def type-ok-def sconf-def*)

```

next
  fix  $t\ x\ m\ ta\ x'\ m'\ i\ i''\ t''\ x''$ 
  assume  $mred: mred\ P\ t\ (x, m)\ ta\ (x', m')$ 
    and  $sconf\text{-}type\text{-}ok\ i\ t\ x\ m$ 
    and  $sconf\text{-}type\text{-}ok\ i''\ t''\ x''\ m$ 
  moreover obtain  $e\ l$  where  $x\ [simp]: x = (e, l)$  by(cases  $x$ , auto)
  moreover obtain  $e'\ l'$  where  $x'\ [simp]: x' = (e', l')$  by(cases  $x'$ , auto)
  moreover obtain  $E\ T$  where  $i\ [simp]: i = (E, T)$  by(cases  $i$ , auto)
  moreover obtain  $e''\ l''$  where  $x''\ [simp]: x'' = (e'', l'')$  by(cases  $x''$ , auto)
  moreover obtain  $E''\ T''$  where  $i''\ [simp]: i'' = (E'', T'')$  by(cases  $i''$ , auto)
  ultimately have  $sconf\text{-}type: sconf\text{-}type\text{-}ok\ (E, T)\ t\ (e, l)\ m$ 
    and  $red: P, t \vdash \langle e, (m, l) \rangle \text{-}ta \rightarrow \langle e', (m', l') \rangle$ 
    and  $sc: sconf\text{-}type\text{-}ok\ (E'', T'')\ t''\ (e'', l'')\ m$  by auto
  from  $sconf\text{-}type$  obtain  $T'$  where  $P, E, m \vdash e : T'$  and  $P, m \vdash t \sqrt{t}$ 
    by(auto simp add: sconf-type-ok-def type-ok-def)
  from  $sc$  have  $sconf: E'' \vdash (m, l'') \sqrt{\phantom{x}}$  and  $type\text{-}ok\ P\ (E'', T'')\ e''\ m$  and  $P, m \vdash t'' \sqrt{t}$ 
    by(auto simp add: sconf-type-ok-def)
  then obtain  $T'''$  where  $P, E'', m \vdash e'' : T'''$   $P \vdash T''' \leq T''$  by(auto simp add: type-ok-def)
  moreover from  $red\ \langle P, E'', m \vdash e'' : T''' \rangle$  have  $P, E'', m' \vdash e'' : T'''$ 
    by(rule red-preserve-welltype)
  moreover from  $sconf\ red\ \langle P, E, m \vdash e : T' \rangle$  have  $hconf\ m'$ 
    unfolding sconf-def by(auto dest: red-preserves-hconf)
  moreover {
    from  $red$  have  $hext\ m\ m'$  by(auto dest: red-hext-incr)
    moreover from  $sconf$  have  $P, m \vdash l'' (\leq) E''$  preallocated  $m$ 
      by(simp-all add: sconf-def)
    ultimately have  $P, m' \vdash l'' (\leq) E''$  preallocated  $m'$ 
      by(blast intro: lconf-hext preallocated-hext)+ }
  moreover from  $mred\ \langle P, m \vdash t \sqrt{t} \rangle\ \langle P, m \vdash t'' \sqrt{t} \rangle$ 
    have  $P, m' \vdash t'' \sqrt{t}$  by(rule red-tconf.preserves-other)
  ultimately have  $sconf\text{-}type\text{-}ok\ (E'', T'')\ t''\ (e'', l'')\ m'$ 
    by(auto simp add: sconf-type-ok-def sconf-def type-ok-def)
  thus  $sconf\text{-}type\text{-}ok\ i''\ t''\ x''\ m'$  by simp
qed
end

```

#### 4.14.4 wf-red

context  $J$ -progress begin

context begin

```

declare  $red\text{-}mthr.\text{actions}\text{-}ok\text{-}iff\ [simp\ del]$ 
declare  $red\text{-}mthr.\text{actions}\text{-}ok.\text{cases}\ [rule\ del]$ 
declare  $red\text{-}mthr.\text{actions}\text{-}ok.\text{intros}\ [rule\ del]$ 

```

lemma assumes  $wf: wf\text{-}prog\ wf\text{-}md\ P$

shows  $red\text{-}wf\text{-}red\text{-}aux:$

```

[[  $P, t \vdash \langle e, s \rangle \text{-}ta \rightarrow \langle e', s' \rangle; \neg red\text{-}mthr.\text{actions}\text{-}ok'\ (ls, (ts, m), ws, is)\ t\ ta;$ 
   $sync\text{-}ok\ e; hconf\ (hp\ s); P, hp\ s \vdash t \sqrt{t};$ 
   $\forall l. has\text{-}locks\ (ls\ \$\ l)\ t \geq expr\text{-}locks\ e\ l;$ 
   $ws\ t = None \vee$ 
   $(\exists a\ vs\ w\ T\ Ts\ Tr\ D. call\ e = \lfloor (a, wait, vs) \rfloor \wedge typeof\text{-}addr\ (hp\ s)\ a = \lfloor T \rfloor \wedge P \vdash class\text{-}type\text{-}of\ T$ 

```

*sees wait*:  $Ts \rightarrow Tr = \text{Native in } D \wedge ws \ t = \lfloor \text{PostWS } w \rfloor \rfloor$   
 $\implies \exists e'' s'' ta'. P, t \vdash \langle e, s \rangle -ta' \rightarrow \langle e'', s'' \rangle \wedge$   
 $(\text{red-mthr.actions-ok } (ls, (ts, m), ws, is) \ t \ ta' \vee$   
 $\text{red-mthr.actions-ok}' (ls, (ts, m), ws, is) \ t \ ta' \wedge \text{red-mthr.actions-subset } ta' \ ta)$   
**(is**  $\llbracket -; -; -; -; -; ?\text{wakeup } e \ s \rrbracket \implies ?\text{concl } e \ s \ ta)$   
**and** *reds-wf-red-aux*:  
 $\llbracket P, t \vdash \langle e, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \neg \text{red-mthr.actions-ok}' (ls, (ts, m), ws, is) \ t \ ta;$   
 $\text{sync-oks } es; h\text{conf } (hp \ s); P, hp \ s \vdash t \ \sqrt{t};$   
 $\forall l. \text{has-locks } (ls \ \$ \ l) \ t \geq \text{expr-lockss } es \ l;$   
 $ws \ t = \text{None} \vee$   
 $(\exists a \ vs \ w \ T \ Ts \ T \ Tr \ D. \text{calls } es = \lfloor (a, \text{wait}, ws) \rfloor \wedge \text{typeof-addr } (hp \ s) \ a = \lfloor T \rfloor \wedge P \vdash \text{class-type-of}$   
 $T \text{ sees wait: } Ts \rightarrow Tr = \text{Native in } D \wedge ws \ t = \lfloor \text{PostWS } w \rfloor \rfloor \rrbracket$   
 $\implies \exists es'' s'' ta'. P, t \vdash \langle es, s \rangle [-ta' \rightarrow] \langle es'', s'' \rangle \wedge$   
 $(\text{red-mthr.actions-ok } (ls, (ts, m), ws, is) \ t \ ta' \vee$   
 $\text{red-mthr.actions-ok}' (ls, (ts, m), ws, is) \ t \ ta' \wedge \text{red-mthr.actions-subset } ta' \ ta)$   
**proof**(*induct rule: red-reds.inducts*)  
**case** (*SynchronizedRed2*  $e \ s \ ta \ e' \ s' \ a$ )  
**note**  $IH = \langle \llbracket \neg \text{red-mthr.actions-ok}' (ls, (ts, m), ws, is) \ t \ ta; \text{sync-ok } e; h\text{conf } (hp \ s); P, hp \ s \vdash t \ \sqrt{t};$   
 $\forall l. \text{expr-locks } e \ l \leq \text{has-locks } (ls \ \$ \ l) \ t; ?\text{wakeup } e \ s \rrbracket$   
 $\implies ?\text{concl } e \ s \ ta \rangle$   
**note**  $\langle \neg \text{red-mthr.actions-ok}' (ls, (ts, m), ws, is) \ t \ ta \rangle$   
**moreover from**  $\langle \text{sync-ok } (\text{insync}(a) \ e) \rangle$  **have**  $\text{sync-ok } e$  **by** *simp*  
**moreover note**  $\langle h\text{conf } (hp \ s) \rangle \langle P, hp \ s \vdash t \ \sqrt{t} \rangle$   
**moreover from**  $\langle \forall l. \text{expr-locks } (\text{insync}(a) \ e) \ l \leq \text{has-locks } (ls \ \$ \ l) \ t \rangle$   
**have**  $\forall l. \text{expr-locks } e \ l \leq \text{has-locks } (ls \ \$ \ l) \ t$  **by**(*force split: if-split-asm*)  
**moreover from**  $\langle ?\text{wakeup } (\text{insync}(a) \ e) \ s \rangle$  **have**  $?\text{wakeup } e \ s$  **by** *auto*  
**ultimately have**  $?\text{concl } e \ s \ ta$  **by**(*rule IH*)  
**thus**  $?\text{case}$  **by**(*fastforce intro: red-reds.SynchronizedRed2*)  
**next**  
**case** *RedCall* **thus**  $?\text{case}$   
**by**(*auto simp add: is-val-iff contains-insync-conv contains-insyncs-conv red-mthr.actions-ok'-empty*  
*red-mthr.actions-ok'-ta-upd-obs dest: sees-method-fun*)  
**next**  
**case** (*RedCallExternal*  $s \ a \ U \ M \ Ts \ T \ D \ vs \ ta \ va \ h' \ ta' \ e' \ s'$ )  
**from**  $\langle ?\text{wakeup } (\text{addr } a \cdot M(\text{map } \text{Val } vs)) \ s \rangle$   
**have**  $\text{wset } (ls, (ts, m), ws, is) \ t = \text{None} \vee (M = \text{wait} \wedge (\exists w. \text{wset } (ls, (ts, m), ws, is) \ t = \lfloor \text{PostWS } w \rfloor))$  **by** *auto*  
**with**  $wf \ \langle P, t \vdash \langle a \cdot M(vs), hp \ s \rangle -ta \rightarrow \text{ext } \langle va, h' \rangle \rangle \langle P, hp \ s \vdash t \ \sqrt{t} \rangle \langle h\text{conf } (hp \ s) \rangle$   
**obtain**  $ta'' \ va' \ h''$  **where**  $\text{red}' : P, t \vdash \langle a \cdot M(vs), hp \ s \rangle -ta'' \rightarrow \text{ext } \langle va', h'' \rangle$   
**and**  $aok : \text{red-mthr.actions-ok } (ls, (ts, m), ws, is) \ t \ ta'' \vee$   
 $\text{red-mthr.actions-ok}' (ls, (ts, m), ws, is) \ t \ ta'' \wedge \text{final-thread.actions-subset } ta'' \ ta$   
**by**(*rule red-external-wf-red*)  
**from**  $aok \ \langle ta' = \text{extTA2J } P \ ta \rangle$   
**have**  $\text{red-mthr.actions-ok } (ls, (ts, m), ws, is) \ t \ (\text{extTA2J } P \ ta'') \vee$   
 $\text{red-mthr.actions-ok}' (ls, (ts, m), ws, is) \ t \ (\text{extTA2J } P \ ta') \wedge \text{red-mthr.actions-subset } (\text{extTA2J } P \ ta') \ ta'$   
**by**(*auto simp add: red-mthr.actions-ok'-convert-extTA red-mthr.actions-ok-iff elim: final-thread.actions-subset.cases*  
*del: subsetI*)  
**moreover from**  $\text{red}' \ \langle \text{typeof-addr } (hp \ s) \ a = \lfloor U \rfloor \rangle \langle P \vdash \text{class-type-of } U \text{ sees } M : Ts \rightarrow T = \text{Native in } D \rangle$   
**obtain**  $s'' \ e''$  **where**  $P, t \vdash \langle \text{addr } a \cdot M(\text{map } \text{Val } vs), s \rangle -\text{extTA2J } P \ ta'' \rightarrow \langle e'', s'' \rangle$   
**by**(*fastforce intro: red-reds.RedCallExternal*)  
**ultimately show**  $?\text{case}$  **by** *blast*  
**next**

```

case LockSynchronized
hence False by(auto simp add: lock-ok-las'-def finfun-upd-apply ta-upd-simps)
thus ?case ..
next
case (UnlockSynchronized a v s)
from  $\langle \forall l. \text{expr-locks } (\text{insync}(a) \text{ Val } v) \ l \leq \text{has-locks } (ls \ \$ \ l) \ t \rangle$ 
have has-lock (ls $ a) t by(force split: if-split-asm)
with UnlockSynchronized have False by(auto simp add: lock-ok-las'-def finfun-upd-apply ta-upd-simps)
thus ?case ..
next
case (SynchronizedThrow2 a ad s)
from  $\langle \forall l. \text{expr-locks } (\text{insync}(a) \text{ Throw } ad) \ l \leq \text{has-locks } (ls \ \$ \ l) \ t \rangle$ 
have has-lock (ls $ a) t by(force split: if-split-asm)
with SynchronizedThrow2 have False
  by(auto simp add: lock-ok-las'-def finfun-upd-apply ta-upd-simps)
thus ?case ..
next
case BlockRed thus ?case by(simp)(blast intro: red-reds.intros)
qed
(simp-all add: is-val-iff contains-insync-conv contains-insyncs-conv red-mthr.actions-ok'-empty
  red-mthr.actions-ok'-ta-upd-obs thread-action'-to-thread-action.simps red-mthr.actions-ok-iff
  split: if-split-asm del: split-paired-Ex,
  (blast intro: red-reds.intros elim: add-leE)+)

end

end

context J-heap-base begin

lemma shows red-ta-satisfiable:
  P, t  $\vdash \langle e, s \rangle \text{--ta--} \langle e', s' \rangle \implies \exists s. \text{red-mthr.actions-ok } s \ t \ ta$ 
  and reds-ta-satisfiable:
  P, t  $\vdash \langle es, s \rangle \text{--ta--} \langle es', s' \rangle \implies \exists s. \text{red-mthr.actions-ok } s \ t \ ta$ 
apply(induct rule: red-reds.inducts)
apply(fastforce simp add: lock-ok-las-def finfun-upd-apply intro: exI[where x=K$ None] exI[where
  x=K$ [(t, 0)]] may-lock.intros dest: red-external-ta-satisfiable[where final=final-expr :: ('addr expr
   $\times \text{'addr locals}) \Rightarrow \text{bool}]$ )+
done

end

context J-typesafe begin

lemma wf-progress:
  assumes wf: wf-J-prog P
  shows progress final-expr (mred P)
    (red-mthr.wset-Suspend-ok P ( $\{s. \text{sync-es-ok } (\text{thr } s) \ (\text{shr } s) \wedge \text{lock-ok } (\text{locks } s) \ (\text{thr } s)\} \cap \{s.$ 
 $\exists Es. \text{sconf-type-ts-ok } Es \ (\text{thr } s) \ (\text{shr } s)\} \cap \{s. \text{def-ass-ts-ok } (\text{thr } s) \ (\text{shr } s)\}$ ))
  (is progress - - ?wf-state)
proof
  {
    fix s t x ta x' m' w
    assume mred P t (x, shr s) ta (x', m')

```

```

    and Suspend: Suspend  $w \in \text{set } \llbracket ta \rrbracket_w$ 
  moreover obtain  $e \ xs$  where  $x: x = (e, xs)$  by(cases  $x$ )
  moreover obtain  $e' \ xs'$  where  $x': x' = (e', xs')$  by(cases  $x'$ )
  ultimately have  $\text{red}: P, t \vdash \langle e, (\text{shr } s, xs) \rangle -ta\rightarrow \langle e', (m', xs') \rangle$  by simp
  from red-Suspend-is-call[OF red Suspend]
  show  $\neg \text{final-expr } x'$  by(auto simp add:  $x'$ )
}
note Suspend-final = this
{
  fix s
  assume  $s: s \in ?\text{wf-state}$ 
  hence lock-thread-ok (locks  $s$ ) (thr  $s$ )
    by(auto dest: red-mthr.wset-Suspend-okD1 intro: lock-ok-lock-thread-ok)
  moreover
  have red-mthr.wset-final-ok (wset  $s$ ) (thr  $s$ )
  proof(rule red-mthr.wset-final-okI)
    fix  $t \ w$ 
    assume  $\text{wset } s \ t = \lfloor w \rfloor$ 
    from red-mthr.wset-Suspend-okD2[OF  $s$  this]
    obtain  $x0 \ ta \ x \ m1 \ w' \ ln''$  and  $s0 :: ('addr, 'thread-id, 'heap) \ J\text{-state}$ 
      where  $mred: mred \ P \ t \ (x0, \text{shr } s0) \ ta \ (x, m1)$ 
      and Suspend: Suspend  $w' \in \text{set } \llbracket ta \rrbracket_w$ 
      and  $tst: \text{thr } s \ t = \lfloor (x, ln'') \rfloor$  by blast
    from Suspend-final[OF  $mred$  Suspend]  $tst$ 
    show  $\exists x \ ln. \text{thr } s \ t = \lfloor (x, ln) \rfloor \wedge \neg \text{final-expr } x$  by blast
  qed
  ultimately show lock-thread-ok (locks  $s$ ) (thr  $s$ )  $\wedge$  red-mthr.wset-final-ok (wset  $s$ ) (thr  $s$ ) ..
}
next
fix  $s \ t \ ex \ ta \ e'x' \ m'$ 
assume  $\text{wfs}: s \in ?\text{wf-state}$ 
  and  $\text{thr } s \ t = \lfloor (ex, \text{no-wait-locks}) \rfloor$ 
  and  $mred \ P \ t \ (ex, \text{shr } s) \ ta \ (e'x', m')$ 
  and  $\text{wait}: \neg \text{waiting } (\text{wset } s \ t)$ 
moreover obtain  $ls \ ts \ m \ ws \ is$  where  $s: s = (ls, (ts, m), ws, is)$  by(cases  $s$ ) fastforce
moreover obtain  $e \ x$  where  $ex: ex = (e, x)$  by(cases  $ex$ )
moreover obtain  $e' \ x'$  where  $e'x': e'x' = (e', x')$  by(cases  $e'x'$ )
ultimately have  $tst: ts \ t = \lfloor (ex, \text{no-wait-locks}) \rfloor$ 
  and  $\text{red}: P, t \vdash \langle e, (m, x) \rangle -ta\rightarrow \langle e', (m', x') \rangle$  by auto
from  $wf$  have  $wwf: ww\text{-}J\text{-prog } P$  by(rule  $wf\text{-}prog\text{-}wwf\text{-}prog$ )
from  $\text{wfs } s$  obtain  $Es$  where  $\text{aeos}: \text{sync-es-ok } ts \ m$ 
  and  $\text{lockok}: \text{lock-ok } ls \ ts$ 
  and  $\text{sconf-type-ts-ok } Es \ ts \ m$ 
  by(auto dest: red-mthr.wset-Suspend-okD1)
with  $tst \ ex$  obtain  $E \ T$  where  $\text{sconf}: \text{sconf-type-ok } (E, T) \ t \ (e, x) \ m$ 
  and  $\text{aoe}: \text{sync-ok } e$  by(fastforce dest:  $ts\text{-okD } ts\text{-invD}$ )
then obtain  $T'$  where  $\text{hconf } m \ P, E, m \vdash e : T' \text{ preallocated } m$ 
  by(auto simp add:  $\text{sconf-type-ok-def } \text{sconf-def } \text{type-ok-def}$ )
from  $\langle \text{sconf-type-ts-ok } Es \ ts \ m \rangle \ s$  have  $\text{thread-conf } P \ (\text{thr } s) \ (\text{shr } s)$ 
  by(auto dest:  $ts\text{-invD}$  intro!:  $ts\text{-okI}$  simp add:  $\text{sconf-type-ok-def}$ )
with  $\langle \text{thr } s \ t = \lfloor (ex, \text{no-wait-locks}) \rfloor \rangle$  have  $P, \text{shr } s \vdash t \ \sqrt{t}$  by(auto dest:  $ts\text{-okD}$ )

show  $\exists ta' \ x' \ m'. mred \ P \ t \ (ex, \text{shr } s) \ ta' \ (x', m') \wedge$ 
  ( $\text{red-mthr.actions-ok } s \ t \ ta' \vee \text{red-mthr.actions-ok}' \ s \ t \ ta' \wedge \text{red-mthr.actions-subset } ta' \ ta$ )

```

```

proof(cases red-mthr.actions-ok' s t ta)
  case True
    have red-mthr.actions-subset ta ta ..
    with True  $\langle mred\ P\ t\ (ex,\ shr\ s)\ ta\ (e'x',\ m') \rangle$  show ?thesis by blast
  next
    case False
    from lock-okD2[OF lockok, OF tst[unfolded ex]]
    have locks:  $\forall l.\ has\_locks\ (ls\ \$\ l)\ t \geq expr\_locks\ e\ l$  by simp
    have ws t = None  $\vee (\exists a\ vs\ w\ T\ Ts\ Tr\ D.\ call\ e = \lfloor (a,\ wait,\ vs) \rfloor \wedge typeof\_addr\ (hp\ (m,\ x))\ a = \lfloor T \rfloor \wedge P \vdash class\_type\_of\ T\ sees\ wait: Ts \rightarrow Tr = Native\ in\ D \wedge ws\ t = \lfloor PostWS\ w \rfloor)$ 
    proof(cases ws t)
      case None thus ?thesis ..
    next
      case (Some w)
      with red-mthr.wset-Suspend-okD2[OF wfs, of t w] tst ex s
      obtain e0 x0 m0 ta0 w' s1 tta1
        where red0:  $P, t \vdash \langle e0,\ (m0,\ x0) \rangle - ta0 \rightarrow \langle e,\ (shr\ s1,\ x) \rangle$ 
        and Suspend:  $Suspend\ w' \in set\ \{ta0\}_w$ 
        and s1:  $P \vdash s1 \rightarrow^* s$  by auto
      from red-Suspend-is-call[OF red0 Suspend] obtain a vs T Ts Tr D
        where call:  $call\ e = \lfloor (a,\ wait,\ vs) \rfloor$ 
        and type:  $typeof\_addr\ m0\ a = \lfloor T \rfloor$ 
        and iec:  $P \vdash class\_type\_of\ T\ sees\ wait: Ts \rightarrow Tr = Native\ in\ D$  by fastforce
      from red0 have m0  $\trianglelefteq shr\ s1$  by(auto dest: red-hext-incr)
      also from s1 have shr s1  $\trianglelefteq shr\ s$  by(rule RedT-hext-incr)
      finally have  $typeof\_addr\ (shr\ s)\ a = \lfloor T \rfloor$  using type
        by(rule typeof-addr-hext-mono)
      moreover from Some wait s obtain w' where ws t =  $\lfloor PostWS\ w' \rfloor$ 
        by(auto simp add: not-waiting-iff)
      ultimately show ?thesis using call iec s by auto
    qed
    from red-wf-red-aux[OF wf red False[unfolded s] aoe - - locks, OF - - this]  $\langle hconf\ m \rangle \langle P, shr\ s \vdash t \sqrt{t} \rangle ex\ s$ 
    show ?thesis by fastforce
  qed
next
  fix s t x
  assume wfs:  $s \in ?wf\_state$ 
  and tst:  $thr\ s\ t = \lfloor (x,\ no\_wait\_locks) \rfloor$ 
  and nfin:  $\neg final\_expr\ x$ 
  obtain e xs where  $x: x = (e,\ xs)$  by(cases x)
  from wfs have def-ass-ts-ok (thr s) (shr s) by(auto dest: red-mthr.wset-Suspend-okD1)
  with tst x have DA:  $\mathcal{D}\ e\ \lfloor dom\ xs \rfloor$  by(auto dest: ts-okD)
  from wfs obtain Es where sconf-type-ts-ok Es (thr s) (shr s)
    by(auto dest: red-mthr.wset-Suspend-okD1)
  with tst x obtain E T where sconf-type-ok (E, T) t (e, xs) (shr s) by(auto dest: ts-invD)
  then obtain T' where  $hconf\ (shr\ s)\ P, E, shr\ s \vdash e : T'$ 
    by(auto simp add: sconf-type-ok-def sconf-def type-ok-def)
  from red-progress(1)[OF wf-prog-wwf-prog[OF wf] this DA, where extTA=extTA2J P and t=t] nfin
  show  $\exists ta\ x'\ m'. mred\ P\ t\ (x,\ shr\ s)\ ta\ (x',\ m')$  by fastforce
next
  fix s t x xm ta xm'
  assume s  $\in ?wf\_state$ 

```



```

  and thr s t = [(x, no-wait-locks)]
  and mred P t xm ta xm'
  and Notified ∈ set {ta}_w ∨ WokenUp ∈ set {ta}_w
thus collect-waits ta = {}
  by(auto dest: red-ta-Wakeup-no-Join-no-Lock-no-Interrupt simp: split-beta)
next
  fix s t x ta x' m'
  assume s ∈ ?wf-state
  and thr s t = [(x, no-wait-locks)]
  and mred P t (x, shr s) ta (x', m')
thus ∃ s'. red-mthr.actions-ok s' t ta
  by(fastforce simp add: split-beta dest!: red-ta-satisfiable)
qed

```

**lemma** *redT-progress-deadlock:*

```

  assumes wf: wf-J-prog P
  and wf-start: wf-start-state P C M vs
  and Red: P ⊢ J-start-state P C M vs ->ttas->* s
  and ndead: ¬ red-mthr.deadlock P s
  shows ∃ t' ta' s'. P ⊢ s -t'>ta'> s'

```

**proof** –

```

  let ?wf-state = red-mthr.wset-Suspend-ok P ({s. sync-es-ok (thr s) (shr s) ∧ lock-ok (locks s) (thr s)}) ∩ {s. ∃ Es. sconf-type-ts-ok Es (thr s) (shr s)} ∩ {s. def-ass-ts-ok (thr s) (shr s)}

```

**interpret** *red-mthr: progress*

*final-expr* mred P convert-RA ?wf-state

**using** wf **by**(rule wf-progress)

**from** wf-start **obtain** Ts T pns body D

**where** start: start-heap-ok P ⊢ C sees M:Ts→T = [(pns, body)] in D P, start-heap ⊢ vs [≤] Ts

**by**(cases) auto

**from** start **have** len: length Ts = length vs **by**(auto dest: list-all2-lengthD)

**have** invariant3p (mred T P) ?wf-state

```

  by(rule red-mthr.invariant3p-wset-Suspend-ok) (intro invariant3p-IntI invariant3p-sync-es-ok-lock-ok[OF wf]
lifting-inv.invariant3p-ts-inv[OF lifting-inv-sconf-subject-ok[OF wf]] lifting-wf.invariant3p-ts-ok[OF
lifting-wf-def-ass[OF wf]])

```

**moreover note** Red **moreover**

**have** start': J-start-state P C M vs ∈ ?wf-state

**apply**(rule red-mthr.wset-Suspend-okI)

```

  apply(blast intro: sconf-type-ts-ok-J-start-state sync-es-ok-J-start-state lock-ok-J-start-state def-ass-ts-ok-J-start-state
start wf len len[symmetric] wf-start)

```

**apply**(simp add: start-state-def split-beta)

**done**

**ultimately have** s ∈ ?wf-state **unfolding** red-mthr.RedT-def

**by**(rule invariant3p-rtrancl3p)

**thus** ?thesis **using** ndead **by**(rule red-mthr.redT-progress)

**qed**

**lemma** *redT-progress-deadlocked:*

```

  assumes wf: wf-J-prog P
  and wf-start: wf-start-state P C M vs
  and Red: P ⊢ J-start-state P C M vs ->ttas->* s
  and ndead: red-mthr.not-final-thread s t ¬ t ∈ red-mthr.deadlocked P s
  shows ∃ t' ta' s'. P ⊢ s -t'>ta'> s'
using wf wf-start Red

```

```

proof(rule redT-progress-deadlock)
  from ndead show  $\neg$  red-mthr.deadlock  $P\ s$ 
    unfolding red-mthr.deadlock-eq-deadlocked'
    by(auto simp add: red-mthr.deadlocked'-def)
qed

```

#### 4.14.5 Type safety proof

```

theorem TypeSafetyT:
  fixes  $C$  and  $M$  and  $ttas$  and  $Es$ 
  defines  $Es == J\text{-sconf-type-ET-start } P\ C\ M$ 
  and  $Es' == \text{upd-invs } Es\ \text{sconf-type-ok } (\text{concat } (\text{map } (\text{thr-a} \circ \text{snd})\ ttas))$ 
  assumes  $wf: wf\text{-J-prog } P$ 
  and  $\text{start-wf}: wf\text{-start-state } P\ C\ M\ vs$ 
  and  $\text{RedT}: P \vdash J\text{-start-state } P\ C\ M\ vs \dashv\triangleright ttas \rightarrow^* s'$ 
  and  $\text{nored}: \neg (\exists t\ ta\ s''. P \vdash s' -t\triangleright ta \rightarrow s'')$ 
  shows  $\text{thread-conf } P\ (\text{thr } s')\ (\text{shr } s')$ 
  and  $\text{thr } s' t = \lfloor ((e', x'), \text{ln}') \rfloor \implies$ 
     $(\exists v.\ e' = \text{Val } v \wedge (\exists E\ T.\ Es' t = \lfloor (E, T) \rfloor \wedge P, \text{shr } s' \vdash v : \leq T) \wedge \text{ln}' = \text{no-wait-locks})$ 
     $\vee (\exists a\ C.\ e' = \text{Throw } a \wedge \text{typeof-addr } (\text{shr } s')\ a = \lfloor \text{Class-type } C \rfloor \wedge P \vdash C \preceq^* \text{Throwable} \wedge \text{ln}'$ 
     $= \text{no-wait-locks})$ 
     $\vee (t \in \text{red-mthr.deadlocked } P\ s' \wedge (\exists E\ T.\ Es' t = \lfloor (E, T) \rfloor \wedge (\exists T'. P, E, \text{shr } s' \vdash e' : T' \wedge P$ 
     $\vdash T' \leq T)))$ 
    (is -  $\implies$  ?thesis2)
  and  $Es \subseteq_m Es'$ 
proof -
  from start-wf obtain  $Ts\ T\ pns$  body  $D$ 
    where start-heap: start-heap-ok
    and sees:  $P \vdash C\ \text{sees } M: Ts \rightarrow T = \lfloor (pns, \text{body}) \rfloor$  in  $D$ 
    and conf:  $P, \text{start-heap} \vdash vs [\leq] Ts$ 
    by cases auto

  from RedT show  $\text{thread-conf } P\ (\text{thr } s')\ (\text{shr } s')$ 
    by(rule red-tconf.RedT-preserves)(rule thread-conf-start-state[OF start-heap wf-prog-wf-syscls[OF wf]])

  show  $Es \subseteq_m Es'$  using RedT ts-inv-ok-J-sconf-type-ET-start
    unfolding Es'-def Es-def by(rule red-mthr.RedT-upd-inv-ext)

  assume  $\text{thr } s' t = \lfloor ((e', x'), \text{ln}') \rfloor$ 
  moreover obtain  $ls'\ ts'\ m'\ ws'\ is'$  where  $s'[\text{simp}]: s' = (ls', (ts', m'), ws', is')$  by(cases  $s'$ ) fastforce
  ultimately have  $es't: ts' t = \lfloor ((e', x'), \text{ln}') \rfloor$  by simp
  from wf have wwf:  $wf\text{-J-prog } P$  by(rule wf-prog-wwf-prog)
  from conf have len:  $\text{length } vs = \text{length } Ts$  by(rule list-all2-lengthD)
  from RedT def-ass-ts-ok-J-start-state[OF wf sees len] have defass':  $\text{def-ass-ts-ok } ts'\ m'$ 
    by(fastforce dest: lifting-wf.RedT-preserves[OF lifting-wf-def-ass, OF wf])
  from RedT sync-es-ok-J-start-state[OF wf sees len[symmetric]] lock-ok-J-start-state[OF wf sees len[symmetric]]
  have lock':  $\text{lock-ok } ls'\ ts'$  by (fastforce dest: RedT-preserves-lock-ok[OF wf])
  from RedT sync-es-ok-J-start-state[OF wf sees len[symmetric]] have addr':  $\text{sync-es-ok } ts'\ m'$ 
    by(fastforce dest: RedT-preserves-sync-ok[OF wf])
  from RedT sconf-type-ts-ok-J-start-state[OF wf start-wf]
  have sconf-subject':  $\text{sconf-type-ts-ok } Es'\ ts'\ m'$  unfolding Es'-def Es-def
    by(fastforce dest: lifting-inv.RedT-invariant[OF lifting-inv-sconf-subject-ok, OF wf] intro: thread-conf-start-state
    - wf-prog-wf-syscls[OF wf])

```

```

with  $es't$  obtain  $E\ T$  where  $ET: Es' t = \lfloor (E, T) \rfloor$ 
  and  $sconf\text{-}type\text{-}ok\ (E, T)\ t\ (e', x')\ m'$  by  $(auto\ dest!: ts\text{-}invD)$ 
{ assume  $final\ e'$ 
  have  $ln' = no\text{-}wait\text{-}locks$ 
  proof(rule ccontr)
    assume  $ln' \neq no\text{-}wait\text{-}locks$ 
    then obtain  $l$  where  $ln' \$ l > 0$ 
      by  $(auto\ simp\ add: neq\text{-}no\text{-}wait\text{-}locks\text{-}conv)$ 
    from  $lock'\ es't$  have  $has\text{-}locks\ (ls' \$ l)\ t + ln' \$ l = expr\text{-}locks\ e'\ l$ 
      by  $(auto\ dest: lock\text{-}okD2)$ 
    with  $\langle ln' \$ l > 0 \rangle$  have  $expr\text{-}locks\ e'\ l > 0$  by  $simp$ 
    moreover from  $\langle final\ e' \rangle$  have  $expr\text{-}locks\ e'\ l = 0$  by  $(rule\ final\text{-}locks)$ 
    ultimately show  $False$  by  $simp$ 
  qed }
note  $ln' = this$ 
{ assume  $\exists v. e' = Val\ v$ 
  then obtain  $v$  where  $v: e' = Val\ v$  by  $blast$ 
  with  $sconf\text{-}subject'\ ET\ es't$  have  $P, m' \vdash v : \leq T$ 
    by  $(auto\ dest: ts\text{-}invD\ simp\ add: type\text{-}ok\text{-}def\ sconf\text{-}type\text{-}ok\text{-}def\ conf\text{-}def)$ 
  moreover from  $v\ ln'$  have  $ln' = no\text{-}wait\text{-}locks$  by  $(auto)$ 
  ultimately have  $\exists v. e' = Val\ v \wedge (\exists E\ T. Es' t = \lfloor (E, T) \rfloor \wedge P, m' \vdash v : \leq T \wedge ln' = no\text{-}wait\text{-}locks)$ 
    using  $ET\ v$  by  $blast$  }
moreover
{ assume  $\exists a. e' = Throw\ a$ 
  then obtain  $a$  where  $a: e' = Throw\ a$  by  $blast$ 
  with  $sconf\text{-}subject'\ ET\ es't$  have  $\exists T'. P, E, m' \vdash e' : T' \wedge P \vdash T' \leq T$ 
    apply –
    apply  $(drule\ ts\text{-}invD, assumption)$ 
    by  $(clarsimp\ simp\ add: type\text{-}ok\text{-}def\ sconf\text{-}type\text{-}ok\text{-}def)$ 
  then obtain  $T'$  where  $P, E, m' \vdash e' : T'$  and  $P \vdash T' \leq T$  by  $blast$ 
  with  $a$  have  $\exists C. typeof\text{-}addr\ m'\ a = \lfloor Class\text{-}type\ C \rfloor \wedge P \vdash C \preceq^* Throwable$ 
    by  $(auto\ simp\ add: widen\text{-}Class)$ 
  moreover from  $a\ ln'$  have  $ln' = no\text{-}wait\text{-}locks$  by  $(auto)$ 
  ultimately have  $\exists a\ C. e' = Throw\ a \wedge typeof\text{-}addr\ m'\ a = \lfloor Class\text{-}type\ C \rfloor \wedge P \vdash C \preceq^* Throwable$ 
    using  $a$  by  $blast$  }
moreover
{ assume  $nfine': \neg final\ e'$ 
  with  $es't$  have  $red\text{-}mthr.\text{not}\text{-}final\text{-}thread\ s'\ t$ 
    by  $(auto\ intro: red\text{-}mthr.\text{not}\text{-}final\text{-}thread.\text{intros})$ 
  with  $nored$  have  $t \in red\text{-}mthr.\text{deadlocked}\ P\ s'$ 
    by  $-(erule\ contrapos\text{-}np, rule\ redT\text{-}progress\text{-}deadlocked[OF\ wf\ start\text{-}wf\ RedT])$ 
  moreover
  from  $\langle sconf\text{-}type\text{-}ok\ (E, T)\ t\ (e', x')\ m' \rangle$ 
  obtain  $T''$  where  $P, E, m' \vdash e' : T''\ P \vdash T'' \leq T$ 
    by  $(auto\ simp\ add: sconf\text{-}type\text{-}ok\text{-}def\ type\text{-}ok\text{-}def)$ 
  with  $ET$  have  $\exists E\ T. Es' t = \lfloor (E, T) \rfloor \wedge (\exists T'. P, E, m' \vdash e' : T' \wedge P \vdash T' \leq T)$ 
    by  $blast$ 
  ultimately have  $t \in red\text{-}mthr.\text{deadlocked}\ P\ s' \wedge (\exists E\ T. Es' t = \lfloor (E, T) \rfloor \wedge (\exists T'. P, E, m' \vdash e' : T' \wedge P \vdash T' \leq T)) \dots$ 
    by  $blast$ 
  ultimately show  $?thesis2$  by  $simp(blast)$ 
}
qed
end

```

end

## 4.15 Preservation of Deadlock

**theory** *Deadlocked*

**imports**

*ProgressThreaded*

**begin**

**context** *J-progress* **begin**

**lemma** *red-wt-hconf-hext*:

**assumes** *wf*: *wf-J-prog P*

**and** *hconf*: *hconf H*

**and** *tconf*:  $P, H \vdash t \sqrt{t}$

**shows**  $\llbracket \text{convert-extTA } \text{extNTA}, P, t \vdash \langle e, s \rangle \rightarrow \langle e', s' \rangle; P, E, H \vdash e : T; \text{hext } H \text{ (hp } s) \rrbracket$

$\implies \exists ta' e' s'. \text{convert-extTA } \text{extNTA}, P, t \vdash \langle e, (H, \text{lcl } s) \rangle \rightarrow \langle e', s' \rangle \wedge$

$\text{collect-locks } \llbracket ta \rrbracket_l = \text{collect-locks } \llbracket ta' \rrbracket_l \wedge \text{collect-cond-actions } \llbracket ta \rrbracket_c = \text{collect-cond-actions } \llbracket ta' \rrbracket_c \wedge$

$\text{collect-interrupts } \llbracket ta \rrbracket_i = \text{collect-interrupts } \llbracket ta' \rrbracket_i$

**and**  $\llbracket \text{convert-extTA } \text{extNTA}, P, t \vdash \langle es, s \rangle \rightarrow \langle es', s' \rangle; P, E, H \vdash es [:] Ts; \text{hext } H \text{ (hp } s) \rrbracket$

$\implies \exists ta' es' s'. \text{convert-extTA } \text{extNTA}, P, t \vdash \langle es, (H, \text{lcl } s) \rangle \rightarrow \langle es', s' \rangle \wedge$

$\text{collect-locks } \llbracket ta \rrbracket_l = \text{collect-locks } \llbracket ta' \rrbracket_l \wedge \text{collect-cond-actions } \llbracket ta \rrbracket_c = \text{collect-cond-actions } \llbracket ta' \rrbracket_c \wedge$

$\text{collect-interrupts } \llbracket ta \rrbracket_i = \text{collect-interrupts } \llbracket ta' \rrbracket_i$

**proof**(*induct arbitrary: E T and E Ts rule: red-reds.inducts*)

**case** (*RedNew h' a h C l*)

**thus** ?*case*

**by**(*cases allocate H (Class-type C) = {}*)(*fastforce simp add: ta-upd-simps intro: RedNewFail red-reds.RedNew*)**+**

**next**

**case** (*RedNewFail h C l*)

**thus** ?*case*

**by**(*cases allocate H (Class-type C) = {}*)(*fastforce simp add: ta-upd-simps intro: red-reds.RedNewFail RedNew*)**+**

**next**

**case** *NewArrayRed* **thus** ?*case* **by**(*fastforce intro: red-reds.intros*)

**next**

**case** (*RedNewArray i h' a h T l E T'*)

**thus** ?*case*

**by**(*cases allocate H (Array-type T (nat (sint i))) = {}*)(*fastforce simp add: ta-upd-simps intro: red-reds.RedNewArray RedNewArrayFail*)**+**

**next**

**case** *RedNewArrayNegative* **thus** ?*case* **by**(*fastforce intro: red-reds.intros*)

**next**

**case** (*RedNewArrayFail i h T l E T'*)

**thus** ?*case*

**by**(*cases allocate H (Array-type T (nat (sint i))) = {}*)(*fastforce simp add: ta-upd-simps intro: RedNewArray red-reds.RedNewArrayFail*)**+**

**next**

**case** *CastRed* **thus** ?*case* **by**(*fastforce intro: red-reds.intros*)

**next**

```

case (RedCast  $s\ v\ U\ T\ E\ T'$ )
from  $\langle P, E, H \vdash \text{Cast } T\ (\text{Val } v) : T' \rangle$  show ?case
proof(rule WTrt-elim-cases)
  fix  $T''$ 
  assume  $wt: P, E, H \vdash \text{Val } v : T''\ T' = T$ 
  thus ?thesis
    by(cases  $P \vdash T'' \leq T$ )(fastforce intro: red-reds.RedCast red-reds.RedCastFail)+
qed
next
case (RedCastFail  $s\ v\ U\ T\ E\ T'$ )
from  $\langle P, E, H \vdash \text{Cast } T\ (\text{Val } v) : T' \rangle$ 
obtain  $T''$  where  $P, E, H \vdash \text{Val } v : T''\ T = T'$  by auto
thus ?case
  by(cases  $P \vdash T'' \leq T$ )(fastforce intro: red-reds.RedCast red-reds.RedCastFail)+
next
case InstanceOfRed thus ?case by(fastforce intro: red-reds.intros)
next
case RedInstanceOf thus ?case
  using [[hypsubst-thin = true]]
  by auto((rule exI conjI red-reds.RedInstanceOf)+, auto)
next
case BinOpRed1 thus ?case by(fastforce intro: red-reds.intros)
next
case BinOpRed2 thus ?case by(fastforce intro: red-reds.intros)
next
case RedBinOp thus ?case by(fastforce intro: red-reds.intros)
next
case RedBinOpFail thus ?case by(fastforce intro: red-reds.intros)
next
case RedVar thus ?case by(fastforce intro: red-reds.intros)
next
case LAssRed thus ?case by(fastforce intro: red-reds.intros)
next
case RedLAss thus ?case by(fastforce intro: red-reds.intros)
next
case AAccRed1 thus ?case by(fastforce intro: red-reds.intros)
next
case AAccRed2 thus ?case by(fastforce intro: red-reds.intros)
next
case RedAAccNull thus ?case by(fastforce intro: red-reds.intros)
next
case RedAAccBounds thus ?case
  by(fastforce intro: red-reds.RedAAccBounds dest: hext-arrD)
next
case (RedAAcc  $h\ a\ T\ n\ i\ v\ l\ E\ T'$ )
from  $\langle P, E, H \vdash \text{addr } a\ [\text{Val } (\text{Intg } i)] : T' \rangle$ 
have  $wt: P, E, H \vdash \text{addr } a : T'[]$  by(auto)
with  $\langle H \sqsubseteq_{hp} (h, l) \rangle \langle \text{typeof-addr } h\ a = [\text{Array-type } T\ n] \rangle$ 
have  $H_a: \text{typeof-addr } H\ a = [\text{Array-type } T\ n]$  by(auto dest: hext-arrD)
with  $\langle 0 \leq i \rangle \langle \text{sint } i < \text{int } n \rangle$ 
have  $\text{nat } (\text{sint } i) < n$ 
  by (simp add: word-sle-eq nat-less-iff)
with  $H_a$  have  $P, H \vdash a@ACell\ (\text{nat } (\text{sint } i)) : T$ 
  by(auto intro: addr-loc-type.intros)

```

```

from heap-read-total[OF hconf this]
obtain v where heap-read H a (ACell (nat (sint i))) v by blast
with Ha  $\langle 0 \leq s \ i \rangle \langle \text{sint } i < \text{int } n \rangle$  show ?case
  by(fastforce intro: red-reds.RedAAcc simp add: ta-upd-simps)
next
  case AAssRed1 thus ?case by(fastforce intro: red-reds.intros)
next
  case AAssRed2 thus ?case by(fastforce intro: red-reds.intros)
next
  case AAssRed3 thus ?case by(fastforce intro: red-reds.intros)
next
  case RedAAssNull thus ?case by(fastforce intro: red-reds.intros)
next
  case RedAAssBounds thus ?case by(fastforce intro: red-reds.RedAAssBounds dest: hext-arrD)
next
  case (RedAAssStore s a T n i w U E T')
    from  $\langle P, E, H \vdash \text{addr } a \lfloor \text{Val } (\text{Intg } i) \rfloor := \text{Val } w : T' \rangle$ 
    obtain T'' T''' where wt:  $P, E, H \vdash \text{addr } a : T'' \lfloor$ 
      and wtw:  $P, E, H \vdash \text{Val } w : T'''$  by auto
    with  $\langle H \sqsubseteq_{hp} s \rangle \langle \text{typeof-addr } (hp \ s) \ a = \lfloor \text{Array-type } T \ n \rfloor \rangle$ 
    have Ha:  $\text{typeof-addr } H \ a = \lfloor \text{Array-type } T \ n \rfloor$  by(auto dest: hext-arrD)
    from  $\langle \text{typeof}_{hp} \ s \ w = \lfloor U \rfloor \rangle$  wtw  $\langle H \sqsubseteq_{hp} s \rangle$  have  $\text{typeof}_H \ w = \lfloor U \rfloor$ 
      by(auto dest: type-of-hext-type-of)
    with Ha  $\langle 0 \leq s \ i \rangle \langle \text{sint } i < \text{int } n \rangle \langle \neg P \vdash U \leq T \rangle$  show ?case
      by(fastforce intro: red-reds.RedAAssStore)
next
  case (RedAAss h a T n i w U h' l E T')
    from  $\langle P, E, H \vdash \text{addr } a \lfloor \text{Val } (\text{Intg } i) \rfloor := \text{Val } w : T' \rangle$ 
    obtain T'' T''' where wt:  $P, E, H \vdash \text{addr } a : T'' \lfloor$ 
      and wtw:  $P, E, H \vdash \text{Val } w : T'''$  by auto
    with  $\langle H \sqsubseteq_{hp} (h, l) \rangle \langle \text{typeof-addr } h \ a = \lfloor \text{Array-type } T \ n \rfloor \rangle$ 
    have Ha:  $\text{typeof-addr } H \ a = \lfloor \text{Array-type } T \ n \rfloor$  by(auto dest: hext-arrD)
    from  $\langle \text{typeof}_h \ w = \lfloor U \rfloor \rangle$  wtw  $\langle H \sqsubseteq_{hp} (h, l) \rangle$  have  $\text{typeof}_H \ w = \lfloor U \rfloor$ 
      by(auto dest: type-of-hext-type-of)
    moreover
    with  $\langle P \vdash U \leq T \rangle$  have conf:  $P, H \vdash w : \leq T$ 
      by(auto simp add: conf-def)
    from  $\langle 0 \leq s \ i \rangle \langle \text{sint } i < \text{int } n \rangle$ 
    have nat (sint i) < n
      by (simp add: word-sle-eq nat-less-iff)
    with Ha have  $P, H \vdash a @ \text{ACell } (\text{nat } (\text{sint } i)) : T$ 
      by(auto intro: addr-loc-type.intros)
    from heap-write-total[OF hconf this conf]
    obtain H' where heap-write H a (ACell (nat (sint i))) w H' ..
    ultimately show ?case using  $\langle 0 \leq s \ i \rangle \langle \text{sint } i < \text{int } n \rangle$  Ha  $\langle P \vdash U \leq T \rangle$ 
      by(fastforce simp del: split-paired-Ex intro: red-reds.RedAAss)
next
  case ALengthRed thus ?case by(fastforce intro: red-reds.intros)
next
  case (RedALength h a T n l E T')
    from  $\langle P, E, H \vdash \text{addr } a \cdot \text{length} : T' \rangle$ 
    obtain T'' where [simp]:  $T' = \text{Integer}$ 
      and wta:  $P, E, H \vdash \text{addr } a : T'' \lfloor$  by(auto)
    then obtain n'' where  $\text{typeof-addr } H \ a = \lfloor \text{Array-type } T'' \ n'' \rfloor$  by(auto)

```

```

thus ?case by(fastforce intro: red-reds.RedALength)
next
  case RedALengthNull show ?case by(fastforce intro: red-reds.RedALengthNull)
next
  case FAccRed thus ?case by(fastforce intro: red-reds.intros)
next
  case (RedFAcc h a D F v l E T)
  from  $\langle P, E, H \vdash \text{addr } a \cdot F\{D\} : T \rangle$  obtain U C' fm
    where wt:  $P, E, H \vdash \text{addr } a : U$ 
    and icto: class-type-of' U =  $\lfloor C' \rfloor$ 
    and has:  $P \vdash C' \text{ has } F:T \text{ (fm) in } D$ 
    by(auto)
  then obtain hU where Ha: typeof-addr H a =  $\lfloor hU \rfloor$  U = ty-of-htype hU by(auto)
  with icto  $\langle P \vdash C' \text{ has } F:T \text{ (fm) in } D \rangle$  have  $P, H \vdash a @ CField D F : T$ 
    by(auto intro: addr-loc-type.intros)
  from heap-read-total[OF hconf this]
  obtain v where heap-read H a (CField D F) v by blast
  thus ?case by(fastforce intro: red-reds.RedFAcc simp add: ta-upd-simps)
next
  case RedFAccNull thus ?case by(fastforce intro: red-reds.intros)
next
  case FAssRed1 thus ?case by(fastforce intro: red-reds.intros)
next
  case FAssRed2 thus ?case by(fastforce intro: red-reds.intros)
next
  case RedFAssNull thus ?case by(fastforce intro: red-reds.intros)
next
  case (RedFAss h a D F v h' l E T)
  from  $\langle P, E, H \vdash \text{addr } a \cdot F\{D\} := \text{Val } v : T \rangle$  obtain U C' T' T2 fm
    where wt:  $P, E, H \vdash \text{addr } a : U$ 
    and icto: class-type-of' U =  $\lfloor C' \rfloor$ 
    and has:  $P \vdash C' \text{ has } F:T' \text{ (fm) in } D$ 
    and wtv:  $P, E, H \vdash \text{Val } v : T2$ 
    and T2T:  $P \vdash T2 \leq T' \text{ by (auto) }$ 
  moreover from wt obtain hU where Ha: typeof-addr H a =  $\lfloor hU \rfloor$  U = ty-of-htype hU by(auto)
  with icto has have adal:  $P, H \vdash a @ CField D F : T' \text{ by (auto intro: addr-loc-type.intros) }$ 
  from wtv T2T have  $P, H \vdash v : \leq T' \text{ by (auto simp add: conf-def) }$ 
  from heap-write-total[OF hconf adal this]
  obtain h' where heap-write H a (CField D F) v h' ..
  thus ?case by(fastforce intro: red-reds.RedFAss)
next
  case CASRed1 thus ?case by(fastforce intro: red-reds.intros)
next
  case CASRed2 thus ?case by(fastforce intro: red-reds.intros)
next
  case CASRed3 thus ?case by(fastforce intro: red-reds.intros)
next
  case CASNull thus ?case by(fastforce intro: red-reds.intros)
next
  case (RedCASSucceed h a D F v v' h' l)
  note split-paired-Ex[simp del]
  from RedCASSucceed.prem1 obtain T' fm T2 T3 U C where *:
    T = Boolean class-type-of' U =  $\lfloor C \rfloor$   $P \vdash C \text{ has } F:T' \text{ (fm) in } D$ 
    volatile fm  $P \vdash T2 \leq T' \text{ } P \vdash T3 \leq T'$ 

```

```

  P,E,H ⊢ Val v : T2 P,E,H ⊢ Val v' : T3 P,E,H ⊢ addr a : U by auto
then have adal: P,H ⊢ a@CField D F : T' by(auto intro: addr-loc-type.intros)
from heap-read-total[OF hconf this] obtain v'' where v': heap-read H a (CField D F) v'' by blast
show ?case
proof(cases v'' = v)
  case True
    from * have P,H ⊢ v' :≤ T' by(auto simp add: conf-def)
    from heap-write-total[OF hconf adal this] True * v'
    show ?thesis by(fastforce intro: red-reds.RedCASSucceed)
  next
    case False
    then show ?thesis using * v' by(fastforce intro: RedCASFail)
  qed
next
  case (RedCASFail h a D F v'' v v' l)
  note split-paired-Ex[simp del]
  from RedCASFail.prem1 obtain T' fm T2 T3 U C where *:
    T = Boolean class-type-of' U = ⌊C⌋ P ⊢ C has F:T' (fm) in D
    volatile fm P ⊢ T2 ≤ T' P ⊢ T3 ≤ T'
    P,E,H ⊢ Val v : T2 P,E,H ⊢ Val v' : T3 P,E,H ⊢ addr a : U by auto
  then have adal: P,H ⊢ a@CField D F : T' by(auto intro: addr-loc-type.intros)
  from heap-read-total[OF hconf this] obtain v''' where v'': heap-read H a (CField D F) v''' by blast
  show ?case
  proof(cases v''' = v)
    case True
      from * have P,H ⊢ v' :≤ T' by(auto simp add: conf-def)
      from heap-write-total[OF hconf adal this] True * v''
      show ?thesis by(fastforce intro: red-reds.RedCASSucceed)
    next
      case False
      then show ?thesis using * v'' by(fastforce intro: red-reds.RedCASFail)
    qed
  next
  case CallObj thus ?case by(fastforce intro: red-reds.intros)
next
  case CallParams thus ?case by(fastforce intro: red-reds.intros)
next
  case (RedCall s a U M Ts T pns body D vs E T')
  from ⟨P,E,H ⊢ addr a·M(map Val vs) : T'⟩
  obtain U' C' Ts' meth D' Ts''
    where wta: P,E,H ⊢ addr a : U'
    and icto: class-type-of' U' = ⌊C'⌋
    and sees: P ⊢ C' sees M: Ts'→T' = meth in D'
    and wtes: P,E,H ⊢ map Val vs [:] Ts''
    and widens: P ⊢ Ts'' [≤] Ts' by auto
  from wta obtain hU' where Ha: typeof-addr H a = ⌊hU'⌋ U' = ty-of-htype hU' by(auto)
  moreover from ⟨typeof-addr (hp s) a = ⌊U⌋⟩ ⟨H ⊆ hp s⟩ Ha
  have [simp]: U = hU' by(auto dest: typeof-addr-heret-mono)
  from wtes have length vs = length Ts''
    by(auto intro: map-eq-imp-length-eq)
  moreover from widens have length Ts'' = length Ts'
    by(auto dest: widens-lengthD)
  moreover from sees icto sees ⟨P ⊢ class-type-of U sees M: Ts'→T = ⌊(pns, body)⌋ in D⟩ Ha
  have [simp]: meth = ⌊(pns, body)⌋ by(auto dest: sees-method-fun)

```



```

with sees wf have wf-mdecl wf-J-mdecl  $P\ D'\ (M,\ Ts',\ T',\ [(pns,\ body)])$ 
  by(auto intro: sees-wf-mdecl)
hence length pns = length Ts' by(simp add: wf-mdecl-def)
ultimately show ?case using sees icto
  by(fastforce intro: red-reds.RedCall)
next
case (RedCallExternal s a U M Ts T' D vs ta va h' ta' e' s')
from  $\langle P, E, H \vdash \text{addr } a \cdot M(\text{map Val vs}) : T \rangle$ 
obtain U' C' Ts' meth D' Ts''
  where wta:  $P, E, H \vdash \text{addr } a : U'$  and icto: class-type-of' U' = [C']
  and sees:  $P \vdash C' \text{ sees } M : Ts' \rightarrow T = \text{meth in } D'$ 
  and wtvs:  $P, E, H \vdash \text{map Val vs } [:] Ts''$ 
  and sub:  $P \vdash Ts'' [\leq] Ts'$  by auto
from wta  $\langle \text{typeof-addr } (hp\ s) \ a = [U] \rangle \langle \text{hext } H \ (hp\ s) \rangle$  have [simp]:  $U' = \text{ty-of-htype } U$ 
  by(auto dest: typeof-addr-hext-mono)
with icto have [simp]:  $C' = \text{class-type-of } U$  by(auto)
from sees  $\langle P \vdash \text{class-type-of } U \text{ sees } M : Ts \rightarrow T' = \text{Native in } D \rangle$ 
have [simp]: meth = Native by(auto dest: sees-method-fun)
with wta sees icto wtvs sub have  $P, H \vdash a \cdot M(vs) : T$ 
  by(cases U)(auto 4 4 simp add: external-WT'-iff)
from red-external-wt-hconf-hext[OF wf  $\langle P, t \vdash \langle a \cdot M(vs), hp\ s \rangle -ta \rightarrow \text{ext } \langle va, h' \rangle \langle H \trianglelefteq hp\ s \rangle \text{ this } tconf\ hconf]$ 
  wta icto sees  $\langle ta' = \text{convert-extTA extNTA } ta \rangle \langle e' = \text{extRet2J } (\text{addr } a \cdot M(\text{map Val vs}))\ va \rangle \langle s' = \langle h', \text{lcl } s \rangle \rangle$ 
show ?case by(cases U)(auto 4 5 intro: red-reds.RedCallExternal simp del: split-paired-Ex)
next
case RedCallNull thus ?case by(fastforce intro: red-reds.intros)
next
case (BlockRed e h l V vo ta e' h' l' T E T')
note IH = BlockRed.hyps(2)
from IH[of E(V  $\mapsto$  T) T']  $\langle P, E, H \vdash \{V:T=vo; e\} : T' \rangle \langle \text{hext } H \ (hp\ (h, l)) \rangle$ 
show ?case by(fastforce dest: red-reds.BlockRed)
next
case RedBlock thus ?case by(fastforce intro: red-reds.intros)
next
case SynchronizedRed1 thus ?case by(fastforce intro: red-reds.intros)
next
case SynchronizedNull thus ?case by(fastforce intro: red-reds.intros)
next
case LockSynchronized thus ?case by(fastforce intro: red-reds.intros)
next
case SynchronizedRed2 thus ?case by(fastforce intro: red-reds.intros)
next
case UnlockSynchronized thus ?case by(fastforce intro: red-reds.intros)
next
case SeqRed thus ?case by(fastforce intro: red-reds.intros)
next
case RedSeq thus ?case by(fastforce intro: red-reds.intros)
next
case CondRed thus ?case by(fastforce intro: red-reds.intros)
next
case RedCondT thus ?case by(fastforce intro: red-reds.intros)
next
case RedCondF thus ?case by(fastforce intro: red-reds.intros)

```

```

next
  case RedWhile thus ?case by(fastforce intro: red-reds.intros)
next
  case ThrowRed thus ?case by(fastforce intro: red-reds.intros)
next
  case RedThrowNull thus ?case by(fastforce intro: red-reds.intros)
next
  case TryRed thus ?case by(fastforce intro: red-reds.intros)
next
  case RedTry thus ?case by(fastforce intro: red-reds.intros)
next
  case (RedTryCatch s a D C V e2 E T)
  from  $\langle P, E, H \vdash \text{try Throw } a \text{ catch}(C \ V) \ e2 : T \rangle$ 
  obtain  $T'$  where  $P, E, H \vdash \text{addr } a : T'$  by auto
  with  $\langle \text{typeof-addr } (hp \ s) \ a = \lfloor \text{Class-type } D \rfloor \rangle \langle \text{hext } H \ (hp \ s) \rangle$ 
  have  $H_a: \text{typeof-addr } H \ a = \lfloor \text{Class-type } D \rfloor$ 
  by(auto dest: typeof-addr-hext-mono)
  with  $\langle P \vdash D \preceq^* C \rangle$  show ?case
  by(fastforce intro: red-reds.RedTryCatch)
next
  case (RedTryFail s a D C V e2 E T)
  from  $\langle P, E, H \vdash \text{try Throw } a \text{ catch}(C \ V) \ e2 : T \rangle$ 
  obtain  $T'$  where  $P, E, H \vdash \text{addr } a : T'$  by auto
  with  $\langle \text{typeof-addr } (hp \ s) \ a = \lfloor \text{Class-type } D \rfloor \rangle \langle \text{hext } H \ (hp \ s) \rangle$ 
  have  $H_a: \text{typeof-addr } H \ a = \lfloor \text{Class-type } D \rfloor$ 
  by(auto dest: typeof-addr-hext-mono)
  with  $\langle \neg P \vdash D \preceq^* C \rangle$  show ?case
  by(fastforce intro: red-reds.RedTryFail)
next
  case ListRed1 thus ?case by(fastforce intro: red-reds.intros)
next
  case ListRed2 thus ?case by(fastforce intro: red-reds.intros)
next
  case NewArrayThrow thus ?case by(fastforce intro: red-reds.intros)
next
  case CastThrow thus ?case by(fastforce intro: red-reds.intros)
next
  case InstanceOfThrow thus ?case by(fastforce intro: red-reds.intros)
next
  case BinOpThrow1 thus ?case by(fastforce intro: red-reds.intros)
next
  case BinOpThrow2 thus ?case by(fastforce intro: red-reds.intros)
next
  case LAssThrow thus ?case by(fastforce intro: red-reds.intros)
next
  case AAccThrow1 thus ?case by(fastforce intro: red-reds.intros)
next
  case AAccThrow2 thus ?case by(fastforce intro: red-reds.intros)
next
  case AAssThrow1 thus ?case by(fastforce intro: red-reds.intros)
next
  case AAssThrow2 thus ?case by(fastforce intro: red-reds.intros)
next
  case AAssThrow3 thus ?case by(fastforce intro: red-reds.intros)

```

```

next
  case ALengthThrow thus ?case by(fastforce intro: red-reds.intros)
next
  case FAccThrow thus ?case by(fastforce intro: red-reds.intros)
next
  case FAssThrow1 thus ?case by(fastforce intro: red-reds.intros)
next
  case FAssThrow2 thus ?case by(fastforce intro: red-reds.intros)
next
  case CASThrow thus ?case by(fastforce intro: red-reds.intros)
next
  case CASThrow2 thus ?case by(fastforce intro: red-reds.intros)
next
  case CASThrow3 thus ?case by(fastforce intro: red-reds.intros)
next
  case CallThrowObj thus ?case by(fastforce intro: red-reds.intros)
next
  case CallThrowParams thus ?case by(fastforce intro: red-reds.intros)
next
  case BlockThrow thus ?case by(fastforce intro: red-reds.intros)
next
  case SynchronizedThrow1 thus ?case by(fastforce intro: red-reds.intros)
next
  case SynchronizedThrow2 thus ?case by(fastforce intro: red-reds.intros)
next
  case SeqThrow thus ?case by(fastforce intro: red-reds.intros)
next
  case CondThrow thus ?case by(fastforce intro: red-reds.intros)
next
  case ThrowThrow thus ?case by(fastforce intro: red-reds.intros)
qed

```

**lemma** *can-lock-devreserp*:

$$\llbracket \text{wf-}J\text{-prog } P; \text{red-mthr.can-sync } P \ t \ (e, l) \ h' \ L; P, E, h \vdash e : T; P, h \vdash t \ \sqrt{t}; h\text{conf } h; h \trianglelefteq h' \rrbracket \\ \implies \text{red-mthr.can-sync } P \ t \ (e, l) \ h \ L$$

**apply**(*erule* *red-mthr.can-syncE*)

**apply**(*clarsimp*)

**apply**(*drule* *red-wt-hconf-hext*, *assumption*+)

**apply**(*simp*)

**apply**(*fastforce* intro!: *red-mthr.can-syncI*)

**done**

**end**

**context** *J-typesafe* **begin**

**lemma** *preserve-deadlocked*:

**assumes** *wf*: *wf-J-prog* *P*

**shows** *preserve-deadlocked final-expr* (*mred* *P*) *convert-RA* ( $\{s. \text{sync-es-ok } (thr \ s) \ (shr \ s) \wedge \text{lock-ok } (locks \ s) \ (thr \ s)\} \cap \{s. \exists Es. \text{sconf-type-ts-ok } Es \ (thr \ s) \ (shr \ s)\} \cap \{s. \text{def-ass-ts-ok } (thr \ s) \ (shr \ s)\}$ )  
*(is preserve-deadlocked - - ?wf-state)*

**proof**(*unfold-locales*)

**show** *inv*: *invariant3p* (*mredT* *P*) ?*wf-state*

**by**(intro *invariant3p-IntI* *invariant3p-sync-es-ok-lock-ok*[*OF wf*] *lifting-inv.invariant3p-ts-inv*[*OF*

*lifting-inv-sconf-subject-ok*[*OF wf*]] *lifting-wf.invariant3p-ts-ok*[*OF lifting-wf-def-ass*[*OF wf*]]])

```

fix s t' ta' s' t x ln
assume wfs: s ∈ ?wf-state
  and redT: P ⊢ s -t'▷ta'→ s'
  and tst: thr s t = [(x, ln)]
from redT have hext: shr s ⊑ shr s' by(rule redT-hext-incr)

from inv redT wfs have wfs': s' ∈ ?wf-state by(rule invariant3pD)
from redT tst obtain x' ln' where ts't: thr s' t = [(x', ln')]
  by(cases thr s' t)(cases s, cases s', auto dest: red-mthr.redT-thread-not-disappear)

from wfs tst obtain E T where wt: P, E, shr s ⊢ fst x : T
  and hconf: hconf (shr s)
  and da: D (fst x) [dom (snd x)]
  and tconf: P, shr s ⊢ t √t
  by(force dest: ts-invD ts-okD simp add: type-ok-def sconf-def sconf-type-ok-def)
from wt hext have wt': P, E, shr s' ⊢ fst x : T by(rule WTrt-hext-mono)
from wfs' ts't have hconf': hconf (shr s')
  by(auto dest: ts-invD simp add: type-ok-def sconf-def sconf-type-ok-def)

{
  assume cs: red-mthr.must-sync P t x (shr s)
  from cs have  $\neg \text{final } (fst x)$  by(auto elim!: red-mthr.must-syncE simp add: split-beta)

  from progress[OF wf-prog-wwf-prog[OF wf] hconf' wt' da this, of extTA2J P t]
  obtain e' h x' ta where  $P, t \vdash \langle fst x, (shr s', snd x) \rangle -ta \rightarrow \langle e', (h, x') \rangle$  by auto
  with red-ta-satisfiable[OF this]
  show red-mthr.must-sync P t x (shr s')
    by-(rule red-mthr.must-syncI, fastforce simp add: split-beta)
next
  fix LT
  assume red-mthr.can-sync P t x (shr s') LT
  with can-lock-devreserp[OF wf - wt tconf hconf hext, of snd x LT]
  show  $\exists LT' \subseteq LT. \text{red-mthr.can-sync } P \ t \ x \ (shr \ s) \ LT' \text{ by auto}$ 
}
qed

end

end

```

## 4.16 Program annotation

**theory** *Annotate*

**imports**

*WellType*

**begin**

**abbreviation** (*output*)

*unanFAcc* :: '*addr expr*  $\Rightarrow$  *vname*  $\Rightarrow$  '*addr expr* (( $\dots$ ) [*10,10*] *90*)

**where**

*unanFAcc e F*  $\equiv$  *FAcc e F (STR ''')*

**abbreviation (output)**

$unanFAss :: 'addr\ expr \Rightarrow vname \Rightarrow 'addr\ expr \Rightarrow 'addr\ expr \ ((\dots := -) [10,0,90] 90)$

**where**

$unanFAss\ e\ F\ e' \equiv FAss\ e\ F\ (STR\ ''')\ e'$

**definition** *array-length-field-name* :: *vname*

**where** *array-length-field-name* = *STR* "length"

**notation (output)** *array-length-field-name* (*length*)**definition** *super* :: *vname*

**where** *super* = *STR* "super"

**lemma** *super-neq-this* [*simp*]: *super*  $\neq$  *this* *this*  $\neq$  *super*

**by**(*simp-all add: this-def super-def*)

**inductive** *Anno* :: (*ty*  $\Rightarrow$  *ty*  $\Rightarrow$  *ty*  $\Rightarrow$  *bool*)  $\Rightarrow$  '*addr J-prog*  $\Rightarrow$  *env*  $\Rightarrow$  '*addr expr*  $\Rightarrow$  '*addr expr*  $\Rightarrow$  *bool*

( $\neg, -, \vdash - \rightsquigarrow -$  [*51,51,0,0,51*]50)

**and** *Annos* :: (*ty*  $\Rightarrow$  *ty*  $\Rightarrow$  *ty*  $\Rightarrow$  *bool*)  $\Rightarrow$  '*addr J-prog*  $\Rightarrow$  *env*  $\Rightarrow$  '*addr expr list*  $\Rightarrow$  '*addr expr list*  $\Rightarrow$  *bool*

( $\neg, -, \vdash - [\rightsquigarrow] -$  [*51,51,0,0,51*]50)

**for** *is-lub* :: *ty*  $\Rightarrow$  *ty*  $\Rightarrow$  *ty*  $\Rightarrow$  *bool* **and** *P* :: '*addr J-prog*

**where**

*AnnoNew*: *is-lub*, *P*, *E*  $\vdash$  *new C*  $\rightsquigarrow$  *new C*

| *AnnoNewArray*: *is-lub*, *P*, *E*  $\vdash$  *i*  $\rightsquigarrow$  *i'*  $\implies$  *is-lub*, *P*, *E*  $\vdash$  *newA T*[*i*]  $\rightsquigarrow$  *newA T*[*i'*]

| *AnnoCast*: *is-lub*, *P*, *E*  $\vdash$  *e*  $\rightsquigarrow$  *e'*  $\implies$  *is-lub*, *P*, *E*  $\vdash$  *Cast C e*  $\rightsquigarrow$  *Cast C e'*

| *AnnoInstanceOf*: *is-lub*, *P*, *E*  $\vdash$  *e*  $\rightsquigarrow$  *e'*  $\implies$  *is-lub*, *P*, *E*  $\vdash$  *e instanceof T*  $\rightsquigarrow$  *e' instanceof T*

| *AnnoVal*: *is-lub*, *P*, *E*  $\vdash$  *Val v*  $\rightsquigarrow$  *Val v*

| *AnnoVarVar*:  $\llbracket E\ V = \lfloor T \rfloor; V \neq super \rrbracket \implies is-lub, P, E \vdash Var\ V \rightsquigarrow Var\ V$

| *AnnoVarField*:

— There is no need to handle access of array fields explicitly, because arrays do not implement methods, i.e. *this* is always of a *Class* type.

$\llbracket E\ V = None; V \neq super; E\ this = \lfloor Class\ C \rfloor; P \vdash C\ sees\ V:T\ (fm)\ in\ D \rrbracket$

$\implies is-lub, P, E \vdash Var\ V \rightsquigarrow Var\ this \cdot V\{D\}$

| *AnnoBinOp*:

$\llbracket is-lub, P, E \vdash e1 \rightsquigarrow e1'; is-lub, P, E \vdash e2 \rightsquigarrow e2' \rrbracket$

$\implies is-lub, P, E \vdash e1 \llbracket bop \rrbracket e2 \rightsquigarrow e1' \llbracket bop \rrbracket e2'$

| *AnnoLAssVar*:

$\llbracket E\ V = \lfloor T \rfloor; V \neq super; is-lub, P, E \vdash e \rightsquigarrow e' \rrbracket \implies is-lub, P, E \vdash V := e \rightsquigarrow V := e'$

| *AnnoLAssField*:

$\llbracket E\ V = None; V \neq super; E\ this = \lfloor Class\ C \rfloor; P \vdash C\ sees\ V:T\ (fm)\ in\ D; is-lub, P, E \vdash e \rightsquigarrow e' \rrbracket$

$\implies is-lub, P, E \vdash V := e \rightsquigarrow Var\ this \cdot V\{D\} := e'$

| *AnnoAAcc*:

$\llbracket is-lub, P, E \vdash a \rightsquigarrow a'; is-lub, P, E \vdash i \rightsquigarrow i' \rrbracket \implies is-lub, P, E \vdash a[i] \rightsquigarrow a'[i']$

| *AnnoAAss*:

$\llbracket is-lub, P, E \vdash a \rightsquigarrow a'; is-lub, P, E \vdash i \rightsquigarrow i'; is-lub, P, E \vdash e \rightsquigarrow e' \rrbracket \implies is-lub, P, E \vdash a[i] := e \rightsquigarrow a'[i'] := e'$

| *AnnoALength*:

$is-lub, P, E \vdash a \rightsquigarrow a' \implies is-lub, P, E \vdash a.length \rightsquigarrow a'.length$

— All arrays implicitly declare a final field called *length* to store the array length, which hides a potential field of the same name in *Object* (cf. JLS 6.4.5). The last premise implements the hiding because field lookup does not model the implicit declaration.

*AnnoFAcc:*

$\llbracket is-lub, P, E \vdash e \rightsquigarrow e'; is-lub, P, E \vdash e' :: U; class-type-of' U = \lfloor C \rfloor; P \vdash C \text{ sees } F:T (fm) \text{ in } D; is-Array U \longrightarrow F \neq array-length-field-name \rrbracket$   
 $\implies is-lub, P, E \vdash e \cdot F\{STR'''\} \rightsquigarrow e' \cdot F\{D\}$

| *AnnoFAccALength:*

$\llbracket is-lub, P, E \vdash e \rightsquigarrow e'; is-lub, P, E \vdash e' :: T[] \rrbracket$   
 $\implies is-lub, P, E \vdash e \cdot array-length-field-name\{STR'''\} \rightsquigarrow e' \cdot length$

| *AnnoFAccSuper:*

— In class C with super class D, "super" is syntactic sugar for "(D) this)" (cf. JLS, 15.11.2)  
 $\llbracket E \text{ this} = \lfloor Class C \rfloor; C \neq Object; class P C = \lfloor (D, fs, ms) \rfloor; P \vdash D \text{ sees } F:T (fm) \text{ in } D' \rrbracket$   
 $\implies is-lub, P, E \vdash Var \text{ super} \cdot F\{STR'''\} \rightsquigarrow (Cast (Class D) (Var \text{ this})) \cdot F\{D'\}$

| *AnnoFAss:*

$\llbracket is-lub, P, E \vdash e1 \rightsquigarrow e1'; is-lub, P, E \vdash e2 \rightsquigarrow e2'; is-lub, P, E \vdash e1' :: U; class-type-of' U = \lfloor C \rfloor; P \vdash C \text{ sees } F:T (fm) \text{ in } D; is-Array U \longrightarrow F \neq array-length-field-name \rrbracket$   
 $\implies is-lub, P, E \vdash e1 \cdot F\{STR'''\} := e2 \rightsquigarrow e1' \cdot F\{D\} := e2'$

| *AnnoFAssSuper:*

$\llbracket E \text{ this} = \lfloor Class C \rfloor; C \neq Object; class P C = \lfloor (D, fs, ms) \rfloor; P \vdash D \text{ sees } F:T (fm) \text{ in } D'; is-lub, P, E \vdash e \rightsquigarrow e' \rrbracket$   
 $\implies is-lub, P, E \vdash Var \text{ super} \cdot F\{STR'''\} := e \rightsquigarrow (Cast (Class D) (Var \text{ this})) \cdot F\{D'\} := e'$

| *AnnoCAS:*

$\llbracket is-lub, P, E \vdash e1 \rightsquigarrow e1'; is-lub, P, E \vdash e2 \rightsquigarrow e2'; is-lub, P, E \vdash e3 \rightsquigarrow e3' \rrbracket$   
 $\implies is-lub, P, E \vdash e1 \cdot compareAndSwap(D \cdot F, e2, e3) \rightsquigarrow e1' \cdot compareAndSwap(D \cdot F, e2', e3')$

| *AnnoCall:*

$\llbracket is-lub, P, E \vdash e \rightsquigarrow e'; is-lub, P, E \vdash es [\rightsquigarrow] es' \rrbracket$   
 $\implies is-lub, P, E \vdash Call e M es \rightsquigarrow Call e' M es'$

| *AnnoBlock:*

$is-lub, P, E(V \mapsto T) \vdash e \rightsquigarrow e' \implies is-lub, P, E \vdash \{V:T=vo; e\} \rightsquigarrow \{V:T=vo; e'\}$

| *AnnoSync:*

$\llbracket is-lub, P, E \vdash e1 \rightsquigarrow e1'; is-lub, P, E \vdash e2 \rightsquigarrow e2' \rrbracket$   
 $\implies is-lub, P, E \vdash sync(e1) e2 \rightsquigarrow sync(e1') e2'$

| *AnnoComp:*

$\llbracket is-lub, P, E \vdash e1 \rightsquigarrow e1'; is-lub, P, E \vdash e2 \rightsquigarrow e2' \rrbracket$   
 $\implies is-lub, P, E \vdash e1;;e2 \rightsquigarrow e1';;e2'$

| *AnnoCond:*

$\llbracket is-lub, P, E \vdash e \rightsquigarrow e'; is-lub, P, E \vdash e1 \rightsquigarrow e1'; is-lub, P, E \vdash e2 \rightsquigarrow e2' \rrbracket$   
 $\implies is-lub, P, E \vdash if (e) e1 \text{ else } e2 \rightsquigarrow if (e') e1' \text{ else } e2'$

| *AnnoLoop:*

$\llbracket is-lub, P, E \vdash e \rightsquigarrow e'; is-lub, P, E \vdash c \rightsquigarrow c' \rrbracket$   
 $\implies is-lub, P, E \vdash while (e) c \rightsquigarrow while (e') c'$

| *AnnoThrow:*

$is-lub, P, E \vdash e \rightsquigarrow e' \implies is-lub, P, E \vdash throw e \rightsquigarrow throw e'$

| *AnnoTry:*

$\llbracket is-lub, P, E \vdash e1 \rightsquigarrow e1'; is-lub, P, E(V \mapsto Class C) \vdash e2 \rightsquigarrow e2' \rrbracket$   
 $\implies is-lub, P, E \vdash try e1 catch(C V) e2 \rightsquigarrow try e1' catch(C V) e2'$

| *AnnoNil:*

$is-lub, P, E \vdash [] [\rightsquigarrow] []$

| *AnnoCons:*

$\llbracket is-lub, P, E \vdash e \rightsquigarrow e'; is-lub, P, E \vdash es [\rightsquigarrow] es' \rrbracket \implies is-lub, P, E \vdash e\#es [\rightsquigarrow] e'\#es'$

**inductive-cases** *Anno-cases* [elim!]:

$is-lub', P, E \vdash new C \rightsquigarrow e$

$is-lub', P, E \vdash newA\ T[e] \rightsquigarrow e'$   
 $is-lub', P, E \vdash Cast\ T\ e \rightsquigarrow e'$   
 $is-lub', P, E \vdash e\ instanceof\ T \rightsquigarrow e'$   
 $is-lub', P, E \vdash Val\ v \rightsquigarrow e'$   
 $is-lub', P, E \vdash Var\ V \rightsquigarrow e'$   
 $is-lub', P, E \vdash e1\ \langle\!\langle bop \rangle\!\rangle\ e2 \rightsquigarrow e'$   
 $is-lub', P, E \vdash V := e \rightsquigarrow e'$   
 $is-lub', P, E \vdash e1[e2] \rightsquigarrow e'$   
 $is-lub', P, E \vdash e1[e2] := e3 \rightsquigarrow e'$   
 $is-lub', P, E \vdash e.length \rightsquigarrow e'$   
 $is-lub', P, E \vdash e.F\{D\} \rightsquigarrow e'$   
 $is-lub', P, E \vdash e1.F\{D\} := e2 \rightsquigarrow e'$   
 $is-lub', P, E \vdash e1.compareAndSwap(D.F, e2, e3) \rightsquigarrow e'$   
 $is-lub', P, E \vdash e.M(es) \rightsquigarrow e'$   
 $is-lub', P, E \vdash \{V:T=vo; e\} \rightsquigarrow e'$   
 $is-lub', P, E \vdash sync(e1)\ e2 \rightsquigarrow e'$   
 $is-lub', P, E \vdash insync(a)\ e2 \rightsquigarrow e'$   
 $is-lub', P, E \vdash e1;; e2 \rightsquigarrow e'$   
 $is-lub', P, E \vdash if\ (e)\ e1\ else\ e2 \rightsquigarrow e'$   
 $is-lub', P, E \vdash while(e1)\ e2 \rightsquigarrow e'$   
 $is-lub', P, E \vdash throw\ e \rightsquigarrow e'$   
 $is-lub', P, E \vdash try\ e1\ catch(C\ V)\ e2 \rightsquigarrow e'$

**inductive-cases** *Annos-cases* [*elim!*]:

$is-lub', P, E \vdash [] \rightsquigarrow es'$   
 $is-lub', P, E \vdash e \# es \rightsquigarrow es'$

**abbreviation** *Anno'* :: 'addr J-prog  $\Rightarrow$  env  $\Rightarrow$  'addr expr  $\Rightarrow$  'addr expr  $\Rightarrow$  bool (·, ·  $\vdash$  ·  $\rightsquigarrow$  · - [51, 0, 0, 51] 50)

**where** *Anno'* *P*  $\equiv$  *Anno* (*TypeRel.is-lub P*) *P*

**abbreviation** *Annos'* :: 'addr J-prog  $\Rightarrow$  env  $\Rightarrow$  'addr expr list  $\Rightarrow$  'addr expr list  $\Rightarrow$  bool (·, ·  $\vdash$  · [ $\rightsquigarrow$ ] - [51, 0, 0, 51] 50)

**where** *Annos'* *P*  $\equiv$  *Annos* (*TypeRel.is-lub P*) *P*

**definition** *annotate* :: 'addr J-prog  $\Rightarrow$  env  $\Rightarrow$  'addr expr  $\Rightarrow$  'addr expr

**where** *annotate P E e* = *THE-default e* ( $\lambda e'. P, E \vdash e \rightsquigarrow e'$ )

**lemma fixes** *is-lub* :: *ty*  $\Rightarrow$  *ty*  $\Rightarrow$  *ty*  $\Rightarrow$  bool ( $\vdash\ lub'((- / -)') = -$  [51, 51, 51] 50)

**assumes** *is-lub-unique*:  $\bigwedge T1\ T2\ T3\ T4. \llbracket \vdash\ lub(T1, T2) = T3; \vdash\ lub(T1, T2) = T4 \rrbracket \Longrightarrow T3 = T4$

**shows** *Anno-fun*:  $\llbracket is-lub, P, E \vdash e \rightsquigarrow e'; is-lub, P, E \vdash e \rightsquigarrow e'' \rrbracket \Longrightarrow e' = e''$

**and** *Annos-fun*:  $\llbracket is-lub, P, E \vdash es \rightsquigarrow es'; is-lub, P, E \vdash es \rightsquigarrow es'' \rrbracket \Longrightarrow es' = es''$

**proof**(*induct arbitrary: e'' and es'' rule: Anno-Annos.inducts*)

**case** (*AnnoFAcc E e e' U C F T fm D*)

**from**  $\langle is-lub, P, E \vdash e.F\{STR\ \text{''''}\} \rightsquigarrow e'' \rangle$  **show** ?*case*

**proof**(*rule Anno-cases*)

**fix** *e''' U' C' T' fm' D'*

**assume** *is-lub, P, E*  $\vdash e \rightsquigarrow e'''$  *is-lub, P, E*  $\vdash e''' :: U'$

**and** *class-type-of' U'* =  $\lfloor C' \rfloor$

**and** *P*  $\vdash C'$  *sees* *F:T' (fm')* *in D'* *e''* = *e'''*.*F{D'}*

**from**  $\langle is-lub, P, E \vdash e \rightsquigarrow e''' \rangle$  **have** *e'* = *e'''* **by**(*rule AnnoFAcc*)

**with**  $\langle is-lub, P, E \vdash e' :: U \rangle$   $\langle is-lub, P, E \vdash e''' :: U' \rangle$

**have** *U* = *U'* **by**(*auto intro: WT-unique is-lub-unique*)

```

with ⟨class-type-of' U = [C]⟩ ⟨class-type-of' U' = [C']⟩
have C = C' by(auto)
with ⟨P ⊢ C' sees F:T' (fm') in D'⟩ ⟨P ⊢ C sees F:T (fm) in D⟩
have D' = D by(auto dest: sees-field-fun)
with ⟨e'' = e'''·F{D'}⟩ ⟨e' = e'''⟩ show ?thesis by simp
next
fix e''' T
assume e'' = e'''·length
  and is-lub,P,E ⊢ e''' :: T[]
  and is-lub,P,E ⊢ e ∼ e'''
  and F = array-length-field-name
from ⟨is-lub,P,E ⊢ e ∼ e'''⟩ have e' = e''' by(rule AnnoFAcc)
with ⟨is-lub,P,E ⊢ e' :: U⟩ ⟨is-lub,P,E ⊢ e''' :: T[]⟩ have U = T[] by(auto intro: WT-unique
is-lub-unique)
with ⟨class-type-of' U = [C]⟩ ⟨is-Array U ⟶ F ≠ array-length-field-name⟩
show ?thesis using ⟨F = array-length-field-name⟩ by simp
next
fix C' D' fs ms T D''
assume E this = [Class C']
  and class P C' = [(D', fs, ms)]
  and e = Var super
  and e'' = Cast (Class D') (Var this)·F{D''}
with ⟨is-lub,P,E ⊢ e ∼ e'⟩ have False by(auto)
thus ?thesis ..
qed
next
case AnnoFAccALength thus ?case by(fastforce intro: WT-unique[OF is-lub-unique])
next
case (AnnoFAss E e1 e1' e2 e2' U C F T fm D)
from ⟨is-lub,P,E ⊢ e1·F{STR '''} := e2 ∼ e'⟩
show ?case
proof(rule Anno-cases)
fix e1'' e2'' U' C' T' fm' D'
assume is-lub,P,E ⊢ e1 ∼ e1'' is-lub,P,E ⊢ e2 ∼ e2''
  and is-lub,P,E ⊢ e1'' :: U' and class-type-of' U' = [C']
  and P ⊢ C' sees F:T' (fm') in D'
  and e'' = e1''·F{D'} := e2''
from ⟨is-lub,P,E ⊢ e1 ∼ e1''⟩ have e1' = e1'' by(rule AnnoFAss)
moreover with ⟨is-lub,P,E ⊢ e1' :: U⟩ ⟨is-lub,P,E ⊢ e1'' :: U'⟩
have U = U' by(auto intro: WT-unique is-lub-unique)
with ⟨class-type-of' U = [C]⟩ ⟨class-type-of' U' = [C']⟩
have C = C' by(auto)
with ⟨P ⊢ C' sees F:T' (fm') in D'⟩ ⟨P ⊢ C sees F:T (fm) in D⟩
have D' = D by(auto dest: sees-field-fun)
moreover from ⟨is-lub,P,E ⊢ e2 ∼ e2''⟩ have e2' = e2'' by(rule AnnoFAss)
ultimately show ?thesis using ⟨e'' = e1''·F{D'} := e2''⟩ by simp
next
fix C' D' fs ms T' fm' D'' e'''
assume e'' = Cast (Class D') (Var this)·F{D''} := e'''
  and E this = [Class C']
  and class P C' = [(D', fs, ms)]
  and P ⊢ D' sees F:T' (fm') in D''
  and is-lub,P,E ⊢ e2 ∼ e'''
  and e1 = Var super

```



```

  with ⟨is-lub, P, E ⊢ e1 ∼ e1'⟩ have False by (auto elim: Anno-cases)
  thus ?thesis ..
qed
qed(fastforce dest: sees-field-fun)+

```

#### 4.16.1 Code generation

**definition** Anno-code :: 'addr J-prog ⇒ env ⇒ 'addr expr ⇒ 'addr expr ⇒ bool (·, · ⊢ · ∼) -  
 [51, 0, 0, 51] 50)  
**where** Anno-code P = Anno (is-lub-sup P) P

**definition** Annos-code :: 'addr J-prog ⇒ env ⇒ 'addr expr list ⇒ 'addr expr list ⇒ bool (·, · ⊢ · [∼])  
 - [51, 0, 0, 51] 50)  
**where** Annos-code P = Annos (is-lub-sup P) P

**primrec** block-types :: ('a, 'b, 'addr) exp ⇒ ty list  
**and** blocks-types :: ('a, 'b, 'addr) exp list ⇒ ty list  
**where**

```

  block-types (new C) = []
| block-types (newA T[e]) = block-types e
| block-types (Cast U e) = block-types e
| block-types (e instanceof U) = block-types e
| block-types (e1 «bop» e2) = block-types e1 @ block-types e2
| block-types (Val v) = []
| block-types (Var V) = []
| block-types (V := e) = block-types e
| block-types (a[i]) = block-types a @ block-types i
| block-types (a[i] := e) = block-types a @ block-types i @ block-types e
| block-types (a.length) = block-types a
| block-types (e.F{D}) = block-types e
| block-types (e.F{D} := e') = block-types e @ block-types e'
| block-types (e.compareAndSwap(D.F, e', e'')) = block-types e @ block-types e' @ block-types e''
| block-types (e.M(es)) = block-types e @ blocks-types es
| block-types {V:T=vo; e} = T # block-types e
| block-types (syncV(e) e') = block-types e @ block-types e'
| block-types (insyncV(a) e) = block-types e
| block-types (e;;e') = block-types e @ block-types e'
| block-types (if (e) e1 else e2) = block-types e @ block-types e1 @ block-types e2
| block-types (while (b) c) = block-types b @ block-types c
| block-types (throw e) = block-types e
| block-types (try e catch(C V) e') = block-types e @ Class C # block-types e'

| blocks-types [] = []
| blocks-types (e#es) = block-types e @ blocks-types es

```

**lemma** fixes is-lub1 :: ty ⇒ ty ⇒ ty ⇒ bool (⊢1 lub'((·, / ·)')) = - [51, 51, 51] 50)  
**and** is-lub2 :: ty ⇒ ty ⇒ ty ⇒ bool (⊢2 lub'((·, / ·)')) = - [51, 51, 51] 50)  
**assumes** wf: wf-prog wf-md P  
**and** is-lub1-into-is-lub2: ∧ T1 T2 T3. [⊢1 lub(T1, T2) = T3; is-type P T1; is-type P T2] ⇒ ⊢2  
 lub(T1, T2) = T3  
**and** is-lub2-is-type: ∧ T1 T2 T3. [⊢2 lub(T1, T2) = T3; is-type P T1; is-type P T2] ⇒ is-type  
 P T3

**shows** Anno-change-is-lub:

[ is-lub1, P, E ⊢ e ∼ e'; ran E ∪ set (block-types e) ⊆ types P ] ⇒ is-lub2, P, E ⊢ e ∼ e'

**and** *Annos-change-is-lub*:  
 $\llbracket is-lub1, P, E \vdash es [\rightsquigarrow] es'; ran\ E \cup set\ (blocks-types\ es) \subseteq types\ P \rrbracket \implies is-lub2, P, E \vdash es [\rightsquigarrow] es'$   
**proof**(*induct rule: Anno-Annos.inducts*)  
**case** (*AnnoBlock E V T e e' vo*)  
**from**  $\langle ran\ E \cup set\ (block-types\ \{V:T=vo;\ e\}) \subseteq types\ P \rangle$   
**have**  $ran\ (E(V \mapsto T)) \cup set\ (block-types\ e) \subseteq types\ P$   
**by**(*auto simp add: ran-def*)  
**thus** ?case **using** *AnnoBlock* **by**(*blast intro: Anno-Annos.intros*)  
**next**  
**case** (*AnnoTry E e1 e1' V C e2 e2'*)  
**from**  $\langle ran\ E \cup set\ (block-types\ (try\ e1\ catch(C\ V)\ e2)) \subseteq types\ P \rangle$   
**have**  $ran\ (E(V \mapsto Class\ C)) \cup set\ (block-types\ e2) \subseteq types\ P$   
**by**(*auto simp add: ran-def*)  
**thus** ?case **using** *AnnoTry* **by**(*simp del: fun-upd-apply*)(*blast intro: Anno-Annos.intros*)  
**qed**(*simp-all del: is-Array.simps is-Array-conv, (blast intro: Anno-Annos.intros WT-change-is-lub[OF wf, where ?is-lub1.0=is-lub1 and ?is-lub2.0=is-lub2] is-lub1-into-is-lub2 is-lub2-is-type)+*)

**lemma assumes** *wf: wf-prog wf-md P*  
**shows** *Anno-into-Anno-code*:  $\llbracket P, E \vdash e \rightsquigarrow e'; ran\ E \cup set\ (block-types\ e) \subseteq types\ P \rrbracket \implies P, E \vdash e \rightsquigarrow' e'$   
**and** *Annos-into-Annos-code*:  $\llbracket P, E \vdash es [\rightsquigarrow] es'; ran\ E \cup set\ (blocks-types\ es) \subseteq types\ P \rrbracket \implies P, E \vdash es [\rightsquigarrow'] es'$   
**proof** –  
**assume** *anno*:  $P, E \vdash e \rightsquigarrow e'$   
**and** *ran*:  $ran\ E \cup set\ (block-types\ e) \subseteq types\ P$   
**show**  $P, E \vdash e \rightsquigarrow' e'$  **unfolding** *Anno-code-def*  
**by**(*rule Anno-change-is-lub[OF wf - anno ran]*)(*blast intro!: is-lub-sup.intros intro: is-lub-subD[OF wf] sup-is-type[OF wf] elim!: is-lub-sup.cases*)+  
**next**  
**assume** *annos*:  $P, E \vdash es [\rightsquigarrow] es'$   
**and** *ran*:  $ran\ E \cup set\ (blocks-types\ es) \subseteq types\ P$   
**show**  $P, E \vdash es [\rightsquigarrow'] es'$  **unfolding** *Annos-code-def*  
**by**(*rule Annos-change-is-lub[OF wf - annos ran]*)(*blast intro!: is-lub-sup.intros intro: is-lub-subD[OF wf] sup-is-type[OF wf] elim!: is-lub-sup.cases*)+  
**qed**

**lemma assumes** *wf: wf-prog wf-md P*  
**shows** *Anno-code-into-Anno*:  $\llbracket P, E \vdash e \rightsquigarrow' e'; ran\ E \cup set\ (block-types\ e) \subseteq types\ P \rrbracket \implies P, E \vdash e \rightsquigarrow e'$   
**and** *Annos-code-into-Annos*:  $\llbracket P, E \vdash es [\rightsquigarrow'] es'; ran\ E \cup set\ (blocks-types\ es) \subseteq types\ P \rrbracket \implies P, E \vdash es [\rightsquigarrow] es'$   
**proof** –  
**assume** *anno*:  $P, E \vdash e \rightsquigarrow' e'$   
**and** *ran*:  $ran\ E \cup set\ (block-types\ e) \subseteq types\ P$   
**show**  $P, E \vdash e \rightsquigarrow e'$   
**by**(*rule Anno-change-is-lub[OF wf - anno[unfolded Anno-code-def] ran]*)(*blast elim!: is-lub-sup.cases intro: sup-is-lubI[OF wf] is-lub-is-type[OF wf]*)+  
**next**  
**assume** *annos*:  $P, E \vdash es [\rightsquigarrow'] es'$   
**and** *ran*:  $ran\ E \cup set\ (blocks-types\ es) \subseteq types\ P$   
**show**  $P, E \vdash es [\rightsquigarrow] es'$   
**by**(*rule Annos-change-is-lub[OF wf - annos[unfolded Annos-code-def] ran]*)(*blast elim!: is-lub-sup.cases intro: sup-is-lubI[OF wf] is-lub-is-type[OF wf]*)+  
**qed**

**lemma** fixes *is-lub*

assumes *wf*: *wf-prog wf-md P*

shows *WT-block-types-is-type*:  $is-lub, P, E \vdash e :: T \implies set (block-types\ e) \subseteq types\ P$

and *WTs-blocks-types-is-type*:  $is-lub, P, E \vdash es \ [::]\ Ts \implies set (blocks-types\ es) \subseteq types\ P$

**apply**(*induct rule*: *WT-WTs.inducts*)

**apply**(*auto intro*: *is-class-sub-Throwable[OF wf]*)

**done**

**lemma** fixes *is-lub*

shows *Anno-block-types*:  $is-lub, P, E \vdash e \rightsquigarrow e' \implies block-types\ e = block-types\ e'$

and *Annos-blocks-types*:  $is-lub, P, E \vdash es \ [\rightsquigarrow]\ es' \implies blocks-types\ es = blocks-types\ es'$

**by**(*induct rule*: *Anno-Annos.inducts*) *auto*

**code-pred**

(*modes*: ( $i \Rightarrow i \Rightarrow o \Rightarrow bool$ )  $\Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow bool$ )

[*detect-switches, skip-proof*]

*Anno*

.

**definition** *annotate-code* :: '*addr J-prog*  $\Rightarrow env \Rightarrow 'addr\ expr \Rightarrow 'addr\ expr$

**where** *annotate-code* *P E e* = *THE-default e* ( $\lambda e'. P, E \vdash e \rightsquigarrow' e'$ )

**code-pred**

(*modes*:  $i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow bool$ )

[*inductify*]

*Anno-code*

.

**lemma** *eval-Anno-i-i-i-o-conv*:

*Predicate.eval* (*Anno-code-i-i-i-o P E e*) = ( $\lambda e'. P, E \vdash e \rightsquigarrow' e'$ )

**by**(*auto intro!*: *ext intro*: *Anno-code-i-i-i-oI elim*: *Anno-code-i-i-i-oE*)

**lemma** *annotate-code* [*code*]:

*annotate-code P E e* = *Predicate.singleton* ( $\lambda -. Code.abort\ (STR\ "annotate")\ (\lambda -. e)$ ) (*Anno-code-i-i-i-o P E e*)

**by**(*simp add*: *THE-default-def Predicate.singleton-def annotate-code-def eval-Anno-i-i-i-o-conv*)

**end**

**theory** *J-Main*

**imports**

*State*

*Deadlocked*

*Annotate*

**begin**

**end**



## Chapter 5

# Jinja Virtual Machine

### 5.1 State of the JVM

```
theory JVMState
imports
  ../Common/Observable-Events
begin
```

#### 5.1.1 Frame Stack

```
type-synonym
  pc = nat

type-synonym
  'addr frame = 'addr val list  $\times$  'addr val list  $\times$  cname  $\times$  mname  $\times$  pc
  — operand stack
  — registers (including this pointer, method parameters, and local variables)
  — name of class where current method is defined
  — parameter types
  — program counter within frame
```

```
print-translation <
  let
    fun tr'
      [Const (@{type-syntax list}, -) $ (Const (@{type-syntax val}, -) $ a1),
       Const (@{type-syntax prod}, -) $
         (Const (@{type-syntax list}, -) $ (Const (@{type-syntax val}, -) $ a2)) $
         (Const (@{type-syntax prod}, -) $
           Const (@{type-syntax String.literal}, -) $
             (Const (@{type-syntax prod}, -) $
               Const (@{type-syntax String.literal}, -) $
                 Const (@{type-syntax nat}, -)))] =
        if a1 = a2 then Syntax.const @{type-syntax frame} $ a1
        else raise Match;
      in [(@{type-syntax prod}, K tr')]
    end
  >
typ 'addr frame
```

### 5.1.2 Runtime State

**type-synonym**

$('addr, 'heap) \text{ jvm-state} = 'addr \text{ option} \times 'heap \times 'addr \text{ frame list}$   
 — exception flag, heap, frames

**type-synonym**

$'addr \text{ jvm-thread-state} = 'addr \text{ option} \times 'addr \text{ frame list}$   
 — exception flag, frames, thread lock state

**type-synonym**

$('addr, 'thread-id, 'heap) \text{ jvm-thread-action} = ('addr, 'thread-id, 'addr \text{ jvm-thread-state}, 'heap) \text{ Jinja-thread-action}$

**type-synonym**

$('addr, 'thread-id, 'heap) \text{ jvm-ta-state} = ('addr, 'thread-id, 'heap) \text{ jvm-thread-action} \times ('addr, 'heap) \text{ jvm-state}$

**print-translation**  $\langle$

```

  let
    fun tr'
      [a1, t
      , Const (@{type-syntax prod}, -) $
        (Const (@{type-syntax option}, -) $ a2) $
        (Const (@{type-syntax list}, -) $
          (Const (@{type-syntax prod}, -) $
            (Const (@{type-syntax list}, -) $ (Const (@{type-syntax val}, -) $ a3)) $
            (* Next bit: same syntax translation as for frame *)
            (Const (@{type-syntax prod}, -) $
              (Const (@{type-syntax list}, -) $ (Const (@{type-syntax val}, -) $ a4)) $
              (Const (@{type-syntax prod}, -) $
                Const (@{type-syntax String.literal}, -) $
                (Const (@{type-syntax prod}, -) $
                  Const (@{type-syntax String.literal}, -) $
                  Const (@{type-syntax nat}, -))))))
        , h] =
      if a1 = a2 andalso a2 = a3 andalso a3 = a4 then Syntax.const @{type-syntax jvm-thread-action}
      $ a1 $ t $ h
      else raise Match;
      in [(@{type-syntax Jinja-thread-action}, K tr')]
      end
    >
  typ ('addr, 'thread-id, 'heap) jvm-thread-action

```

**end**

## 5.2 Instructions of the JVM

**theory** *JVMInstructions*

**imports**

*JVMState*

*../Common/BinOp*

**begin**

**datatype** *'addr instr*

<i>= Load nat</i>	— load from local variable
<i>Store nat</i>	— store into local variable
<i>Push 'addr val</i>	— push a value (constant)
<i>New cname</i>	— create object
<i>NewArray ty</i>	— create array for elements of given type
<i>ALoad</i>	— Load array element from heap to stack
<i>ASore</i>	— Set element in array
<i>ALength</i>	— Return the length of the array
<i>Getfield vname cname</i>	— Fetch field from object
<i>Putfield vname cname</i>	— Set field in object
<i>CAS vname cname</i>	— Compare-and-swap instruction
<i>Checkcast ty</i>	— Check whether object is of given type
<i>Instanceof ty</i>	— instanceof test
<i>Invoke mname nat</i>	— inv. instance meth of an object
<i>Return</i>	— return from method
<i>Pop</i>	— pop top element from opstack
<i>Dup</i>	— duplicate top stack element
<i>Swap</i>	— swap top stack elements
<i>BinOpInstr bop</i>	— binary operator instruction
<i>Goto int</i>	— goto relative address
<i>IfFalse int</i>	— branch if top of stack false
<i>ThrowExc</i>	— throw top of stack as exception
<i>MEnter</i>	— enter the monitor of object on top of the stack
<i>MExit</i>	— exit the monitor of object on top of the stack

**abbreviation** *CmpEq :: 'addr instr*

**where** *CmpEq*  $\equiv$  *BinOpInstr Eq*

**abbreviation** *CmpLeq :: 'addr instr*

**where** *CmpLeq*  $\equiv$  *BinOpInstr LessOrEqual*

**abbreviation** *CmpGeq :: 'addr instr*

**where** *CmpGeq*  $\equiv$  *BinOpInstr GreaterOrEqual*

**abbreviation** *CmpLt :: 'addr instr*

**where** *CmpLt*  $\equiv$  *BinOpInstr LessThan*

**abbreviation** *CmpGt :: 'addr instr*

**where** *CmpGt*  $\equiv$  *BinOpInstr GreaterThan*

**abbreviation** *IAdd :: 'addr instr*

**where** *IAdd*  $\equiv$  *BinOpInstr Add*

**abbreviation** *ISub :: 'addr instr*

**where** *ISub*  $\equiv$  *BinOpInstr Subtract*

**abbreviation** *IMult :: 'addr instr*

**where** *IMult*  $\equiv$  *BinOpInstr Mult*

**abbreviation** *IDiv :: 'addr instr*

**where** *IDiv*  $\equiv$  *BinOpInstr Div*

**abbreviation** *IMod :: 'addr instr*

**where**  $IMod \equiv BinOpInstr\ Mod$

**abbreviation**  $IShl :: 'addr\ instr$

**where**  $IShl \equiv BinOpInstr\ ShiftLeft$

**abbreviation**  $IShr :: 'addr\ instr$

**where**  $IShr \equiv BinOpInstr\ ShiftRightSigned$

**abbreviation**  $IUShr :: 'addr\ instr$

**where**  $IUShr \equiv BinOpInstr\ ShiftRightZeros$

**abbreviation**  $IAnd :: 'addr\ instr$

**where**  $IAnd \equiv BinOpInstr\ BinAnd$

**abbreviation**  $IOr :: 'addr\ instr$

**where**  $IOr \equiv BinOpInstr\ BinOr$

**abbreviation**  $IXor :: 'addr\ instr$

**where**  $IXor \equiv BinOpInstr\ BinXor$

**type-synonym**

$'addr\ bytecode = 'addr\ instr\ list$

**type-synonym**

$ex\text{-}entry = pc \times pc \times cname\ option \times pc \times nat$

— start-pc, end-pc, exception type (None = Any), handler-pc, remaining stack depth

**type-synonym**

$ex\text{-}table = ex\text{-}entry\ list$

**type-synonym**

$'addr\ jvm\text{-}method = nat \times nat \times 'addr\ bytecode \times ex\text{-}table$

— max stacksize

— number of local variables. Add 1 + no. of parameters to get no. of registers

— instruction sequence

— exception handler table

**type-synonym**

$'addr\ jvm\text{-}prog = 'addr\ jvm\text{-}method\ prog$

**end**

### 5.3 Abstract heap locales for byte code programs

**theory**  $JVMHeap$

**imports**

$../Common/Conform$

$JVMInstructions$

**begin**

**locale**  $JVM\text{-}heap\text{-}base =$

$heap\text{-}base +$

**constrains**  $addr2thread\text{-}id :: ('addr :: addr) \Rightarrow 'thread\text{-}id$



```

and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype
and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool

```

```

locale JVM-heap =
  JVM-heap-base +
  heap +
  constrains addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
  and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
  and spurious-wakeups :: bool
  and empty-heap :: 'heap
  and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
  and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype
  and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
  and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
  and P :: 'addr jvm-prog

```

```

locale JVM-heap-conf-base =
  heap-conf-base +
  constrains addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
  and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
  and spurious-wakeups :: bool
  and empty-heap :: 'heap
  and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
  and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype
  and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
  and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
  and hconf :: 'heap  $\Rightarrow$  bool
  and P :: 'addr jvm-prog

```

```

sublocale JVM-heap-conf-base < JVM-heap-base .

```

```

locale JVM-heap-conf-base' =
  JVM-heap-conf-base +
  heap +
  constrains addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
  and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
  and spurious-wakeups :: bool
  and empty-heap :: 'heap
  and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
  and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype
  and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
  and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
  and hconf :: 'heap  $\Rightarrow$  bool
  and P :: 'addr jvm-prog

```

```

sublocale JVM-heap-conf-base' < JVM-heap by(unfold-locales)

```

```

locale JVM-heap-conf =
  JVM-heap-conf-base' +

```

```

heap-conf +
constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool
and P :: 'addr jvm-prog

```

```

locale JVM-progress =
  heap-progress +
  JVM-heap-conf-base' +
constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool
and P :: 'addr jvm-prog

```

```

locale JVM-conf-read =
  heap-conf-read +
  JVM-heap-conf +
constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool
and P :: 'addr jvm-prog

```

```

locale JVM-typesafe =
  heap-typesafe +
  JVM-conf-read +
  JVM-progress +
constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool
and P :: 'addr jvm-prog

```

end

## 5.4 JVM Instruction Semantics

**theory** *JVMExecInstr*

**imports**

*JVMInstructions*

*JVMHeap*

*../Common/ExternalCall*

**begin**

**primrec** *extRet2JVM* ::

*nat*  $\Rightarrow$  *'heap*  $\Rightarrow$  *'addr val list*  $\Rightarrow$  *'addr val list*  $\Rightarrow$  *cname*  $\Rightarrow$  *mname*  $\Rightarrow$  *pc*  $\Rightarrow$  *'addr frame list*  
 $\Rightarrow$  *'addr extCallRet*  $\Rightarrow$  (*'addr*, *'heap*) *jvm-state*

**where**

*extRet2JVM* *n h stk loc C M pc frs* (*RetVal* *v*) = (*None*, *h*, (*v* # *drop* (*Suc* *n*) *stk*, *loc*, *C*, *M*, *pc* + 1) # *frs*)  
 $\mid$  *extRet2JVM* *n h stk loc C M pc frs* (*RetExc* *a*) = ( $\lfloor a \rfloor$ , *h*, (*stk*, *loc*, *C*, *M*, *pc*) # *frs*)  
 $\mid$  *extRet2JVM* *n h stk loc C M pc frs* *RetStaySame* = (*None*, *h*, (*stk*, *loc*, *C*, *M*, *pc*) # *frs*)

**lemma** *eq-extRet2JVM-conv* [*simp*]:

(*xcp*, *h'*, *frs'*) = *extRet2JVM* *n h stk loc C M pc frs va*  $\longleftrightarrow$   
*h'* = *h*  $\wedge$  (*case* *va* of *RetVal* *v*  $\Rightarrow$  *xcp* = *None*  $\wedge$  *frs'* = (*v* # *drop* (*Suc* *n*) *stk*, *loc*, *C*, *M*, *pc* + 1) # *frs*  
 $\mid$  *RetExc* *a*  $\Rightarrow$  *xcp* =  $\lfloor a \rfloor$   $\wedge$  *frs'* = (*stk*, *loc*, *C*, *M*, *pc*) # *frs*  
 $\mid$  *RetStaySame*  $\Rightarrow$  *xcp* = *None*  $\wedge$  *frs'* = (*stk*, *loc*, *C*, *M*, *pc*) # *frs*)

**by**(*cases* *va*) *auto*

**definition** *extNTA2JVM* :: *'addr jvm-prog*  $\Rightarrow$  (*cname*  $\times$  *mname*  $\times$  *'addr*)  $\Rightarrow$  *'addr jvm-thread-state*

**where** *extNTA2JVM* *P*  $\equiv$  ( $\lambda$ (*C*, *M*, *a*). *let* (*D*, *M'*, *Ts*, *meth*) = *method* *P C M*; (*mxs*, *mxl0*, *ins*, *xt*) = *the meth*

*in* (*None*, ( $\lfloor \rfloor$ , *Addr* *a* # *replicate* *mxl0* *undefined-value*, *D*, *M*, 0)))

**abbreviation** *extTA2JVM* ::

*'addr jvm-prog*  $\Rightarrow$  (*'addr*, *'thread-id*, *'heap*) *external-thread-action*  $\Rightarrow$  (*'addr*, *'thread-id*, *'heap*) *jvm-thread-action*  
**where** *extTA2JVM* *P*  $\equiv$  *convert-extTA* (*extNTA2JVM* *P*)

**context** *JVM-heap-base* **begin**

**primrec** *exec-instr* ::

*'addr instr*  $\Rightarrow$  *'addr jvm-prog*  $\Rightarrow$  *'thread-id*  $\Rightarrow$  *'heap*  $\Rightarrow$  *'addr val list*  $\Rightarrow$  *'addr val list*  
 $\Rightarrow$  *cname*  $\Rightarrow$  *mname*  $\Rightarrow$  *pc*  $\Rightarrow$  *'addr frame list*  $\Rightarrow$   
 ((*'addr*, *'thread-id*, *'heap*) *jvm-thread-action*  $\times$  (*'addr*, *'heap*) *jvm-state*) *set*

**where**

*exec-instr-Load*:

*exec-instr* (*Load* *n*) *P t h stk loc C<sub>0</sub> M<sub>0</sub> pc frs* =  
 {( $\varepsilon$ , (*None*, *h*, ((*loc* ! *n*) # *stk*, *loc*, *C<sub>0</sub>*, *M<sub>0</sub>*, *pc* + 1) # *frs*))}

$\mid$  *exec-instr* (*Store* *n*) *P t h stk loc C<sub>0</sub> M<sub>0</sub> pc frs* =  
 {( $\varepsilon$ , (*None*, *h*, (*tl* *stk*, *loc*[*n* := *hd* *stk*], *C<sub>0</sub>*, *M<sub>0</sub>*, *pc* + 1) # *frs*))}

$\mid$  *exec-instr-Push*:

$exec\_instr (Push\ v)\ P\ t\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$   
 $\{(\varepsilon, (None, h, (v \# stk, loc, C_0, M_0, pc+1)\#frs))\}$

$| exec\_instr-New:$   
 $exec\_instr (New\ C)\ P\ t\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$   
 $(let\ HA = allocate\ h\ (Class-type\ C)$   
 $in\ if\ HA = \{\} then\ \{(\varepsilon, \lfloor addr-of-sys-xcpt\ OutOfMemory \rfloor, h, (stk, loc, C_0, M_0, pc) \# frs)\}$   
 $else\ (\lambda(h', a). (\llbracket NewHeapElem\ a\ (Class-type\ C) \rrbracket, None, h', (Addr\ a \# stk, loc, C_0, M_0, pc +$   
 $1)\#frs))\ 'HA)$

$| exec\_instr-NewArray:$   
 $exec\_instr (NewArray\ T)\ P\ t\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$   
 $(let\ si = the-Intg\ (hd\ stk);$   
 $i = nat\ (sint\ si)$   
 $in\ (if\ si < s\ 0$   
 $then\ \{(\varepsilon, \lfloor addr-of-sys-xcpt\ NegativeArraySize \rfloor, h, (stk, loc, C_0, M_0, pc) \# frs)\}$   
 $else\ let\ HA = allocate\ h\ (Array-type\ T\ i)$   
 $in\ if\ HA = \{\} then\ \{(\varepsilon, \lfloor addr-of-sys-xcpt\ OutOfMemory \rfloor, h, (stk, loc, C_0, M_0, pc) \# frs)\}$   
 $else\ (\lambda(h', a). (\llbracket NewHeapElem\ a\ (Array-type\ T\ i) \rrbracket, None, h', (Addr\ a \# tl\ stk, loc, C_0,$   
 $M_0, pc + 1) \# frs))\ 'HA))$

$| exec\_instr-ALoad:$   
 $exec\_instr ALoad\ P\ t\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$   
 $(let\ i = the-Intg\ (hd\ stk);$   
 $va = hd\ (tl\ stk);$   
 $a = the-Addr\ va;$   
 $len = alen-of-htype\ (the\ (typeof-addr\ h\ a))$   
 $in\ (if\ va = Null\ then\ \{(\varepsilon, \lfloor addr-of-sys-xcpt\ NullPointer \rfloor, h, (stk, loc, C_0, M_0, pc) \# frs)\}$   
 $else\ if\ i < s\ 0 \vee int\ len \leq sint\ i\ then$   
 $\{(\varepsilon, \lfloor addr-of-sys-xcpt\ ArrayIndexOutOfBounds \rfloor, h, (stk, loc, C_0, M_0, pc) \# frs)\}$   
 $else\ \{(\llbracket ReadMem\ a\ (ACell\ (nat\ (sint\ i)))\ v \rrbracket, None, h, (v \# tl\ (tl\ stk), loc, C_0, M_0, pc + 1)$   
 $\# frs)\ | v.$   
 $heap-read\ h\ a\ (ACell\ (nat\ (sint\ i)))\ v\ \})$

$| exec\_instr-ASore:$   
 $exec\_instr AStore\ P\ t\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$   
 $(let\ ve = hd\ stk;$   
 $vi = hd\ (tl\ stk);$   
 $va = hd\ (tl\ (tl\ stk))$   
 $in\ (if\ va = Null\ then\ \{(\varepsilon, \lfloor addr-of-sys-xcpt\ NullPointer \rfloor, h, (stk, loc, C_0, M_0, pc) \# frs)\}$   
 $else\ (let\ i = the-Intg\ vi;$   
 $idx = nat\ (sint\ i);$   
 $a = the-Addr\ va;$   
 $hT = the\ (typeof-addr\ h\ a);$   
 $T = ty-of-htype\ hT;$   
 $len = alen-of-htype\ hT;$   
 $U = the\ (typeof_h\ ve)$   
 $in\ (if\ i < s\ 0 \vee int\ len \leq sint\ i\ then$   
 $\{(\varepsilon, \lfloor addr-of-sys-xcpt\ ArrayIndexOutOfBounds \rfloor, h, (stk, loc, C_0, M_0, pc) \# frs)\}$   
 $else\ if\ P \vdash U \leq the-Array\ T\ then$   
 $\{(\llbracket WriteMem\ a\ (ACell\ idx)\ ve \rrbracket, None, h', (tl\ (tl\ (tl\ stk)), loc, C_0, M_0, pc+1) \# frs)$   
 $| h'. heap-write\ h\ a\ (ACell\ idx)\ ve\ h'\}$   
 $else\ \{(\varepsilon, (\lfloor addr-of-sys-xcpt\ ArrayStore \rfloor, h, (stk, loc, C_0, M_0, pc) \# frs))\})$

| *exec-instr-ALength*:  
*exec-instr ALength P t h stk loc C0 M0 pc frs* =  
 $\{(\varepsilon, (\text{let } va = \text{hd } stk$   
 $\quad \text{in if } va = \text{Null}$   
 $\quad \quad \text{then } (\lfloor \text{addr-of-sys-xcpt NullPointer} \rfloor, h, (stk, loc, C0, M0, pc) \# frs)$   
 $\quad \quad \text{else } (None, h, (\text{Intg } (\text{word-of-int } (\text{int } (\text{alen-of-htype } (\text{the } (\text{typeof-addr } h) (\text{the-Addr } va)))))) \#$   
 $\quad \text{tl } stk, loc, C0, M0, pc+1) \# frs)))\}$

| *exec-instr (Getfield F C) P t h stk loc C0 M0 pc frs* =  
 $(\text{let } v = \text{hd } stk$   
 $\quad \text{in if } v = \text{Null then } \{(\varepsilon, \lfloor \text{addr-of-sys-xcpt NullPointer} \rfloor, h, (stk, loc, C0, M0, pc) \# frs)\}$   
 $\quad \quad \text{else let } a = \text{the-Addr } v$   
 $\quad \quad \text{in } \{(\lfloor \text{ReadMem } a (CField C F) v \rfloor, None, h, (v' \# (\text{tl } stk), loc, C0, M0, pc + 1) \# frs) \mid$   
 $\quad v'. \quad \text{heap-read } h a (CField C F) v'\}$

| *exec-instr (Putfield F C) P t h stk loc C0 M0 pc frs* =  
 $(\text{let } v = \text{hd } stk;$   
 $\quad r = \text{hd } (\text{tl } stk)$   
 $\quad \text{in if } r = \text{Null then } \{(\varepsilon, \lfloor \text{addr-of-sys-xcpt NullPointer} \rfloor, h, (stk, loc, C0, M0, pc) \# frs)\}$   
 $\quad \quad \text{else let } a = \text{the-Addr } r$   
 $\quad \quad \text{in } \{(\lfloor \text{WriteMem } a (CField C F) v \rfloor, None, h', (\text{tl } (\text{tl } stk), loc, C0, M0, pc + 1) \# frs) \mid h'. \quad$   
 $\quad \quad \text{heap-write } h a (CField C F) v h'\}$

| *exec-instr (CAS F C) P t h stk loc C0 M0 pc frs* =  
 $(\text{let } v'' = \text{hd } stk; v' = \text{hd } (\text{tl } stk); v = \text{hd } (\text{tl } (\text{tl } stk)))$   
 $\quad \text{in if } v = \text{Null then } \{(\varepsilon, \lfloor \text{addr-of-sys-xcpt NullPointer} \rfloor, h, (stk, loc, C0, M0, pc) \# frs)\}$   
 $\quad \quad \text{else let } a = \text{the-Addr } v$   
 $\quad \quad \text{in } \{(\lfloor \text{ReadMem } a (CField C F) v', \text{WriteMem } a (CField C F) v'' \rfloor, None, h', (\text{Bool True } \#$   
 $\quad \text{tl } (\text{tl } (\text{tl } stk)), loc, C0, M0, pc + 1) \# frs) \mid h'. \quad$   
 $\quad \quad \text{heap-read } h a (CField C F) v' \wedge \text{heap-write } h a (CField C F) v'' h'\} \cup$   
 $\quad \quad \{(\lfloor \text{ReadMem } a (CField C F) v'' \rfloor, None, h, (\text{Bool False } \# \text{tl } (\text{tl } (\text{tl } stk)), loc, C0, M0, pc$   
 $\quad + 1) \# frs) \mid v''. \quad$   
 $\quad \quad \text{heap-read } h a (CField C F) v'' \wedge v'' \neq v'\}$

| *exec-instr (Checkcast T) P t h stk loc C0 M0 pc frs* =  
 $\{(\varepsilon, \text{let } U = \text{the } (\text{typeof}_h (\text{hd } stk))$   
 $\quad \text{in if } P \vdash U \leq T \text{ then } (None, h, (stk, loc, C0, M0, pc + 1) \# frs)$   
 $\quad \quad \text{else } (\lfloor \text{addr-of-sys-xcpt ClassCast} \rfloor, h, (stk, loc, C0, M0, pc) \# frs))\}$

| *exec-instr (Instanceof T) P t h stk loc C0 M0 pc frs* =  
 $\{(\varepsilon, None, h, (\text{Bool } (\text{hd } stk \neq \text{Null} \wedge P \vdash \text{the } (\text{typeof}_h (\text{hd } stk)) \leq T) \# \text{tl } stk, loc, C0, M0, pc +$   
 $\quad 1) \# frs)\}$

| *exec-instr-Invoke*:  
*exec-instr (Invoke M n) P t h stk loc C0 M0 pc frs* =  
 $(\text{let } ps = \text{rev } (\text{take } n \text{ stk});$   
 $\quad r = \text{stk } ! n;$   
 $\quad a = \text{the-Addr } r;$   
 $\quad T = \text{the } (\text{typeof-addr } h a)$   
 $\quad \text{in } (\text{if } r = \text{Null then } \{(\varepsilon, \lfloor \text{addr-of-sys-xcpt NullPointer} \rfloor, h, (stk, loc, C0, M0, pc) \# frs)\}$   
 $\quad \quad \text{else}$   
 $\quad \quad \text{let } C = \text{class-type-of } T;$   
 $\quad \quad (D, M', Ts, \text{meth}) = \text{method } P C M$

*in case meth of*  
*Native*  $\Rightarrow$   
 $\{(extTA2JVM\ P\ ta,\ extRet2JVM\ n\ h'\ stk\ loc\ C0\ M0\ pc\ frs\ va) \mid ta\ va\ h'. \\ (ta,\ va,\ h') \in red\text{-}external\text{-}aggr\ P\ t\ a\ M\ ps\ h\}$   
 $\mid \lfloor (m\ x\ s, m\ x\ l_0, i\ n\ s, x\ t) \rfloor \Rightarrow$   
 $let\ f' = (\lfloor, [r] @ ps @ (replicate\ m\ x\ l_0\ undefined\text{-}value), D, M, 0)$   
 $in\ \{(\varepsilon,\ None,\ h,\ f' \# (stk,\ loc,\ C0,\ M0,\ pc) \# frs)\}$

$\mid exec\text{-}instr\ Return\ P\ t\ h\ stk_0\ loc_0\ C_0\ M_0\ pc\ frs =$   
 $\{(\varepsilon,\ (if\ frs = \lfloor\ then\ (None,\ h,\ \lfloor)\ else$   
 $let\ v = hd\ stk_0;$   
 $(stk, loc, C, m, pc) = hd\ frs;$   
 $n = length\ (fst\ (snd\ (method\ P\ C_0\ M_0)))$   
 $in\ (None,\ h,\ (v \# (drop\ (n+1)\ stk), loc, C, m, pc+1) \# tl\ frs))\ \}$

$\mid exec\text{-}instr\ Pop\ P\ t\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$   
 $\{(\varepsilon,\ (None,\ h,\ (tl\ stk,\ loc,\ C_0,\ M_0,\ pc+1) \# frs))\}$

$\mid exec\text{-}instr\ Dup\ P\ t\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$   
 $\{(\varepsilon,\ (None,\ h,\ (hd\ stk \# stk,\ loc,\ C_0,\ M_0,\ pc+1) \# frs))\}$

$\mid exec\text{-}instr\ Swap\ P\ t\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$   
 $\{(\varepsilon,\ (None,\ h,\ (hd\ (tl\ stk) \# hd\ stk \# tl\ (tl\ stk), loc,\ C_0,\ M_0,\ pc+1) \# frs))\}$

$\mid exec\text{-}instr\ (BinOpInstr\ bop)\ P\ t\ h\ stk\ loc\ C0\ M0\ pc\ frs =$   
 $\{(\varepsilon,$   
 $case\ the\ (binop\ bop\ (hd\ (tl\ stk))\ (hd\ stk))\ of$   
 $Inl\ v \Rightarrow (None,\ h,\ (v \# tl\ (tl\ stk), loc,\ C0,\ M0,\ pc+1) \# frs)$   
 $\mid Inr\ a \Rightarrow (Some\ a,\ h,\ (stk,\ loc,\ C0,\ M0,\ pc) \# frs))\}$

$\mid exec\text{-}instr\ (IfFalse\ i)\ P\ t\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$   
 $\{(\varepsilon,\ (let\ pc' = if\ hd\ stk = Bool\ False\ then\ nat(int\ pc+i)\ else\ pc+1$   
 $in\ (None,\ h,\ (tl\ stk,\ loc,\ C_0,\ M_0,\ pc') \# frs))\ \}$

$\mid exec\text{-}instr\ Goto:$   
 $exec\text{-}instr\ (Goto\ i)\ P\ t\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$   
 $\{(\varepsilon,\ (None,\ h,\ (stk,\ loc,\ C_0,\ M_0,\ nat(int\ pc+i) \# frs))\ \}$

$\mid exec\text{-}instr\ ThrowExc\ P\ t\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$   
 $\{(\varepsilon,\ (let\ xp' = if\ hd\ stk = Null\ then\ \lfloor addr\text{-}of\text{-}sys\text{-}xcpt\ NullPointer \rfloor\ else\ \lfloor the\text{-}Addr(hd\ stk) \rfloor$   
 $in\ (xp',\ h,\ (stk,\ loc,\ C_0,\ M_0,\ pc) \# frs))\ \}$

$\mid exec\text{-}instr\ MEnter:$   
 $exec\text{-}instr\ MEnter\ P\ t\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$   
 $\{let\ v = hd\ stk$   
 $in\ if\ v = Null$   
 $then\ (\varepsilon,\ \lfloor addr\text{-}of\text{-}sys\text{-}xcpt\ NullPointer \rfloor,\ h,\ (stk,\ loc,\ C_0,\ M_0,\ pc) \# frs)$   
 $else\ (\{\!| Lock \rightarrow the\text{-}Addr\ v, SyncLock\ (the\text{-}Addr\ v) \!\},\ None,\ h,\ (tl\ stk,\ loc,\ C_0,\ M_0,\ pc + 1) \# frs)\}$

$\mid exec\text{-}instr\ MExit:$   
 $exec\text{-}instr\ MExit\ P\ t\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$   
 $(let\ v = hd\ stk$   
 $in\ if\ v = Null$   
 $then\ \{(\varepsilon,\ \lfloor addr\text{-}of\text{-}sys\text{-}xcpt\ NullPointer \rfloor,\ h,\ (stk,\ loc,\ C_0,\ M_0,\ pc) \# frs)\}$

```

    else {( $\llbracket \text{Unlock} \rightarrow \text{the-Addr } v \rrbracket$ ,  $\text{SyncUnlock } (\text{the-Addr } v)$ ),  $\text{None}$ ,  $h$ , ( $\text{tl } \text{stk}$ ,  $\text{loc}$ ,  $C_0$ ,  $M_0$ ,  $pc + 1$ ) #  $\text{frs}$ ),
      ( $\llbracket \text{UnlockFail} \rightarrow \text{the-Addr } v \rrbracket$ ,  $\llbracket \text{addr-of-sys-xcpt } \text{IllegalMonitorState} \rrbracket$ ,  $h$ , ( $\text{stk}$ ,  $\text{loc}$ ,  $C_0$ ,  $M_0$ ,  $pc$ ) #  $\text{frs}$ ))}

```

**end**

**end**

## 5.5 Exception handling in the JVM

**theory** *JVMExceptions*

**imports**

*JVMInstructions*

**begin**

**abbreviation** *Any* :: *cname* option

**where** *Any*  $\equiv$  *None*

**definition** *matches-ex-entry* :: '*m prog*  $\Rightarrow$  *cname*  $\Rightarrow$  *pc*  $\Rightarrow$  *ex-entry*  $\Rightarrow$  *bool*

**where**

```

matches-ex-entry P C pc xcp  $\equiv$ 
  let (s, e, C', h, d) = xcp in
    s  $\leq$  pc  $\wedge$  pc < e  $\wedge$  (case C' of None  $\Rightarrow$  True |  $\llbracket C'' \rrbracket \Rightarrow P \vdash C \preceq^* C''$ )

```

**primrec**

*match-ex-table* :: '*m prog*  $\Rightarrow$  *cname*  $\Rightarrow$  *pc*  $\Rightarrow$  *ex-table*  $\Rightarrow$  (*pc*  $\times$  *nat*) option

**where**

```

match-ex-table P C pc [] = None
| match-ex-table P C pc (e # es) = (if matches-ex-entry P C pc e
  then Some (snd(snd(snd e)))
  else match-ex-table P C pc es)

```

**abbreviation** *ex-table-of* :: '*addr jvm-prog*  $\Rightarrow$  *cname*  $\Rightarrow$  *mname*  $\Rightarrow$  *ex-table*

**where** *ex-table-of* *P C M* == *snd* (*snd* (*snd* (*the* (*snd* (*snd* (*snd* (*method* *P C M*)))))))

**lemma** *match-ex-table-SomeD*:

```

match-ex-table P C pc xt = Some (pc', d')  $\implies$ 
   $\exists (f, t, D, h, d) \in \text{set } xt. \text{ matches-ex-entry } P C pc (f, t, D, h, d) \wedge h = pc' \wedge d = d'$ 
by (induct xt) (auto split: if-split-asm)

```

**end**

## 5.6 Program Execution in the JVM

**theory** *JVMExec*

**imports**

*JVMExecInstr*

*JVMExceptions*

*../Common/StartConfig*

**begin**

**abbreviation** *instrs-of* :: 'addr jvm-prog  $\Rightarrow$  cname  $\Rightarrow$  mname  $\Rightarrow$  'addr instr list  
**where** *instrs-of* *P C M* == fst(snd(snd(the(snd(snd(snd(method *P C M*)))))))

### 5.6.1 single step execution

**context** *JVM-heap-base* **begin**

**fun** *exception-step* :: 'addr jvm-prog  $\Rightarrow$  'addr  $\Rightarrow$  'heap  $\Rightarrow$  'addr frame  $\Rightarrow$  'addr frame list  $\Rightarrow$  ('addr, 'heap) jvm-state

**where**

*exception-step* *P a h* (*stk*, *loc*, *C*, *M*, *pc*) *frs* =  
 (case match-ex-table *P* (cname-of *h a*) *pc* (ex-table-of *P C M*) of  
   None  $\Rightarrow$  ([*a*], *h*, *frs*)  
   | Some (*pc'*, *d*)  $\Rightarrow$  (None, *h*, (Addr *a* # drop (size *stk* - *d*) *stk*, *loc*, *C*, *M*, *pc'*) # *frs*))

**lemma** *exception-step-def-raw*:

*exception-step* =  
 ( $\lambda P a h$  (*stk*, *loc*, *C*, *M*, *pc*) *frs*.  
 case match-ex-table *P* (cname-of *h a*) *pc* (ex-table-of *P C M*) of  
   None  $\Rightarrow$  ([*a*], *h*, *frs*)  
   | Some (*pc'*, *d*)  $\Rightarrow$  (None, *h*, (Addr *a* # drop (size *stk* - *d*) *stk*, *loc*, *C*, *M*, *pc'*) # *frs*))

**by**(intro ext) auto

**fun** *exec* :: 'addr jvm-prog  $\Rightarrow$  'thread-id  $\Rightarrow$  ('addr, 'heap) jvm-state  $\Rightarrow$  ('addr, 'thread-id, 'heap) jvm-ta-state set **where**  
*exec* *P t* (*xcp*, *h*, []) = {}  
 | *exec* *P t* (None, *h*, (*stk*, *loc*, *C*, *M*, *pc*) # *frs*) = *exec-instr* (*instrs-of* *P C M* ! *pc*) *P t h stk loc C M pc frs*  
 | *exec* *P t* ([*a*], *h*, *fr* # *frs*) = {( $\epsilon$ , *exception-step* *P a h fr frs*)}

### 5.6.2 relational view

**inductive** *exec-1* ::

'addr jvm-prog  $\Rightarrow$  'thread-id  $\Rightarrow$  ('addr, 'heap) jvm-state  
 $\Rightarrow$  ('addr, 'thread-id, 'heap) jvm-thread-action  $\Rightarrow$  ('addr, 'heap) jvm-state  $\Rightarrow$  bool  
 (-,  $\vdash$  / - ---jvm $\rightarrow$  / - [61,0,61,0,61] 60)  
**for** *P* :: 'addr jvm-prog **and** *t* :: 'thread-id

**where**

*exec-1I*:  
 (*ta*,  $\sigma'$ )  $\in$  *exec* *P t*  $\sigma \implies P, t \vdash \sigma$  -ta-jvm $\rightarrow$   $\sigma'$

**lemma** *exec-1-iff*:

$P, t \vdash \sigma$  -ta-jvm $\rightarrow$   $\sigma' \iff (ta, \sigma') \in$  *exec* *P t*  $\sigma$

**by**(auto intro: *exec-1I* elim: *exec-1.cases*)

**end**

The start configuration of the JVM: in the start heap, we call a method *m* of class *C* in program *P* with parameters *vs*. The *this* pointer of the frame is set to *Null* to simulate a static method invocation.

**abbreviation** *JVM-local-start* ::

cname  $\Rightarrow$  mname  $\Rightarrow$  ty list  $\Rightarrow$  ty  $\Rightarrow$  'addr jvm-method  $\Rightarrow$  'addr val list  
 $\Rightarrow$  'addr jvm-thread-state

**where**



*JVM-local-start*  $\equiv$   
 $\lambda C M Ts T (mxs, mxl0, b) vs.$   
 $(None, ([\square], Null \# vs @ replicate mxl0 undefined-value, C, M, 0))$

**context** *JVM-heap-base* **begin**

**abbreviation** *JVM-start-state* ::

$'addr \text{ jvm-prog} \Rightarrow cname \Rightarrow mname \Rightarrow 'addr \text{ val list} \Rightarrow ('addr, 'thread-id, 'addr \text{ jvm-thread-state}, 'heap, 'addr)$   
*state*

**where**

*JVM-start-state*  $\equiv start-state \text{ JVM-local-start}$

**definition** *JVM-start-state'* ::  $'addr \text{ jvm-prog} \Rightarrow cname \Rightarrow mname \Rightarrow 'addr \text{ val list} \Rightarrow ('addr, 'heap)$   
*jvm-state*

**where**

*JVM-start-state'*  $P C M vs \equiv$   
 $let (D, Ts, T, meth) = method P C M;$   
 $(mxs, mxl0, ins, xt) = the meth$   
 $in (None, start-heap, ([\square], Null \# vs @ replicate mxl0 undefined-value, D, M, 0))$

**end**

**end**

## 5.7 A Defensive JVM

**theory** *JVMDefensive*

**imports** *JVMExec* ../Common/ExternalCallWF

**begin**

Extend the state space by one element indicating a type error (or other abnormal termination)

**datatype**  $'a \text{ type-error} = TypeError \mid Normal 'a$

**context** *JVM-heap-base* **begin**

**definition** *is-Array-ref* ::  $'addr \text{ val} \Rightarrow 'heap \Rightarrow bool$  **where**

*is-Array-ref*  $v h \equiv$   
 $is-Ref v \wedge$   
 $(v \neq Null \longrightarrow typeof-addr h (the-Addr v) \neq None \wedge is-Array (ty-of-htype (the (typeof-addr h (the-Addr v))))))$

**declare** *is-Array-ref-def*[simp]

**primrec** *check-instr* ::  $[ 'addr \text{ instr}, 'addr \text{ jvm-prog}, 'heap, 'addr \text{ val list}, 'addr \text{ val list},$   
 $cname, mname, pc, 'addr \text{ frame list}] \Rightarrow bool$

**where**

*check-instr-Load*:  
 $check-instr (Load n) P h stk loc C M_0 pc frs =$   
 $(n < length loc)$

| *check-instr-Store*:  
 $check-instr (Store n) P h stk loc C_0 M_0 pc frs =$

$(0 < \text{length } \text{stk} \wedge n < \text{length } \text{loc})$

| *check-instr-Push*:

$\text{check-instr } (\text{Push } v) P h \text{ stk } \text{loc } C_0 M_0 \text{ pc } \text{frs} =$   
 $(\neg \text{is-Addr } v)$

| *check-instr-New*:

$\text{check-instr } (\text{New } C) P h \text{ stk } \text{loc } C_0 M_0 \text{ pc } \text{frs} =$   
 $\text{is-class } P C$

| *check-instr-NewArray*:

$\text{check-instr } (\text{NewArray } T) P h \text{ stk } \text{loc } C_0 M_0 \text{ pc } \text{frs} =$   
 $(\text{is-type } P (T[]) \wedge 0 < \text{length } \text{stk} \wedge \text{is-Intg } (\text{hd } \text{stk}))$

| *check-instr-ALoad*:

$\text{check-instr } \text{ALoad } P h \text{ stk } \text{loc } C_0 M_0 \text{ pc } \text{frs} =$   
 $(1 < \text{length } \text{stk} \wedge \text{is-Intg } (\text{hd } \text{stk}) \wedge \text{is-Array-ref } (\text{hd } (\text{tl } \text{stk})) h)$

| *check-instr-ASStore*:

$\text{check-instr } \text{ASStore } P h \text{ stk } \text{loc } C_0 M_0 \text{ pc } \text{frs} =$   
 $(2 < \text{length } \text{stk} \wedge \text{is-Intg } (\text{hd } (\text{tl } \text{stk})) \wedge \text{is-Array-ref } (\text{hd } (\text{tl } (\text{tl } \text{stk}))) h \wedge \text{typeof}_h (\text{hd } \text{stk}) \neq \text{None})$

| *check-instr-ALength*:

$\text{check-instr } \text{ALength } P h \text{ stk } \text{loc } C_0 M_0 \text{ pc } \text{frs} =$   
 $(0 < \text{length } \text{stk} \wedge \text{is-Array-ref } (\text{hd } \text{stk}) h)$

| *check-instr-Getfield*:

$\text{check-instr } (\text{Getfield } F C) P h \text{ stk } \text{loc } C_0 M_0 \text{ pc } \text{frs} =$   
 $(0 < \text{length } \text{stk} \wedge (\exists C' T \text{ fm}. P \vdash C \text{ sees } F:T (\text{fm}) \text{ in } C') \wedge$   
 $(\text{let } (C', T, \text{fm}) = \text{field } P C F; \text{ref} = \text{hd } \text{stk} \text{ in}$   
 $C' = C \wedge \text{is-Ref } \text{ref} \wedge (\text{ref} \neq \text{Null} \rightarrow$   
 $(\exists T. \text{typeof-addr } h (\text{the-Addr } \text{ref}) = \lfloor T \rfloor \wedge P \vdash \text{class-type-of } T \preceq^* C))))$

| *check-instr-Putfield*:

$\text{check-instr } (\text{Putfield } F C) P h \text{ stk } \text{loc } C_0 M_0 \text{ pc } \text{frs} =$   
 $(1 < \text{length } \text{stk} \wedge (\exists C' T \text{ fm}. P \vdash C \text{ sees } F:T (\text{fm}) \text{ in } C') \wedge$   
 $(\text{let } (C', T, \text{fm}) = \text{field } P C F; v = \text{hd } \text{stk}; \text{ref} = \text{hd } (\text{tl } \text{stk}) \text{ in}$   
 $C' = C \wedge \text{is-Ref } \text{ref} \wedge (\text{ref} \neq \text{Null} \rightarrow$   
 $(\exists T'. \text{typeof-addr } h (\text{the-Addr } \text{ref}) = \lfloor T' \rfloor \wedge P \vdash \text{class-type-of } T' \preceq^* C \wedge P, h \vdash v : \leq T))))$

| *check-instr-CAS*:

$\text{check-instr } (\text{CAS } F C) P h \text{ stk } \text{loc } C_0 M_0 \text{ pc } \text{frs} =$   
 $(2 < \text{length } \text{stk} \wedge (\exists C' T \text{ fm}. P \vdash C \text{ sees } F:T (\text{fm}) \text{ in } C') \wedge$   
 $(\text{let } (C', T, \text{fm}) = \text{field } P C F; v'' = \text{hd } \text{stk}; v' = \text{hd } (\text{tl } \text{stk}); v = \text{hd } (\text{tl } (\text{tl } \text{stk})) \text{ in}$   
 $C' = C \wedge \text{is-Ref } v \wedge \text{volatile } \text{fm} \wedge (v \neq \text{Null} \rightarrow$   
 $(\exists T'. \text{typeof-addr } h (\text{the-Addr } v) = \lfloor T' \rfloor \wedge P \vdash \text{class-type-of } T' \preceq^* C \wedge P, h \vdash v' : \leq T \wedge P, h \vdash$   
 $v'' : \leq T))))$

| *check-instr-Checkcast*:

$\text{check-instr } (\text{Checkcast } T) P h \text{ stk } \text{loc } C_0 M_0 \text{ pc } \text{frs} =$   
 $(0 < \text{length } \text{stk} \wedge \text{is-type } P T)$

| *check-instr-Instanceof*:

$\text{check-instr } (\text{Instanceof } T) P h \text{ stk } \text{loc } C_0 M_0 \text{ pc } \text{frs} =$

$(0 < \text{length } stk \wedge \text{is-type } P \ T \wedge \text{is-Ref } (hd \ stk))$

| *check-instr-Invoke*:

$\text{check-instr } (\text{Invoke } M \ n) \ P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$   
 $(n < \text{length } stk \wedge \text{is-Ref } (stk!n) \wedge$   
 $(stk!n \neq \text{Null} \longrightarrow$   
 $(\text{let } a = \text{the-Addr } (stk!n);$   
 $\quad T = \text{the } (\text{typeof-addr } h \ a);$   
 $\quad C = \text{class-type-of } T;$   
 $\quad (D, Ts, Tr, \text{meth}) = \text{method } P \ C \ M$   
 $\text{in } \text{typeof-addr } h \ a \neq \text{None} \wedge P \vdash C \text{ has } M \wedge$   
 $P, h \vdash \text{rev } (\text{take } n \ stk) \ [:\leq] \ Ts \wedge$   
 $(\text{meth} = \text{None} \longrightarrow D \cdot M(Ts) :: Tr))))$

| *check-instr-Return*:

$\text{check-instr } \text{Return } P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$   
 $(0 < \text{length } stk \wedge ((0 < \text{length } frs) \longrightarrow$   
 $(P \vdash C_0 \text{ has } M_0) \wedge$   
 $(\text{let } v = hd \ stk;$   
 $\quad T = fst \ (snd \ (snd \ (\text{method } P \ C_0 \ M_0)))$   
 $\text{in } P, h \vdash v : \leq T)))$

| *check-instr-Pop*:

$\text{check-instr } \text{Pop } P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$   
 $(0 < \text{length } stk)$

| *check-instr-Dup*:

$\text{check-instr } \text{Dup } P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$   
 $(0 < \text{length } stk)$

| *check-instr-Swap*:

$\text{check-instr } \text{Swap } P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$   
 $(1 < \text{length } stk)$

| *check-instr-BinOpInstr*:

$\text{check-instr } (\text{BinOpInstr } bop) \ P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$   
 $(1 < \text{length } stk \wedge (\exists T1 \ T2 \ T. \text{typeof}_h \ (hd \ stk) = \lfloor T2 \rfloor \wedge \text{typeof}_h \ (hd \ (tl \ stk)) = \lfloor T1 \rfloor \wedge P \vdash$   
 $T1 \ll bop \gg T2 : T))$

| *check-instr-IfFalse*:

$\text{check-instr } (\text{IfFalse } b) \ P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$   
 $(0 < \text{length } stk \wedge \text{is-Bool } (hd \ stk) \wedge 0 \leq \text{int } pc + b)$

| *check-instr-Goto*:

$\text{check-instr } (\text{Goto } b) \ P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$   
 $(0 \leq \text{int } pc + b)$

| *check-instr-Throw*:

$\text{check-instr } \text{ThrowExc } P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$   
 $(0 < \text{length } stk \wedge \text{is-Ref } (hd \ stk) \wedge P \vdash \text{the } (\text{typeof}_h \ (hd \ stk)) \leq \text{Class } \text{Throwable})$

| *check-instr-MEnter*:

$\text{check-instr } \text{MEnter } P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$   
 $(0 < \text{length } stk \wedge \text{is-Ref } (hd \ stk))$

| *check-instr-MExit*:  
*check-instr MExit P h stk loc C<sub>0</sub> M<sub>0</sub> pc frs =*  
*(0 < length stk ∧ is-Ref (hd stk))*

**definition** *check-xcpt* :: 'addr jvm-prog ⇒ 'heap ⇒ nat ⇒ pc ⇒ ex-table ⇒ 'addr ⇒ bool  
**where**  
*check-xcpt P h n pc xt a ⟷*  
*(∃ C. typeof-addr h a = [Class-type C] ∧*  
*(case match-ex-table P C pc xt of None ⇒ True | Some (pc', d') ⇒ d' ≤ n))*

**definition** *check* :: 'addr jvm-prog ⇒ ('addr, 'heap) jvm-state ⇒ bool  
**where**  
*check P σ ≡ let (xcpt, h, frs) = σ in*  
*(case frs of [] ⇒ True | (stk, loc, C, M, pc) # frs' ⇒*  
*P ⊢ C has M ∧*  
*(let (C', Ts, T, meth) = method P C M; (mxs, mxl<sub>0</sub>, ins, xt) = the meth; i = ins!pc in*  
*meth ≠ None ∧ pc < size ins ∧ size stk ≤ mxs ∧*  
*(case xcpt of None ⇒ check-instr i P h stk loc C M pc frs'*  
*| Some a ⇒ check-xcpt P h (length stk) pc xt a)))*

**definition** *exec-d* ::  
'addr jvm-prog ⇒ 'thread-id ⇒ ('addr, 'heap) jvm-state ⇒ ('addr, 'thread-id, 'heap) jvm-ta-state set  
type-error  
**where**  
*exec-d P t σ ≡ if check P σ then Normal (exec P t σ) else TypeError*

**inductive**  
*exec-1-d* ::  
'addr jvm-prog ⇒ 'thread-id ⇒ ('addr, 'heap) jvm-state type-error  
⇒ ('addr, 'thread-id, 'heap) jvm-thread-action ⇒ ('addr, 'heap) jvm-state type-error ⇒ bool  
(·, · ⊢ - ---jvmd→ - [61, 0, 61, 0, 61] 60)  
**for** *P* :: 'addr jvm-prog **and** *t* :: 'thread-id  
**where**  
*exec-1-d-ErrorI: exec-d P t σ = TypeError ⟹ P, t ⊢ Normal σ -ε-jvmd→ TypeError*  
| *exec-1-d-NormalI: [ exec-d P t σ = Normal Σ; (tas, σ') ∈ Σ ] ⟹ P, t ⊢ Normal σ -tas-jvmd→ Normal σ'*

**lemma** *jvmd-NormalD*:  
*P, t ⊢ Normal σ -ta-jvmd→ Normal σ' ⟹ check P σ ∧ (ta, σ') ∈ exec P t σ ∧ (∃ xcp h f frs. σ = (xcp, h, f # frs))*  
**apply**(erule *exec-1-d.cases*, auto simp add: *exec-d-def split: if-split-asm*)  
**apply**(case-tac *b*, auto)  
**done**

**lemma** *jvmd-NormalE*:  
**assumes** *P, t ⊢ Normal σ -ta-jvmd→ Normal σ'*  
**obtains** *xcp h f frs* **where** *check P σ (ta, σ') ∈ exec P t σ σ = (xcp, h, f # frs)*  
**using** *assms*  
**by**(auto dest: *jvmd-NormalD*)

**lemma** *exec-d-eq-TypeError*: *exec-d P t σ = TypeError ⟷ ¬ check P σ*  
**by**(simp add: *exec-d-def*)

**lemma** *exec-d-eq-Normal*:  $\text{exec-d } P \ t \ \sigma = \text{Normal } (\text{exec } P \ t \ \sigma) \longleftrightarrow \text{check } P \ \sigma$   
**by**(*auto simp add: exec-d-def*)

**end**

**declare** *split-paired-All* [*simp del*]

**declare** *split-paired-Ex* [*simp del*]

**lemma** *if-neq* [*dest!*]:

$(\text{if } P \text{ then } A \text{ else } B) \neq B \implies P$

**by** (*cases P, auto*)

**context** *JVM-heap-base* **begin**

**lemma** *exec-d-no-errorI* [*intro*]:

$\text{check } P \ \sigma \implies \text{exec-d } P \ t \ \sigma \neq \text{TypeError}$

**by** (*unfold exec-d-def simp*)

**theorem** *no-type-error-commutes*:

$\text{exec-d } P \ t \ \sigma \neq \text{TypeError} \implies \text{exec-d } P \ t \ \sigma = \text{Normal } (\text{exec } P \ t \ \sigma)$

**by** (*unfold exec-d-def, auto*)

**lemma** *defensive-imp-aggressive-1*:

$P, t \vdash (\text{Normal } \sigma) \text{ --tas-jvmd--} (\text{Normal } \sigma') \implies P, t \vdash \sigma \text{ --tas-jvm--} \sigma'$

**by**(*auto elim!: exec-1-d.cases intro!: exec-1.intros simp add: exec-d-def split: if-split-asm*)

**end**

**context** *JVM-heap* **begin**

**lemma** *check-exec-hext*:

**assumes** *exec*:  $(ta, xcp', h', frs') \in \text{exec } P \ t \ (xcp, h, frs)$

**and** *check*:  $\text{check } P \ (xcp, h, frs)$

**shows**  $h \sqsubseteq h'$

**proof** –

**from** *exec* **have**  $frs \neq []$  **by**(*auto*)

**then obtain**  $f \ Frs$  **where**  $frs$  [*simp*]:  $frs = f \ \# \ Frs$

**by**(*fastforce simp add: neq-Nil-conv*)

**obtain**  $stk \ loc \ C0 \ M0 \ pc$  **where**  $f$  [*simp*]:  $f = (stk, loc, C0, M0, pc)$

**by**(*cases f, blast*)

**show** *?thesis*

**proof**(*cases xcp*)

**case** *None*

**with** *check* **obtain**  $C' \ Ts \ T \ mxs \ mxl0 \ ins \ xt$

**where** *mthd*:  $P \vdash C0 \ \text{sees } M0 : Ts \rightarrow T = \lfloor (mxs, mxl0, ins, xt) \rfloor$  *in*  $C'$

*method*  $P \ C0 \ M0 = (C', Ts, T, \lfloor (mxs, mxl0, ins, xt) \rfloor)$

**and** *check-ins*:  $\text{check-instr } (ins \ ! \ pc) \ P \ h \ stk \ loc \ C0 \ M0 \ pc \ Frs$

**and**  $pc < \text{length } ins$

**and**  $\text{length } stk \leq mxs$

**by**(*auto simp add: check-def has-method-def*)

**from** *None exec mthd*

**have** *xexec*:  $(ta, xcp', h', frs') \in \text{exec-instr } (ins \ ! \ pc) \ P \ t \ h \ stk \ loc \ C0 \ M0 \ pc \ Frs$  **by**(*clarsimp*)

**thus** *?thesis*

```

proof(cases ins ! pc)
  case (New C)
    with xexec show ?thesis
      by(auto intro: hext-allocate split: if-split-asm)
  next
    case (NewArray T)
      with xexec show ?thesis
        by(auto intro: hext-allocate split: if-split-asm)
  next
    case AStore
      with xexec check-ins show ?thesis
        by(auto simp add: split-beta split: if-split-asm intro: hext-heap-write)
  next
    case Putfield
      with xexec check-ins show ?thesis
        by(auto intro: hext-heap-write simp add: split-beta split: if-split-asm)
  next
    case CAS
      with xexec check-ins show ?thesis
        by(auto intro: hext-heap-write simp add: split-beta split: if-split-asm)
  next
    case (Invoke M n)
      with xexec check-ins show ?thesis
        apply(auto simp add: min-def split-beta is-Ref-def extRet2JVM-def has-method-def
          split: if-split-asm intro: red-external-aggr-hext)
        apply(case-tac va)
        apply(auto 4 3 intro: red-external-aggr-hext is-native.intros)
        done
  next
    case (BinOpInstr bop)
      with xexec check-ins show ?thesis by(auto split: sum.split-asm)
  qed(auto simp add: split-beta split: if-split-asm)
next
  case (Some a)
    with exec have h' = h by auto
    thus ?thesis by auto
qed
qed

lemma exec-1-d-hext:
   $\llbracket P, t \vdash \text{Normal } (xcp, h, frs) \text{ --ta-jvmd--> Normal } (xcp', h', frs') \rrbracket \implies h \trianglelefteq h'$ 
by(auto elim!: exec-1-d.cases simp add: exec-d-def split: if-split-asm intro: check-exec-hext)

end

end

```

## 5.8 Instantiating the framework semantics with the JVM

```

theory JVMThreaded
imports
  JVMDefensive
  ../Common/ConformThreaded

```

```

../Framework/FWLiftingSem
../Framework/FWProgressAux
begin

primrec JVM-final :: 'addr jvm-thread-state  $\Rightarrow$  bool
where
  JVM-final (xcp, frs) = (frs = [])

  The aggressive JVM

context JVM-heap-base begin

abbreviation mexec ::
  'addr jvm-prog  $\Rightarrow$  'thread-id  $\Rightarrow$  ('addr jvm-thread-state  $\times$  'heap)
 $\Rightarrow$  ('addr, 'thread-id, 'heap) jvm-thread-action  $\Rightarrow$  ('addr jvm-thread-state  $\times$  'heap)  $\Rightarrow$  bool
where
  mexec P t  $\equiv$  ( $\lambda((xcp, frstls), h)$  ta ((xcp', frstls'), h'). P, t  $\vdash$  (xcp, h, frstls)  $-ta-jvm\rightarrow$  (xcp', h', frstls'))

lemma NewThread-memory-exec-instr:
   $\llbracket (ta, s) \in \text{exec-instr } I P t h \text{ stk loc } C M pc \text{ frs}; \text{NewThread } t' x m \in \text{set } \{ta\}_t \rrbracket \Longrightarrow m = \text{fst } (\text{snd } s)$ 
apply(cases I)
apply(auto split: if-split-asm simp add: split-beta ta-upd-simps)
apply(auto dest!: red-ext-aggr-new-thread-heap simp add: extRet2JVM-def split: extCallRet.split)
done

lemma NewThread-memory-exec:
   $\llbracket P, t \vdash \sigma -ta-jvm\rightarrow \sigma'; \text{NewThread } t' x m \in \text{set } \{ta\}_t \rrbracket \Longrightarrow m = (\text{fst } (\text{snd } \sigma'))$ 
apply(erule exec-1.cases)
apply(clarsimp)
apply(case-tac bb, simp)
apply(case-tac ag, auto simp add: exception-step-def-raw split: list.split-asm)
apply(drule NewThread-memory-exec-instr, simp+)
done

lemma exec-instr-Wakeup-no-Lock-no-Join-no-Interrupt:
   $\llbracket (ta, s) \in \text{exec-instr } I P t h \text{ stk loc } C M pc \text{ frs}; \text{Notified} \in \text{set } \{ta\}_w \vee \text{WokenUp} \in \text{set } \{ta\}_w \rrbracket$ 
 $\Longrightarrow \text{collect-locks } \{ta\}_l = \{\} \wedge \text{collect-cond-actions } \{ta\}_c = \{\} \wedge \text{collect-interrupts } \{ta\}_i = \{\}$ 
apply(cases I)
apply(auto split: if-split-asm simp add: split-beta ta-upd-simps dest: red-external-aggr-Wakeup-no-Join)
done

lemma mexec-instr-Wakeup-no-Join:
   $\llbracket P, t \vdash \sigma -ta-jvm\rightarrow \sigma'; \text{Notified} \in \text{set } \{ta\}_w \vee \text{WokenUp} \in \text{set } \{ta\}_w \rrbracket$ 
 $\Longrightarrow \text{collect-locks } \{ta\}_l = \{\} \wedge \text{collect-cond-actions } \{ta\}_c = \{\} \wedge \text{collect-interrupts } \{ta\}_i = \{\}$ 
apply(erule exec-1.cases)
apply(clarsimp)
apply(case-tac bb, simp)
apply(case-tac ag, clarsimp simp add: exception-step-def-raw split: list.split-asm del: disjE)
apply(drule exec-instr-Wakeup-no-Lock-no-Join-no-Interrupt)
apply auto
done

lemma mexec-final:
   $\llbracket \text{mexec } P t (x, m) ta (x', m'); \text{JVM-final } x \rrbracket \Longrightarrow \text{False}$ 

```

**by**(cases  $x$ )(auto simp add: exec-1-iff)

**lemma** *exec-mthr: multithreaded JVM-final (mexec P)*

**apply**(unfold-locales)

**apply**(clarsimp, drule NewThread-memory-exec, fastforce, simp)

**apply**(erule (1) mexec-final)

**done**

**end**

**sublocale** *JVM-heap-base* < *exec-mthr*:

*multithreaded*

*JVM-final*

*mexec P*

*convert-RA*

**for**  $P$

**by**(rule *exec-mthr*)

**context** *JVM-heap-base* **begin**

**abbreviation** *mexecT* ::

*'addr jvm-prog*

$\Rightarrow$  (*'addr, 'thread-id, 'addr jvm-thread-state, 'heap, 'addr*) *state*

$\Rightarrow$  *'thread-id*  $\times$  (*'addr, 'thread-id, 'heap*) *jvm-thread-action*

$\Rightarrow$  (*'addr, 'thread-id, 'addr jvm-thread-state, 'heap, 'addr*) *state*  $\Rightarrow$  *bool*

**where**

*mexecT P*  $\equiv$  *exec-mthr.redT P*

**abbreviation** *mexecT-syntax1* ::

*'addr jvm-prog*  $\Rightarrow$  (*'addr, 'thread-id, 'addr jvm-thread-state, 'heap, 'addr*) *state*

$\Rightarrow$  *'thread-id*  $\Rightarrow$  (*'addr, 'thread-id, 'heap*) *jvm-thread-action*

$\Rightarrow$  (*'addr, 'thread-id, 'addr jvm-thread-state, 'heap, 'addr*) *state*  $\Rightarrow$  *bool*

( $\vdash - \dashv \rightarrow \rightarrow_{jvm} - [50, 0, 0, 0, 50] \ 80$ )

**where**

*mexecT-syntax1 P s t ta s'*  $\equiv$  *mexecT P s (t, ta) s'*

**abbreviation** *mExecT-syntax1* ::

*'addr jvm-prog*  $\Rightarrow$  (*'addr, 'thread-id, 'addr jvm-thread-state, 'heap, 'addr*) *state*

$\Rightarrow$  (*'thread-id*  $\times$  (*'addr, 'thread-id, 'heap*) *jvm-thread-action*) *list*

$\Rightarrow$  (*'addr, 'thread-id, 'addr jvm-thread-state, 'heap, 'addr*) *state*  $\Rightarrow$  *bool*

( $\vdash - \dashv \rightarrow \rightarrow_{jvm*} - [50, 0, 0, 50] \ 80$ )

**where**

$P \vdash s \dashv \rightarrow ttas \rightarrow_{jvm*} s' \equiv \text{exec-mthr.RedT } P \ s \ ttas \ s'$

The defensive JVM

**abbreviation** *mexecd* ::

*'addr jvm-prog*  $\Rightarrow$  *'thread-id*  $\Rightarrow$  *'addr jvm-thread-state*  $\times$  *'heap*

$\Rightarrow$  (*'addr, 'thread-id, 'heap*) *jvm-thread-action*  $\Rightarrow$  *'addr jvm-thread-state*  $\times$  *'heap*  $\Rightarrow$  *bool*

**where**

*mexecd P t*  $\equiv$  ( $\lambda((xcp, frstls), h) \ ta \ ((xcp', frstls'), h'). \ P, t \vdash \text{Normal } (xcp, h, frstls) \dashv \rightarrow \text{Normal } (xcp', h', frstls')$ )

**lemma** *execd-mthr: multithreaded JVM-final (mexecd P)*



```

apply(unfold-locales)
  apply(fastforce dest: defensive-imp-aggressive-1 NewThread-memory-exec)
apply(auto elim: jvmd-NormalE)
done

```

```

end

```

```

sublocale JVM-heap-base < execd-mthr:

```

```

  multithreaded

```

```

  JVM-final

```

```

  mexecd P

```

```

  convert-RA

```

```

  for P

```

```

by(rule execd-mthr)

```

```

context JVM-heap-base begin

```

```

abbreviation mexecdT ::

```

```

  'addr jvm-prog  $\Rightarrow$  ('addr, 'thread-id, 'addr jvm-thread-state, 'heap, 'addr) state
```

```

 $\Rightarrow$  'thread-id  $\times$  ('addr, 'thread-id, 'heap) jvm-thread-action
```

```

 $\Rightarrow$  ('addr, 'thread-id, 'addr jvm-thread-state, 'heap, 'addr) state  $\Rightarrow$  bool
```

```

where

```

```

  mexecdT P  $\equiv$  execd-mthr.redT P

```

```

abbreviation mexecdT-syntax1 ::

```

```

  'addr jvm-prog  $\Rightarrow$  ('addr, 'thread-id, 'addr jvm-thread-state, 'heap, 'addr) state
```

```

 $\Rightarrow$  'thread-id  $\Rightarrow$  ('addr, 'thread-id, 'heap) jvm-thread-action
```

```

 $\Rightarrow$  ('addr, 'thread-id, 'addr jvm-thread-state, 'heap, 'addr) state  $\Rightarrow$  bool
```

```

  ( $\vdash - \dashv\rightarrow_{jvmd} - [50, 0, 0, 0, 50] 80$ )

```

```

where

```

```

  mexecdT-syntax1 P s t ta s'  $\equiv$  mexecdT P s (t, ta) s'

```

```

abbreviation mExecdT-syntax1 ::

```

```

  'addr jvm-prog  $\Rightarrow$  ('addr, 'thread-id, 'addr jvm-thread-state, 'heap, 'addr) state
```

```

 $\Rightarrow$  ('thread-id  $\times$  ('addr, 'thread-id, 'heap) jvm-thread-action) list
```

```

 $\Rightarrow$  ('addr, 'thread-id, 'addr jvm-thread-state, 'heap, 'addr) state  $\Rightarrow$  bool
```

```

  ( $\vdash - \dashv\rightarrow_{jvmd^*} - [50, 0, 0, 50] 80$ )

```

```

where

```

```

   $P \vdash s \dashv\rightarrow_{ttas} s' \equiv \text{execd-mthr.RedT } P \text{ } s \text{ } ttas \text{ } s'$ 

```

```

lemma mexecd-Suspend-Invoke:

```

```

   $\llbracket \text{mexecd } P \text{ } t \text{ } (x, m) \text{ } ta \text{ } (x', m'); \text{Suspend } w \in \text{set } \{\{ta\}_w \} \rrbracket$ 
```

```

 $\implies \exists \text{stk loc } C \text{ } M \text{ } pc \text{ } frs' \text{ } n \text{ } a \text{ } T \text{ } Ts \text{ } Tr \text{ } D. x' = (\text{None}, (\text{stk}, \text{loc}, C, M, pc) \# \text{frs}') \wedge \text{instrs-of } P$ 
 $C \text{ } M \text{ } ! pc = \text{Invoke wait } n \wedge \text{stk} \text{ } ! n = \text{Addr } a \wedge \text{typeof-addr } m \text{ } a = \lfloor T \rfloor \wedge P \vdash \text{class-type-of } T \text{ sees}$ 
 $\text{wait: } Ts \rightarrow Tr = \text{Native in } D \wedge D \cdot \text{wait}(Ts) :: Tr$ 

```

```

apply(cases x')

```

```

apply(cases x)

```

```

apply(cases fst x)

```

```

apply(auto elim!: jvmd-NormalE simp add: split-beta)

```

```

apply(rename-tac [!] stk loc C M pc frs)

```

```

apply(case-tac [!] instrs-of P C M ! pc)

```

```

apply(auto split: if-split-asm simp add: split-beta check-def is-Ref-def has-method-def)

```

```

apply(frule red-external-aggr-Suspend-StaySame, simp, drule red-external-aggr-Suspend-waitD, simp,

```

*fastforce*)+  
done

end

context *JVM-heap* begin

**lemma** *exec-instr-New-Thread-exists-thread-object*:

$\llbracket (ta, xcp', h', frs') \in \text{exec-instr ins } P \ t \ h \ \text{stk loc } C \ M \ pc \ frs;$   
 $\text{check-instr ins } P \ h \ \text{stk loc } C \ M \ pc \ frs;$   
 $\text{NewThread } t' \ x \ h'' \in \text{set } \llbracket ta \rrbracket_t \rrbracket$   
 $\implies \exists C. \text{typeof-addr } h' (\text{thread-id2addr } t') = \lfloor \text{Class-type } C \rfloor \wedge P \vdash C \preceq^* \text{Thread}$

**apply**(*cases ins*)

**apply**(*fastforce simp add: split-beta ta-upd-simps split: if-split-asm intro: red-external-aggr-new-thread-exists-thread*)  
done

**lemma** *exec-New-Thread-exists-thread-object*:

$\llbracket P, t \vdash \text{Normal } (xcp, h, frs) \text{ --ta--jvmd--} \text{Normal } (xcp', h', frs'); \text{NewThread } t' \ x \ h'' \in \text{set } \llbracket ta \rrbracket_t \rrbracket$   
 $\implies \exists C. \text{typeof-addr } h' (\text{thread-id2addr } t') = \lfloor \text{Class-type } C \rfloor \wedge P \vdash C \preceq^* \text{Thread}$

**apply**(*cases xcp*)

**apply**(*case-tac [!]* *frs*)

**apply**(*auto simp add: check-def elim!: jvmd-NormalE dest!: exec-instr-New-Thread-exists-thread-object*)  
done

**lemma** *exec-instr-preserve-tconf*:

$\llbracket (ta, xcp', h', frs') \in \text{exec-instr ins } P \ t \ h \ \text{stk loc } C \ M \ pc \ frs;$   
 $\text{check-instr ins } P \ h \ \text{stk loc } C \ M \ pc \ frs;$   
 $P, h \vdash t' \sqrt{t} \rrbracket$   
 $\implies P, h' \vdash t' \sqrt{t}$

**apply**(*cases ins*)

**apply**(*auto intro: tconf-hext-mono hext-allocate hext-heap-write red-external-aggr-preserves-tconf split: if-split-asm sum.split-asm simp add: split-beta has-method-def intro!: is-native.intros cong del: image-cong-simp*)

done

**lemma** *exec-preserve-tconf*:

$\llbracket P, t \vdash \text{Normal } (xcp, h, frs) \text{ --ta--jvmd--} \text{Normal } (xcp', h', frs'); P, h \vdash t' \sqrt{t} \rrbracket \implies P, h' \vdash t' \sqrt{t}$

**apply**(*cases xcp*)

**apply**(*case-tac [!]* *frs*)

**apply**(*auto simp add: check-def elim!: jvmd-NormalE elim!: exec-instr-preserve-tconf*)

done

**lemma** *lifting-wf-thread-conf*: *lifting-wf JVM-final (mexecd P) ( $\lambda t \ x \ m. P, m \vdash t \sqrt{t}$ )*

**by**(*unfold-locales*)(*auto intro: exec-preserve-tconf dest: exec-New-Thread-exists-thread-object intro: tconfI*)

end

**sublocale** *JVM-heap* < *execd-tconf*: *lifting-wf JVM-final mexecd P convert-RA  $\lambda t \ x \ m. P, m \vdash t \sqrt{t}$*   
**by**(*rule lifting-wf-thread-conf*)

context *JVM-heap* begin

**lemma** *execd-hext*:

$P \vdash s \text{ --t>ta--} \text{jvmd } s' \implies \text{shr } s \leq \text{shr } s'$

```

by(auto elim!: execd-mthr.redT.cases dest!: exec-1-d-hext intro: hext-trans)

lemma Execd-hext:
  assumes  $P \vdash s \multimap_{tta \rightarrow jvmd}^* s'$ 
  shows  $shr\ s \sqsubseteq shr\ s'$ 
using assms unfolding execd-mthr.RedT-def
by(induct)(auto dest!: execd-hext intro: hext-trans simp add: execd-mthr.RedT-def)

end

end
theory JVM-Main
imports
  JVMState
  JVMThreaded
begin

end

```



## Chapter 6

# Bytecode verifier

### 6.1 The JVM Type System as Semilattice

```
theory JVM-SemiType
imports
  ../Common/SemiType
begin

type-synonym  $ty_l = ty\ err\ list$ 
type-synonym  $ty_s = ty\ list$ 
type-synonym  $ty_i = ty_s \times ty_l$ 
type-synonym  $ty_i' = ty_i\ option$ 
type-synonym  $ty_m = ty_i'\ list$ 
type-synonym  $ty_P = mname \Rightarrow cname \Rightarrow ty_m$ 

definition  $stk-esl :: 'c\ prog \Rightarrow nat \Rightarrow ty_s\ esl$ 
where
   $stk-esl\ P\ mxs \equiv upto-esl\ mxs\ (SemiType.esl\ P)$ 

definition  $loc-sl :: 'c\ prog \Rightarrow nat \Rightarrow ty_l\ sl$ 
where
   $loc-sl\ P\ mxl \equiv Listn.sl\ mxl\ (Err.sl\ (SemiType.esl\ P))$ 

definition  $sl :: 'c\ prog \Rightarrow nat \Rightarrow nat \Rightarrow ty_i'\ err\ sl$ 
where
   $sl\ P\ mxs\ mxl \equiv$ 
     $Err.sl(Opt.esl(Product.esl\ (stk-esl\ P\ mxs)\ (Err.esl(loc-sl\ P\ mxl))))$ 

definition  $states :: 'c\ prog \Rightarrow nat \Rightarrow nat \Rightarrow ty_i'\ err\ set$ 
where
   $states\ P\ mxs\ mxl \equiv fst(sl\ P\ mxs\ mxl)$ 

definition  $le :: 'c\ prog \Rightarrow nat \Rightarrow nat \Rightarrow ty_i'\ err\ ord$ 
where
   $le\ P\ mxs\ mxl \equiv fst(snd(sl\ P\ mxs\ mxl))$ 

definition  $sup :: 'c\ prog \Rightarrow nat \Rightarrow nat \Rightarrow ty_i'\ err\ binop$ 
where
   $sup\ P\ mxs\ mxl \equiv snd(snd(sl\ P\ mxs\ mxl))$ 
```

**definition**  $\text{sup-ty-opt} :: ['c \text{ prog}, \text{ty} \text{ err}, \text{ty} \text{ err}] \Rightarrow \text{bool}$   
 $(- \vdash - \leq_{\top} - [71, 71, 71] \ 70)$

**where**

$\text{sup-ty-opt } P \equiv \text{Err.le } (\text{widen } P)$

**definition**  $\text{sup-state} :: ['c \text{ prog}, \text{ty}_i, \text{ty}_i] \Rightarrow \text{bool}$   
 $(- \vdash - \leq_i - [71, 71, 71] \ 70)$

**where**

$\text{sup-state } P \equiv \text{Product.le } (\text{Listn.le } (\text{widen } P)) (\text{Listn.le } (\text{sup-ty-opt } P))$

**definition**  $\text{sup-state-opt} :: ['c \text{ prog}, \text{ty}_i', \text{ty}_i'] \Rightarrow \text{bool}$   
 $(- \vdash - \leq'' - [71, 71, 71] \ 70)$

**where**

$\text{sup-state-opt } P \equiv \text{Opt.le } (\text{sup-state } P)$

**abbreviation**  $\text{sup-loc} :: ['c \text{ prog}, \text{ty}_l, \text{ty}_l] \Rightarrow \text{bool } (- \vdash - [\leq_{\top}] - [71, 71, 71] \ 70)$

**where**  $P \vdash LT [\leq_{\top}] LT' \equiv \text{list-all2 } (\text{sup-ty-opt } P) \ LT \ LT'$

**notation** (*ASCII*)

$\text{sup-ty-opt } (- \mid - \leq = T - [71, 71, 71] \ 70) \text{ and}$

$\text{sup-state } (- \mid - \leq = i - [71, 71, 71] \ 70) \text{ and}$

$\text{sup-state-opt } (- \mid - \leq = ' - [71, 71, 71] \ 70) \text{ and}$

$\text{sup-loc } (- \mid - [\leq = T] - [71, 71, 71] \ 70)$

### 6.1.1 Unfolding

**lemma** *JVM-states-unfold*:

$\text{states } P \ mxs \ mxl \equiv \text{err}(\text{opt}((\text{Union } \{\text{list } n \ (\text{types } P) \mid n. n \leq mxs\}) \times$   
 $\text{list } mxl \ (\text{err}(\text{types } P))))$

**apply** ( $\text{unfold } \text{states-def } \text{sl-def } \text{Opt.esl-def } \text{Err.sl-def}$

$\text{stk-esl-def } \text{loc-sl-def } \text{Product.esl-def}$

$\text{Listn.sl-def } \text{upto-esl-def } \text{SemiType.esl-def } \text{Err.esl-def}$ )

**apply** *simp*

**done**

**lemma** *JVM-le-unfold*:

$\text{le } P \ m \ n \equiv$

$\text{Err.le}(\text{Opt.le}(\text{Product.le}(\text{Listn.le}(\text{widen } P))(\text{Listn.le}(\text{Err.le}(\text{widen } P)))))$

**apply** ( $\text{unfold } \text{le-def } \text{sl-def } \text{Opt.esl-def } \text{Err.sl-def}$

$\text{stk-esl-def } \text{loc-sl-def } \text{Product.esl-def}$

$\text{Listn.sl-def } \text{upto-esl-def } \text{SemiType.esl-def } \text{Err.esl-def}$ )

**apply** *simp*

**done**

**lemma** *sl-def2*:

$\text{JVM-SemiType.sl } P \ mxs \ mxl \equiv$

$(\text{states } P \ mxs \ mxl, \text{JVM-SemiType.le } P \ mxs \ mxl, \text{JVM-SemiType.sup } P \ mxs \ mxl)$

**by** ( $\text{unfold } \text{JVM-SemiType.sup-def } \text{states-def } \text{JVM-SemiType.le-def}$ ) *simp*

**lemma** *JVM-le-conv*:

$\text{le } P \ m \ n \ (\text{OK } t1) \ (\text{OK } t2) = P \vdash t1 \leq' t2$

**by** (*simp add: JVM-le-unfold Err.le-def lesub-def sup-state-opt-def*  
 $\text{sup-state-def } \text{sup-ty-opt-def}$ )

**lemma** *JVM-le-Err-conv*:  
 $le\ P\ m\ n = Err.le\ (sup\ state\ opt\ P)$   
**by** (*unfold sup-state-opt-def sup-state-def*  
*sup-ty-opt-def JVM-le-unfold*) *simp*

**lemma** *err-le-unfold* [*iff*]:  
 $Err.le\ r\ (OK\ a)\ (OK\ b) = r\ a\ b$   
**by** (*simp add: Err.le-def lesub-def*)

### 6.1.2 Semilattice

**lemma** *order-sup-state-opt* [*intro, simp*]:  
 $wf\ prog\ wf\ mb\ P \implies order\ (sup\ state\ opt\ P)$   
**by** (*unfold sup-state-opt-def sup-state-def sup-ty-opt-def*) *blast*

**lemma** *semilat-JVM* [*intro?*]:  
 $wf\ prog\ wf\ mb\ P \implies semilat\ (JVM\ SemiType.sl\ P\ mxs\ mxl)$   
**apply** (*unfold JVM-SemiType.sl-def stk-esl-def loc-sl-def*)  
**apply** (*blast intro: err-semilat-Product-esl err-semilat-upto-esl*  
*Listn-sl err-semilat-JType-esl*)  
**done**

**lemma** *acc-JVM* [*intro*]:  
 $wf\ prog\ wf\ mb\ P \implies acc\ (JVM\ SemiType.states\ P\ mxs\ mxl)\ (JVM\ SemiType.le\ P\ mxs\ mxl)$   
**by** (*unfold JVM-le-unfold JVM-states-unfold*) *blast*

### 6.1.3 Widening with $\top$

**lemma** *widen-refl*[*iff*]:  $widen\ P\ t\ t$  **by** (*simp add: fun-of-def*)

**lemma** *sup-ty-opt-refl* [*iff*]:  $P \vdash T \leq_{\top} T$   
**apply** (*unfold sup-ty-opt-def*)  
**apply** (*fold lesub-def*)  
**apply** (*rule le-err-refl*)  
**apply** (*simp add: lesub-def*)  
**done**

**lemma** *Err-any-conv* [*iff*]:  $P \vdash Err \leq_{\top} T = (T = Err)$   
**by** (*unfold sup-ty-opt-def*) (*rule Err-le-conv [simplified lesub-def]*)

**lemma** *any-Err* [*iff*]:  $P \vdash T \leq_{\top} Err$   
**by** (*unfold sup-ty-opt-def*) (*rule le-Err [simplified lesub-def]*)

**lemma** *OK-OK-conv* [*iff*]:  
 $P \vdash OK\ T \leq_{\top} OK\ T' = P \vdash T \leq T'$   
**by** (*simp add: sup-ty-opt-def fun-of-def*)

**lemma** *any-OK-conv* [*iff*]:  
 $P \vdash X \leq_{\top} OK\ T' = (\exists T. X = OK\ T \wedge P \vdash T \leq T')$   
**apply** (*unfold sup-ty-opt-def*)  
**apply** (*rule le-OK-conv [simplified lesub-def]*)  
**done**

**lemma** *OK-any-conv*:

$P \vdash OK\ T \leq_{\top} X = (X = Err \vee (\exists T'. X = OK\ T' \wedge P \vdash T \leq T'))$   
**apply** (*unfold sup-ty-opt-def*)  
**apply** (*rule OK-le-conv [simplified lesub-def]*)  
**done**

**lemma** *sup-ty-opt-trans* [*intro?*, *trans*]:

$\llbracket P \vdash a \leq_{\top} b; P \vdash b \leq_{\top} c \rrbracket \implies P \vdash a \leq_{\top} c$

**by** (*auto intro: widen-trans*

*simp add: sup-ty-opt-def Err.le-def lesub-def fun-of-def*

*split: err.splits*)

#### 6.1.4 Stack and Registers

**lemma** *stk-convert*:

$P \vdash ST \leq ST' = Listn.le\ (widen\ P)\ ST\ ST'$

**by** (*simp add: Listn.le-def lesub-def*)

**lemma** *sup-loc-refl* [*iff*]:  $P \vdash LT \leq_{\top} LT$

**by** (*rule list-all2-refl*) *simp*

**lemmas** *sup-loc-Cons1* [*iff*] = *list-all2-Cons1* [*of sup-ty-opt P*] **for** *P*

**lemma** *sup-loc-def*:

$P \vdash LT \leq_{\top} LT' \equiv Listn.le\ (sup-ty-opt\ P)\ LT\ LT'$

**by** (*simp add: Listn.le-def lesub-def*)

**lemma** *sup-loc-widens-conv* [*iff*]:

$P \vdash map\ OK\ Ts \leq_{\top} map\ OK\ Ts' = P \vdash Ts \leq Ts'$

**by** (*simp add: list-all2-map1 list-all2-map2*)

**lemma** *sup-loc-trans* [*intro?*, *trans*]:

$\llbracket P \vdash a \leq_{\top} b; P \vdash b \leq_{\top} c \rrbracket \implies P \vdash a \leq_{\top} c$

**by** (*rule list-all2-trans, rule sup-ty-opt-trans*)

#### 6.1.5 State Type

**lemma** *sup-state-conv* [*iff*]:

$P \vdash (ST, LT) \leq_i (ST', LT') = (P \vdash ST \leq ST' \wedge P \vdash LT \leq_{\top} LT')$

**by** (*auto simp add: sup-state-def stk-convert lesub-def Product.le-def sup-loc-def*)

**lemma** *sup-state-conv2*:

$P \vdash s1 \leq_i s2 = (P \vdash fst\ s1 \leq fst\ s2 \wedge P \vdash snd\ s1 \leq_{\top} snd\ s2)$

**by** (*cases s1, cases s2*) *simp*

**lemma** *sup-state-refl* [*iff*]:  $P \vdash s \leq_i s$

**by** (*auto simp add: sup-state-conv2 intro: list-all2-refl*)

**lemma** *sup-state-trans* [*intro?*, *trans*]:

$\llbracket P \vdash a \leq_i b; P \vdash b \leq_i c \rrbracket \implies P \vdash a \leq_i c$

**by** (*auto intro: sup-loc-trans widens-trans simp add: sup-state-conv2*)

**lemma** *sup-state-opt-None-any* [*iff*]:

$P \vdash None \leq' s$



```

by (simp add: sup-state-opt-def Opt.le-def)

lemma sup-state-opt-any-None [iff]:
   $P \vdash s \leq' \text{None} = (s = \text{None})$ 
by (simp add: sup-state-opt-def Opt.le-def)

lemma sup-state-opt-Some-Some [iff]:
   $P \vdash \text{Some } a \leq' \text{Some } b = P \vdash a \leq_i b$ 
by (simp add: sup-state-opt-def Opt.le-def lesub-def)

lemma sup-state-opt-any-Some:
   $P \vdash (\text{Some } s) \leq' X = (\exists s'. X = \text{Some } s' \wedge P \vdash s \leq_i s')$ 
by (simp add: sup-state-opt-def Opt.le-def lesub-def)

lemma sup-state-opt-refl [iff]:  $P \vdash s \leq' s$ 
by (simp add: sup-state-opt-def Opt.le-def lesub-def)

lemma sup-state-opt-trans [intro?, trans]:
   $\llbracket P \vdash a \leq' b; P \vdash b \leq' c \rrbracket \implies P \vdash a \leq' c$ 
  apply (unfold sup-state-opt-def Opt.le-def lesub-def)
  apply (simp del: split-paired-All)
  apply (rule sup-state-trans, assumption+)
  done

end

```

## 6.2 Effect of Instructions on the State Type

```

theory Effect
imports
  JVM-SemiType
  ../JVM/JVMExceptions
begin

locale jvm-method = prog +
  fixes mxs :: nat
  fixes mxl0 :: nat
  fixes Ts :: ty list
  fixes Tr :: ty
  fixes is :: 'addr instr list
  fixes xt :: ex-table

  fixes mxl :: nat
  defines mxl-def:  $mxl \equiv 1 + \text{size } Ts + mxl_0$ 

  Program counter of successor instructions:

primrec succs :: 'addr instr  $\Rightarrow$  tyi  $\Rightarrow$  pc  $\Rightarrow$  pc list
where
  succs (Load idx)  $\tau$  pc    = [pc+1]
| succs (Store idx)  $\tau$  pc   = [pc+1]
| succs (Push v)  $\tau$  pc      = [pc+1]
| succs (Getfield F C)  $\tau$  pc = [pc+1]
| succs (Putfield F C)  $\tau$  pc = [pc+1]
| succs (CAS F C)  $\tau$  pc     = [pc+1]

```

$| \text{succs } (\text{New } C) \tau pc = [pc+1]$   
 $| \text{succs } (\text{NewArray } T) \tau pc = [pc+1]$   
 $| \text{succs } A\text{Load } \tau pc = (\text{if } (fst \tau)!1 = NT \text{ then } [] \text{ else } [pc+1])$   
 $| \text{succs } A\text{Store } \tau pc = (\text{if } (fst \tau)!2 = NT \text{ then } [] \text{ else } [pc+1])$   
 $| \text{succs } A\text{Length } \tau pc = (\text{if } (fst \tau)!0 = NT \text{ then } [] \text{ else } [pc+1])$   
 $| \text{succs } (\text{Checkcast } C) \tau pc = [pc+1]$   
 $| \text{succs } (\text{Instanceof } T) \tau pc = [pc+1]$   
 $| \text{succs } \text{Pop } \tau pc = [pc+1]$   
 $| \text{succs } \text{Dup } \tau pc = [pc+1]$   
 $| \text{succs } \text{Swap } \tau pc = [pc+1]$   
 $| \text{succs } (\text{BinOpInstr } b) \tau pc = [pc+1]$   
 $| \text{succs-IfFalse:}$   
 $| \text{succs } (\text{IfFalse } b) \tau pc = [pc+1, \text{nat } (int \text{ pc } + b)]$   
 $| \text{succs-Goto:}$   
 $| \text{succs } (\text{Goto } b) \tau pc = [\text{nat } (int \text{ pc } + b)]$   
 $| \text{succs-Return:}$   
 $| \text{succs } \text{Return } \tau pc = []$   
 $| \text{succs-Invoke:}$   
 $| \text{succs } (\text{Invoke } M \ n) \tau pc = (\text{if } (fst \tau)!n = NT \text{ then } [] \text{ else } [pc+1])$   
 $| \text{succs-Throw:}$   
 $| \text{succs } \text{ThrowExc } \tau pc = []$   
 $| \text{succs } M\text{Enter } \tau pc = (\text{if } (fst \tau)!0 = NT \text{ then } [] \text{ else } [pc+1])$   
 $| \text{succs } M\text{Exit } \tau pc = (\text{if } (fst \tau)!0 = NT \text{ then } [] \text{ else } [pc+1])$

Effect of instruction on the state type:

**fun**  $\text{eff}_i :: 'addr \text{ instr} \times 'm \text{ prog} \times ty_i \Rightarrow ty_i$   
**where**  
 $\text{eff}_i\text{-Load:}$   
 $\text{eff}_i (\text{Load } n, P, (ST, LT)) = (\text{ok-val } (LT ! n) \# ST, LT)$   
 $| \text{eff}_i\text{-Store:}$   
 $\text{eff}_i (\text{Store } n, P, (T\#ST, LT)) = (ST, LT[n:= OK T])$   
 $| \text{eff}_i\text{-Push:}$   
 $\text{eff}_i (\text{Push } v, P, (ST, LT)) = (\text{the } (typeof \ v) \# ST, LT)$   
 $| \text{eff}_i\text{-Getfield:}$   
 $\text{eff}_i (\text{Getfield } F \ C, P, (T\#ST, LT)) = (fst (snd (field \ P \ C \ F)) \# ST, LT)$   
 $| \text{eff}_i\text{-Putfield:}$   
 $\text{eff}_i (\text{Putfield } F \ C, P, (T_1\#T_2\#ST, LT)) = (ST, LT)$   
 $| \text{eff}_i\text{-CAS:}$   
 $\text{eff}_i (\text{CAS } F \ C, P, (T_1\#T_2\#T_3\#ST, LT)) = (\text{Boolean} \# ST, LT)$   
 $| \text{eff}_i\text{-New:}$   
 $\text{eff}_i (\text{New } C, P, (ST, LT)) = (\text{Class } C \# ST, LT)$   
 $| \text{eff}_i\text{-NewArray:}$   
 $\text{eff}_i (\text{NewArray } Ty, P, (T\#ST, LT)) = (Ty[] \# ST, LT)$   
 $| \text{eff}_i\text{-ALoad:}$   
 $\text{eff}_i (\text{ALoad}, P, (T_1\#T_2\#ST, LT)) = (\text{the-Array } T_2 \# ST, LT)$

<i>eff<sub>i</sub>-AStore</i> :	
<i>eff<sub>i</sub></i> ( <i>AStore</i> , <i>P</i> , ( <i>T1</i> # <i>T2</i> # <i>T3</i> # <i>ST</i> , <i>LT</i> ))	= ( <i>ST</i> , <i>LT</i> )
<i>eff<sub>i</sub>-ALength</i> :	
<i>eff<sub>i</sub></i> ( <i>ALength</i> , <i>P</i> , ( <i>T1</i> # <i>ST</i> , <i>LT</i> ))	= ( <i>Integer</i> # <i>ST</i> , <i>LT</i> )
<i>eff<sub>i</sub>-Checkcast</i> :	
<i>eff<sub>i</sub></i> ( <i>Checkcast</i> <i>Ty</i> , <i>P</i> , ( <i>T</i> # <i>ST</i> , <i>LT</i> ))	= ( <i>Ty</i> # <i>ST</i> , <i>LT</i> )
<i>eff<sub>i</sub>-Instanceof</i> :	
<i>eff<sub>i</sub></i> ( <i>Instanceof</i> <i>Ty</i> , <i>P</i> , ( <i>T</i> # <i>ST</i> , <i>LT</i> ))	= ( <i>Boolean</i> # <i>ST</i> , <i>LT</i> )
<i>eff<sub>i</sub>-Pop</i> :	
<i>eff<sub>i</sub></i> ( <i>Pop</i> , <i>P</i> , ( <i>T</i> # <i>ST</i> , <i>LT</i> ))	= ( <i>ST</i> , <i>LT</i> )
<i>eff<sub>i</sub>-Dup</i> :	
<i>eff<sub>i</sub></i> ( <i>Dup</i> , <i>P</i> , ( <i>T</i> # <i>ST</i> , <i>LT</i> ))	= ( <i>T</i> # <i>T</i> # <i>ST</i> , <i>LT</i> )
<i>eff<sub>i</sub>-Swap</i> :	
<i>eff<sub>i</sub></i> ( <i>Swap</i> , <i>P</i> , ( <i>T1</i> # <i>T2</i> # <i>ST</i> , <i>LT</i> ))	= ( <i>T2</i> # <i>T1</i> # <i>ST</i> , <i>LT</i> )
<i>eff<sub>i</sub>-BinOpInstr</i> :	
<i>eff<sub>i</sub></i> ( <i>BinOpInstr</i> <i>bop</i> , <i>P</i> , ( <i>T2</i> # <i>T1</i> # <i>ST</i> , <i>LT</i> ))	= (( <i>THE T. P</i> ⊢ <i>T1</i> « <i>bop</i> » <i>T2</i> : <i>T</i> ) # <i>ST</i> , <i>LT</i> )
<i>eff<sub>i</sub>-IfFalse</i> :	
<i>eff<sub>i</sub></i> ( <i>IfFalse</i> <i>b</i> , <i>P</i> , ( <i>T<sub>1</sub></i> # <i>ST</i> , <i>LT</i> ))	= ( <i>ST</i> , <i>LT</i> )
<i>eff<sub>i</sub>-Invoke</i> :	
<i>eff<sub>i</sub></i> ( <i>Invoke</i> <i>M n</i> , <i>P</i> , ( <i>ST</i> , <i>LT</i> ))	=
( <i>let U = fst (snd (snd (method P (the (class-type-of' (ST ! n))) M)))</i>	
<i>in (U</i> # <i>drop (n+1) ST</i> , <i>LT</i> ))	
<i>eff<sub>i</sub>-Goto</i> :	
<i>eff<sub>i</sub></i> ( <i>Goto</i> <i>n</i> , <i>P</i> , <i>s</i> )	= <i>s</i>
<i>eff<sub>i</sub>-MEnter</i> :	
<i>eff<sub>i</sub></i> ( <i>MEnter</i> , <i>P</i> , ( <i>T1</i> # <i>ST</i> , <i>LT</i> ))	= ( <i>ST</i> , <i>LT</i> )
<i>eff<sub>i</sub>-MExit</i> :	
<i>eff<sub>i</sub></i> ( <i>MExit</i> , <i>P</i> , ( <i>T1</i> # <i>ST</i> , <i>LT</i> ))	= ( <i>ST</i> , <i>LT</i> )

**fun** *is-relevant-class* :: 'addr instr ⇒ 'm prog ⇒ cname ⇒ bool

**where**

<i>rel-Getfield</i> :	
<i>is-relevant-class</i> ( <i>Getfield F D</i> )	= (λ <i>P C. P</i> ⊢ <i>NullPointer</i> ≼* <i>C</i> )
<i>rel-Putfield</i> :	
<i>is-relevant-class</i> ( <i>Putfield F D</i> )	= (λ <i>P C. P</i> ⊢ <i>NullPointer</i> ≼* <i>C</i> )
<i>rel-CAS</i> :	
<i>is-relevant-class</i> ( <i>CAS F D</i> )	= (λ <i>P C. P</i> ⊢ <i>NullPointer</i> ≼* <i>C</i> )
<i>rel-Checcast</i> :	
<i>is-relevant-class</i> ( <i>Checkcast T</i> )	= (λ <i>P C. P</i> ⊢ <i>ClassCast</i> ≼* <i>C</i> )
<i>rel-New</i> :	
<i>is-relevant-class</i> ( <i>New D</i> )	= (λ <i>P C. P</i> ⊢ <i>OutOfMemory</i> ≼* <i>C</i> )

$\mid$  *rel-Throw*:  
 $\text{is-relevant-class ThrowExc} = (\lambda P C. \text{True})$   
 $\mid$  *rel-Invoke*:  
 $\text{is-relevant-class (Invoke } M \ n) = (\lambda P C. \text{True})$   
 $\mid$  *rel-NewArray*:  
 $\text{is-relevant-class (NewArray } T) = (\lambda P C. (P \vdash \text{OutOfMemory} \preceq^* C) \vee (P \vdash \text{NegativeArraySize} \preceq^* C))$   
 $\mid$  *rel-ALoad*:  
 $\text{is-relevant-class ALoad} = (\lambda P C. P \vdash \text{ArrayIndexOutOfBounds} \preceq^* C \vee P \vdash \text{NullPointer} \preceq^* C)$   
 $\mid$  *rel-ASTore*:  
 $\text{is-relevant-class AStore} = (\lambda P C. P \vdash \text{ArrayIndexOutOfBounds} \preceq^* C \vee P \vdash \text{ArrayStore} \preceq^* C \vee P \vdash \text{NullPointer} \preceq^* C)$   
 $\mid$  *rel-ALength*:  
 $\text{is-relevant-class ALength} = (\lambda P C. P \vdash \text{NullPointer} \preceq^* C)$   
 $\mid$  *rel-MEnter*:  
 $\text{is-relevant-class MEnter} = (\lambda P C. P \vdash \text{IllegalMonitorState} \preceq^* C \vee P \vdash \text{NullPointer} \preceq^* C)$   
 $\mid$  *rel-MExit*:  
 $\text{is-relevant-class MExit} = (\lambda P C. P \vdash \text{IllegalMonitorState} \preceq^* C \vee P \vdash \text{NullPointer} \preceq^* C)$   
 $\mid$  *rel-BinOp*:  
 $\text{is-relevant-class (BinOpInstr } bop) = \text{binop-relevant-class } bop$   
 $\mid$  *rel-default*:  
 $\text{is-relevant-class } i = (\lambda P C. \text{False})$

**definition** *is-relevant-entry* :: 'm prog  $\Rightarrow$  'addr instr  $\Rightarrow$  pc  $\Rightarrow$  ex-entry  $\Rightarrow$  bool

**where**

$\text{is-relevant-entry } P \ i \ pc \ e \equiv$   
 $\text{let } (f, t, C, h, d) = e$   
 $\text{in } (\text{case } C \text{ of None} \Rightarrow \text{True} \mid \lfloor C' \rfloor \Rightarrow \text{is-relevant-class } i \ P \ C') \wedge pc \in \{f..<t\}$

**definition** *relevant-entries* :: 'm prog  $\Rightarrow$  'addr instr  $\Rightarrow$  pc  $\Rightarrow$  ex-table  $\Rightarrow$  ex-table

**where**

$\text{relevant-entries } P \ i \ pc \equiv \text{filter } (\text{is-relevant-entry } P \ i \ pc)$

**definition** *xcpt-eff* :: 'addr instr  $\Rightarrow$  'm prog  $\Rightarrow$  pc  $\Rightarrow$  ty<sub>i</sub>  $\Rightarrow$  ex-table  $\Rightarrow$  (pc  $\times$  ty<sub>i</sub>') list

**where**

$\text{xcpt-eff } i \ P \ pc \ \tau \ et \equiv \text{let } (ST, LT) = \tau \text{ in}$   
 $\text{map } (\lambda(f, t, C, h, d). (h, \text{Some } ((\text{case } C \text{ of None} \Rightarrow \text{Class Throwable} \mid \text{Some } C' \Rightarrow \text{Class } C') \# \text{drop } (\text{size } ST - d) \ ST, LT)))) (\text{relevant-entries } P \ i \ pc \ et)$

**definition** *norm-eff* :: 'addr instr  $\Rightarrow$  'm prog  $\Rightarrow$  nat  $\Rightarrow$  ty<sub>i</sub>  $\Rightarrow$  (pc  $\times$  ty<sub>i</sub>') list

**where**  $\text{norm-eff } i \ P \ pc \ \tau \equiv \text{map } (\lambda pc'. (pc', \text{Some } (\text{eff}_i (i, P, \tau)))) (\text{succs } i \ \tau \ pc)$

**definition** *eff* :: 'addr instr  $\Rightarrow$  'm prog  $\Rightarrow$  pc  $\Rightarrow$  ex-table  $\Rightarrow$  ty<sub>i</sub>'  $\Rightarrow$  (pc  $\times$  ty<sub>i</sub>') list

**where**

$\text{eff } i \ P \ pc \ et \ t \equiv$   
 $\text{case } t \text{ of}$   
 $\text{None} \Rightarrow []$   
 $\mid \text{Some } \tau \Rightarrow (\text{norm-eff } i \ P \ pc \ \tau) \ @ \ (\text{xcpt-eff } i \ P \ pc \ \tau \ et)$

**lemma** *eff-None*:

$\text{eff } i \ P \ pc \ xt \ \text{None} = []$

**by** (*simp add: eff-def*)

**lemma** *eff-Some*:

$\text{eff } i \ P \ pc \ xt \ (\text{Some } \tau) = \text{norm-eff } i \ P \ pc \ \tau \ @ \ \text{xcpt-eff } i \ P \ pc \ \tau \ xt$   
**by** (*simp add: eff-def*)

Conditions under which *eff* is applicable:

**fun**  $\text{app}_i :: 'addr \ \text{instr} \times 'm \ \text{prog} \times pc \times nat \times ty \times ty_i \Rightarrow bool$   
**where**  
*app<sub>i</sub>-Load*:  
 $\text{app}_i \ (\text{Load } n, P, pc, mxs, T_r, (ST, LT)) =$   
 $(n < \text{length } LT \wedge LT ! n \neq \text{Err} \wedge \text{length } ST < mxs)$   
*app<sub>i</sub>-Store*:  
 $\text{app}_i \ (\text{Store } n, P, pc, mxs, T_r, (T \# ST, LT)) =$   
 $(n < \text{length } LT)$   
*app<sub>i</sub>-Push*:  
 $\text{app}_i \ (\text{Push } v, P, pc, mxs, T_r, (ST, LT)) =$   
 $(\text{length } ST < mxs \wedge \text{typeof } v \neq \text{None})$   
*app<sub>i</sub>-Getfield*:  
 $\text{app}_i \ (\text{Getfield } F \ C, P, pc, mxs, T_r, (T \# ST, LT)) =$   
 $(\exists T_f \ fm. P \vdash C \ \text{sees } F:T_f \ (fm) \ \text{in } C \wedge P \vdash T \leq \text{Class } C)$   
*app<sub>i</sub>-Putfield*:  
 $\text{app}_i \ (\text{Putfield } F \ C, P, pc, mxs, T_r, (T_1 \# T_2 \# ST, LT)) =$   
 $(\exists T_f \ fm. P \vdash C \ \text{sees } F:T_f \ (fm) \ \text{in } C \wedge P \vdash T_2 \leq (\text{Class } C) \wedge P \vdash T_1 \leq T_f)$   
*app<sub>i</sub>-CAS*:  
 $\text{app}_i \ (\text{CAS } F \ C, P, pc, mxs, T_r, (T_3 \# T_2 \# T_1 \# ST, LT)) =$   
 $(\exists T_f \ fm. P \vdash C \ \text{sees } F:T_f \ (fm) \ \text{in } C \wedge \text{volatile } fm \wedge P \vdash T_1 \leq \text{Class } C \wedge P \vdash T_2 \leq T_f \wedge P \vdash T_3 \leq T_f)$   
*app<sub>i</sub>-New*:  
 $\text{app}_i \ (\text{New } C, P, pc, mxs, T_r, (ST, LT)) =$   
 $(\text{is-class } P \ C \wedge \text{length } ST < mxs)$   
*app<sub>i</sub>-NewArray*:  
 $\text{app}_i \ (\text{NewArray } Ty, P, pc, mxs, T_r, (\text{Integer} \# ST, LT)) =$   
 $\text{is-type } P \ (Ty[])$   
*app<sub>i</sub>-ALoad*:  
 $\text{app}_i \ (\text{ALoad}, P, pc, mxs, T_r, (T1 \# T2 \# ST, LT)) =$   
 $(T1 = \text{Integer} \wedge (T2 \neq NT \longrightarrow (\exists Ty. T2 = Ty[])))$   
*app<sub>i</sub>-AStore*:  
 $\text{app}_i \ (\text{AStore}, P, pc, mxs, T_r, (T1 \# T2 \# T3 \# ST, LT)) =$   
 $(T2 = \text{Integer} \wedge (T3 \neq NT \longrightarrow (\exists Ty. T3 = Ty[])))$   
*app<sub>i</sub>-ALength*:  
 $\text{app}_i \ (\text{ALength}, P, pc, mxs, T_r, (T1 \# ST, LT)) =$   
 $(T1 = NT \vee (\exists Ty. T1 = Ty[]))$   
*app<sub>i</sub>-Checkcast*:  
 $\text{app}_i \ (\text{Checkcast } Ty, P, pc, mxs, T_r, (T \# ST, LT)) =$   
 $(\text{is-type } P \ Ty)$   
*app<sub>i</sub>-Instanceof*:  
 $\text{app}_i \ (\text{Instanceof } Ty, P, pc, mxs, T_r, (T \# ST, LT)) =$   
 $(\text{is-type } P \ Ty \wedge \text{is-refT } T)$   
*app<sub>i</sub>-Pop*:  
 $\text{app}_i \ (\text{Pop}, P, pc, mxs, T_r, (T \# ST, LT)) =$   
 $\text{True}$   
*app<sub>i</sub>-Dup*:  
 $\text{app}_i \ (\text{Dup}, P, pc, mxs, T_r, (T \# ST, LT)) =$   
 $(\text{Suc } (\text{length } ST) < mxs)$

$| \text{app}_i\text{-Swap:}$   
 $\text{app}_i (\text{Swap}, P, pc, mxs, T_r, (T1 \# T2 \# ST, LT)) = \text{True}$   
 $| \text{app}_i\text{-BinOpInstr:}$   
 $\text{app}_i (\text{BinOpInstr } bop, P, pc, mxs, T_r, (T2 \# T1 \# ST, LT)) = (\exists T. P \vdash T1 \ll bop \gg T2 : T)$   
 $| \text{app}_i\text{-IfFalse:}$   
 $\text{app}_i (\text{IfFalse } b, P, pc, mxs, T_r, (\text{Boolean} \# ST, LT)) =$   
 $(0 \leq \text{int } pc + b)$   
 $| \text{app}_i\text{-Goto:}$   
 $\text{app}_i (\text{Goto } b, P, pc, mxs, T_r, s) = (0 \leq \text{int } pc + b)$   
 $| \text{app}_i\text{-Return:}$   
 $\text{app}_i (\text{Return}, P, pc, mxs, T_r, (T \# ST, LT)) = (P \vdash T \leq T_r)$   
 $| \text{app}_i\text{-Throw:}$   
 $\text{app}_i (\text{ThrowExc}, P, pc, mxs, T_r, (T \# ST, LT)) =$   
 $(T = NT \vee (\exists C. T = \text{Class } C \wedge P \vdash C \preceq^* \text{Throwable}))$   
 $| \text{app}_i\text{-Invoke:}$   
 $\text{app}_i (\text{Invoke } M \ n, P, pc, mxs, T_r, (ST, LT)) =$   
 $(n < \text{length } ST \wedge$   
 $(ST!n \neq NT \rightarrow$   
 $(\exists C \ D \ Ts \ T \ m. \text{class-type-of}' (ST \ ! \ n) = \lfloor C \rfloor \wedge P \vdash C \text{ sees } M:Ts \rightarrow T = m \text{ in } D \wedge P \vdash \text{rev}$   
 $(\text{take } n \ ST) \leq Ts)))$   
 $| \text{app}_i\text{-MEnter:}$   
 $\text{app}_i (\text{MEnter}, P, pc, mxs, T_r, (T \# ST, LT)) = (\text{is-ref } T \ T)$   
 $| \text{app}_i\text{-MExit:}$   
 $\text{app}_i (\text{MExit}, P, pc, mxs, T_r, (T \# ST, LT)) = (\text{is-ref } T \ T)$   
 $| \text{app}_i\text{-default:}$   
 $\text{app}_i (i, P, pc, mxs, T_r, s) = \text{False}$

**definition**  $xcpt\text{-app} :: 'addr \ instr \Rightarrow 'm \ prog \Rightarrow pc \Rightarrow nat \Rightarrow ex\text{-table} \Rightarrow ty_i \Rightarrow bool$

**where**

$xcpt\text{-app } i \ P \ pc \ mxs \ xt \ \tau \equiv \forall (f, t, C, h, d) \in \text{set } (\text{relevant-entries } P \ i \ pc \ xt). (\text{case } C \text{ of } \text{None} \Rightarrow \text{True}$   
 $| \text{Some } C' \Rightarrow \text{is-class } P \ C') \wedge d \leq \text{size } (fst \ \tau) \wedge d < mxs$

**definition**  $app :: 'addr \ instr \Rightarrow 'm \ prog \Rightarrow nat \Rightarrow ty \Rightarrow nat \Rightarrow nat \Rightarrow ex\text{-table} \Rightarrow ty_i' \Rightarrow bool$

**where**

$app \ i \ P \ mxs \ T_r \ pc \ mpc \ xt \ t \equiv \text{case } t \text{ of } \text{None} \Rightarrow \text{True} \mid \text{Some } \tau \Rightarrow$   
 $app_i (i, P, pc, mxs, T_r, \tau) \wedge xcpt\text{-app } i \ P \ pc \ mxs \ xt \ \tau \wedge$   
 $(\forall (pc', \tau') \in \text{set } (eff \ i \ P \ pc \ xt \ t). pc' < mpc)$

**lemma**  $app\text{-Some:}$

$app \ i \ P \ mxs \ T_r \ pc \ mpc \ xt \ (\text{Some } \tau) =$   
 $(app_i (i, P, pc, mxs, T_r, \tau) \wedge xcpt\text{-app } i \ P \ pc \ mxs \ xt \ \tau \wedge$   
 $(\forall (pc', s') \in \text{set } (eff \ i \ P \ pc \ xt \ (\text{Some } \tau)). pc' < mpc))$

**by** ( $\text{simp add: app-def}$ )

**locale**  $eff = \text{jvm-method} +$

**fixes**  $eff_i$  **and**  $app_i$  **and**  $eff$  **and**  $app$

**fixes**  $norm\text{-eff}$  **and**  $xcpt\text{-app}$  **and**  $xcpt\text{-eff}$

**fixes**  $mpc$

**defines**  $mpc \equiv \text{size } is$

**defines**  $eff_i \ i \ \tau \equiv \text{Effect.eff}_i (i, P, \tau)$

```

notes  $\text{eff}_i\text{-simps}$  [simp] =  $\text{Effect.eff}_i.\text{simps}$  [where  $P = P$ , folded  $\text{eff}_i\text{-def}$ ]

defines  $\text{app}_i$   $i$   $pc$   $\tau \equiv \text{Effect.app}_i (i, P, pc, mxs, T_r, \tau)$ 
notes  $\text{app}_i\text{-simps}$  [simp] =  $\text{Effect.app}_i.\text{simps}$  [where  $P=P$  and  $mxs=mxs$  and  $T_r=T_r$ , folded  $\text{app}_i\text{-def}$ ]

defines  $\text{xcpt-eff}$   $i$   $pc$   $\tau \equiv \text{Effect.xcpt-eff } i P pc \tau xt$ 
notes  $\text{xcpt-eff} = \text{Effect.xcpt-eff-def}$  [of -  $P$  -  $xt$ , folded  $\text{xcpt-eff-def}$ ]

defines  $\text{norm-eff}$   $i$   $pc$   $\tau \equiv \text{Effect.norm-eff } i P pc \tau$ 
notes  $\text{norm-eff} = \text{Effect.norm-eff-def}$  [of -  $P$ , folded  $\text{norm-eff-def}$   $\text{eff}_i\text{-def}$ ]

defines  $\text{eff}$   $i$   $pc \equiv \text{Effect.eff } i P pc xt$ 
notes  $\text{eff} = \text{Effect.eff-def}$  [of -  $P$  -  $xt$ , folded  $\text{eff-def}$   $\text{norm-eff-def}$   $\text{xcpt-eff-def}$ ]

defines  $\text{xcpt-app}$   $i$   $pc$   $\tau \equiv \text{Effect.xcpt-app } i P pc mxs xt \tau$ 
notes  $\text{xcpt-app} = \text{Effect.xcpt-app-def}$  [of -  $P$  -  $mxs$   $xt$ , folded  $\text{xcpt-app-def}$ ]

defines  $\text{app}$   $i$   $pc \equiv \text{Effect.app } i P mxs T_r pc mpc xt$ 
notes  $\text{app} = \text{Effect.app-def}$  [of -  $P$   $mxs$   $T_r$  -  $mpc$   $xt$ , folded  $\text{app-def}$   $\text{xcpt-app-def}$   $\text{app}_i\text{-def}$   $\text{eff-def}$ ]

lemma  $\text{length-cases2}$ :
  assumes  $\bigwedge LT. P ([], LT)$ 
  assumes  $\bigwedge l ST LT. P (l\#ST, LT)$ 
  shows  $P s$ 
  by ( $\text{cases } s, \text{cases fst } s$ ) ( $\text{auto intro!; assms}$ )

lemma  $\text{length-cases3}$ :
  assumes  $\bigwedge LT. P ([], LT)$ 
  assumes  $\bigwedge l LT. P ([l], LT)$ 
  assumes  $\bigwedge l l' ST LT. P (l\#l'\#ST, LT)$ 
  shows  $P s$ 
  apply( $\text{rule length-cases2; (rule assms)?}$ )
  subgoal for  $l ST LT$  by( $\text{cases } ST; \text{clarsimp simp: assms}$ )
  done

lemma  $\text{length-cases4}$ :
  assumes  $\bigwedge LT. P ([], LT)$ 
  assumes  $\bigwedge l LT. P ([l], LT)$ 
  assumes  $\bigwedge l l' LT. P ([l, l'], LT)$ 
  assumes  $\bigwedge l l' l'' ST LT. P (l\#l'\#l''\#ST, LT)$ 
  shows  $P s$ 
  apply( $\text{rule length-cases3; (rule assms)?}$ )
  subgoal for  $l l' ST LT$  by( $\text{cases } ST; \text{clarsimp simp: assms}$ )
  done

lemma  $\text{length-cases5}$ :
  assumes  $\bigwedge LT. P ([], LT)$ 
  assumes  $\bigwedge l LT. P ([l], LT)$ 
  assumes  $\bigwedge l l' LT. P ([l, l'], LT)$ 
  assumes  $\bigwedge l l' l'' LT. P ([l, l', l''], LT)$ 
  assumes  $\bigwedge l l' l'' l''' ST LT. P (l\#l'\#l''\#l'''\#ST, LT)$ 

```

**shows**  $P\ s$   
**apply**(*rule length-cases4*; (*rule assms*)?)  
**subgoal for**  $l\ l'\ l''\ ST\ LT$  **by**(*cases ST*; *clarsimp simp*; *assms*)  
**done**

simp rules for *app*

**lemma** *appNone[simp]*:  $app\ i\ P\ mxs\ T_r\ pc\ mpc\ et\ None = True$   
**by** (*simp add: app-def*)

**lemma** *appLoad[simp]*:  
 $app_i\ (Load\ idx,\ P,\ T_r,\ mxs,\ pc,\ s) = (\exists\ ST\ LT.\ s = (ST,LT) \wedge idx < length\ LT \wedge LT!idx \neq Err \wedge length\ ST < mxs)$   
**by** (*cases s, simp*)

**lemma** *appStore[simp]*:  
 $app_i\ (Store\ idx,\ P,\ pc,\ mxs,\ T_r,\ s) = (\exists\ ts\ ST\ LT.\ s = (ts\#ST,LT) \wedge idx < length\ LT)$   
**by** (*rule length-cases2, auto*)

**lemma** *appPush[simp]*:  
 $app_i\ (Push\ v,\ P,\ pc,\ mxs,\ T_r,\ s) =$   
 $(\exists\ ST\ LT.\ s = (ST,LT) \wedge length\ ST < mxs \wedge typeof\ v \neq None)$   
**by** (*cases s, simp*)

**lemma** *appGetField[simp]*:  
 $app_i\ (Getfield\ F\ C,\ P,\ pc,\ mxs,\ T_r,\ s) =$   
 $(\exists\ oT\ vT\ ST\ LT\ fm.\ s = (oT\#ST,\ LT) \wedge$   
 $P \vdash C\ sees\ F:vT\ (fm)\ in\ C \wedge P \vdash oT \leq (Class\ C))$   
**by** (*rule length-cases2 [of - s] auto*)

**lemma** *appPutField[simp]*:  
 $app_i\ (Putfield\ F\ C,\ P,\ pc,\ mxs,\ T_r,\ s) =$   
 $(\exists\ vT\ vT'\ oT\ ST\ LT\ fm.\ s = (vT\#oT\#ST,\ LT) \wedge$   
 $P \vdash C\ sees\ F:vT'\ (fm)\ in\ C \wedge P \vdash oT \leq (Class\ C) \wedge P \vdash vT \leq vT')$   
**by** (*rule length-cases4 [of - s], auto*)

**lemma** *appCAS[simp]*:  
 $app_i\ (CAS\ F\ C,\ P,\ pc,\ mxs,\ T_r,\ s) =$   
 $(\exists\ T1\ T2\ T3\ T'\ ST\ LT\ fm.\ s = (T3\ \# \ T2\ \# \ T1\ \# \ ST,\ LT) \wedge$   
 $P \vdash C\ sees\ F:T'\ (fm)\ in\ C \wedge volatile\ fm \wedge P \vdash T1 \leq Class\ C \wedge P \vdash T2 \leq T' \wedge P \vdash T3 \leq T')$   
**by**(*rule length-cases4[of - s] auto*)

**lemma** *appNew[simp]*:  
 $app_i\ (New\ C,\ P,\ pc,\ mxs,\ T_r,\ s) =$   
 $(\exists\ ST\ LT.\ s = (ST,LT) \wedge is-class\ P\ C \wedge length\ ST < mxs)$   
**by** (*cases s, simp*)

**lemma** *appNewArray[simp]*:  
 $app_i\ (NewArray\ Ty,\ P,\ pc,\ mxs,\ T_r,\ s) =$   
 $(\exists\ ST\ LT.\ s = (Integer\#ST,LT) \wedge is-type\ P\ (Ty[]))$   
**by** (*cases s, simp, cases fst s, simp*)(*cases hd (fst s), auto*)

**lemma** *appALoad[simp]*:  
 $app_i\ (ALoad,\ P,\ pc,\ mxs,\ T_r,\ s) =$



$(\exists T \ ST \ LT. s = (Integer \# T \# ST, LT) \wedge (T \neq NT \longrightarrow (\exists T'. T = T'[])))$

**proof** –

**obtain**  $ST \ LT$  **where**  $[simp]: s = (ST, LT)$  **by**  $(cases \ s)$   
**have**  $ST = [] \vee (\exists T. ST = [T]) \vee (\exists T_1 \ T_2 \ ST'. ST = T_1 \# T_2 \# ST')$   
**by**  $(cases \ ST, auto, case-tac \ list, auto)$   
**moreover**  
**{ assume**  $ST = []$  **hence**  $?thesis$  **by**  $simp$  **}**  
**moreover**  
**{ fix**  $T$  **assume**  $ST = [T]$  **hence**  $?thesis$  **by**  $(cases \ T, auto)$  **}**  
**moreover**  
**{ fix**  $T_1 \ T_2 \ ST'$  **assume**  $ST = T_1 \# T_2 \# ST'$   
**hence**  $?thesis$  **by**  $(cases \ T_1, auto)$   
**}**  
**ultimately show**  $?thesis$  **by**  $blast$   
**qed**

**lemma**  $appAStore[simp]:$

$app_i (AStore, P, pc, mxs, T_r, s) =$   
 $(\exists T \ U \ ST \ LT. s = (T \# Integer \# U \# ST, LT) \wedge (U \neq NT \longrightarrow (\exists T'. U = T'[])))$

**proof** –

**obtain**  $ST \ LT$  **where**  $[simp]: s = (ST, LT)$  **by**  $(cases \ s)$   
**have**  $ST = [] \vee (\exists T. ST = [T]) \vee (\exists T_1 \ T_2. ST = [T_1, T_2]) \vee (\exists T_1 \ T_2 \ T_3 \ ST'. ST = T_1 \# T_2 \# T_3 \# ST')$   
**by**  $(cases \ ST, auto, case-tac \ list, auto, case-tac \ lista, auto)$   
**moreover**  
**{ assume**  $ST = []$  **hence**  $?thesis$  **by**  $simp$  **}**  
**moreover**  
**{ fix**  $T$  **assume**  $ST = [T]$  **hence**  $?thesis$  **by**  $(simp)$  **}**  
**moreover**  
**{ fix**  $T_1 \ T_2$  **assume**  $ST = [T_1, T_2]$  **hence**  $?thesis$  **by**  $simp$  **}**  
**moreover**  
**{ fix**  $T_1 \ T_2 \ T_3 \ ST'$  **assume**  $ST = T_1 \# T_2 \# T_3 \# ST'$  **hence**  $?thesis$  **by**  $(cases \ T_2, auto)$  **}**  
**ultimately show**  $?thesis$  **by**  $blast$   
**qed**

**lemma**  $appALength[simp]:$

$app_i (ALength, P, pc, mxs, T_r, s) =$   
 $(\exists T \ ST \ LT. s = (T \# ST, LT) \wedge (T \neq NT \longrightarrow (\exists T'. T = T'[])))$   
**by**  $(cases \ s, cases \ fst \ s, simp \ add: app-def) (cases \ hd \ (fst \ s), auto)$

**lemma**  $appCheckcast[simp]:$

$app_i (Checkcast \ Ty, P, pc, mxs, T_r, s) =$   
 $(\exists T \ ST \ LT. s = (T \# ST, LT) \wedge is-type \ P \ Ty)$   
**by**  $(cases \ s, cases \ fst \ s, simp \ add: app-def) (cases \ hd \ (fst \ s), auto)$

**lemma**  $appInstanceof[simp]:$

$app_i (Instanceof \ Ty, P, pc, mxs, T_r, s) =$   
 $(\exists T \ ST \ LT. s = (T \# ST, LT) \wedge is-type \ P \ Ty \wedge is-refT \ T)$   
**by**  $(cases \ s, cases \ fst \ s, simp \ add: app-def) (cases \ hd \ (fst \ s), auto)$

**lemma**  $app_iPop[simp]:$

$app_i (Pop, P, pc, mxs, T_r, s) = (\exists ts \ ST \ LT. s = (ts \# ST, LT))$   
**by**  $(rule \ length-cases2, auto)$

**lemma** *appDup[simp]*:  
 $app_i (Dup, P, pc, mxs, T_r, s) =$   
 $(\exists T ST LT. s = (T \# ST, LT) \wedge Suc (length ST) < mxs)$   
**by** (*cases s, cases fst s, simp-all*)

**lemma** *app\_iSwap[simp]*:  
 $app_i (Swap, P, pc, mxs, T_r, s) = (\exists T1 T2 ST LT. s = (T1 \# T2 \# ST, LT))$   
**by**(*rule length-cases4*) *auto*

**lemma** *appBinOp[simp]*:  
 $app_i (BinOpInstr bop, P, pc, mxs, T_r, s) = (\exists T1 T2 ST LT T. s = (T2 \# T1 \# ST, LT) \wedge P \vdash T1 \ll bop \gg T2 : T)$

**proof** –

**obtain** *ST LT* **where** [*simp*]:  $s = (ST, LT)$  **by** (*cases s*)  
**have**  $ST = [] \vee (\exists T. ST = [T]) \vee (\exists T1 T2 ST'. ST = T1 \# T2 \# ST')$   
**by** (*cases ST, auto, case-tac list, auto*)  
**moreover**  
**{ assume**  $ST = []$  **hence** *?thesis* **by** *simp* **}**  
**moreover**  
**{ fix** *T* **assume**  $ST = [T]$  **hence** *?thesis* **by** (*cases T, auto*) **}**  
**moreover**  
**{ fix**  $T1 T2 ST'$  **assume**  $ST = T1 \# T2 \# ST'$   
**hence** *?thesis* **by** *simp*  
**}**  
**ultimately show** *?thesis* **by** *blast*

**qed**

**lemma** *appIfFalse [simp]*:  
 $app_i (IfFalse b, P, pc, mxs, T_r, s) =$   
 $(\exists ST LT. s = (Boolean \# ST, LT) \wedge 0 \leq int pc + b)$   
**apply** (*rule length-cases2*)  
**apply** *simp*  
**apply** (*case-tac l*)  
**apply** *auto*  
**done**

**lemma** *appReturn[simp]*:  
 $app_i (Return, P, pc, mxs, T_r, s) = (\exists T ST LT. s = (T \# ST, LT) \wedge P \vdash T \leq T_r)$   
**by** (*rule length-cases2, auto*)

**lemma** *appThrow[simp]*:  
 $app_i (ThrowExc, P, pc, mxs, T_r, s) = (\exists T ST LT. s = (T \# ST, LT) \wedge (T = NT \vee (\exists C. T = Class C \wedge P \vdash C \preceq^* Throwable)))$   
**by** (*rule length-cases2, auto*)

**lemma** *appMEnter[simp]*:  
 $app_i (MEnter, P, pc, mxs, T_r, s) = (\exists T ST LT. s = (T \# ST, LT) \wedge is-refT T)$   
**by** (*rule length-cases2, auto*)

**lemma** *appMExit[simp]*:  
 $app_i (MExit, P, pc, mxs, T_r, s) = (\exists T ST LT. s = (T \# ST, LT) \wedge is-refT T)$   
**by** (*rule length-cases2, auto*)

**lemma** *effNone*:

$(pc', s') \in \text{set } (\text{eff } i \ P \ pc \ \text{et } \text{None}) \implies s' = \text{None}$   
**by** (auto simp add: eff-def xcpt-eff-def norm-eff-def)

**lemma** relevant-entries-append [simp]:

relevant-entries  $P \ i \ pc \ (xt \ @ \ xt') = \text{relevant-entries } P \ i \ pc \ xt \ @ \ \text{relevant-entries } P \ i \ pc \ xt'$   
**by** (unfold relevant-entries-def) simp

**lemma** xcpt-app-append [iff]:

$\text{xcpt-app } i \ P \ pc \ mxs \ (xt@xt') \ \tau = (\text{xcpt-app } i \ P \ pc \ mxs \ xt \ \tau \wedge \text{xcpt-app } i \ P \ pc \ mxs \ xt' \ \tau)$

**unfolding** xcpt-app-def **by** force

**lemma** xcpt-eff-append [simp]:

$\text{xcpt-eff } i \ P \ pc \ \tau \ (xt@xt') = \text{xcpt-eff } i \ P \ pc \ \tau \ xt \ @ \ \text{xcpt-eff } i \ P \ pc \ \tau \ xt'$

**by** (unfold xcpt-eff-def, cases  $\tau$ ) simp

**lemma** app-append [simp]:

$\text{app } i \ P \ pc \ T \ mxs \ mpc \ (xt@xt') \ \tau = (\text{app } i \ P \ pc \ T \ mxs \ mpc \ xt \ \tau \wedge \text{app } i \ P \ pc \ T \ mxs \ mpc \ xt' \ \tau)$

**by** (unfold app-def eff-def) auto

### 6.2.1 Code generator setup

**declare** list-all2-Nil [code]

**declare** list-all2-Cons [code]

**lemma** eff<sub>i</sub>-BinOpInstr-code:

$\text{eff}_i \ (\text{BinOpInstr } \text{bop}, P, (T2\#T1\#ST, LT)) = (\text{Predicate.the } (WTrt\text{-binop-}i\text{-}i\text{-}i\text{-}o \ P \ T1 \ \text{bop} \ T2) \# ST, LT)$

**by**(simp add: the-WTrt-binop-code)

**lemmas** eff<sub>i</sub>-code[code] =

eff<sub>i</sub>-Load eff<sub>i</sub>-Store eff<sub>i</sub>-Push eff<sub>i</sub>-Getfield eff<sub>i</sub>-Putfield eff<sub>i</sub>-New eff<sub>i</sub>-NewArray eff<sub>i</sub>-ALoad  
 eff<sub>i</sub>-ASTore eff<sub>i</sub>-ALength eff<sub>i</sub>-Checkcast eff<sub>i</sub>-InstanceOf eff<sub>i</sub>-Pop eff<sub>i</sub>-Dup eff<sub>i</sub>-Swap eff<sub>i</sub>-BinOpInstr-code  
 eff<sub>i</sub>-IfFalse eff<sub>i</sub>-Invoke eff<sub>i</sub>-Goto eff<sub>i</sub>-MEnter eff<sub>i</sub>-MExit

**lemma** app<sub>i</sub>-Getfield-code:

$\text{app}_i \ (\text{Getfield } F \ C, P, pc, mxs, T_r, (T\#ST, LT)) \longleftrightarrow$

$\text{Predicate.holds } (\text{Predicate.bind } (\text{sees-field-}i\text{-}i\text{-}i\text{-}o\text{-}i \ P \ C \ F \ C) \ (\lambda T. \text{Predicate.single } ())) \wedge P \vdash T \leq \text{Class } C$

**apply**(clarsimp simp add: Predicate.bind-def Predicate.single-def holds-eq eval-sees-field- $i\text{-}i\text{-}i\text{-}o\text{-}i\text{-}conv$ )  
**done**

**lemma** app<sub>i</sub>-Putfield-code:

$\text{app}_i \ (\text{Putfield } F \ C, P, pc, mxs, T_r, (T_1\#T_2\#ST, LT)) \longleftrightarrow$

$P \vdash T_2 \leq (\text{Class } C) \wedge$

$\text{Predicate.holds } (\text{Predicate.bind } (\text{sees-field-}i\text{-}i\text{-}i\text{-}o\text{-}i \ P \ C \ F \ C) \ (\lambda(T, fm). \text{if } P \vdash T_1 \leq T \text{ then } \text{Predicate.single } () \text{ else bot}))$

**by** (auto simp add: holds-eq eval-sees-field- $i\text{-}i\text{-}i\text{-}o\text{-}i\text{-}conv$  split: if-splits)

**lemma** app<sub>i</sub>-CAS-code:

$\text{app}_i \ (\text{CAS } F \ C, P, pc, mxs, T_r, (T_3\#T_2\#T_1\#ST, LT)) \longleftrightarrow$

$P \vdash T_1 \leq \text{Class } C \wedge$

$\text{Predicate.holds } (\text{Predicate.bind } (\text{sees-field-}i\text{-}i\text{-}i\text{-}o\text{-}i \ P \ C \ F \ C) \ (\lambda(T, fm). \text{if } P \vdash T_2 \leq T \wedge P \vdash T_3 \leq T \wedge \text{volatile } fm \text{ then } \text{Predicate.single } () \text{ else bot}))$

**by**(*auto simp add: holds-eq eval-sees-field-i-i-i-o-i-conv*)

**lemma** *app<sub>i</sub>-ALoad-code*:

*app<sub>i</sub>* (*ALoad*, *P*, *pc*, *mxs*, *T<sub>r</sub>*, (*T1* # *T2* # *ST*, *LT*)) =  
 (*T1* = *Integer* ∧ (case *T2* of *Ty* [] ⇒ *True* | *NT* ⇒ *True* | - ⇒ *False*))

**by**(*simp add: split: ty.split*)

**lemma** *app<sub>i</sub>-AStore-code*:

*app<sub>i</sub>* (*AStore*, *P*, *pc*, *mxs*, *T<sub>r</sub>*, (*T1* # *T2* # *T3* # *ST*, *LT*)) =  
 (*T2* = *Integer* ∧ (case *T3* of *Ty* [] ⇒ *True* | *NT* ⇒ *True* | - ⇒ *False*))

**by**(*simp add: split: ty.split*)

**lemma** *app<sub>i</sub>-ALength-code*:

*app<sub>i</sub>* (*ALength*, *P*, *pc*, *mxs*, *T<sub>r</sub>*, (*T1* # *ST*, *LT*)) =  
 (case *T1* of *Ty* [] ⇒ *True* | *NT* ⇒ *True* | - ⇒ *False*)

**by**(*simp add: split: ty.split*)

**lemma** *app<sub>i</sub>-BinOpInstr-code*:

*app<sub>i</sub>* (*BinOpInstr* *bop*, *P*, *pc*, *mxs*, *T<sub>r</sub>*, (*T2* # *T1* # *ST*, *LT*)) =  
*Predicate.holds* (*Predicate.bind* (*WTrt-binop-i-i-i-i-o* *P* *T1* *bop* *T2*) (λ*T*. *Predicate.single* ()))

**by** (*auto simp add: holds-eq eval-WTrt-binop-i-i-i-i-o*)

**lemma** *app<sub>i</sub>-Invoke-code*:

*app<sub>i</sub>* (*Invoke* *M* *n*, *P*, *pc*, *mxs*, *T<sub>r</sub>*, (*ST*, *LT*)) =  
 (*n* < *length* *ST* ∧  
 (*ST*! *n* ≠ *NT* →  
 (case *class-type-of'* (*ST* ! *n*) of *Some* *C* ⇒  
*Predicate.holds* (*Predicate.bind* (*Method-i-i-i-o-o-o-o* *P* *C* *M*)  
 (λ(*Ts*, -). if *P* ⊢ *rev* (*take* *n* *ST*) [≤] *Ts* then *Predicate.single* () else  
*bot*)))  
 | - ⇒ *False*)))

**proof** –

**have** *bind-Ex*: ∧*P* *f*. *Predicate.bind* *P* *f* = *Predicate.Pred* (λ*x*. (∃ *y*. *Predicate.eval* *P* *y* ∧ *Predicate.eval* (*f* *y*) *x*))

**by** (*rule pred-eqI*) *auto*

**thus** ?*thesis*

**by** (*auto simp add: bind-Ex Predicate.single-def holds-eq eval-Method-i-i-i-o-o-o-o-conv split: ty.split*)

**qed**

**lemma** *app<sub>i</sub>-Throw-code*:

*app<sub>i</sub>* (*ThrowExc*, *P*, *pc*, *mxs*, *T<sub>r</sub>*, (*T* # *ST*, *LT*)) =  
 (case *T* of *NT* ⇒ *True* | *Class* *C* ⇒ *P* ⊢ *C* ≤\* *Throwable* | - ⇒ *False*)

**by**(*simp split: ty.split*)

**lemmas** *app<sub>i</sub>-code* [*code*] =

*app<sub>i</sub>-Load* *app<sub>i</sub>-Store* *app<sub>i</sub>-Push*  
*app<sub>i</sub>-Getfield-code* *app<sub>i</sub>-Putfield-code* *app<sub>i</sub>-CAS-code*  
*app<sub>i</sub>-New* *app<sub>i</sub>-NewArray*  
*app<sub>i</sub>-ALoad-code* *app<sub>i</sub>-AStore-code* *app<sub>i</sub>-ALength-code*  
*app<sub>i</sub>-Checkcast* *app<sub>i</sub>-InstanceOf*  
*app<sub>i</sub>-Pop* *app<sub>i</sub>-Dup* *app<sub>i</sub>-Swap* *app<sub>i</sub>-BinOpInstr-code* *app<sub>i</sub>-IfFalse* *app<sub>i</sub>-Goto*  
*app<sub>i</sub>-Return* *app<sub>i</sub>-Throw-code* *app<sub>i</sub>-Invoke-code* *app<sub>i</sub>-MEnter* *app<sub>i</sub>-MExit*  
*app<sub>i</sub>-default*

end

## 6.3 The Bytecode Verifier

**theory** *BVSpec*

**imports**

*Effect*

**begin**

This theory contains a specification of the BV. The specification describes correct typings of method bodies; it corresponds to type *checking*.

**definition** *check-types* :: '*m prog*  $\Rightarrow$  *nat*  $\Rightarrow$  *nat*  $\Rightarrow$  *ty<sub>i</sub>* *err list*  $\Rightarrow$  *bool*

**where**

*check-types* *P mxs mxl*  $\tau s \equiv \text{set } \tau s \subseteq \text{states } P \text{ mxs mxl}$

— An instruction is welltyped if it is applicable and its effect

— is compatible with the type at all successor instructions:

**definition** *wt-instr* :: [*m prog, ty, nat, pc, ex-table, 'addr instr, pc, ty<sub>m</sub>*]  $\Rightarrow$  *bool*

( $\neg, \neg, \neg, \neg \vdash \neg, \neg :: - [60, 0, 0, 0, 0, 0, 0, 61] \ 60$ )

**where**

*P, T, mxs, mpc, xt*  $\vdash i, pc :: \tau s \equiv$

*app* *i P mxs T pc mpc xt* ( $\tau s!pc$ )  $\wedge$

( $\forall (pc', \tau') \in \text{set } (\text{eff } i \ P \ pc \ xt \ (\tau s!pc)). \ P \vdash \tau' \leq' \tau s!pc'$ )

— The type at *pc=0* conforms to the method calling convention:

**definition** *wt-start* :: [*m prog, cname, ty list, nat, ty<sub>m</sub>*]  $\Rightarrow$  *bool*

**where**

*wt-start* *P C Ts mxl<sub>0</sub>*  $\tau s \equiv$

$P \vdash \text{Some } ([], \text{OK } (\text{Class } C) \# \text{map OK } Ts @ \text{replicate mxl}_0 \text{ Err}) \leq' \tau s!0$

— A method is welltyped if the body is not empty,

— if the method type covers all instructions and mentions

— declared classes only, if the method calling convention is respected, and

— if all instructions are welltyped.

**definition** *wt-method* :: [*m prog, cname, ty list, ty, nat, nat, 'addr instr list, ex-table, ty<sub>m</sub>*]  $\Rightarrow$  *bool*

**where**

*wt-method* *P C Ts T<sub>r</sub> mxs mxl<sub>0</sub>* *is xt*  $\tau s \equiv$

$0 < \text{size is} \wedge \text{size } \tau s = \text{size is} \wedge$

*check-types* *P mxs* ( $1 + \text{size } Ts + \text{mxl}_0$ ) ( $\text{map OK } \tau s$ )  $\wedge$

*wt-start* *P C Ts mxl<sub>0</sub>*  $\tau s \wedge$

( $\forall pc < \text{size is}. \ P, T_r, mxs, \text{size is}, xt \vdash \text{is!pc}, pc :: \tau s$ )

— A program is welltyped if it is wellformed and all methods are welltyped

**definition** *wf-jvm-prog-phi* :: *ty<sub>P</sub>*  $\Rightarrow$  '*addr jvm-prog*  $\Rightarrow$  *bool* (*wf'-jvm'-prog-*)

**where**

*wf-jvm-prog*  $\Phi \equiv$

*wf-prog* ( $\lambda P \ C \ (M, Ts, T_r, (mxs, mxl_0, is, xt)).$

*wt-method* *P C Ts T<sub>r</sub> mxs mxl<sub>0</sub>* *is xt* ( $\Phi \ C \ M$ ))

**definition** *wf-jvm-prog* :: '*addr jvm-prog*  $\Rightarrow$  *bool*

**where**

*wf-jvm-prog* *P*  $\equiv \exists \Phi. \ wf\text{-}jvm\text{-}prog_{\Phi} \ P$

**lemma** *wt-jvm-progD*:

$wf\text{-}jvm\text{-}prog_{\Phi} P \implies \exists wt. wf\text{-}prog wt P$

**lemma** *wt-jvm-prog-impl-wt-instr*:

$\llbracket wf\text{-}jvm\text{-}prog_{\Phi} P;$   
 $P \vdash C \text{ sees } M:Ts \rightarrow T = \lfloor (m\bar{x}s, m\bar{x}l_0, ins, xt) \rfloor \text{ in } C; pc < size\ ins \rrbracket$   
 $\implies P, T, m\bar{x}s, size\ ins, xt \vdash ins!pc, pc :: \Phi\ C\ M$

**lemma** *wt-jvm-prog-impl-wt-start*:

$\llbracket wf\text{-}jvm\text{-}prog_{\Phi} P;$   
 $P \vdash C \text{ sees } M:Ts \rightarrow T = \lfloor (m\bar{x}s, m\bar{x}l_0, ins, xt) \rfloor \text{ in } C \rrbracket \implies$   
 $0 < size\ ins \wedge wt\text{-}start\ P\ C\ Ts\ m\bar{x}l_0\ (\Phi\ C\ M)$

**end**

## 6.4 BV Type Safety Invariant

**theory** *BVConform*

**imports**

*BVSpec*

*../JVM/JVMExec*

**begin**

**context** *JVM-heap-base* **begin**

**definition** *confT* ::  $'c\ prog \Rightarrow 'heap \Rightarrow 'addr\ val \Rightarrow ty\ err \Rightarrow bool$

$(-, - \vdash - : \leq_{\top} - [51, 51, 51, 51])\ 50)$

**where**

$P, h \vdash v : \leq_{\top} E \equiv case\ E\ of\ Err \Rightarrow True \mid OK\ T \Rightarrow P, h \vdash v : \leq T$

**notation** (*ASCII*)

$confT\ (-, - \mid - : \leq T - [51, 51, 51, 51])\ 50)$

**abbreviation** *confTs* ::  $'c\ prog \Rightarrow 'heap \Rightarrow 'addr\ val\ list \Rightarrow ty_l \Rightarrow bool$

$(-, - \vdash - [: \leq_{\top}] - [51, 51, 51, 51])\ 50)$

**where**

$P, h \vdash vs [: \leq_{\top}] Ts \equiv list\text{-}all2\ (confT\ P\ h)\ vs\ Ts$

**notation** (*ASCII*)

$confTs\ (-, - \mid - [: \leq T] - [51, 51, 51, 51])\ 50)$

**definition** *conf-f* ::  $'addr\ jvm\text{-}prog \Rightarrow 'heap \Rightarrow ty_i \Rightarrow 'addr\ bytecode \Rightarrow 'addr\ frame \Rightarrow bool$

**where**

$conf\text{-}f\ P\ h \equiv \lambda(ST, LT)\ is\ (stk, loc, C, M, pc). P, h \vdash stk [: \leq] ST \wedge P, h \vdash loc [: \leq_{\top}] LT \wedge pc < size\ is$

**primrec** *conf-fs* ::  $'[addr\ jvm\text{-}prog, 'heap, ty_P, mname, nat, ty, 'addr\ frame\ list] \Rightarrow bool$

**where**

$conf\text{-}fs\ P\ h\ \Phi\ M_0\ n_0\ T_0\ [] = True$

$\mid conf\text{-}fs\ P\ h\ \Phi\ M_0\ n_0\ T_0\ (f\#\bar{f}rs) =$

$(let\ (stk, loc, C, M, pc) = f\ in$

$(\exists ST\ LT\ Ts\ T\ m\bar{x}s\ m\bar{x}l_0\ is\ xt.$

$\Phi\ C\ M\ !\ pc = Some\ (ST, LT) \wedge$

$(P \vdash C \text{ sees } M:Ts \rightarrow T = \lfloor (m\bar{x}s, m\bar{x}l_0, is, xt) \rfloor \text{ in } C) \wedge$

$(\exists Ts'\ T'\ D\ m\ D'.$

$is!pc = (Invoke\ M_0\ n_0) \wedge class\text{-}type\text{-}of'\ (ST!n_0) = \lfloor D \rfloor \wedge P \vdash D \text{ sees } M_0:Ts' \rightarrow T' = m\ in\ D')$

$\wedge P \vdash T_0 \leq T') \wedge$   
 $\text{conf-f } P \ h \ (ST, LT) \text{ is } f \wedge \text{conf-fs } P \ h \ \Phi \ M \ (\text{size } Ts) \ T \ \text{frs}))$

**primrec**  $\text{conf-xcp} :: 'addr \text{ jvm-prog} \Rightarrow 'heap \Rightarrow 'addr \text{ option} \Rightarrow 'addr \text{ instr} \Rightarrow \text{bool}$  **where**  
 $\text{conf-xcp } P \ h \ \text{None} \ i = \text{True}$   
 $| \text{conf-xcp } P \ h \ [a] \quad i = (\exists D. \text{typeof-addr } h \ a = \lfloor \text{Class-type } D \rfloor \wedge P \vdash D \preceq^* \text{Throwable} \wedge$   
 $(\forall D'. P \vdash D \preceq^* D' \longrightarrow \text{is-relevant-class } i \ P \ D'))$

**end**

**context**  $JVM\text{-heap-conf-base}$  **begin**

**definition**  $\text{correct-state} :: [ty_P, 'thread\text{-id}, ('addr, 'heap) \text{ jvm-state}] \Rightarrow \text{bool}$

**where**

$\text{correct-state } \Phi \ t \equiv \lambda(xp, h, frs).$   
 $P, h \vdash t \sqrt{t} \wedge h\text{conf } h \wedge \text{preallocated } h \wedge$   
 $(\text{case } frs \text{ of}$   
 $\quad [] \Rightarrow \text{True}$   
 $\quad | (f \# frs) \Rightarrow$   
 $\quad (\text{let } (stk, loc, C, M, pc) = f$   
 $\quad \text{in } \exists Ts \ T \ mxs \ mxl_0 \ \text{is } xt \ \tau.$   
 $\quad (P \vdash C \text{ sees } M:Ts \rightarrow T = \lfloor (mxs, mxl_0, is, xt) \rfloor \text{ in } C) \wedge$   
 $\quad \Phi \ C \ M \ ! \ pc = \text{Some } \tau \wedge$   
 $\quad \text{conf-f } P \ h \ \tau \text{ is } f \wedge \text{conf-fs } P \ h \ \Phi \ M \ (\text{size } Ts) \ T \ \text{fs} \wedge$   
 $\quad \text{conf-xcp } P \ h \ xp \ (\text{is } ! \ pc) \ ))$

**notation**

$\text{correct-state} \ (- \vdash \text{:-} \sqrt{\quad} \ [61, 0, 0] \ 61)$

**notation** ( $ASCII$ )

$\text{correct-state} \ (- \mid \text{:-} \ [ok] \ [61, 0, 0] \ 61)$

**end**

**context**  $JVM\text{-heap-base}$  **begin**

**lemma**  $\text{conf-f-def2}$ :

$\text{conf-f } P \ h \ (ST, LT) \text{ is } (stk, loc, C, M, pc) \equiv$   
 $P, h \vdash stk \ [:\leq] \ ST \wedge P, h \vdash loc \ [:\leq_{\top}] \ LT \wedge pc < \text{size} \text{ is}$   
**by** ( $\text{simp add: conf-f-def}$ )

#### 6.4.1 Values and $\top$

**lemma**  $\text{confT-Err}$  [ $\text{iff}$ ]:  $P, h \vdash x : \leq_{\top} \text{Err}$   
**by** ( $\text{simp add: confT-def}$ )

**lemma**  $\text{confT-OK}$  [ $\text{iff}$ ]:  $P, h \vdash x : \leq_{\top} \text{OK } T = (P, h \vdash x : \leq T)$   
**by** ( $\text{simp add: confT-def}$ )

**lemma**  $\text{confT-cases}$ :

$P, h \vdash x : \leq_{\top} X = (X = \text{Err} \vee (\exists T. X = \text{OK } T \wedge P, h \vdash x : \leq T))$   
**by** ( $\text{cases } X$ )  $\text{auto}$

**lemma**  $\text{confT-widen}$  [ $\text{intro?}, \text{trans}$ ]:

$\llbracket P, h \vdash x : \leq_{\top} T; P \vdash T \leq_{\top} T' \rrbracket \Longrightarrow P, h \vdash x : \leq_{\top} T'$   
**by** (*cases*  $T'$ , *auto intro: conf-widen*)

**end**

**context** *JVM-heap* **begin**

**lemma** *confT-hext* [*intro?*, *trans*]:  
 $\llbracket P, h \vdash x : \leq_{\top} T; h \sqsubseteq h' \rrbracket \Longrightarrow P, h' \vdash x : \leq_{\top} T$   
**by** (*cases*  $T$ ) (*blast intro: conf-hext*)**+**

**end**

## 6.4.2 Stack and Registers

**context** *JVM-heap-base* **begin**

**lemma** *confTs-Cons1* [*iff*]:  
 $P, h \vdash x \# xs \llbracket : \leq_{\top} \rrbracket Ts = (\exists z zs. Ts = z \# zs \wedge P, h \vdash x : \leq_{\top} z \wedge \text{list-all2} (\text{confT } P \ h) \ xs \ zs)$   
**by**(*rule list-all2-Cons1*)

**lemma** *confTs-confT-sup*:  
 $\llbracket P, h \vdash \text{loc} \llbracket : \leq_{\top} \rrbracket LT; n < \text{size } LT; LT!n = OK \ T; P \vdash T \leq T' \rrbracket$   
 $\Longrightarrow P, h \vdash (\text{loc}!n) : \leq T'$   
**apply** (*frule list-all2-lengthD*)  
**apply** (*drule list-all2-nthD, simp*)  
**apply** *simp*  
**apply** (*erule conf-widen, assumption*)**+**  
**done**

**lemma** *confTs-widen* [*intro?*, *trans*]:  
 $P, h \vdash \text{loc} \llbracket : \leq_{\top} \rrbracket LT \Longrightarrow P \vdash LT \llbracket \leq_{\top} \rrbracket LT' \Longrightarrow P, h \vdash \text{loc} \llbracket : \leq_{\top} \rrbracket LT'$   
**by** (*rule list-all2-trans, rule confT-widen*)

**lemma** *confTs-map* [*iff*]:  
 $(P, h \vdash vs \llbracket : \leq_{\top} \rrbracket \text{map } OK \ Ts) = (P, h \vdash vs \llbracket : \leq \rrbracket Ts)$   
**by** (*induct Ts arbitrary: vs*) (*auto simp add: list-all2-Cons2*)

**lemma** (*in*  $-$ ) *reg-widen-Err*:  
 $(P \vdash \text{replicate } n \ Err \llbracket \leq_{\top} \rrbracket LT) = (LT = \text{replicate } n \ Err)$   
**by** (*induct n arbitrary: LT*) (*auto simp add: list-all2-Cons1*)

**declare** *reg-widen-Err* [*iff*]

**lemma** *confTs-Err* [*iff*]:  
 $P, h \vdash \text{replicate } n \ v \llbracket : \leq_{\top} \rrbracket \text{replicate } n \ Err$   
**by** (*induct n*) *auto*

**end**

**context** *JVM-heap* **begin**

**lemma** *confTs-hext* [*intro?*]:  
 $P, h \vdash \text{loc} \llbracket : \leq_{\top} \rrbracket LT \Longrightarrow h \sqsubseteq h' \Longrightarrow P, h' \vdash \text{loc} \llbracket : \leq_{\top} \rrbracket LT$



by (fast elim: list-all2-mono confT-hext)

### 6.4.3 correct-frames

declare fun-upd-apply[simp del]

lemma conf-f-hext:

$\llbracket \text{conf-f } P \ h \ \Phi \ M \ f; h \sqsubseteq h' \rrbracket \implies \text{conf-f } P \ h' \ \Phi \ M \ f$   
 by(cases f, cases  $\Phi$ , auto simp add: conf-f-def intro: confs-hext confTs-hext)

lemma conf-fs-hext:

$\llbracket \text{conf-fs } P \ h \ \Phi \ M \ n \ T_r \ \text{frs}; h \sqsubseteq h' \rrbracket \implies \text{conf-fs } P \ h' \ \Phi \ M \ n \ T_r \ \text{frs}$   
 apply (induct frs arbitrary:  $M \ n \ T_r$ )  
 apply simp  
 apply clarify  
 apply (simp (no-asm-use))  
 apply clarify  
 apply (unfold conf-f-def)  
 apply (simp (no-asm-use) split: if-split-asm)  
 apply (fast elim!: confs-hext confTs-hext)+  
 done

declare fun-upd-apply[simp]

lemma conf-xcp-hext:

$\llbracket \text{conf-xcp } P \ h \ xcp \ i; h \sqsubseteq h' \rrbracket \implies \text{conf-xcp } P \ h' \ xcp \ i$   
 by(cases xcp)(auto elim: typeof-addr-hext-mono)

end

context JVM-heap-conf-base begin

lemmas defs1 = correct-state-def conf-f-def wt-instr-def eff-def norm-eff-def app-def xcpt-app-def

lemma correct-state-impl-Some-method:

$\Phi \vdash t: (\text{None}, h, (\text{stk}, \text{loc}, C, M, \text{pc}) \# \text{frs}) \checkmark$   
 $\implies \exists m \ Ts \ T. P \vdash C \text{ sees } M: Ts \rightarrow T = \lfloor m \rfloor \text{ in } C$   
 by(fastforce simp add: defs1)

end

context JVM-heap-conf-base' begin

lemma correct-state-hext-mono:

$\llbracket \Phi \vdash t: (xcp, h, \text{frs}) \checkmark; h \sqsubseteq h'; h\text{conf } h' \rrbracket \implies \Phi \vdash t: (xcp, h', \text{frs}) \checkmark$   
 unfolding correct-state-def  
 by(fastforce elim: tconf-hext-mono preallocated-hext conf-f-hext conf-fs-hext conf-xcp-hext split: list.split)

end

end

## 6.5 BV Type Safety Proof

```

theory BVSpecTypeSafe
imports
  BVConform
  ../Common/ExternalCallWF
begin

```

```

declare listE-length [simp del]

```

This theory contains proof that the specification of the bytecode verifier only admits type safe programs.

### 6.5.1 Preliminaries

Simp and intro setup for the type safety proof:

```

context JVM-heap-conf-base begin

```

```

lemmas widen-rules [intro] = conf-widen confT-widen confs-widens confTs-widen

```

```

end

```

### 6.5.2 Exception Handling

For the *Invoke* instruction the BV has checked all handlers that guard the current *pc*.

**lemma** *Invoke-handlers*:

```

  match-ex-table P C pc xt = Some (pc',d')  $\implies$ 
   $\exists (f,t,D,h,d) \in \text{set } (\text{relevant-entries } P (\text{Invoke } n \ M) \ pc \ xt).$ 
   $(\text{case } D \text{ of } \text{None} \implies \text{True} \mid \text{Some } D' \implies P \vdash C \preceq^* D') \wedge pc \in \{f..<t\} \wedge pc' = h \wedge d' = d$ 
by (induct xt) (auto simp add: relevant-entries-def matches-ex-entry-def
  is-relevant-entry-def split: if-split-asm)

```

**lemma** *match-is-relevant*:

```

  assumes rv:  $\bigwedge D'. P \vdash D \preceq^* D' \implies \text{is-relevant-class } (ins \ ! \ i) \ P \ D'$ 
  assumes match: match-ex-table P D pc xt = Some (pc',d')
  shows  $\exists (f,t,D',h,d) \in \text{set } (\text{relevant-entries } P (ins \ ! \ i) \ pc \ xt). (\text{case } D' \text{ of } \text{None} \implies \text{True} \mid \text{Some } D''$ 
 $\implies P \vdash D \preceq^* D'') \wedge pc \in \{f..<t\} \wedge pc' = h \wedge d' = d$ 
using rv match
by (fastforce simp add: relevant-entries-def is-relevant-entry-def matches-ex-entry-def dest: match-ex-table-SomeD)

```

```

context JVM-heap-conf-base begin

```

**lemma** *exception-step-conform*:

```

  fixes  $\sigma' :: ('addr, 'heap) \text{ jvm-state}$ 
  assumes wtp: wf-jvm-prog $\Phi \ P$ 
  assumes correct:  $\Phi \vdash t:([xcp], h, fr \ \# \ frs) \ \checkmark$ 
  shows  $\Phi \vdash t:\text{exception-step } P \ xcp \ h \ fr \ frs \ \checkmark$ 
proof –
  obtain stk loc C M pc where fr: fr = (stk, loc, C, M, pc) by (cases fr)
  from correct obtain Ts T mxs mxl0 ins xt
  where meth:  $P \vdash C \text{ sees } M:Ts \rightarrow T = \lfloor (mxs, mxl_0, ins, xt) \rfloor \text{ in } C$ 
  by (simp add: correct-state-def fr) blast

```

```

from correct meth fr obtain  $D$ 
  where  $h\text{xcp}$ : typeof-addr  $h$   $\text{xcp} = \lfloor \text{Class-type } D \rfloor$  and  $D\text{subThrowable}$ :  $P \vdash D \preceq^* \text{Throwable}$ 
  and  $rv$ :  $\bigwedge D'. P \vdash D \preceq^* D' \implies \text{is-relevant-class } (\text{instrs-of } P \ C \ M \ ! \ pc) \ P \ D'$ 
  by(fastforce simp add: correct-state-def dest: sees-method-fun)

from meth have [simp]: ex-table-of  $P \ C \ M = xt$  by simp

from correct have  $t\text{conf}$ :  $P, h \vdash t \sqrt{t}$  by(simp add: correct-state-def)

show ?thesis
proof(cases match-ex-table P D pc xt)
  case None
    with correct fr meth hxcp show ?thesis
    by(fastforce simp add: correct-state-def cname-of-def split: list.split)
  next
    case (Some pc-d)
    then obtain  $pc' \ d'$  where  $pc\text{-}d = (pc', d')$ 
    and  $match$ : match-ex-table  $P \ D \ pc \ xt = \text{Some } (pc', d')$  by (cases pc-d) auto
    from match-is-relevant[OF rv match] meth obtain  $f \ t \ D'$ 
    where  $rv$ :  $(f, t, D', pc', d') \in \text{set } (\text{relevant-entries } P \ (\text{ins } ! \ pc) \ pc \ xt)$ 
    and  $D\text{sub}D'$ :  $(\text{case } D' \text{ of } \text{None} \Rightarrow \text{True} \mid \text{Some } D'' \Rightarrow P \vdash D \preceq^* D'')$  and  $pc$ :  $pc \in \{f..<t\}$ 
by(auto)

from correct meth obtain  $ST \ LT$ 
  where  $h\text{-ok}$ : hconf  $h$ 
  and  $\Phi\text{-pc}$ :  $\Phi \ C \ M \ ! \ pc = \text{Some } (ST, LT)$ 
  and  $\text{frame}$ : conf-f  $P \ h \ (ST, LT) \ \text{ins } (stk, loc, C, M, pc)$ 
  and  $\text{frames}$ : conf-fs  $P \ h \ \Phi \ M \ (\text{size } Ts) \ T \ \text{frs}$ 
  and  $\text{preh}$ : preallocated  $h$ 
  unfolding correct-state-def fr by(auto dest: sees-method-fun)

from frame obtain  $stk$ :  $P, h \vdash stk \ [:\leq] \ ST$ 
  and  $loc$ :  $P, h \vdash loc \ [:\leq_{\top}] \ LT$  and  $pc$ :  $pc < \text{size ins}$ 
  by (unfold conf-f-def) auto

from  $stk$  have [simp]: size  $stk = \text{size } ST \ ..$ 

from wt meth correct fr have  $wt$ :  $P, T, m\text{x}s, \text{size ins}, xt \vdash \text{ins}!pc, pc :: \Phi \ C \ M$ 
  by (auto simp add: correct-state-def conf-f-def
    dest: sees-method-fun
    elim!: wt-jvm-prog-impl-wt-instr)

from  $wt \ \Phi\text{-pc}$  have
  eff:  $\forall (pc', s') \in \text{set } (\text{xcpt-eff } (\text{ins}!pc) \ P \ pc \ (ST, LT) \ xt).$ 
     $pc' < \text{size ins} \wedge P \vdash s' \leq' \Phi \ C \ M!pc'$ 
  by (auto simp add: defs1)

let  $?stk' = \text{Addr } xcp \ \# \ \text{drop } (\text{length } stk - d') \ stk$ 
let  $?f = (?stk', loc, C, M, pc')$ 

have  $\text{conf}$ :  $P, h \vdash \text{Addr } xcp \ :\leq \ \text{Class } (\text{case } D' \text{ of } \text{None} \Rightarrow \text{Throwable} \mid \text{Some } D'' \Rightarrow D'')$ 
  using  $D\text{sub}D' \ h\text{xcp} \ D\text{subThrowable}$  by(auto simp add: conf-def)

```

```

obtain  $ST' LT'$  where
   $\Phi\text{-}pc'$ :  $\Phi C M ! pc' = \text{Some } (ST', LT')$  and
   $pc'$ :  $pc' < \text{size ins}$  and
   $less$ :  $P \vdash (\text{Class } D \# \text{drop } (\text{size } ST - d') ST, LT) \leq_i (ST', LT')$ 
proof(cases  $D'$ )
  case Some
    thus ?thesis using eff rv DsubD' conf that
    by(fastforce simp add: xcpt-eff-def sup-state-opt-any-Some intro: widen-trans[OF widen-subcls])
  next
  case None
    with that eff rv conf DsubThrowable show ?thesis
    by(fastforce simp add: xcpt-eff-def sup-state-opt-any-Some intro: widen-trans[OF widen-subcls])
  qed

with conf loc stk hxcpc have conf-f P h (ST',LT') ins ?f
  by (auto simp add: defs1 conf-def intro: list-all2-dropI)

with meth h-ok frames  $\Phi\text{-}pc'$  fr match hxcpc tconf preh
show ?thesis unfolding correct-state-def
  by(fastforce dest: sees-method-fun simp add: cname-of-def)
qed
qed
end

```

### 6.5.3 Single Instructions

In this subsection we prove for each single (welltyped) instruction that the state after execution of the instruction still conforms. Since we have already handled raised exceptions above, we can now assume that no exception has been raised in this step.

**context** *JVM-conf-read* **begin**

**declare** *defs1 [simp]*

**lemma** *Invoke-correct*:

```

fixes  $\sigma' :: ('addr, 'heap) \text{jvm-state}$ 
assumes wtprog:  $wf\text{-jvm-prog}_{\Phi} P$ 
assumes meth-C:  $P \vdash C \text{ sees } M:Ts \rightarrow T = [(mxs, mxl_0, ins, xt)] \text{ in } C$ 
assumes ins:  $ins ! pc = \text{Invoke } M' n$ 
assumes wti:  $P, T, mxs, \text{size ins}, xt \vdash ins!pc, pc :: \Phi C M$ 
assumes approx:  $\Phi \vdash t: (None, h, (stk, loc, C, M, pc) \# frs) \checkmark$ 
assumes exec:  $(tas, \sigma) \in \text{exec-instr } (ins!pc) P t h stk loc C M pc frs$ 
shows  $\Phi \vdash t:\sigma \checkmark$ 

```

**proof** –

**note** *split-paired-Ex [simp del]*

```

from wtprog obtain wfmb where wfprog:  $wf\text{-prog } wfmb P$ 
  by (simp add: wf-jvm-prog-phi-def)

```

**from** *ins meth-C approx* **obtain**  $ST LT$  **where**

```

  heap-ok:  $hconf h$  and
  tconf:  $P, h \vdash t \checkmark$  and
   $\Phi\text{-}pc$ :  $\Phi C M!pc = \text{Some } (ST, LT)$  and

```

*frame*:  $\text{conf-f } P \ h \ (ST, LT) \ \text{ins} \ (stk, loc, C, M, pc) \ \text{and}$   
*frames*:  $\text{conf-fs } P \ h \ \Phi \ M \ (\text{size } Ts) \ T \ \text{frs} \ \text{and}$   
*preh*:  $\text{preallocated } h$   
**by** (*fastforce dest: sees-method-fun*)

**from** *ins wti*  $\Phi\text{-pc}$   
**have**  $n: n < \text{size } ST$  **by** *simp*

**show** *?thesis*

**proof**(*cases*  $stk!n = \text{Null}$ )

**case** *True*

**with** *ins heap-ok*  $\Phi\text{-pc}$  *frame frames exec meth-C tconf preh* **show** *?thesis*  
**by**(*fastforce elim: wf-preallocatedE[OF wfprog, where C=NullPointer]*)

**next**

**case** *False*

**note**  $\text{Null} = \text{this}$

**have**  $NT: ST!n \neq NT$

**proof**

**assume**  $ST!n = NT$

**moreover from** *frame* **have**  $P, h \vdash stk [: \leq] ST$  **by** *simp*

**with**  $n$  **have**  $P, h \vdash stk!n : \leq ST!n$  **by** (*simp add: list-all2-conv-all-nth*)

**ultimately have**  $stk!n = \text{Null}$  **by** *simp*

**with**  $\text{Null}$  **show** *False* **by** *contradiction*

**qed**

**from** *frame* **obtain**

$stk: P, h \vdash stk [: \leq] ST$  **and**

$loc: P, h \vdash loc [: \leq_{\top}] LT$  **by** *simp*

**from**  $NT$  *ins wti*  $\Phi\text{-pc}$  **have**  $pc': pc+1 < \text{size } ins$  **by** *simp*

**from**  $NT$  *ins wti*  $\Phi\text{-pc}$  **obtain**  $ST' \ LT'$

**where**  $pc': pc+1 < \text{size } ins$

**and**  $\Phi': \Phi \ C \ M \ ! \ (pc+1) = \text{Some} \ (ST', \ LT')$

**and**  $LT': P \vdash LT [: \leq_{\top}] LT'$

**by**(*auto simp add: neq-Nil-conv sup-state-opt-any-Some split: if-split-asm*)

**with**  $NT$  *ins wti*  $\Phi\text{-pc}$  **obtain**  $D \ D' \ TTs \ TT \ m$

**where**  $D: \text{class-type-of}' \ (ST!n) = \lfloor D \rfloor$

**and**  $m\text{-}D: P \vdash D \ \text{sees} \ M': TTs \rightarrow TT = m \ \text{in} \ D'$

**and**  $Ts: P \vdash \text{rev} \ (\text{take } n \ ST) [: \leq] TTs$

**and**  $ST': P \vdash (TT \ \# \ \text{drop} \ (n+1) \ ST) [: \leq] ST'$

**by**(*auto*)

**from**  $n \ stk \ D$  **have**  $P, h \vdash stk!n : \leq ST \ ! \ n$

**by** (*auto simp add: list-all2-conv-all-nth*)

**from**  $\langle P, h \vdash stk!n : \leq ST \ ! \ n \rangle \ \text{Null } D$

**obtain**  $U \ a$  **where**

*Addr*:  $stk!n = \text{Addr } a$  **and**

*obj*:  $\text{typeof-addr } h \ a = \text{Some } U$  **and**

*UsubSTn*:  $P \vdash \text{ty-of-htype } U \leq ST \ ! \ n$

**by**(*cases*  $stk \ ! \ n$ )(*auto simp add: conf-def widen-Class*)

**from**  $D \ UsubSTn$  **obtain**  $C'$  **where**

$C'$ : *class-type-of'* (*ty-of-htype*  $U$ ) =  $\lfloor C' \rfloor$  and  $C'_{\text{sub}D}$ :  $P \vdash C' \preceq^* D$   
**by** (*rule widen-is-class-type-of*) *simp*

**with** *wfprog*  $m-D$   
**obtain**  $Ts' T' D'' \text{meth}'$  **where**  
 $m-C'$ :  $P \vdash C'$  *sees*  $M'$ :  $Ts' \rightarrow T' = \text{meth}'$  in  $D''$  **and**  
 $T'$ :  $P \vdash T' \leq TT$  **and**  
 $Ts'$ :  $P \vdash TTs \leq Ts'$   
**by** (*auto dest: sees-method-mono*)

**from**  $Ts \ n$  **have** [*simp*]: *size*  $TTs = n$   
**by** (*auto dest: list-all2-lengthD simp: min-def*)  
**with**  $Ts'$  **have** [*simp*]: *size*  $Ts' = n$   
**by** (*auto dest: list-all2-lengthD*)

**from**  $m-C'$  *wfprog*  
**obtain**  $mD''$ :  $P \vdash D''$  *sees*  $M'$ :  $Ts' \rightarrow T' = \text{meth}'$  in  $D''$   
**by** (*fast dest: sees-method-idemp*)

{ **fix**  $mxs' \ mxl' \ ins' \ xt'$   
**assume** [*simp*]:  $\text{meth}' = \lfloor (mxs', mxl', ins', xt') \rfloor$   
**let**  $?loc' = \text{Addr } a \ \# \ \text{rev } (take \ n \ stk) \ @ \ replicate \ mxl' \ \text{undefined-value}$   
**let**  $?f' = (\ [], \ ?loc', \ D'', \ M', \ 0)$   
**let**  $?f = (stk, loc, C, M, pc)$

**from** *Addr obj*  $m-C'$  *ins* *meth-C* *exec*  $C'$  *False*  
**have**  $s': \sigma = (None, h, ?f' \ \# \ ?f \ \# \ frs)$  **by** (*auto split: if-split-asm*)

**moreover**

**from** *wtprog*  $mD''$

**obtain** *start*: *wt-start*  $P \ D'' \ Ts' \ mxl' \ (\Phi \ D'' \ M')$  **and**  $ins': ins' \neq []$   
**by** (*auto dest: wt-jvm-prog-impl-wt-start*)

**then obtain**  $LT_0$  **where**  $LT_0$ :  $\Phi \ D'' \ M' ! 0 = \text{Some } ([], LT_0)$

**by** (*clarsimp simp add: wt-start-def defs1 sup-state-opt-any-Some*)

**moreover**

**have** *conf-f*  $P \ h \ ([], LT_0) \ ins' \ ?f'$

**proof** –

**let**  $?LT = OK \ (Class \ D'') \ \# \ (map \ OK \ Ts') \ @ \ (replicate \ mxl' \ Err)$

**from** *stk* **have**  $P, h \vdash take \ n \ stk \ [:\leq] \ take \ n \ ST \ ..$

**hence**  $P, h \vdash rev \ (take \ n \ stk) \ [:\leq] \ rev \ (take \ n \ ST)$  **by** *simp*

**also note**  $Ts$  **also note**  $Ts'$  **finally**

**have**  $P, h \vdash rev \ (take \ n \ stk) \ [:\leq_{\top}] \ map \ OK \ Ts'$  **by** *simp*

**also**

**have**  $P, h \vdash replicate \ mxl' \ \text{undefined-value} \ [:\leq_{\top}] \ replicate \ mxl' \ Err$  **by** *simp*

**also from**  $m-C'$  **have**  $P \vdash C' \preceq^* D''$  **by** (*rule sees-method-decl-above*)

**from** *obj heap-ok* **have** *is-htype*  $P \ U$  **by** (*rule typeof-addr-is-type*)

**with**  $C'$  **have**  $P \vdash ty\text{-of-htype } U \leq Class \ C'$

**by** (*cases U*) (*simp-all add: widen-array-object*)

**with**  $\langle P \vdash C' \preceq^* D'' \rangle$  *obj*  $C'$  **have**  $P, h \vdash Addr \ a \ :_{\leq} Class \ D''$

**by** (*auto simp add: conf-def intro: widen-trans*)

**ultimately**

**have**  $P, h \vdash ?loc' \ [:\leq_{\top}] \ ?LT$  **by** *simp*

**also from** *start*  $LT_0$  **have**  $P \vdash \dots \ [:\leq_{\top}] \ LT_0$  **by** (*simp add: wt-start-def*)

```

    finally have  $P, h \vdash ?loc' [:\leq_{\top}] LT_0$  .
    thus  $?thesis$  using  $ins'$  by simp
qed
ultimately have  $?thesis$  using  $s' \Phi$ -pc approx meth-C m-D  $T'$  ins  $D$  tconf  $C'$  mD''
  by (fastforce dest: sees-method-fun [of - C]) }
moreover
{ assume [simp]:  $meth' = Native$ 
  with wfprog m-C' have  $D'' \cdot M'(Ts') :: T'$  by (simp add: sees-wf-native)
  with  $C'$  m-C' have nec: is-native  $P$   $U$   $M'$  by (auto intro: is-native.intros)

  from ins n Addr obj exec m-C'  $C'$ 
  obtain va  $h'$  tas' where va:  $(tas', va, h') \in red\text{-}external\text{-}aggr$   $P$   $t$   $a$   $M'$  (rev (take n stk))  $h$ 
    and  $\sigma$ :  $\sigma = extRet2JVM$   $n$   $h'$   $stk$   $loc$   $C$   $M$   $pc$   $frs$   $va$  by (auto)
  from va nec obj have hext:  $h \sqsubseteq h'$  by (auto intro: red-external-aggr-hext)
  with frames have frames':  $conf\text{-}fs$   $P$   $h'$   $\Phi$   $M$  (length  $Ts$ )  $T$   $frs$  by (rule conf-fs-hext)
  from preh hext have preh': preallocated  $h'$  by (rule preallocated-hext)
  from va nec obj tconf have tconf':  $P, h' \vdash t \sqrt{t}$ 
    by (auto dest: red-external-aggr-preserves-tconf)
  from hext obj have obj':  $typeof\text{-}addr$   $h' a = \lfloor U \rfloor$  by (rule typeof-addr-hext-mono)

  from stk have  $P, h \vdash take\ n\ stk [:\leq]$  take  $n$   $ST$  by (rule list-all2-takeI)
  then obtain  $Us$  where  $map\ typeof_h (take\ n\ stk) = map\ Some\ Us$   $P \vdash Us [:\leq]$  take  $n$   $ST$ 
    by (auto simp add: confs-conv-map)
  hence  $Us$ :  $map\ typeof_h (rev (take\ n\ stk)) = map\ Some (rev\ Us)$   $P \vdash rev\ Us [:\leq]$  rev (take  $n$   $ST$ )
    by- (simp only: rev-map[symmetric], simp)
  from  $\langle P \vdash rev\ Us [:\leq] rev (take\ n\ ST) \rangle Ts\ Ts'$ 
  have  $P \vdash rev\ Us [:\leq] Ts'$  by (blast intro: widens-trans)
  with obj  $\langle map\ typeof_h (rev (take\ n\ stk)) = map\ Some (rev\ Us) \rangle C' m\text{-}C'$ 
  have wtext':  $P, h \vdash a \cdot M'(rev (take\ n\ stk)) : T'$  by (simp add: external-WT'.intros)
  from va have va':  $P, t \vdash \langle a \cdot M'(rev (take\ n\ stk)), h \rangle -tas' \rightarrow ext \langle va, h \rangle$ 
    by (unfold WT-red-external-list-conv[OF wfprog wtext' tconf])
  with heap-ok wtext' tconf wfprog have heap-ok': hconf  $h'$  by (auto dest: external-call-hconf)

  have  $?thesis$ 
  proof (cases va)
    case (RetExc a')
    from frame hext have conf-f  $P$   $h'$  ( $ST$ ,  $LT$ ) ins ( $stk$ ,  $loc$ ,  $C$ ,  $M$ ,  $pc$ ) by (rule conf-f-hext)
      with  $\sigma$  tconf' heap-ok' meth-C  $\Phi$ -pc frames' RetExc red-external-conf-extRet[OF wfprog va'
wtext' heap-ok preh tconf] ins preh'
      show  $?thesis$  by (fastforce simp add: conf-def widen-Class)
    next
    case RetStaySame
    from frame hext have conf-f  $P$   $h'$  ( $ST$ ,  $LT$ ) ins ( $stk$ ,  $loc$ ,  $C$ ,  $M$ ,  $pc$ ) by (rule conf-f-hext)
      with  $\sigma$  heap-ok' meth-C  $\Phi$ -pc RetStaySame frames' tconf' preh' show  $?thesis$  by fastforce
    next
    case (RetVal v)
    with  $\sigma$  have  $\sigma$ :  $\sigma = (None, h', (v \# drop\ (n+1)\ stk, loc, C, M, pc+1) \# frs)$  by simp
    from heap-ok wtext' va' RetVal preh tconf have  $P, h' \vdash v : \leq T'$ 
      by (auto dest: red-external-conf-extRet[OF wfprog])
    from stk have  $P, h \vdash drop\ (n+1)\ stk [:\leq]$  drop  $(n+1)$   $ST$  by (rule list-all2-dropI)
    hence  $P, h' \vdash drop\ (n+1)\ stk [:\leq]$  drop  $(n+1)$   $ST$  using hext by (rule confs-hext)
    with  $\langle P, h' \vdash v : \leq T' \rangle$  have  $P, h' \vdash v \# drop\ (n+1)\ stk [:\leq] T' \# drop\ (n+1)\ ST$ 
      by (auto simp add: conf-def intro: widen-trans)
    also

```

```

with  $NT$  ins wti  $\Phi$ -pc  $\Phi'$  nec False  $D$  m-D  $T'$ 
have  $P \vdash (T' \# \text{drop } (n + 1) ST) [\leq] ST'$ 
  by(auto dest: sees-method-fun intro: widen-trans)
also from loc hext have  $P, h' \vdash \text{loc } [\leq_{\top}] LT$  by(rule confTs-hext)
hence  $P, h' \vdash \text{loc } [\leq_{\top}] LT'$  using  $LT'$  by(rule confTs-widen)
ultimately show ?thesis using  $\langle h\text{conf } h' \rangle \sigma$  meth-C  $\Phi'$  pc' frames' tconf' preh' by fastforce
qed }
ultimately show ?thesis by(cases meth') auto
qed
qed

declare list-all2-Cons2 [iff]

lemma Return-correct:
  assumes wt-prog: wf-jvm-prog $_{\Phi}$   $P$ 
  assumes meth:  $P \vdash C$  sees  $M: Ts \rightarrow T = [(m\text{x}s, m\text{x}l_0, \text{ins}, \text{xt})]$  in  $C$ 
  assumes ins:  $\text{ins} ! \text{pc} = \text{Return}$ 
  assumes wt:  $P, T, m\text{x}s, \text{size } \text{ins}, \text{xt} \vdash \text{ins} ! \text{pc}, \text{pc} :: \Phi \ C \ M$ 
  assumes correct:  $\Phi \vdash t: (\text{None}, h, (\text{stk}, \text{loc}, C, M, \text{pc}) \# \text{frs}) \checkmark$ 
  assumes s':  $(\text{tas}, \sigma') \in \text{exec } P \ t \ (\text{None}, h, (\text{stk}, \text{loc}, C, M, \text{pc}) \# \text{frs})$ 
  shows  $\Phi \vdash t: \sigma' \checkmark$ 
proof –
  from wt-prog
  obtain wfmb where wf: wf-prog wfmb  $P$  by (simp add: wf-jvm-prog-phi-def)

  from meth ins s' correct
  have frs =  $[] \implies ?thesis$  by (simp add: correct-state-def)
  moreover
  { fix  $f$  frs' assume frs':  $\text{frs} = f \# \text{frs}'$ 
    moreover obtain stk' loc'  $C'$   $M'$  pc' where
       $f: f = (\text{stk}', \text{loc}', C', M', \text{pc}')$  by (cases f)
    moreover note meth ins s'
    ultimately
    have  $\sigma'$ :
       $\sigma' = (\text{None}, h, (\text{hd } \text{stk} \# (\text{drop } (1 + \text{size } Ts) \text{stk}'), \text{loc}', C', M', \text{pc}' + 1) \# \text{frs}')$ 
      (is  $\sigma' = (\text{None}, h, ?f' \# \text{frs}')$ )
    by simp

    from correct meth
    obtain  $ST \ LT$  where
      h-ok:  $h\text{conf } h$  and
      tconf:  $P, h \vdash t \ \checkmark$  and
       $\Phi$ -pc:  $\Phi \ C \ M ! \text{pc} = \text{Some } (ST, LT)$  and
      frame:  $\text{conf-f } P \ h \ (ST, LT) \ \text{ins } (\text{stk}, \text{loc}, C, M, \text{pc})$  and
      frames:  $\text{conf-fs } P \ h \ \Phi \ M \ (\text{size } Ts) \ T \ \text{frs}$  and
      preh: preallocated  $h$ 
      by (auto dest: sees-method-fun)

    from  $\Phi$ -pc ins wt
    obtain  $U \ ST_0$  where  $ST = U \# ST_0$   $P \vdash U \leq T$ 
      by (simp add: wt-instr-def app-def) blast
    with wf frame
    have hd-stk:  $P, h \vdash \text{hd } \text{stk} : \leq T$  by (auto simp add: conf-f-def)
  }

```



**from**  $f$   $frs'$  *frames*  
**obtain**  $ST' LT' Ts'' T'' m\bar{x}s' m\bar{x}l_0' ins' xt' Ts' T'$  **where**  
 $\Phi'$ :  $\Phi C' M' ! pc' = Some (ST', LT')$  **and**  
 $meth-C'$ :  $P \vdash C' sees M': Ts'' \rightarrow T'' = [(m\bar{x}s', m\bar{x}l_0', ins', xt')] in C'$  **and**  
 $ins'$ :  $ins' ! pc' = Invoke M (size Ts)$  **and**  
 $D$ :  $\exists D m D'. class\text{-}type\text{-}of' (ST' ! (size Ts)) = Some D \wedge P \vdash D sees M: Ts' \rightarrow T' = m in D'$  **and**  
 $T'$ :  $P \vdash T \leq T'$  **and**  
 $frame'$ :  $conf\text{-}f P h (ST', LT') ins' f$  **and**  
 $conf\text{-}fs$ :  $conf\text{-}fs P h \Phi M' (size Ts'') T'' frs'$   
**by** *clarsimp blast*

**from**  $f$   $frame'$  **obtain**  
 $stk'$ :  $P, h \vdash stk' [: \leq] ST'$  **and**  
 $loc'$ :  $P, h \vdash loc' [: \leq \top] LT'$  **and**  
 $pc'$ :  $pc' < size ins'$   
**by** (*simp add: conf-f-def*)

**from**  $wt\text{-}prog meth-C' pc'$   
**have**  $wti$ :  $P, T'', m\bar{x}s', size ins', xt' \vdash ins' ! pc', pc' :: \Phi C' M'$   
**by** (*rule wt-jvm-prog-impl-wt-instr*)

**obtain**  $aTs ST'' LT''$  **where**  
 $\Phi\text{-}suc$ :  $\Phi C' M' ! Suc pc' = Some (ST'', LT'')$  **and**  
 $less$ :  $P \vdash (T' \# drop (size Ts + 1) ST', LT') \leq_i (ST'', LT'')$  **and**  
 $suc\text{-}pc'$ :  $Suc pc' < size ins'$   
**using**  $ins' \Phi' D T' wti$   
**by** (*fastforce simp add: sup-state-opt-any-Some split: if-split-asm*)

**from**  $hd\text{-}stk T'$  **have**  $hd\text{-}stk'$ :  $P, h \vdash hd\ stk : \leq T' ..$

**have**  $frame''$ :  
 $conf\text{-}f P h (ST'', LT'') ins' ?f'$   
**proof** –  
**from**  $stk'$   
**have**  $P, h \vdash drop (1 + size Ts) stk' [: \leq] drop (1 + size Ts) ST' ..$   
**moreover**  
**with**  $hd\text{-}stk' less$   
**have**  $P, h \vdash hd\ stk \# drop (1 + size Ts) stk' [: \leq] ST''$  **by** *auto*  
**moreover**  
**from**  $wf loc' less$  **have**  $P, h \vdash loc' [: \leq \top] LT''$  **by** *auto*  
**moreover** **note**  $suc\text{-}pc'$   
**ultimately** **show**  $?thesis$  **by** (*simp add: conf-f-def*)  
**qed**

**with**  $\sigma' frs' f meth h\text{-}ok hd\text{-}stk \Phi\text{-}suc frames meth\text{-}C' \Phi' tconf preh$   
**have**  $?thesis$  **by** (*fastforce dest: sees-method-fun [of - C']*)  
**}**  
**ultimately**  
**show**  $?thesis$  **by** (*cases frs*) *blast+*  
**qed**

**declare** *sup-state-opt-any-Some* [*iff*]  
**declare** *not-Err-eq* [*iff*]

**lemma** *Load-correct:*

```

[[ wf-prog wt P;
   P ⊢ C sees M:Ts→T=[(mxs,mxl0,ins,xt)] in C;
   ins!pc = Load idx;
   P,T,mxs,size ins,xt ⊢ ins!pc,pc :: Φ C M;
   Φ ⊢ t:(None, h, (stk,loc,C,M,pc)#frs)√;
   (tas, σ') ∈ exec P t (None, h, (stk,loc,C,M,pc)#frs) ]]
⇒ Φ ⊢ t:σ'√
  by (fastforce dest: sees-method-fun [of - C] elim!: confTs-confT-sup)

```

**declare** [[simproc del: list-to-set-comprehension]]

**lemma** *Store-correct:*

```

[[ wf-prog wt P;
   P ⊢ C sees M:Ts→T=[(mxs,mxl0,ins,xt)] in C;
   ins!pc = Store idx;
   P,T,mxs,size ins,xt ⊢ ins!pc,pc :: Φ C M;
   Φ ⊢ t:(None, h, (stk,loc,C,M,pc)#frs)√;
   (tas, σ') ∈ exec P t (None, h, (stk,loc,C,M,pc)#frs) ]]
⇒ Φ ⊢ t:σ'√
  apply clarsimp
  apply (drule (1) sees-method-fun)
  apply clarsimp
  apply (blast intro!: list-all2-update-cong)
done

```

**lemma** *Push-correct:*

```

[[ wf-prog wt P;
   P ⊢ C sees M:Ts→T=[(mxs,mxl0,ins,xt)] in C;
   ins!pc = Push v;
   P,T,mxs,size ins,xt ⊢ ins!pc,pc :: Φ C M;
   Φ ⊢ t:(None, h, (stk,loc,C,M,pc)#frs)√;
   (tas, σ') ∈ exec P t (None, h, (stk,loc,C,M,pc)#frs) ]]
⇒ Φ ⊢ t:σ'√
  apply clarsimp
  apply (drule (1) sees-method-fun)
  apply clarsimp
  apply (blast dest: typeof-lit-conf)
done

```

**declare** [[simproc add: list-to-set-comprehension]]

**lemma** *Checkcast-correct:*

```

[[ wf-jvm-progΦ P;
   P ⊢ C sees M:Ts→T=[(mxs,mxl0,ins,xt)] in C;
   ins!pc = Checkcast D;
   P,T,mxs,size ins,xt ⊢ ins!pc,pc :: Φ C M;
   Φ ⊢ t:(None, h, (stk,loc,C,M,pc)#frs)√;
   (tas, σ) ∈ exec-instr (ins!pc) P t h stk loc C M pc frs ]]
⇒ Φ ⊢ t:σ√
using wf-preallocatedD[of λP C (M, Ts, Tr, mxs, mxl0, is, xt). wt-method P C Ts Tr mxs mxl0 is xt
(Φ C M) P h ClassCast]
apply (clarsimp simp add: wf-jvm-prog-phi-def split: if-split-asm)
apply (drule (1) sees-method-fun)

```

```

apply(fastforce simp add: conf-def intro: widen-trans)
apply (drule (1) sees-method-fun)
apply(fastforce simp add: conf-def intro: widen-trans)
done

```

**lemma** *Instanceof-correct*:

```

[[ wf-jvm-progΦ P;
  P ⊢ C sees M:Ts→T=[(mxs,mxl0,ins,xt)] in C;
  ins!pc = Instanceof Ty;
  P,T,msx,size ins,xt ⊢ ins!pc,pc :: Φ C M;
  Φ ⊢ t:(None, h, (stk,loc,C,M,pc)#frs)√;
  (tas, σ) ∈ exec-instr (ins!pc) P t h stk loc C M pc frs ]]
⇒ Φ ⊢ t:σ √
apply (clarsimp simp add: wf-jvm-prog-phi-def split: if-split-asm)
apply (drule (1) sees-method-fun)
apply fastforce
done

```

**declare** *split-paired-All* [simp del]

**end**

**lemma** *widens-Cons* [iff]:

```

P ⊢ (T # Ts) [≤] Us = (∃ z zs. Us = z # zs ∧ P ⊢ T ≤ z ∧ P ⊢ Ts [≤] zs)
by(rule list-all2-Cons1)

```

**context** *heap-conf-base* **begin**

**end**

**context** *JVM-conf-read* **begin**

**lemma** *Getfield-correct*:

```

assumes wf: wf-prog wt P
assumes mC: P ⊢ C sees M:Ts→T=[(mxs,mxl0,ins,xt)] in C
assumes i: ins!pc = Getfield F D
assumes wt: P,T,msx,size ins,xt ⊢ ins!pc,pc :: Φ C M
assumes cf: Φ ⊢ t:(None, h, (stk,loc,C,M,pc)#frs)√
assumes xc: (tas, σ') ∈ exec-instr (ins!pc) P t h stk loc C M pc frs

```

**shows** Φ ⊢ t:σ'√

**proof** –

```

from mC cf obtain ST LT where
  h√: hconf h and
  tconf: P,h ⊢ t √t and
  Φ: Φ C M ! pc = Some (ST,LT) and
  stk: P,h ⊢ stk [≤] ST and loc: P,h ⊢ loc [≤τ] LT and
  pc: pc < size ins and
  fs: conf-fs P h Φ M (size Ts) T frs and
  preh: preallocated h
by (fastforce dest: sees-method-fun)

```

**from** i Φ wt **obtain** oT ST'' vT ST' LT' vT' fm **where**

```

oT: P ⊢ oT ≤ Class D and
ST: ST = oT # ST'' and
F: P ⊢ D sees F:vT (fm) in D and
pc': pc+1 < size ins and
Φ': Φ C M ! (pc+1) = Some (vT'#ST', LT') and
ST': P ⊢ ST'' [≤] ST' and LT': P ⊢ LT [≤⊤] LT' and
vT': P ⊢ vT ≤ vT'
by fastforce

from stk ST obtain ref stk' where
  stk': stk = ref # stk' and
  ref: P, h ⊢ ref :≤ oT and
  ST'': P, h ⊢ stk' [≤] ST''
by auto

show ?thesis
proof(cases ref = Null)
  case True
  with tconf h√ i xc stk' mC fs Φ ST'' ref ST loc pc'
    wf-preallocatedD[OF wf, of h NullPointer] preh
  show ?thesis by(fastforce)
next
  case False
  from ref oT have P, h ⊢ ref :≤ Class D ..
  with False obtain a U' D' where a: ref = Addr a
    and h: typeof-addr h a = Some U'
    and U': D' = class-type-of U' and D': P ⊢ D' ≤* D
    by (blast dest: non-npD2)

  { fix v
    assume read: heap-read h a (CField D F) v
    from D' F have has-field: P ⊢ D' has F:vT (fm) in D
      by (blast intro: has-field-mono has-visible-field)
    with h have P, h ⊢ a@CField D F : vT unfolding U' ..
    with read have v: P, h ⊢ v :≤ vT using h√
      by(rule heap-read-conf)

    from ST'' ST' have P, h ⊢ stk' [≤] ST' ..
    moreover
    from v vT' have P, h ⊢ v :≤ vT' by blast
    moreover
    from loc LT' have P, h ⊢ loc [≤⊤] LT' ..
    moreover
    note h√ mC Φ' pc' v fs tconf preh
    ultimately have Φ ⊢ t:(None, h, (v#stk', loc, C, M, pc+1)#frs) √ by fastforce }
  with a h i mC stk' xc
  show ?thesis by auto
qed
qed

lemma Putfield-correct:
  assumes wf: wf-prog wt P
  assumes mC: P ⊢ C sees M:Ts→T=[(mxs,mxl0,ins,xt)] in C
  assumes i: ins!pc = Putfield F D

```

**assumes**  $wt: P, T, mcs, size\ ins, xt \vdash ins!pc, pc :: \Phi\ C\ M$   
**assumes**  $cf: \Phi \vdash t: (None, h, (stk, loc, C, M, pc) \# frs) \checkmark$   
**assumes**  $xc: (tas, \sigma') \in exec-instr\ (ins!pc)\ P\ t\ h\ stk\ loc\ C\ M\ pc\ frs$   
**shows**  $\Phi \vdash t: \sigma' \checkmark$

**proof** –

**from**  $mC\ cf$  **obtain**  $ST\ LT$  **where**  
 $h\checkmark: hconf\ h$  **and**  
 $tconf: P, h \vdash t \checkmark$  **and**  
 $\Phi: \Phi\ C\ M\ !\ pc = Some\ (ST, LT)$  **and**  
 $stk: P, h \vdash stk [\leq] ST$  **and**  $loc: P, h \vdash loc [\leq_{\top}] LT$  **and**  
 $pc: pc < size\ ins$  **and**  
 $fs: conf-fs\ P\ h\ \Phi\ M\ (size\ Ts)\ T\ frs$  **and**  
 $preh: preallocated\ h$   
**by**  $(fastforce\ dest: sees-method-fun)$

**from**  $i\ \Phi\ wt$  **obtain**  $vT\ vT'\ oT\ ST''\ ST'\ LT'\ fm$  **where**  
 $ST: ST = vT \# oT \# ST''$  **and**  
 $field: P \vdash D\ sees\ F: vT'\ (fm)\ in\ D$  **and**  
 $oT: P \vdash oT \leq Class\ D$  **and**  $vT: P \vdash vT \leq vT'$  **and**  
 $pc': pc+1 < size\ ins$  **and**  
 $\Phi': \Phi\ C\ M!(pc+1) = Some\ (ST', LT')$  **and**  
 $ST': P \vdash ST'' [\leq] ST'$  **and**  $LT': P \vdash LT [\leq_{\top}] LT'$   
**by**  $clarsimp$

**from**  $stk\ ST$  **obtain**  $v\ ref\ stk'$  **where**  
 $stk': stk = v \# ref \# stk'$  **and**  
 $v: P, h \vdash v \leq vT$  **and**  
 $ref: P, h \vdash ref \leq oT$  **and**  
 $ST'': P, h \vdash stk' [\leq] ST''$   
**by**  $auto$

**show**  $?thesis$

**proof**  $(cases\ ref = Null)$

**case**  $True$

**with**  $tconf\ h\checkmark\ i\ xc\ stk'\ mC\ fs\ \Phi\ ST''\ ref\ ST\ loc\ pc'\ v$   
 $wf-preallocatedD[OF\ wf,\ of\ h\ NullPointer]\ preh$   
**show**  $?thesis$  **by**  $(fastforce)$

**next**

**case**  $False$

**from**  $ref\ oT$  **have**  $P, h \vdash ref \leq Class\ D \dots$

**with**  $False$  **obtain**  $a\ U'\ D'$  **where**

$a: ref = Addr\ a$  **and**  $h: typeof-addr\ h\ a = Some\ U'$   
**and**  $U': D' = class-type-of\ U'$  **and**  $D': P \vdash D' \preceq^* D$   
**by**  $(blast\ dest: non-npD2)$

**from**  $v\ vT$  **have**  $vT': P, h \vdash v \leq vT' \dots$

**from**  $field\ D'$  **have**  $has-field: P \vdash D'\ has\ F: vT'\ (fm)\ in\ D$

**by**  $(blast\ intro: has-field-mono\ has-visible-field)$

**with**  $h$  **have**  $al: P, h \vdash a @ CField\ D\ F : vT'\ unfolding\ U' \dots$

**let**  $?f' = (stk', loc, C, M, pc+1)$

**{** **fix**  $h'$

**assume**  $write: heap-write\ h\ a\ (CField\ D\ F)\ v\ h'$

hence  $hext: h \sqsubseteq h'$  **by** (rule  $hext\text{-heap}\text{-write}$ )  
 with  $preh$  **have**  $preallocated\ h'$  **by** (rule  $preallocated\text{-}hext$ )  
**moreover**  
 from  $write\ h\sqrt{\quad} al\ vT'$  **have**  $hconf\ h'$  **by** (rule  $hconf\text{-heap}\text{-write}\text{-mono}$ )  
**moreover**  
 from  $ST''\ ST'$  **have**  $P, h \vdash stk' [\leq] ST' ..$   
 from  $this\ hext$  **have**  $P, h' \vdash stk' [\leq] ST'$  **by** (rule  $confs\text{-}hext$ )  
**moreover**  
 from  $loc\ LT'$  **have**  $P, h \vdash loc [\leq_{\top}] LT' ..$   
 from  $this\ hext$  **have**  $P, h' \vdash loc [\leq_{\top}] LT'$  **by** (rule  $confTs\text{-}hext$ )  
**moreover**  
 from  $fs\ hext$   
 have  $conf\text{-}fs\ P\ h'\ \Phi\ M\ (size\ Ts)\ T\ frs$  **by** (rule  $conf\text{-}fs\text{-}hext$ )  
**moreover**  
 note  $mC\ \Phi'\ pc'$   
**moreover**  
 from  $tconf\ hext$  **have**  $P, h' \vdash t\sqrt{\quad} t$  **by** (rule  $tconf\text{-}hext\text{-mono}$ )  
 ultimately **have**  $\Phi \vdash t: (None, h', ?f'\#frs)\sqrt{\quad}$  **by**  $fastforce\}$   
 with  $a\ h\ i\ mC\ stk'\ xc$  **show**  $?thesis$  **by** (auto  $simp\ del: correct\text{-}state\text{-}def$ )  
 qed  
 qed

**lemma**  $CAS\text{-correct}$ :

assumes  $wf: wf\text{-}prog\ wt\ P$   
 assumes  $mC: P \vdash C\ sees\ M: Ts \rightarrow T = [(maxs, mxl_0, ins, xt)]\ in\ C$   
 assumes  $i: ins!pc = CAS\ F\ D$   
 assumes  $wt: P, T, maxs, size\ ins, xt \vdash ins!pc, pc :: \Phi\ C\ M$   
 assumes  $cf: \Phi \vdash t: (None, h, (stk, loc, C, M, pc)\#frs)\sqrt{\quad}$   
 assumes  $xc: (tas, \sigma') \in exec\text{-}instr\ (ins!pc)\ P\ t\ h\ stk\ loc\ C\ M\ pc\ frs$   
 shows  $\Phi \vdash t: \sigma'\sqrt{\quad}$

**proof** –

from  $mC\ cf$  **obtain**  $ST\ LT$  **where**  
 $h\sqrt{\quad}: hconf\ h$  **and**  
 $tconf: P, h \vdash t\sqrt{\quad} t$  **and**  
 $\Phi: \Phi\ C\ M\ !\ pc = Some\ (ST, LT)$  **and**  
 $stk: P, h \vdash stk [\leq] ST$  **and**  $loc: P, h \vdash loc [\leq_{\top}] LT$  **and**  
 $pc: pc < size\ ins$  **and**  
 $fs: conf\text{-}fs\ P\ h\ \Phi\ M\ (size\ Ts)\ T\ frs$  **and**  
 $preh: preallocated\ h$   
**by** ( $fastforce\ dest: sees\text{-}method\text{-}fun$ )

from  $i\ \Phi\ wt$  **obtain**  $T1\ T2\ T3\ T'\ ST''\ ST'\ LT'\ fm$  **where**  
 $ST: ST = T3\ \#\ T2\ \#\ T1\ \#\ ST''$  **and**  
 $field: P \vdash D\ sees\ F: T' (fm)\ in\ D$  **and**  
 $oT: P \vdash T1 \leq Class\ D$  **and**  $T2: P \vdash T2 \leq T'$  **and**  $T3: P \vdash T3 \leq T'$  **and**  
 $pc': pc+1 < size\ ins$  **and**  
 $\Phi': \Phi\ C\ M!(pc+1) = Some\ (Boolean\ \#\ ST', LT')$  **and**  
 $ST': P \vdash ST'' [\leq] ST'$  **and**  $LT': P \vdash LT [\leq_{\top}] LT'$   
**by**  $clarsimp$

from  $stk\ ST$  **obtain**  $v''\ v'\ v\ stk'$  **where**  
 $stk': stk = v''\ \#\ v'\ \#\ v\ \#\ stk'$  **and**  
 $v: P, h \vdash v : \leq T1$  **and**  
 $v': P, h \vdash v' : \leq T2$  **and**

$v'': P, h \vdash v'' \leq T3$  and  
 $ST'': P, h \vdash stk' [:\leq] ST''$   
 by *auto*

**show** *?thesis*

**proof**(*cases v = Null*)

case *True*

with  $tconf\ h\checkmark\ i\ xc\ stk'\ mC\ fs\ \Phi\ ST''\ v\ ST\ loc\ pc'\ v'\ v''$   
 $wf\text{-}preallocatedD[OF\ wf,\ of\ h\ NullPointer]\ preh$

**show** *?thesis* **by**(*fastforce*)

**next**

case *False*

from  $v\ oT$  **have**  $P, h \vdash v \leq Class\ D\ ..$

with *False* **obtain**  $a\ U'\ D'$  **where**

$a: v = Addr\ a$  and  $h: typeof\text{-}addr\ h\ a = Some\ U'$

and  $U': D' = class\text{-}type\text{-}of\ U'$  and  $D': P \vdash D' \preceq^* D$

**by** (*blast dest: non-npD2*)

from  $v'\ T2$  **have**  $vT': P, h \vdash v' \leq T'\ ..$

from  $v''\ T3$  **have**  $vT'': P, h \vdash v'' \leq T'\ ..$

from *field D'* **have** *has-field:  $P \vdash D'$  has  $F:T'$  (fm) in D*

**by** (*blast intro: has-field-mono has-visible-field*)

with  $h$  **have**  $al: P, h \vdash a @ CField\ D\ F : T'$  **unfolding**  $U'\ ..$

from  $ST''\ ST'$  **have**  $stk'': P, h \vdash stk' [:\leq] ST'\ ..$

from  $loc\ LT'$  **have**  $loc': P, h \vdash loc [:\leq_{\top}] LT'\ ..$

{ **fix**  $h'$

**assume** *write: heap-write  $h\ a\ (CField\ D\ F)\ v''\ h'$*

**hence** *hext:  $h \sqsubseteq h'$*  **by**(*rule hext-heap-write*)

**with** *preh* **have** *preallocated  $h'$*  **by**(*rule preallocated-hext*)

**moreover**

from  $write\ h\checkmark\ al\ vT''$  **have**  $hconf\ h'$  **by**(*rule hconf-heap-write-mono*)

**moreover**

from  $stk''\ hext$  **have**  $P, h' \vdash stk' [:\leq] ST'$  **by** (*rule confs-hext*)

**moreover**

from  $loc'\ hext$  **have**  $P, h' \vdash loc [:\leq_{\top}] LT'$  **by** (*rule confTs-hext*)

**moreover**

from  $fs\ hext$

**have** *conf-fs  $P\ h'\ \Phi\ M\ (size\ Ts)\ T\ frs$*  **by** (*rule conf-fs-hext*)

**moreover**

**note**  $mC\ \Phi'\ pc'$

**moreover**

**let**  $?f' = (Bool\ True\ \# \ stk', loc, C, M, pc+1)$

from  $tconf\ hext$  **have**  $P, h' \vdash t\ \checkmark/t$  **by**(*rule tconf-hext-mono*)

**ultimately** **have**  $\Phi \vdash t:(None, h', ?f'\#frs)\ \checkmark$  **by** *fastforce*

} **moreover** {

**let**  $?f' = (Bool\ False\ \# \ stk', loc, C, M, pc+1)$

**have**  $\Phi \vdash t:(None, h, ?f'\#frs)\ \checkmark$  **using**  $tconf\ h\checkmark\ preh\ mC\ \Phi'\ stk''\ loc'\ pc'\ fs$

**by** *fastforce*

} **ultimately** **show** *?thesis* **using**  $a\ h\ i\ mC\ stk'\ xc$  **by**(*auto simp del: correct-state-def*)

**qed**

**qed**

**lemma** *New-correct*:

**assumes** *wf*:  $wf\text{-prog } wt \ P$   
**assumes** *meth*:  $P \vdash C \text{ sees } M:Ts \rightarrow T = [(mxs, mxl_0, ins, xt)] \text{ in } C$   
**assumes** *ins*:  $ins!pc = New \ X$   
**assumes** *wt*:  $P, T, mxs, size \ ins, xt \vdash ins!pc, pc :: \Phi \ C \ M$   
**assumes** *conf*:  $\Phi \vdash t:(None, h, (stk, loc, C, M, pc) \# frs) \checkmark$   
**assumes** *no-x*:  $(tas, \sigma) \in exec\text{-instr } (ins!pc) \ P \ t \ h \ stk \ loc \ C \ M \ pc \ frs$   
**shows**  $\Phi \vdash t:\sigma \checkmark$

**proof** –

**from** *ins conf meth*  
**obtain** *ST LT* **where**  
*heap-ok*:  $hconf \ h$  **and**  
*tconf*:  $P, h \vdash t \checkmark$  **and**  
*Φ-pc*:  $\Phi \ C \ M!pc = Some \ (ST, LT)$  **and**  
*frame*:  $conf\text{-f } P \ h \ (ST, LT) \ ins \ (stk, loc, C, M, pc)$  **and**  
*frames*:  $conf\text{-fs } P \ h \ \Phi \ M \ (size \ Ts) \ T \ frs$  **and**  
*preh*:  $preallocated \ h$   
**by** (*auto dest: sees-method-fun*)

**from** *Φ-pc ins wt*

**obtain** *ST' LT'* **where**  
*is-class-X*:  $is\text{-class } P \ X$  **and**  
*mxs*:  $size \ ST < mxs$  **and**  
*suc-pc*:  $pc+1 < size \ ins$  **and**  
*Φ-suc*:  $\Phi \ C \ M!(pc+1) = Some \ (ST', LT')$  **and**  
*less*:  $P \vdash (Class \ X \ \# \ ST, LT) \leq_i \ (ST', LT')$   
**by** *auto*

**show** *?thesis*

**proof**(*cases allocate h (Class-type X) = {}*)

**case** *True*

**with** *frame frames tconf suc-pc no-x ins meth Φ-pc*  
*wf-preallocatedD[OF wf, of h OutOfMemory] preh is-class-X heap-ok*  
**show** *?thesis*  
**by**(*fastforce intro: tconf-hext-mono confs-hext confTs-hext conf-fs-hext*)

**next**

**case** *False*

**with** *ins meth no-x* **obtain** *h' oref*  
**where** *new*:  $(h', oref) \in allocate \ h \ (Class\text{-type } X)$   
**and**  $\sigma': \sigma = (None, h', (Addr \ oref \ \# \ stk, loc, C, M, pc+1) \ \# \ frs)$  (**is**  $\sigma = (None, h', ?f \ \# \ frs)$ )  
**by** *auto*

**from** *new* **have** *hext*:  $h \sqsubseteq h'$  **by**(*rule hext-allocate*)

**with** *preh* **have** *preh'*:  $preallocated \ h'$  **by**(*rule preallocated-hext*)

**from** *new heap-ok is-class-X* **have** *heap-ok'*:  $hconf \ h'$

**by**(*auto intro: hconf-allocate-mono*)

**with** *new is-class-X* **have** *h'*:  $typeof\text{-addr } h' \ oref = [Class\text{-type } X]$  **by**(*auto dest: allocate-SomeD*)

**note** *heap-ok' σ'*

**moreover**

**from** *frame less suc-pc wf h' hext*

**have** *conf-f P h' (ST', LT') ins ?f*

**apply** (*clarsimp simp add: fun-upd-apply conf-def split-beta*)

**apply** (*auto intro: confs-hext confTs-hext*)



```

done
moreover
from frames hext have conf-fs P h'  $\Phi$  M (size Ts) T frs by (rule conf-fs-hext)
moreover from tconf hext have P, h'  $\vdash$  t  $\sqrt{t}$  by (rule tconf-hext-mono)
ultimately
show ?thesis using meth  $\Phi$ -suc preh' by fastforce
qed
qed

```

**lemma** *Goto-correct*:

```

[[ wf-prog wt P;
  P  $\vdash$  C sees M:Ts $\rightarrow$ T= $\lfloor$ (mxs,mxl0,ins,xt) $\rfloor$  in C;
  ins ! pc = Goto branch;
  P, T, mxs, size ins, xt  $\vdash$  ins!pc, pc ::  $\Phi$  C M;
   $\Phi \vdash$  t:(None, h, (stk, loc, C, M, pc)#frs) $\sqrt{}$ ;
  (tas,  $\sigma'$ )  $\in$  exec P t (None, h, (stk, loc, C, M, pc)#frs) ]]
 $\Rightarrow$   $\Phi \vdash$  t: $\sigma' \sqrt{}$ 
apply clarsimp
apply (drule (1) sees-method-fun)
apply fastforce
done

```

**declare** [[simproc del: list-to-set-comprehension]]

**lemma** *IfFalse-correct*:

```

[[ wf-prog wt P;
  P  $\vdash$  C sees M:Ts $\rightarrow$ T= $\lfloor$ (mxs,mxl0,ins,xt) $\rfloor$  in C;
  ins ! pc = IfFalse branch;
  P, T, mxs, size ins, xt  $\vdash$  ins!pc, pc ::  $\Phi$  C M;
   $\Phi \vdash$  t:(None, h, (stk, loc, C, M, pc)#frs) $\sqrt{}$ ;
  (tas,  $\sigma'$ )  $\in$  exec P t (None, h, (stk, loc, C, M, pc)#frs) ]]
 $\Rightarrow$   $\Phi \vdash$  t: $\sigma' \sqrt{}$ 
apply clarsimp
apply (drule (1) sees-method-fun)
apply fastforce
done

```

**declare** [[simproc add: list-to-set-comprehension]]

**lemma** *BinOp-correct*:

```

[[ wf-prog wt P;
  P  $\vdash$  C sees M:Ts $\rightarrow$ T= $\lfloor$ (mxs,mxl0,ins,xt) $\rfloor$  in C;
  ins ! pc = BinOpInstr bop;
  P, T, mxs, size ins, xt  $\vdash$  ins!pc, pc ::  $\Phi$  C M;
   $\Phi \vdash$  t:(None, h, (stk, loc, C, M, pc)#frs) $\sqrt{}$ ;
  (tas,  $\sigma'$ )  $\in$  exec P t (None, h, (stk, loc, C, M, pc)#frs) ]]
 $\Rightarrow$   $\Phi \vdash$  t: $\sigma' \sqrt{}$ 
apply clarsimp
apply (drule (1) sees-method-fun)
apply (clarsimp simp add: conf-def)
apply (drule (2) WTrt-binop-widen-mono)
apply clarsimp
apply (frule (2) binop-progress)
apply (clarsimp split: sum.split-asm)

```

```

apply(frule (5) binop-type)
apply(fastforce intro: widen-trans simp add: conf-def)
apply(frule (5) binop-type)
apply(clarsimp simp add: conf-def)
apply(clarsimp simp add: widen-Class)
apply(fastforce intro: widen-trans dest: binop-relevant-class simp add: cname-of-def conf-def)
done

```

**lemma** *Pop-correct:*

```

[[ wf-prog wt P;
  P ⊢ C sees M:Ts→T=[(mxs,mxl0,ins,xt)] in C;
  ins ! pc = Pop;
  P,T,mxs,size ins,xt ⊢ ins!pc,pc :: Φ C M;
  Φ ⊢ t:(None, h, (stk,loc,C,M,pc)#frs)√;
  (tas, σ') ∈ exec P t (None, h, (stk,loc,C,M,pc)#frs) ]]
⇒ Φ ⊢ t:σ'√
apply clarsimp
apply (drule (1) sees-method-fun)
apply fastforce
done

```

**lemma** *Dup-correct:*

```

[[ wf-prog wt P;
  P ⊢ C sees M:Ts→T=[(mxs,mxl0,ins,xt)] in C;
  ins ! pc = Dup;
  P,T,mxs,size ins,xt ⊢ ins!pc,pc :: Φ C M;
  Φ ⊢ t:(None, h, (stk,loc,C,M,pc)#frs)√;
  (tas, σ') ∈ exec P t (None, h, (stk,loc,C,M,pc)#frs) ]]
⇒ Φ ⊢ t:σ'√
apply clarsimp
apply (drule (1) sees-method-fun)
apply fastforce
done

```

**lemma** *Swap-correct:*

```

[[ wf-prog wt P;
  P ⊢ C sees M:Ts→T=[(mxs,mxl0,ins,xt)] in C;
  ins ! pc = Swap;
  P,T,mxs,size ins,xt ⊢ ins!pc,pc :: Φ C M;
  Φ ⊢ t:(None, h, (stk,loc,C,M,pc)#frs)√;
  (tas, σ') ∈ exec P t (None, h, (stk,loc,C,M,pc)#frs) ]]
⇒ Φ ⊢ t:σ'√
apply clarsimp
apply (drule (1) sees-method-fun)
apply fastforce
done

```

**declare** [[simproc del: list-to-set-comprehension]]

**lemma** *Throw-correct:*

```

[[ wf-prog wt P;
  P ⊢ C sees M:Ts→T=[(mxs,mxl0,ins,xt)] in C;
  ins ! pc = ThrowExc;
  P,T,mxs,size ins,xt ⊢ ins!pc,pc :: Φ C M;

```

```

 $\Phi \vdash t:(None, h, (stk, loc, C, M, pc) \# frs) \checkmark;$ 
 $(tas, \sigma') \in exec\_instr (ins!pc) P t h stk loc C M pc frs \parallel$ 
 $\implies \Phi \vdash t:\sigma' \checkmark$ 
using wf-preallocatedD[of wt P h NullPointer]
apply(clarsimp)
apply(drule (1) sees-method-fun)
apply(auto)
  apply fastforce
  apply fastforce
apply(drule (1) non-npD)
apply fastforce+
done

declare [[simproc add: list-to-set-comprehension]]

lemma NewArray-correct:
  assumes wf: wf-prog wt P
  assumes meth:  $P \vdash C \text{ sees } M:Ts \rightarrow T = [(mxs, mxl_0, ins, xt)] \text{ in } C$ 
  assumes ins:  $ins!pc = NewArray X$ 
  assumes wt:  $P, T, mxs, size \ ins, xt \vdash ins!pc, pc :: \Phi \ C \ M$ 
  assumes conf:  $\Phi \vdash t:(None, h, (stk, loc, C, M, pc) \# frs) \checkmark$ 
  assumes no-x:  $(tas, \sigma) \in exec\_instr (ins!pc) P t h stk loc C M pc frs$ 
  shows  $\Phi \vdash t:\sigma \checkmark$ 
proof –
  from ins conf meth
  obtain ST LT where
    heap-ok: hconf h and
    tconf:  $P, h \vdash t \checkmark$  and
     $\Phi\text{-pc}$ :  $\Phi \ C \ M!pc = Some (ST, LT)$  and
    stk:  $P, h \vdash stk [\leq] ST$  and loc:  $P, h \vdash loc [\leq_{\top}] LT$  and
    pc:  $pc < size \ ins$  and
    frame: conf-f P h (ST, LT) ins (stk, loc, C, M, pc) and
    frames: conf-fs P h  $\Phi \ M (size \ Ts) T frs$  and
    preh: preallocated h
  by (auto dest: sees-method-fun)

from ins  $\Phi\text{-pc}$  wt obtain ST'' X' ST' LT' where
  ST:  $ST = Integer \# ST''$  and
  pc':  $pc+1 < size \ ins$  and
   $\Phi'$ :  $\Phi \ C \ M! (pc+1) = Some (X' \# ST', LT')$  and
  ST':  $P \vdash ST'' [\leq] ST'$  and LT':  $P \vdash LT [\leq_{\top}] LT'$  and
  XX':  $P \vdash X[] \leq X'$  and
  suc-pc:  $pc+1 < size \ ins$  and
  is-type-X: is-type P (X[])
  by(fastforce dest: Array-widen)

from stk ST obtain si stk' where si:  $stk = Intg \ si \# stk'$ 
  by(auto simp add: conf-def)

show ?thesis
proof(cases si < s 0  $\vee$  allocate h (Array-type X (nat (sint si))) = {})
  case True
  with frame frames tconf heap-ok suc-pc no-x ins meth  $\Phi\text{-pc}$  si preh
    wf-preallocatedD[OF wf, of h OutOfMemory] wf-preallocatedD[OF wf, of h NegativeArraySize]

```

```

show ?thesis
  by(fastforce intro: tconf-hext-mono confs-hext confTs-hext conf-fs-hext split: if-split-asm)+
next
case False
with ins meth si no-x obtain h' oref
  where new: (h', oref) ∈ allocate h (Array-type X (nat (sint si)))
  and σ': σ = (None, h', (Addr oref#tl stk,loc,C,M,pc+1)#frs) (is σ = (None, h', ?f # frs))
  by(auto split: if-split-asm)
from new have hext: h ≤ h' by(rule hext-allocate)
with preh have preh': preallocated h' by(rule preallocated-hext)
from new heap-ok is-type-X have heap-ok': hconf h' by(auto intro: hconf-allocate-mono)
from False have si': 0 ≤s si by auto
with new is-type-X have h': typeof-addr h' oref = [Array-type X (nat (sint si))]
  by(auto dest: allocate-SomeD)

note σ' heap-ok'
moreover
from frame ST' ST LT' suc-pc wf XX' h' hext
have conf-f P h' (X' # ST', LT') ins ?f
  by(clarsimp simp add: fun-upd-apply conf-def split-beta)(auto intro: confs-hext confTs-hext)
moreover
from frames hext have conf-fs P h' Φ M (size Ts) T frs by (rule conf-fs-hext)
moreover from tconf hext have P,h' ⊢ t √t by(rule tconf-hext-mono)
ultimately
show ?thesis using meth Φ' preh' by fastforce
qed
qed

lemma ALoad-correct:
  assumes wf: wf-prog wt P
  assumes meth: P ⊢ C sees M:Ts→T=[(mxs,mxl0,ins,xt)] in C
  assumes ins: ins!pc = ALoad
  assumes wt: P,T,mxs,size ins,xt ⊢ ins!pc,pc :: Φ C M
  assumes conf: Φ ⊢ t: (None, h, (stk,loc,C,M,pc)#frs)√
  assumes no-x: (tas, σ) ∈ exec-instr (ins!pc) P t h stk loc C M pc frs
  shows Φ ⊢ t:σ √
proof –
  from ins conf meth
  obtain ST LT where
    heap-ok: hconf h and
    tconf: P,h ⊢ t √t and
    Φ-pc: Φ C M!pc = Some (ST,LT) and
    stk: P,h ⊢ stk [:≤] ST and loc: P,h ⊢ loc [:≤⊤] LT and
    pc: pc < size ins and
    frame: conf-f P h (ST,LT) ins (stk,loc,C,M,pc) and
    frames: conf-fs P h Φ M (size Ts) T frs and
    preh: preallocated h
  by (auto dest: sees-method-fun)

from ins wt Φ-pc have lST: length ST > 1 by(auto)

show ?thesis
proof(cases hd (tl stk) = Null)
  case True

```

**with** *ins no-x heap-ok tconf  $\Phi$ -pc stk loc frame frames meth wf-preallocatedD[OF wf, of h Null-Pointer]* *preh*

**show** *?thesis* **by**(*fastforce*)

**next**

**case** *False*

**note** *stkNN = this*

**have** *STNN: hd (tl ST)  $\neq$  NT*

**proof**

**assume** *hd (tl ST) = NT*

**moreover**

**from** *frame* **have** *P, h  $\vdash$  stk  $[:\leq]$  ST* **by** *simp*

**with** *lST* **have** *P, h  $\vdash$  hd (tl stk)  $:\leq$  hd (tl ST)*

**by** (*cases ST, auto, case-tac list, auto*)

**ultimately**

**have** *hd (tl stk) = Null* **by** *simp*

**with** *stkNN* **show** *False* **by** *contradiction*

**qed**

**with** *stkNN ins  $\Phi$ -pc wt* **obtain** *ST'' X X' ST' LT'* **where**

*ST: ST = Integer # X[] # ST''* **and**

*pc': pc+1 < size ins* **and**

*$\Phi'$ :  $\Phi$  C M ! (pc+1) = Some (X'#ST', LT')* **and**

*ST': P  $\vdash$  ST''  $[:\leq]$  ST'* **and** *LT': P  $\vdash$  LT  $[:\leq_{\top}]$  LT'* **and**

*XX': P  $\vdash$  X  $\leq$  X'* **and**

*suc-pc: pc+1 < size ins*

**by**(*fastforce*)

**from** *stk ST* **obtain** *ref idx stk'* **where**

*stk': stk = idx#ref#stk'* **and**

*idx: P, h  $\vdash$  idx  $:\leq$  Integer* **and**

*ref: P, h  $\vdash$  ref  $:\leq$  X[]* **and**

*ST'': P, h  $\vdash$  stk'  $[:\leq]$  ST''*

**by** *auto*

**from** *stkNN stk'* **have** *ref  $\neq$  Null* **by**(*simp*)

**with** *ref* **obtain** *a Xel n*

**where** *a: ref = Addr a*

**and** *ha: typeof-addr h a = [Array-type Xel n]*

**and** *Xel: P  $\vdash$  Xel  $\leq$  X*

**by**(*cases ref*)(*fastforce simp add: conf-def widen-Array*)**+**

**from** *idx* **obtain** *idxI* **where** *idxI: idx = Intg idxI*

**by**(*auto simp add: conf-def*)

**show** *?thesis*

**proof**(*cases 0  $\leq$  s idxI  $\wedge$  sint idxI < int n*)

**case** *True*

**hence** *si': 0  $\leq$  s idxI sint idxI < int n* **by** *auto*

**hence** *nat (sint idxI) < n*

**by** (*simp add: word-sle-eq nat-less-iff*)

**with** *ha* **have** *al: P, h  $\vdash$  a@ACell (nat (sint idxI)) : Xel ..*

**{ fix** *v*

**assume** *read: heap-read h a (ACell (nat (sint idxI))) v*

**hence** *v: P, h  $\vdash$  v  $:\leq$  Xel* **using** *al heap-ok* **by**(*rule heap-read-conf*)

```

let ?f = (v # stk', loc, C, M, pc + 1)

from frame ST' ST LT' suc-pc wf XX' Xel idxI si' v ST''
have conf-f P h (X' # ST', LT') ins ?f
  by (auto intro: widen-trans simp add: conf-def)
hence  $\Phi \vdash t: (None, h, ?f \# frs) \checkmark$ 
  using meth  $\Phi'$  heap-ok  $\Phi$ -pc frames tconf preh by fastforce }
with ins meth si' stk' a ha no-x idxI idx
show ?thesis by (auto simp del: correct-state-def split: if-split-asm)
next
case False
with stk' idxI ins no-x heap-ok tconf meth a ha Xel  $\Phi$ -pc frame frames
  wf-preallocatedD[OF wf, of h ArrayIndexOutOfBounds]
show ?thesis
  by (fastforce simp: preh split: if-split-asm simp del: Listn.lesub-list-impl-same-size)
qed
qed
qed

```

**lemma** AStore-correct:

```

assumes wf: wf-prog wt P
assumes meth:  $P \vdash C \text{ sees } M: Ts \rightarrow T = [(mxs, mxl_0, ins, xt)] \text{ in } C$ 
assumes ins:  $ins!pc = AStore$ 
assumes wt:  $P, T, mxs, size \ ins, xt \vdash ins!pc, pc :: \Phi \ C \ M$ 
assumes conf:  $\Phi \vdash t: (None, h, (stk, loc, C, M, pc) \# frs) \checkmark$ 
assumes no-x:  $(tas, \sigma) \in exec-instr \ (ins!pc) \ P \ t \ h \ stk \ loc \ C \ M \ pc \ frs$ 
shows  $\Phi \vdash t: \sigma \checkmark$ 

```

**proof** –

```

from ins conf meth
obtain ST LT where
  heap-ok: hconf h and
  tconf:  $P, h \vdash t \checkmark$  and
   $\Phi$ -pc:  $\Phi \ C \ M!pc = Some \ (ST, LT)$  and
  stk:  $P, h \vdash stk [: \leq] ST$  and loc:  $P, h \vdash loc [: \leq_{\top}] LT$  and
  pc:  $pc < size \ ins$  and
  frame: conf-f P h (ST, LT) ins (stk, loc, C, M, pc) and
  frames: conf-fs P h  $\Phi \ M \ (size \ Ts) \ T \ frs$  and
  preh: preallocated h
by (auto dest: sees-method-fun)

```

**from** ins wt  $\Phi$ -pc **have** lST:  $length \ ST > 2$  **by** (auto)

**show** ?thesis

**proof** (cases hd (tl (tl stk)) = Null)

**case** True

**with** ins no-x heap-ok tconf  $\Phi$ -pc stk loc frame frames meth wf-preallocatedD[OF wf, of h Null-Pointer] preh

**show** ?thesis **by** (fastforce)

**next**

**case** False

**note** stkNN = this

**have** STNN:  $hd \ (tl \ (tl \ ST)) \neq NT$

**proof**

**assume**  $hd\ (tl\ (tl\ ST)) = NT$

**moreover**

**from**  $frame$  **have**  $P, h \vdash stk\ [: \leq] ST$  **by**  $simp$

**with**  $lST$  **have**  $P, h \vdash hd\ (tl\ (tl\ stk)) : \leq hd\ (tl\ (tl\ ST))$

**by**  $(cases\ ST,\ auto,\ case-tac\ list,\ auto,\ case-tac\ lista,\ auto)$

**ultimately**

**have**  $hd\ (tl\ (tl\ stk)) = Null$  **by**  $simp$

**with**  $stkNN$  **show**  $False$  **by**  $contradiction$

**qed**

**with**  $ins\ stkNN\ \Phi\text{-}pc\ wt$  **obtain**  $ST''\ Y\ X\ ST'\ LT'$  **where**

$ST$ :  $ST = Y \# Integer \# X[] \# ST''$  **and**

$pc'$ :  $pc+1 < size\ ins$  **and**

$\Phi'$ :  $\Phi\ C\ M\ !\ (pc+1) = Some\ (ST', LT')$  **and**

$ST'$ :  $P \vdash ST'' [: \leq] ST'$  **and**  $LT'$ :  $P \vdash LT [: \leq_{\top}] LT'$  **and**

$suc\text{-}pc$ :  $pc+1 < size\ ins$

**by**  $(fastforce)$

**from**  $stk\ ST$  **obtain**  $ref\ e\ idx\ stk'$  **where**

$stk'$ :  $stk = e \# idx \# ref \# stk'$  **and**

$idx$ :  $P, h \vdash idx : \leq Integer$  **and**

$ref$ :  $P, h \vdash ref : \leq X[]$  **and**

$e$ :  $P, h \vdash e : \leq Y$  **and**

$ST''$ :  $P, h \vdash stk' [: \leq] ST''$

**by**  $auto$

**from**  $stkNN\ stk'$  **have**  $ref \neq Null$  **by**  $(simp)$

**with**  $ref$  **obtain**  $a\ Xel\ n$

**where**  $a$ :  $ref = Addr\ a$

**and**  $ha$ :  $typeof\ addr\ h\ a = \lfloor Array\text{-}type\ Xel\ n \rfloor$

**and**  $Xel$ :  $P \vdash Xel \leq X$

**by**  $(cases\ ref)(fastforce\ simp\ add:\ conf\text{-}def\ widen\text{-}Array) +$

**from**  $idx$  **obtain**  $idxI$  **where**  $idxI$ :  $idx = Intg\ idxI$

**by**  $(auto\ simp\ add:\ conf\text{-}def)$

**show**  $?thesis$

**proof**  $(cases\ 0 \leq s\ idxI \wedge sint\ idxI < int\ n)$

**case**  $True$

**hence**  $si'$ :  $0 \leq s\ idxI\ sint\ idxI < int\ n$  **by**  $simp\text{-}all$

**from**  $e$  **obtain**  $Te$  **where**  $Te$ :  $typeof_h\ e = \lfloor Te \rfloor\ P \vdash Te \leq Y$

**by**  $(auto\ simp\ add:\ conf\text{-}def)$

**show**  $?thesis$

**proof**  $(cases\ P \vdash Te \leq Xel)$

**case**  $True$

**with**  $Te$  **have**  $eXel$ :  $P, h \vdash e : \leq Xel$

**by**  $(auto\ simp\ add:\ conf\text{-}def\ intro:\ widen\text{-}trans)$

**{** **fix**  $h'$

**assume**  $write$ :  $heap\text{-}write\ h\ a\ (ACell\ (nat\ (sint\ idxI)))\ e\ h'$

**hence**  $hext$ :  $h \sqsubseteq h'$  **by**  $(rule\ hext\text{-}heap\text{-}write)$

```

with preh have preh': preallocated h' by(rule preallocated-heat)

let ?f = (stk', loc, C, M, pc + 1)

from si' have nat (sint idxI) < n
  by (simp add: word-sle-eq nat-less-iff)
with ha have P,h ⊢ a@ACell (nat (sint idxI)) : Xel ..
with write heap-ok have heap-ok': hconf h' using eXel
  by(rule hconf-heap-write-mono)
moreover
from ST stk stk' ST' have P,h ⊢ stk' [⋅] ST' by auto
with heat have stk'': P,h' ⊢ stk' [⋅] ST'
  by- (rule confs-heat)
moreover
from loc LT' have P,h ⊢ loc [⋅] LT' ..
with heat have P,h' ⊢ loc [⋅] LT' by- (rule confTs-heat)
moreover
with frame ST' ST LT' suc-pc wf Xel idxI si' stk''
have conf-f P h' (ST', LT') ins ?f
  by(clarsimp)
with frames heat have conf-fs P h' Φ M (size Ts) T frs by- (rule conf-fs-heat)
moreover from tconf heat have P,h' ⊢ t √t by(rule tconf-heat-mono)
ultimately have Φ ⊢ t:(None, h', ?f # frs) √ using meth Φ' Φ-pc suc-pc preh'
  by(fastforce) }
with True si' ins meth stk' a ha no-x idxI idx Te
show ?thesis
  by(auto split: if-split-asm simp del: correct-state-def intro: widen-trans)
next
case False
with stk' idxI ins no-x heap-ok tconf meth a ha Xel Te Φ-pc frame frames si'
  wf-preallocatedD[OF wf, of h ArrayStore]
show ?thesis
  by (fastforce split: if-splits list.splits simp: preh
      simp del: Listn.lesub-list-impl-same-size)
qed
next
case True
with stk' idxI ins no-x heap-ok tconf meth a ha Xel Φ-pc frame frames preh
  wf-preallocatedD[OF wf, of h ArrayIndexOutOfBounds]
show ?thesis by(fastforce split: if-split-asm)
qed
qed
qed

```

**lemma** *ALength-correct*:

```

assumes wf: wf-prog wt P
assumes meth: P ⊢ C sees M:Ts→T=[(mxs,mxl0,ins,xt)] in C
assumes ins: ins!pc = ALength
assumes wt: P,T,mxs,size ins,xt ⊢ ins!pc,pc :: Φ C M
assumes conf: Φ ⊢ t: (None, h, (stk,loc,C,M,pc)#frs)√
assumes no-x: (tas, σ) ∈ exec-instr (ins!pc) P t h stk loc C M pc frs
shows Φ ⊢ t: σ √

```

**proof** –

```

from ins conf meth

```



**obtain**  $ST\ LT$  **where**  
*heap-ok*:  $hconf\ h$  **and**  
*tconf*:  $P, h \vdash t \sqrt{t}$  **and**  
 $\Phi$ -*pc*:  $\Phi\ C\ M!pc = Some\ (ST, LT)$  **and**  
*stk*:  $P, h \vdash stk\ [: \leq]\ ST$  **and** *loc*:  $P, h \vdash loc\ [: \leq_{\top}]\ LT$  **and**  
*pc*:  $pc < size\ ins$  **and**  
*frame*:  $conf\text{-}f\ P\ h\ (ST, LT)\ ins\ (stk, loc, C, M, pc)$  **and**  
*frames*:  $conf\text{-}fs\ P\ h\ \Phi\ M\ (size\ Ts)\ T\ frs$  **and**  
*preh*:  $preallocated\ h$   
**by** (*auto dest: sees-method-fun*)

**from** *ins wt*  $\Phi$ -*pc* **have**  $lST$ :  $length\ ST > 0$  **by**(*auto*)

**show** *?thesis*  
**proof**(*cases hd stk = Null*)  
   **case** *True*  
     **with** *ins no-x heap-ok tconf*  $\Phi$ -*pc* *stk loc frame frames meth wf-preallocatedD*[*OF wf, of h Null-Pointer*] *preh*  
     **show** *?thesis* **by**(*fastforce*)  
   **next**  
     **case** *False*  
     **note**  $stkNN = this$   
     **have**  $STNN$ :  $hd\ ST \neq NT$   
     **proof**  
       **assume**  $hd\ ST = NT$   
       **moreover**  
       **from** *frame* **have**  $P, h \vdash stk\ [: \leq]\ ST$  **by** *simp*  
       **with**  $lST$  **have**  $P, h \vdash hd\ stk\ : \leq\ hd\ ST$   
       **by** (*cases ST, auto*)  
       **ultimately**  
       **have**  $hd\ stk = Null$  **by** *simp*  
       **with**  $stkNN$  **show** *False* **by** *contradiction*  
     **qed**

**with**  $stkNN\ ins\ \Phi$ -*pc* *wt* **obtain**  $ST''\ X\ ST'\ LT'$  **where**  
 $ST$ :  $ST = (X[]) \# ST''$  **and**  
 $pc'$ :  $pc+1 < size\ ins$  **and**  
 $\Phi'$ :  $\Phi\ C\ M! (pc+1) = Some\ (ST', LT')$  **and**  
 $ST'$ :  $P \vdash (Integer\ \# ST'')\ [: \leq]\ ST'$  **and**  $LT'$ :  $P \vdash LT\ [: \leq_{\top}]\ LT'$  **and**  
*suc-pc*:  $pc+1 < size\ ins$   
**by**(*fastforce*)

**from**  $stk\ ST$  **obtain**  $ref\ stk'$  **where**  
 $stk'$ :  $stk = ref \# stk'$  **and**  
 $ref$ :  $P, h \vdash ref\ : \leq\ X[]$  **and**  
 $ST''$ :  $P, h \vdash stk'\ [: \leq]\ ST''$   
**by** *auto*

**from**  $stkNN\ stk'$  **have**  $ref \neq Null$  **by**(*simp*)  
**with**  $ref$  **obtain**  $a\ Xel\ n$   
   **where**  $a$ :  $ref = Addr\ a$   
   **and**  $ha$ :  $typeof\ addr\ h\ a = \lfloor Array\text{-}type\ Xel\ n \rfloor$   
   **and**  $Xel$ :  $P \vdash Xel \leq X$   
   **by**(*cases ref*)(*fastforce simp add: conf-def widen-Array*)+

```

from ins meth stk' a ha no-x have  $\sigma'$ :
   $\sigma = (None, h, (Intg\ (word-of-int\ (int\ n)) \# stk', loc, C, M, pc + 1) \# frs)$ 
  (is  $\sigma = (None, h, ?f \# frs)$ )
  by(auto)
moreover
from ST stk stk' ST' have  $P, h \vdash Intg\ si \# stk' [:\leq] ST'$  by(auto)
with frame ST' ST LT' suc-pc wf
have conf-f P h (ST', LT') ins ?f
  by(fastforce intro: widen-trans)
ultimately show ?thesis using meth  $\Phi'$  heap-ok  $\Phi$ -pc frames tconf preh by fastforce
qed
qed

```

**lemma** *MEnter-correct:*

```

assumes wf: wf-prog wt P
assumes meth:  $P \vdash C\ sees\ M:Ts \rightarrow T = [(m\!xs, m\!xl_0, ins, xt)]\ in\ C$ 
assumes ins:  $ins!pc = MEnter$ 
assumes wt:  $P, T, m\!xs, size\ ins, xt \vdash ins!pc, pc :: \Phi\ C\ M$ 
assumes conf:  $\Phi \vdash t: (None, h, (stk, loc, C, M, pc) \# frs) \checkmark$ 
assumes no-x:  $(tas, \sigma) \in exec-instr\ (ins!pc)\ P\ t\ h\ stk\ loc\ C\ M\ pc\ frs$ 
shows  $\Phi \vdash t: \sigma \checkmark$ 
proof –
  from ins conf meth
obtain ST LT where
    heap-ok:  $hconf\ h$  and
    tconf:  $P, h \vdash t \sqrt{t}$  and
     $\Phi$ -pc:  $\Phi\ C\ M!pc = Some\ (ST, LT)$  and
    stk:  $P, h \vdash stk [:\leq] ST$  and loc:  $P, h \vdash loc [:\leq_{\top}] LT$  and
    pc:  $pc < size\ ins$  and
    frame:  $conf-f\ P\ h\ (ST, LT)\ ins\ (stk, loc, C, M, pc)$  and
    frames:  $conf-fs\ P\ h\ \Phi\ M\ (size\ Ts)\ T\ frs$  and
    preh: preallocated h
  by (auto dest: sees-method-fun)

```

```

from ins wt  $\Phi$ -pc have lST: length ST > 0 by(auto)

```

```

show ?thesis

```

```

proof(cases hd stk = Null)

```

```

  case True

```

```

    with ins no-x heap-ok tconf  $\Phi$ -pc stk loc frame frames meth wf-preallocatedD[OF wf, of h Null-Pointer] preh

```

```

    show ?thesis by(fastforce)

```

```

  next

```

```

    case False

```

```

    note stkNN = this

```

```

    have STNN: hd ST  $\neq$  NT

```

```

    proof

```

```

      assume hd ST = NT

```

```

      moreover

```

```

      from frame have  $P, h \vdash stk [:\leq] ST$  by simp

```

```

      with lST have  $P, h \vdash hd\ stk \leq hd\ ST$ 

```

```

      by (cases ST, auto)

```

ultimately  
 have  $hd\ stk = Null$  by *simp*  
 with *stkNN* show *False* by *contradiction*  
 qed

with *stkNN ins*  $\Phi$ -*pc wt* obtain  $ST''\ X\ ST'\ LT'$  where  
 $ST: ST = X \# ST''$  and  
 $refT: is-refT\ X$  and  
 $pc': pc+1 < size\ ins$  and  
 $\Phi': \Phi\ C\ M\ !\ (pc+1) = Some\ (ST',\ LT')$  and  
 $ST': P \vdash ST'' [\leq] ST'$  and  $LT': P \vdash LT [\leq_{\top}] LT'$  and  
 $suc-pc: pc+1 < size\ ins$   
 by(*fastforce*)

from *stk ST* obtain *ref stk'* where  
 $stk': stk = ref \# stk'$  and  
 $ref: P, h \vdash ref : \leq X$   
 by *auto*

from *stkNN stk'* have  $ref \neq Null$  by(*simp*)

moreover

from *loc LT'* have  $P, h \vdash loc [\leq_{\top}] LT' ..$

moreover

from *ST stk stk' ST'*

have  $P, h \vdash stk' [\leq] ST'$  by(*auto*)

ultimately show *?thesis* using *meth*  $\Phi'$  *heap-ok*  $\Phi$ -*pc* *suc-pc* *frames* *loc LT'* *no-x* *ins stk' ST'*

*tconf preh*

by(*fastforce*)

qed

qed

lemma *MExit-correct*:

assumes *wf*: *wf-prog wt P*

assumes *meth*:  $P \vdash C\ sees\ M: Ts \rightarrow T = [(m\ x_s, m\ x_l_0, ins, xt)]$  in *C*

assumes *ins*:  $ins!pc = MExit$

assumes *wt*:  $P, T, m\ x_s, size\ ins, xt \vdash ins!pc, pc :: \Phi\ C\ M$

assumes *conf*:  $\Phi \vdash t: (None, h, (stk, loc, C, M, pc) \# frs) \checkmark$

assumes *no-x*:  $(tas, \sigma) \in exec-instr\ (ins!pc)\ P\ t\ h\ stk\ loc\ C\ M\ pc\ frs$

shows  $\Phi \vdash t: \sigma \checkmark$

proof –

from *ins conf meth*

obtain *ST LT* where

*heap-ok*: *hconf h* and

*tconf*:  $P, h \vdash t \checkmark t$  and

$\Phi$ -*pc*:  $\Phi\ C\ M!pc = Some\ (ST, LT)$  and

*stk*:  $P, h \vdash stk [\leq] ST$  and *loc*:  $P, h \vdash loc [\leq_{\top}] LT$  and

*pc*:  $pc < size\ ins$  and

*frame*:  $conf-f\ P\ h\ (ST, LT)\ ins\ (stk, loc, C, M, pc)$  and

*frames*:  $conf-fs\ P\ h\ \Phi\ M\ (size\ Ts)\ T\ frs$  and

*preh*: *preallocated h*

by (*auto dest: sees-method-fun*)

from *ins wt*  $\Phi$ -*pc* have *lST*:  $length\ ST > 0$  by(*auto*)

```

show ?thesis
proof(cases hd stk = Null)
  case True
    with ins no-x heap-ok tconf  $\Phi$ -pc stk loc frame frames meth wf-preallocatedD[OF wf, of h Null-Pointer] preh
    show ?thesis by(fastforce)
  next
    case False
    note stkNN = this
    have STNN: hd ST  $\neq$  NT
    proof
      assume hd ST = NT
      moreover
        from frame have  $P, h \vdash \text{stk} [\leq] ST$  by simp
        with lST have  $P, h \vdash \text{hd stk} \leq \text{hd ST}$ 
          by (cases ST, auto)
        ultimately
          have hd stk = Null by simp
          with stkNN show False by contradiction
    qed

with stkNN ins  $\Phi$ -pc wt obtain ST'' X ST' LT' where
  ST: ST = X # ST'' and
  refT: is-refT X and
  pc': pc+1 < size ins and
   $\Phi'$ :  $\Phi \ C \ M \ ! \ (pc+1) = \text{Some} \ (ST', LT')$  and
  ST':  $P \vdash ST'' [\leq] ST'$  and  $LT': P \vdash LT [\leq_{\top}] LT'$  and
  suc-pc: pc+1 < size ins
  by(fastforce)

from stk ST obtain ref stk' where
  stk': stk = ref # stk' and
  ref:  $P, h \vdash \text{ref} \leq X$ 
  by auto

from stkNN stk' have ref  $\neq$  Null by(simp)
moreover
from loc LT' have  $P, h \vdash \text{loc} [\leq_{\top}] LT' ..$ 
moreover
from ST stk stk' ST'
have  $P, h \vdash \text{stk}' [\leq] ST'$  by(auto)
ultimately
show ?thesis using meth  $\Phi'$  heap-ok  $\Phi$ -pc suc-pc frames loc LT' no-x ins stk' ST' tconf frame preh
  wf-preallocatedD[OF wf, of h IllegalMonitorState]
  by(fastforce)
qed
qed

```

The next theorem collects the results of the sections above, i.e. exception handling and the execution step for each instruction. It states type safety for single step execution: in welltyped programs, a conforming state is transformed into another conforming state when one instruction is executed.

**theorem** instr-correct:  
 $\llbracket \text{wf-jvm-prog}_{\Phi} P;$

```

   $P \vdash C \text{ sees } M: Ts \rightarrow T = [(m\text{xs}, m\text{x}l_0, \text{ins}, \text{xt})]$  in  $C$ ;
   $(\text{tas}, \sigma') \in \text{exec } P \text{ } t \text{ } (\text{None}, h, (\text{stk}, \text{loc}, C, M, \text{pc}) \# \text{frs})$ ;
   $\Phi \vdash t: (\text{None}, h, (\text{stk}, \text{loc}, C, M, \text{pc}) \# \text{frs}) \checkmark$ 
 $\implies \Phi \vdash t: \sigma' \checkmark$ 
apply (subgoal-tac  $P, T, m\text{xs}, \text{size ins}, \text{xt} \vdash \text{ins!pc}, \text{pc} :: \Phi \text{ } C \text{ } M$ )
prefer 2
apply (erule wt-jvm-prog-impl-wt-instr, assumption)
apply clarsimp
apply (drule (1) sees-method-fun)
apply simp
apply (unfold exec.simps Let-def set-map)
apply (frule wt-jvm-progD, erule exE)
apply (cases ins ! pc)
apply (rule Load-correct, assumption+, fastforce)
apply (rule Store-correct, assumption+, fastforce)
apply (rule Push-correct, assumption+, fastforce)
apply (rule New-correct, assumption+, fastforce)
apply (rule NewArray-correct, assumption+, fastforce)
apply (rule ALoad-correct, assumption+, fastforce)
apply (rule AStore-correct, assumption+, fastforce)
apply (rule ALength-correct, assumption+, fastforce)
apply (rule Getfield-correct, assumption+, fastforce)
apply (rule Putfield-correct, assumption+, fastforce)
apply (rule CAS-correct, assumption+, fastforce)
apply (rule Checkcast-correct, assumption+, fastforce)
apply (rule Instanceof-correct, assumption+, fastforce)
apply (rule Invoke-correct, assumption+, fastforce)
apply (rule Return-correct, assumption+, fastforce simp add: split-beta)
apply (rule Pop-correct, assumption+, fastforce)
apply (rule Dup-correct, assumption+, fastforce)
apply (rule Swap-correct, assumption+, fastforce)
apply (rule BinOp-correct, assumption+, fastforce)
apply (rule Goto-correct, assumption+, fastforce)
apply (rule IfFalse-correct, assumption+, fastforce)
apply (rule Throw-correct, assumption+, fastforce)
apply (rule MEnter-correct, assumption+, fastforce)
apply (rule MExit-correct, assumption+, fastforce)
done

declare defs1 [simp del]

end

```

### 6.5.4 Main

```

lemma (in JVM-conf-read) BV-correct-1 [rule-format]:
 $\bigwedge \sigma. \llbracket \text{wf-jvm-prog}_\Phi P; \Phi \vdash t: \sigma \checkmark \rrbracket \implies P, t \vdash \sigma \text{ --tas-jvm--} \sigma' \longrightarrow \Phi \vdash t: \sigma' \checkmark$ 
apply (simp only: split-tupled-all exec-1-iff)
apply (rename-tac  $xp \text{ } h \text{ } \text{frs}$ )
apply (case-tac  $xp$ )
apply (case-tac  $\text{frs}$ )
apply simp
apply (simp only: split-tupled-all)
apply hypsubst

```

```

apply (frule correct-state-impl-Some-method)
apply clarify
apply (rule instr-correct)
apply assumption +
apply clarify
apply(case-tac frs)
apply simp
apply(clarsimp simp only: exec.simps set-simps)
apply(erule (1) exception-step-conform)
done

theorem (in JVM-progress) progress:
  assumes wt: wf-jvm-progΦ P
  and cs: Φ ⊢ t: (xcp, h, f # frs)✓
  shows  $\exists ta \sigma'. P, t \vdash (xcp, h, f \# frs) -ta-jvm \rightarrow \sigma'$ 
proof -
  obtain stk loc C M pc where f: f = (stk, loc, C, M, pc) by(cases f)
  with cs obtain Ts T mxs mxl0 is xt ST LT
    where hconf: hconf h
    and sees: P ⊢ C sees M: Ts → T = [(mxs, mxl0, is, xt)] in C
    and Φ-pc: Φ C M ! pc = [(ST, LT)]
    and ST: P, h ⊢ stk [:≤] ST
    and LT: P, h ⊢ loc [:≤⊤] LT
    and pc: pc < length is
    by(auto simp add: defs1)
  show ?thesis
  proof(cases xcp)
    case Some thus ?thesis
      unfolding f exec-1-iff by auto
  next
    case [simp]: None
    note [simp del] = split-paired-Ex
    note [simp] = defs1 list-all2-Cons2

  from wt obtain wf-md where wf: wf-prog wf-md P by(auto dest: wt-jvm-progD)
  from wt sees pc have wt: P, T, mxs, size is, xt ⊢ is!pc, pc :: Φ C M
    by(rule wt-jvm-prog-impl-wt-instr)

  have  $\exists ta \sigma'. (ta, \sigma') \in exec-instr (is ! pc) P t h stk loc C M pc frs$ 
  proof(cases is ! pc)
    case [simp]: ALoad
    with wt Φ-pc have lST: length ST > 1 by(auto)
    show ?thesis
    proof(cases hd (tl stk) = Null)
      case True thus ?thesis by simp
    next
      case False
      have STNN: hd (tl ST) ≠ NT
      proof
        assume hd (tl ST) = NT
        moreover
        from ST lST have P, h ⊢ hd (tl stk) :≤ hd (tl ST)

```

```

    by (cases ST)(auto, case-tac list, auto)
    ultimately have hd (tl stk) = Null by simp
    with False show False by contradiction
qed

with False  $\Phi$ -pc wt obtain ST'' X where ST = Integer # X[] # ST'' by auto
with ST obtain ref idx stk' where stk': stk = idx#ref#stk' and idx: P,h  $\vdash$  idx : $\leq$  Integer
and ref: P,h  $\vdash$  ref : $\leq$  X[] by(auto)

from False stk' have ref  $\neq$  Null by(simp)
with ref obtain a Xel n where a: ref = Addr a
and ha: typeof-addr h a = [Array-type Xel n]
and Xel: P  $\vdash$  Xel  $\leq$  X
by(cases ref)(fastforce simp add: conf-def widen-Array)+

from idx obtain idxI where idxI: idx = Intg idxI
by(auto simp add: conf-def)
show ?thesis
proof(cases 0 <=s idxI  $\wedge$  sint idxI < int n)
case True
hence si': 0 <=s idxI sint idxI < int n by auto
hence nat (sint idxI) < n
by (simp add: word-sle-eq nat-less-iff)
with ha have al: P,h  $\vdash$  a@ACell (nat (sint idxI)) : Xel ..
from heap-read-total[OF hconf this] True False ha stk' idxI a
show ?thesis by auto
next
case False with ha stk' idxI a show ?thesis by auto
qed
qed
next
case [simp]: AStore
from wt  $\Phi$ -pc have lST: length ST > 2 by(auto)

show ?thesis
proof(cases hd (tl (tl stk)) = Null)
case True thus ?thesis by(fastforce)
next
case False
note stkNN = this
have STNN: hd (tl (tl ST))  $\neq$  NT
proof
assume hd (tl (tl ST)) = NT
moreover
from ST lST have P,h  $\vdash$  hd (tl (tl stk)) : $\leq$  hd (tl (tl ST))
by (cases ST, auto, case-tac list, auto, case-tac lista, auto)
ultimately have hd (tl (tl stk)) = Null by simp
with stkNN show False by contradiction
qed

with stkNN  $\Phi$ -pc wt obtain ST'' Y X
where ST = Y # Integer # X[] # ST'' by(fastforce)

with ST obtain ref e idx stk' where stk': stk = e#idx#ref#stk'

```

```

and  $idx: P, h \vdash idx \leq Integer$  and  $ref: P, h \vdash ref \leq X[]$ 
and  $e: P, h \vdash e \leq Y$  by auto

from  $stkNN\ stk'$  have  $ref \neq Null$  by (simp)
with  $ref$  obtain  $a\ Xel\ n$  where  $a: ref = Addr\ a$ 
  and  $ha: typeof-addr\ h\ a = [Array-type\ Xel\ n]$ 
  and  $Xel: P \vdash Xel \leq X$ 
  by (cases ref) (fastforce simp add: conf-def widen-Array) +

from  $idx$  obtain  $idxI$  where  $idxI: idx = Intg\ idxI$ 
  by (auto simp add: conf-def)

show ?thesis
proof (cases  $0 \leq s\ idxI \wedge sint\ idxI < int\ n$ )
  case True
  hence  $si': 0 \leq s\ idxI\ sint\ idxI < int\ n$  by simp-all
  hence  $nat\ (sint\ idxI) < n$ 
    by (simp add: word-sle-eq nat-less-iff)
  with  $ha$  have  $adal: P, h \vdash a @ ACell\ (nat\ (sint\ idxI)) : Xel ..$ 

  show ?thesis
  proof (cases  $P \vdash the\ (typeof_h\ e) \leq Xel$ )
    case False
    with  $ha\ stk'\ idxI\ a$  show ?thesis by auto
  next
    case True
    hence  $P, h \vdash e \leq Xel$  using  $e$  by (auto simp add: conf-def)
    from heap-write-total [OF hconf adal this]  $ha\ stk'\ idxI\ a$  show ?thesis by auto
  qed
next
    case False with  $ha\ stk'\ idxI\ a$  show ?thesis by auto
  qed
qed
next
  case [simp]: (Getfield F D)

from  $\Phi-pc\ wt$  obtain  $oT\ ST''\ vT\ fm$  where  $oT: P \vdash oT \leq Class\ D$ 
  and  $ST = oT \# ST''$  and  $F: P \vdash D\ sees\ F:vT\ (fm)\ in\ D$ 
  by fastforce

with  $ST$  obtain  $ref\ stk'$  where  $stk': stk = ref \# stk'$ 
  and  $ref: P, h \vdash ref \leq oT$  by auto

show ?thesis
proof (cases  $ref = Null$ )
  case True thus ?thesis using  $stk'$  by auto
next
  case False
  from  $ref\ oT$  have  $P, h \vdash ref \leq Class\ D ..$ 
  with False obtain  $a\ U'\ D'$  where
     $a: ref = Addr\ a$  and  $h: typeof-addr\ h\ a = Some\ U'$ 
    and  $U': D' = class-type-of\ U'$  and  $D': P \vdash D' \preceq^* D$ 
    by (blast dest: non-npD2)

```



```

from  $D' F$  have has-field:  $P \vdash D'$  has  $F:vT$  (fm) in  $D$ 
  by (blast intro: has-field-mono has-visible-field)
with  $h$  have  $P, h \vdash a @ CField D F : vT$  unfolding  $U'$  ..
from heap-read-total[ $OF$  hconf this]
show ?thesis using  $stk'$   $a$  by auto
qed
next
case [simp]: (Putfield  $F D$ )

from  $\Phi$ -pc wt obtain  $vT vT' oT ST'' fm$  where  $ST = vT \# oT \# ST''$ 
  and field:  $P \vdash D$  sees  $F:vT'$  (fm) in  $D$ 
  and  $oT$ :  $P \vdash oT \leq Class D$ 
  and  $vT'$ :  $P \vdash vT \leq vT'$  by fastforce
with  $ST$  obtain  $v ref stk'$  where  $stk'$ :  $stk = v \# ref \# stk'$ 
  and ref:  $P, h \vdash ref \leq oT$ 
  and  $v$ :  $P, h \vdash v \leq vT$  by auto

show ?thesis
proof(cases ref = Null)
  case True with  $stk'$  show ?thesis by auto
next
  case False
from ref oT have  $P, h \vdash ref \leq Class D$  ..
with False obtain  $a U' D'$  where
   $a$ : ref = Addr a and  $h$ : typeof-addr h a = Some U' and
   $U'$ :  $D' = class\text{-}type\text{-}of U'$  and  $D'$ :  $P \vdash D' \preceq^* D$ 
  by (blast dest: non-npD2)

from field D' have has-field:  $P \vdash D'$  has  $F:vT'$  (fm) in  $D$ 
  by (blast intro: has-field-mono has-visible-field)
with  $h$  have  $al$ :  $P, h \vdash a @ CField D F : vT'$  unfolding  $U'$  ..
from  $v vT'$  have  $P, h \vdash v \leq vT'$  by auto
from heap-write-total[ $OF$  hconf al this]  $v a stk' h$  show ?thesis by auto
qed
next
case [simp]: (CAS  $F D$ )
from  $\Phi$ -pc wt obtain  $T' T1 T2 T3 ST'' fm$  where  $ST = T3 \# T2 \# T1 \# ST''$ 
  and field:  $P \vdash D$  sees  $F:T'$  (fm) in  $D$ 
  and  $oT$ :  $P \vdash T1 \leq Class D$ 
  and  $vT'$ :  $P \vdash T2 \leq T' P \vdash T3 \leq T'$  by fastforce
with  $ST$  obtain  $v v' v'' stk'$  where  $stk'$ :  $stk = v'' \# v' \# v \# stk'$ 
  and  $v$ :  $P, h \vdash v \leq T1$ 
  and  $v'$ :  $P, h \vdash v' \leq T2$ 
  and  $v''$ :  $P, h \vdash v'' \leq T3$  by auto
show ?thesis
proof(cases v = Null)
  case True with  $stk'$  show ?thesis by auto
next
  case False
from  $v oT$  have  $P, h \vdash v \leq Class D$  ..
with False obtain  $a U' D'$  where
   $a$ :  $v = Addr a$  and  $h$ : typeof-addr h a = Some U' and
   $U'$ :  $D' = class\text{-}type\text{-}of U'$  and  $D'$ :  $P \vdash D' \preceq^* D$ 
  by (blast dest: non-npD2)

```

```

from field  $D'$  have has-field:  $P \vdash D' \text{ has } F:T' \text{ (fm) in } D$ 
  by (blast intro: has-field-mono has-visible-field)
with  $h$  have  $al$ :  $P, h \vdash a @ CField\ D\ F : T' \text{ unfolding } U' ..$ 
from  $v' vT'$  have  $P, h \vdash v' : \leq T'$  by auto
from heap-read-total[OF hconf al] obtain  $v'''$  where  $v'''$ : heap-read  $h\ a\ (CField\ D\ F)\ v'''$  by
blast
  show ?thesis
  proof(cases  $v''' = v'$ )
    case True
      from  $v'' vT'$  have  $P, h \vdash v'' : \leq T'$  by auto
      from heap-write-total[OF hconf al this]  $v\ a\ stk'\ h\ v'''$  True show ?thesis by auto
    next
      case False
      from  $v''' v\ a\ stk'\ h$  False show ?thesis by auto
  qed
qed
next
  case [simp]: (Invoke  $M'\ n$ )

from wt  $\Phi$ -pc have  $n$ :  $n < \text{size } ST$  by simp

show ?thesis
proof(cases  $stk!n = \text{Null}$ )
  case True thus ?thesis by simp
next
  case False
  note  $\text{Null} = \text{this}$ 
  have  $NT$ :  $ST!n \neq NT$ 
  proof
    assume  $ST!n = NT$ 
    moreover from  $ST\ n$  have  $P, h \vdash stk!n : \leq ST!n$  by (simp add: list-all2-conv-all-nth)
    ultimately have  $stk!n = \text{Null}$  by simp
    with  $\text{Null}$  show False by contradiction
  qed

from  $NT\ wt\ \Phi$ -pc obtain  $D\ D'\ Ts\ T\ m$ 
  where  $D$ : class-type-of' ( $ST!n$ ) = Some  $D$ 
  and  $m$ - $D$ :  $P \vdash D \text{ sees } M': Ts \rightarrow T = m \text{ in } D'$ 
  and  $Ts$ :  $P \vdash \text{rev } (\text{take } n\ ST) [\leq] Ts$ 
  by auto

from  $n\ ST\ D$  have  $P, h \vdash stk!n : \leq ST!n$ 
  by (auto simp add: list-all2-conv-all-nth)

from  $\langle P, h \vdash stk!n : \leq ST!n \rangle\ \text{Null}\ D$ 
obtain  $a\ T'$  where
  Addr:  $stk!n = \text{Addr } a$  and
  obj: typeof-addr  $h\ a = \text{Some } T'$  and
   $T'_{\text{sub}STn}$ :  $P \vdash \text{ty-of-htype } T' \leq ST!\ n$ 
  by(cases  $stk!\ n$ )(auto simp add: conf-def widen-Class)

from  $D\ T'_{\text{sub}STn}$  obtain  $C'$  where
   $C'$ : class-type-of' (ty-of-htype  $T'$ ) =  $\lfloor C' \rfloor$  and  $C'_{\text{sub}D}$ :  $P \vdash C' \preceq^* D$ 

```

```

by(rule widen-is-class-type-of) simp

from Call-lemma[OF m-D C' subD wf]
obtain D' Ts' T' m'
  where Call':  $P \vdash C' \text{ sees } M': Ts' \rightarrow T' = m' \text{ in } D' P \vdash Ts \leq Ts'$ 
     $P \vdash T' \leq T P \vdash C' \preceq^* D' \text{ is-type } P T' \forall T \in \text{set } Ts'. \text{ is-type } P T$ 
  by blast

show ?thesis
proof(cases m')
  case Some with Call' C' obj Addr C' C' subD show ?thesis by(auto)
next
  case [simp]: None
  from ST have  $P, h \vdash \text{take } n \text{ stk } [: \leq] \text{take } n \text{ ST}$  by(rule list-all2-takeI)
  then obtain Us where  $\text{map typeof}_h (\text{take } n \text{ stk}) = \text{map Some } Us P \vdash Us \leq \text{take } n \text{ ST}$ 
    by(auto simp add: confs-conv-map)
  hence  $Us: \text{map typeof}_h (\text{rev } (\text{take } n \text{ stk})) = \text{map Some } (\text{rev } Us) P \vdash \text{rev } Us \leq \text{rev } (\text{take } n$ 
ST)
    by- (simp only: rev-map[symmetric], simp)
  with  $Ts \langle P \vdash Ts \leq Ts' \rangle$  have  $P \vdash \text{rev } Us \leq Ts'$  by(blast intro: widens-trans)
  with obj Us Call' C' have  $P, h \vdash a.M'(\text{rev } (\text{take } n \text{ stk})) : T'$ 
    by(auto intro!: external-WT'.intros)
  from external-call-progress[OF wf this hconf, of t] obj Addr Call' C'
  show ?thesis by(auto dest!: red-external-imp-red-external-aggr)
qed
qed
qed(auto 4 4 simp add: split-beta split: if-split-asm)
thus ?thesis using sees None
  unfolding f exec-1-iff by(simp del: split-paired-Ex)
qed
qed

lemma (in JVM-heap-conf) BV-correct-initial:
  shows  $\llbracket \text{wf-jvm-prog}_{\Phi} P; \text{start-heap-ok}; P \vdash C \text{ sees } M: Ts \rightarrow T = \lfloor m \rfloor \text{ in } D; P, \text{start-heap} \vdash vs [: \leq] Ts \rrbracket$ 
 $\implies \Phi \vdash \text{start-tid}: \text{JVM-start-state}' P C M vs \checkmark$ 
  apply (cases m)
  apply (unfold JVM-start-state'-def)
  apply (unfold correct-state-def)
  apply (clarsimp)
  apply (frule wt-jvm-progD)
  apply (erule exE)
  apply (frule wf-prog-wf-syscls)
  apply (rule conjI)
  apply (erule (1) tconf-start-heap-start-tid)
  apply (rule conjI)
  apply (simp add: wf-jvm-prog-phi-def hconf-start-heap)
  apply (frule sees-method-idemp)
  apply (frule wt-jvm-prog-impl-wt-start, assumption+)
  apply (unfold conf-f-def wt-start-def)
  apply (auto simp add: sup-state-opt-any-Some)
  apply (erule preallocated-start-heap)
  apply (rule exI conjI | assumption) +
  apply (auto simp add: list-all2-append1)

```

```

    apply(auto dest: list-all2-lengthD intro!: exI)
  done

end

```

## 6.6 Welltyped Programs produce no Type Errors

```

theory BVNoTypeError
imports
  ../JVM/JVMDefensive
  BVSpecTypeSafe
begin

lemma wt-jvm-prog-states:
  
$$\llbracket wf\text{-}jvm\text{-}prog_{\Phi} P; P \vdash C \text{ sees } M: Ts \rightarrow T = \llbracket (m\acute{x}s, m\acute{x}l, ins, et) \rrbracket \text{ in } C;$$

  
$$\Phi \ C \ M \ ! \ pc = \tau; pc < size \ ins \rrbracket$$


$$\implies OK \ \tau \in states \ P \ m\acute{x}s \ (1 + size \ Ts + m\acute{x}l)$$

context JVM-heap-conf-base' begin

declare is-IntI [simp, intro]
declare is-BoolI [simp, intro]
declare is-RefI [simp]

```

The main theorem: welltyped programs do not produce type errors if they are started in a conformant state.

```

theorem no-type-error:
  assumes welltyped: wf-jvm-prog $\Phi$  P and conforms:  $\Phi \vdash t:\sigma \checkmark$ 
  shows exec-d P t  $\sigma \neq$  TypeError

```

## 6.7 Progress result for both of the multithreaded JVMs

```

theory BVProgressThreaded
imports
  ../Framework/FWProgress
  ../Framework/FWLTS
  BVNoTypeError
  ../JVM/JVMThreaded
begin

lemma (in JVM-heap-conf-base') mexec-eq-mexecd:
  
$$\llbracket wf\text{-}jvm\text{-}prog_{\Phi} P; \Phi \vdash t: (xcp, h, frs) \checkmark \rrbracket \implies mexec \ P \ t \ ((xcp, frs), h) = mexecd \ P \ t \ ((xcp, frs), h)$$

  apply(auto intro!: ext)
  apply(unfold exec-1-iff)
  apply(drule no-type-error)
  apply(assumption)
  apply(clarify)
  apply(rule exec-1-d-NormalI)
  apply(assumption)
  apply(simp add: exec-d-def split: if-split-asm)
  apply(erule jvmd-NormalE, auto)
done

```

**context** *JVM-heap-conf-base* **begin**

**abbreviation**

*correct-state-ts* :: *typ*  $\Rightarrow$  ('addr,'thread-id,'addr jvm-thread-state) thread-info  $\Rightarrow$  'heap  $\Rightarrow$  bool

**where**

*correct-state-ts*  $\Phi \equiv ts\text{-}ok (\lambda t (xcp, frstls) h. \Phi \vdash t: (xcp, h, frstls) \checkmark)$

**lemma** *correct-state-ts-thread-conf*:

*correct-state-ts*  $\Phi$  (*thr s*) (*shr s*)  $\Longrightarrow$  *thread-conf* *P* (*thr s*) (*shr s*)

**by**(*erule ts-ok-mono*)(*auto simp add: correct-state-def*)

**lemma** *invoke-new-thread*:

**assumes** *wf-jvm-prog* $_{\Phi}$  *P*

**and**  $P \vdash C \text{ sees } M:Ts \rightarrow T = [(m\acute{x}s, m\acute{x}l0, ins, xt)] \text{ in } C$

**and**  $ins \text{ ! } pc = \text{Invoke Type.start } 0$

**and**  $P, T, m\acute{x}s, size \ ins, xt \vdash ins!pc, pc :: \Phi \ C \ M$

**and**  $\Phi \vdash t: (None, h, (stk, loc, C, M, pc) \# frs) \checkmark$

**and** *typeof-addr* *h* (*thread-id2addr a*) = [Class-type *D*]

**and**  $P \vdash D \preceq^* \text{Thread}$

**and**  $P \vdash D \text{ sees run: } [] \rightarrow \text{Void} = [(m\acute{x}s', m\acute{x}l0', ins', xt')] \text{ in } D'$

**shows**  $\Phi \vdash a: (None, h, [([], \text{Addr} (\text{thread-id2addr } a) \# \text{replicate } m\acute{x}l0' \text{ undefined-value, } D', \text{run, } 0)]) \checkmark$

**proof** –

**from**  $\langle \Phi \vdash t: (None, h, (stk, loc, C, M, pc) \# frs) \checkmark \rangle$

**have** *hconf* *h* **and** *preallocated* *h* **by**(*simp-all add: correct-state-def*)

**moreover**

**from**  $\langle P \vdash D \text{ sees run: } [] \rightarrow \text{Void} = [(m\acute{x}s', m\acute{x}l0', ins', xt')] \text{ in } D' \rangle$

**have**  $P \vdash D' \text{ sees run: } [] \rightarrow \text{Void} = [(m\acute{x}s', m\acute{x}l0', ins', xt')] \text{ in } D'$

**by**(*rule sees-method-idemp*)

**with**  $\langle wf\text{-}jvm\text{-}prog_{\Phi} \ P \rangle$

**have** *wt-start* *P* *D'* [] *m\acute{x}l0'* ( $\Phi \ D' \text{ run}$ ) **and**  $ins' \neq []$

**by**(*auto dest: wt-jvm-prog-impl-wt-start*)

**then obtain** *LT'* **where** *LT'*:  $\Phi \ D' \text{ run ! } 0 = \text{Some} ([, LT'])$

**by** (*clarsimp simp add: wt-start-def defs1 sup-state-opt-any-Some*)

**moreover**

**have** *conf-f* *P* *h* ([, *LT'*)  $ins'$  ([, *Addr* (*thread-id2addr a*) # replicate *m\acute{x}l0'* undefined-value, *D'*, *run*, 0)

**proof** –

**let** *?LT* = *OK* (Class *D'*) # (replicate *m\acute{x}l0'* *Err*)

**have**  $P, h \vdash \text{replicate } m\acute{x}l0' \text{ undefined-value } [:\leq_{\top}] \text{replicate } m\acute{x}l0' \text{ Err}$  **by** *simp*

**also from**  $\langle P \vdash D \text{ sees run: } [] \rightarrow \text{Void} = [(m\acute{x}s', m\acute{x}l0', ins', xt')] \text{ in } D' \rangle$

**have**  $P \vdash D \preceq^* D'$  **by**(*rule sees-method-decl-above*)

**with**  $\langle \text{typeof-addr } h (\text{thread-id2addr } a) = [\text{Class-type } D] \rangle$

**have**  $P, h \vdash \text{Addr} (\text{thread-id2addr } a) :\leq \text{Class } D'$

**by**(*simp add: conf-def*)

**ultimately have**  $P, h \vdash \text{Addr} (\text{thread-id2addr } a) \# \text{replicate } m\acute{x}l0' \text{ undefined-value } [:\leq_{\top}] \text{ ?LT}$

**by**(*simp*)

**also from**  $\langle wt\text{-}start \ P \ D' \ [] \ m\acute{x}l0' \ (\Phi \ D' \text{ run}) \rangle \text{ ?LT'}$

**have**  $P \vdash \dots [:\leq_{\top}] \text{ ?LT'}$  **by**(*simp add: wt-start-def*)

**finally have**  $P, h \vdash \text{Addr} (\text{thread-id2addr } a) \# \text{replicate } m\acute{x}l0' \text{ undefined-value } [:\leq_{\top}] \text{ ?LT'}$ .

**with**  $\langle ins' \neq [] \rangle$  **show** *?thesis* **by**(*simp add: conf-f-def*)

**qed**

**moreover from**  $\langle \text{typeof-addr } h (\text{thread-id2addr } a) = [\text{Class-type } D] \rangle \langle P \vdash D \preceq^* \text{Thread} \rangle$

**have**  $P, h \vdash a \sqrt{t}$  **by** (rule *tconfI*)  
**ultimately show**  $?thesis$  **using**  $\langle P \vdash D' \text{ sees run: } [] \rightarrow Void = [(m\acute{x}s', m\acute{x}l0', ins', xt')] \text{ in } D' \rangle$   
**by** (fastforce simp add: correct-state-def)  
**qed**

**lemma** *exec-new-threadE*:

**assumes** *wf-jvm-prog*  $\Phi$   $P$   
**and**  $P, t \vdash Normal \sigma \rightarrow ta-jvmd \rightarrow Normal \sigma'$   
**and**  $\Phi \vdash t: \sigma \sqrt{}$   
**and**  $\llbracket ta \rrbracket_t \neq []$   
**obtains**  $h \text{ frs } a \text{ stk } loc \ C \ M \ pc \ Ts \ T \ m\acute{x}s \ m\acute{x}l0 \ ins \ xt \ M' \ n \ Ta \ ta' \ va \ Us \ Us' \ U \ m' \ D'$   
**where**  $\sigma = (None, h, (stk, loc, C, M, pc) \# frs)$   
**and**  $(ta, \sigma') \in exec \ P \ t \ (None, h, (stk, loc, C, M, pc) \# frs)$   
**and**  $P \vdash C \text{ sees } M: Ts \rightarrow T = [(m\acute{x}s, m\acute{x}l0, ins, xt)] \text{ in } C$   
**and**  $stk ! n = Addr \ a$   
**and**  $ins ! pc = Invoke \ M' \ n$   
**and**  $n < length \ stk$   
**and**  $typeof-addr \ h \ a = \lfloor Ta \rfloor$   
**and** *is-native*  $P \ Ta \ M'$   
**and**  $ta = extTA2JVM \ P \ ta'$   
**and**  $\sigma' = extRet2JVM \ n \ m' \ stk \ loc \ C \ M \ pc \ frs \ va$   
**and**  $(ta', va, m') \in red-external-aggr \ P \ t \ a \ M' \ (rev \ (take \ n \ stk)) \ h$   
**and**  $map \ typeof_h \ (rev \ (take \ n \ stk)) = map \ Some \ Us$   
**and**  $P \vdash class-type-of \ Ta \text{ sees } M': Us' \rightarrow U = Native \text{ in } D'$   
**and**  $D'.M'(Us') :: U$   
**and**  $P \vdash Us \leq Us'$

**proof** –

**from**  $\langle P, t \vdash Normal \sigma \rightarrow ta-jvmd \rightarrow Normal \sigma' \rangle$  **obtain**  $h \ f \ Frs \ xcp$   
**where** *check*: *check*  $P \ \sigma$   
**and** *exec*:  $(ta, \sigma') \in exec \ P \ t \ \sigma$   
**and** *[simp]*:  $\sigma = (xcp, h, f \# Frs)$   
**by** (rule *jvmd-NormalE*)  
**obtain**  $stk \ loc \ C \ M \ pc$  **where** *[simp]*:  $f = (stk, loc, C, M, pc)$   
**by** (cases *f*, *blast*)  
**from**  $\langle \llbracket ta \rrbracket_t \neq [] \rangle$  *exec* **have** *[simp]*:  $xcp = None$  **by** (cases *xcp*) *auto*  
**from**  $\langle \Phi \vdash t: \sigma \sqrt{ } \rangle$   
**obtain**  $Ts \ T \ m\acute{x}s \ m\acute{x}l0 \ ins \ xt \ ST \ LT$   
**where** *hconf*  $h \ preallocated \ h$   
**and** *sees*:  $P \vdash C \text{ sees } M: Ts \rightarrow T = [(m\acute{x}s, m\acute{x}l0, ins, xt)] \text{ in } C$   
**and**  $\Phi \ C \ M ! pc = \lfloor (ST, LT) \rfloor$   
**and** *conf-f*  $P \ h \ (ST, LT) \ ins \ (stk, loc, C, M, pc)$   
**and** *conf-fs*  $P \ h \ \Phi \ M \ (length \ Ts) \ T \ Frs$   
**by** (fastforce simp add: correct-state-def)  
**from** *check*  $\langle \Phi \ C \ M ! pc = \lfloor (ST, LT) \rfloor \rangle$  *sees*  
**have** *checkins*: *check-instr*  $(ins ! pc) \ P \ h \ stk \ loc \ C \ M \ pc \ Frs$   
**by** (clarsimp simp add: check-def)  
**from** *sees*  $\langle \llbracket ta \rrbracket_t \neq [] \rangle$  *exec* **obtain**  $M' \ n$  **where** *[simp]*:  $ins ! pc = Invoke \ M' \ n$   
**by** (cases  $ins ! pc$ , *auto split*: *if-split-asm* simp add: *split-beta ta-upd-simps*)  
**from**  $\langle wf-jvm-prog_{\Phi} \ P \rangle$  **obtain** *wfmd* **where** *wfp*: *wf-prog* *wfmd*  $P$  **by** (auto dest: *wt-jvm-progD*)  
  
**from** *checkins* **have**  $n < length \ stk$  *is-Ref*  $(stk ! n)$  **by** *auto*  
**moreover from** *exec* *sees*  $\langle \llbracket ta \rrbracket_t \neq [] \rangle$  **have**  $stk ! n \neq Null$  **by** *auto*  
**with**  $\langle is-Ref \ (stk ! n) \rangle$  **obtain**  $a$  **where**  $stk ! n = Addr \ a$   
**by** (auto simp add: *is-Ref-def elim*: *is-AddrE*)

**moreover with checkins obtain**  $Ta$  **where**  $Ta: \text{typeof-addr } h \ a = \lfloor Ta \rfloor$  **by** (*fastforce*)  
**moreover with checkins exec sees**  $\langle n < \text{length } stk \rangle \langle \{ta\}_t \neq [] \rangle \langle stk ! n = \text{Addr } a \rangle$   
**obtain**  $Us \ Us' \ U \ D'$  **where**  $\text{map } \text{typeof}_h (\text{rev } (\text{take } n \ stk)) = \text{map } \text{Some } Us$   
**and**  $P \vdash \text{class-type-of } Ta \text{ sees } M': Us' \rightarrow U = \text{Native in } D' \text{ and } D'.M'(Us') :: U$   
**and**  $P \vdash Us \leq Us'$   
**by** (*auto simp add: confs-conv-map min-def split-beta has-method-def external-WT'-iff split: if-split-asm*)  
**moreover with**  $\langle \text{typeof-addr } h \ a = \lfloor Ta \rfloor \rangle \langle n < \text{length } stk \rangle \text{ exec sees } \langle stk ! n = \text{Addr } a \rangle$   
**obtain**  $ta' \ va \ h'$  **where**  $ta = \text{extTA2JVM } P \ ta' \ \sigma' = \text{extRet2JVM } n \ h' \ stk \ \text{loc } C \ M \ pc \ Frs \ va$   
 $(ta', va, h') \in \text{red-external-aggr } P \ t \ a \ M' (\text{rev } (\text{take } n \ stk)) \ h$   
**by** (*fastforce simp add: min-def*)  
**ultimately show thesis using exec sees**  
**by**  $-(\text{rule that, auto intro!: is-native.intros})$   
**qed**  
**end**

**context** *JVM-conf-read* **begin**

**lemma** *correct-state-new-thread*:

**assumes**  $wf: wf\text{-jvm-prog}_{\Phi} \ P$   
**and**  $\text{red}: P, t \vdash \text{Normal } \sigma \rightarrow \text{Normal } \sigma'$   
**and**  $cs: \Phi \vdash t: \sigma \checkmark$   
**and**  $nt: \text{NewThread } t'' (xcp, frs) \ h'' \in \text{set } \{ta\}_t$   
**shows**  $\Phi \vdash t'': (xcp, h'', frs) \checkmark$   
**proof** –  
**from**  $wf$  **obtain**  $wt$  **where**  $wfp: wf\text{-prog } wt \ P$  **by** (*blast dest: wt-jvm-progD*)  
**from**  $nt$  **have**  $\{ta\}_t \neq []$  **by** *auto*  
**with**  $wf \ \text{red} \ cs$   
**obtain**  $h \ Frs \ a \ stk \ \text{loc } C \ M \ pc \ Ts \ T \ m\acute{x}s \ m\acute{x}l0 \ ins \ xt \ M' \ n \ Ta \ ta' \ va \ h' \ Us \ Us' \ U \ D'$   
**where**  $[simp]: \sigma = (\text{None}, h, (stk, \text{loc}, C, M, pc) \# Frs)$   
**and**  $\text{exec}: (ta, \sigma') \in \text{exec } P \ t \ (\text{None}, h, (stk, \text{loc}, C, M, pc) \# Frs)$   
**and**  $\text{sees}: P \vdash C \text{ sees } M: Ts \rightarrow T = \lfloor (m\acute{x}s, m\acute{x}l0, ins, xt) \rfloor \text{ in } C$   
**and**  $[simp]: stk ! n = \text{Addr } a$   
**and**  $[simp]: ins ! pc = \text{Invoke } M' \ n$   
**and**  $n: n < \text{length } stk$   
**and**  $Ta: \text{typeof-addr } h \ a = \lfloor Ta \rfloor$   
**and**  $\text{iec}: \text{is-native } P \ Ta \ M'$   
**and**  $ta: ta = \text{extTA2JVM } P \ ta'$   
**and**  $\sigma': \sigma' = \text{extRet2JVM } n \ h' \ stk \ \text{loc } C \ M \ pc \ Frs \ va$   
**and**  $\text{rel}: (ta', va, h') \in \text{red-external-aggr } P \ t \ a \ M' (\text{rev } (\text{take } n \ stk)) \ h$   
**and**  $Us: \text{map } \text{typeof}_h (\text{rev } (\text{take } n \ stk)) = \text{map } \text{Some } Us$   
**and**  $wtext: P \vdash \text{class-type-of } Ta \text{ sees } M': Us' \rightarrow U = \text{Native in } D' \ D'.M'(Us') :: U$   
**and**  $\text{sub}: P \vdash Us \leq Us'$   
**by** (*rule exec-new-threadE*)  
**from**  $cs$  **have**  $hconf: hconf \ h$  **and**  $preh: preallocated \ h$   
**and**  $tconf: P, h \vdash t \checkmark$  **by** (*auto simp add: correct-state-def*)  
**from**  $Ta \ Us \ wtext \ \text{sub}$  **have**  $wtext': P, h \vdash a.M'(\text{rev } (\text{take } n \ stk)) : U$   
**by** (*auto intro!: external-WT'.intros*)  
**from**  $\text{rel}$  **have**  $\text{red}: P, t \vdash \langle a.M'(\text{rev } (\text{take } n \ stk)), h \rangle \rightarrow \text{ext } \langle va, h' \rangle$   
**by** (*unfold WT-red-external-list-conv[OF wfp wtext' tconf]*)  
**from**  $ta \ nt$  **obtain**  $D \ M'' \ a'$  **where**  $nt': \text{NewThread } t'' (D, M'', a') \ h'' \in \text{set } \{ta'\}_t$   
 $(xcp, frs) = \text{extNTA2JVM } P \ (D, M'', a') \text{ by } \text{auto}$   
**with**  $\text{red}$  **have**  $[simp]: h'' = h'$  **by**  $-(\text{rule red-ext-new-thread-heap})$   
**from**  $\text{red-external-new-thread-sub-thread[OF red } nt'(1)]$

**have**  $h't''$ :  $\text{typeof\_addr } h' a' = \lfloor \text{Class-type } D \rfloor \ P \vdash D \preceq^* \text{Thread}$  **and**  $[simp]: M'' = \text{run}$  **by** *auto*  
**from** *red-external-new-thread-exists-thread-object* $[OF \text{ red } nt'(1)]$   
**have**  $tconf'$ :  $P, h' \vdash t'' \sqrt{t}$  **by** (*auto intro: tconfI*)  
**from** *sub-Thread-sees-run* $[OF \text{ wfp } \langle P \vdash D \preceq^* \text{Thread} \rangle]$  **obtain**  $mxs' \ mxl0' \ ins' \ xt' \ D'$   
**where** *seesrun*:  $P \vdash D \text{ sees run: } [] \rightarrow \text{Void} = \lfloor (mxs', mxl0', ins', xt') \rfloor$  **in**  $D'$  **by** *auto*  
**with**  $nt' \text{ ta } nt$  **have**  $xcp = \text{None}$   $frs = [([], \text{Addr } a' \# \text{replicate } mxl0' \text{ undefined-value}, D', \text{run}, 0)]$   
**by** (*auto simp add: extNTA2JVM-def split-beta*)  
**moreover**  
**have**  $\Phi \vdash t''$ :  $(\text{None}, h', [([], \text{Addr } a' \# \text{replicate } mxl0' \text{ undefined-value}, D', \text{run}, 0)]) \sqrt{}$   
**proof** –  
**from** *red wtext' <hconf h>* **have** *hconf h'*  
**by** (*rule external-call-hconf*)  
**moreover from** *red* **have**  $h \leq h'$  **by** (*rule red-external-hext*)  
**with** *preh* **have** *preallocated h'* **by** (*rule preallocated-hext*)  
**moreover from** *seesrun*  
**have** *seesrun'*:  $P \vdash D' \text{ sees run: } [] \rightarrow \text{Void} = \lfloor (mxs', mxl0', ins', xt') \rfloor$  **in**  $D'$   
**by** (*rule sees-method-idemp*)  
**moreover with**  $\langle \text{wf-jvm-prog}_{\Phi} P \rangle$   
**obtain** *wt-start*  $P \ D' \ [] \ mxl0' (\Phi \ D' \text{ run}) \ ins' \neq []$   
**by** (*auto dest: wt-jvm-prog-impl-wt-start*)  
**then obtain**  $LT'$  **where**  $\Phi \ D' \text{ run} ! 0 = \text{Some } ([, LT'])$   
**by** (*clarsimp simp add: wt-start-def defs1 sup-state-opt-any-Some*)  
**moreover**  
**have** *conf-f*  $P \ h' ([, LT']) \ ins' ([, \text{Addr } a' \# \text{replicate } mxl0' \text{ undefined-value}, D', \text{run}, 0)$   
**proof** –  
**let**  $?LT = OK \ (\text{Class } D') \# (\text{replicate } mxl0' \text{ Err})$   
**from** *seesrun* **have**  $P \vdash D \preceq^* D'$  **by** (*rule sees-method-decl-above*)  
**hence**  $P, h' \vdash \text{Addr } a' \# \text{replicate } mxl0' \text{ undefined-value} [:\leq_{\top}] ?LT$   
**using**  $h't''$  **by** (*simp add: conf-def*)  
**also from**  $\langle \text{wt-start } P \ D' \ [] \ mxl0' (\Phi \ D' \text{ run}) \rangle \langle \Phi \ D' \text{ run} ! 0 = \text{Some } ([, LT']) \rangle$   
**have**  $P \vdash ?LT [:\leq_{\top}] LT'$  **by** (*simp add: wt-start-def*)  
**finally have**  $P, h' \vdash \text{Addr } a' \# \text{replicate } mxl0' \text{ undefined-value} [:\leq_{\top}] LT'$ .  
**with**  $\langle \text{ins}' \neq [] \rangle$  **show** *?thesis* **by** (*simp add: conf-f-def*)  
**qed**  
**ultimately show** *?thesis* **using**  $tconf'$  **by** (*fastforce simp add: correct-state-def*)  
**qed**  
**ultimately show** *?thesis* **by** (*clarsimp*)  
**qed**

**lemma** *correct-state-heap-change*:

**assumes**  $\text{wf: wf-jvm-prog}_{\Phi} P$   
**and**  $\text{red: } P, t \vdash \text{Normal } (xcp, h, frs) \text{ --ta--jvmd--> Normal } (xcp', h', frs')$   
**and**  $\text{cs: } \Phi \vdash t: (xcp, h, frs) \sqrt{}$   
**and**  $\text{cs'': } \Phi \vdash t'': (xcp'', h, frs'') \sqrt{}$   
**shows**  $\Phi \vdash t'': (xcp'', h', frs'') \sqrt{}$

**proof** (*cases xcp*)

**case** *None*

**from** *cs* **have**  $P, h \vdash t \sqrt{t}$  **by** (*simp add: correct-state-def*)  
**with** *red* **have** *hext h h'* **by** (*auto intro: exec-1-d-hext simp add: tconf-def*)  
**from**  $\langle \text{wf-jvm-prog}_{\Phi} P \rangle$  *cs red* **have**  $\Phi \vdash t: (xcp', h', frs') \sqrt{}$   
**by** (*auto elim!: jvmd-NormalE intro: BV-correct-1 simp add: exec-1-iff*)  
**from**  $\text{cs''}$  **have**  $P, h \vdash t'' \sqrt{t}$  **by** (*simp add: correct-state-def*)  
**with**  $\langle h \leq h' \rangle$  **have**  $tconf'$ :  $P, h' \vdash t'' \sqrt{t}$  **by**–(*rule tconf-hext-mono*)



```

from  $\langle \Phi \vdash t: (xcp', h', frs') \checkmark \rangle$ 
have  $hconf'$ :  $hconf\ h'$  preallocated  $h'$  by(simp-all add: correct-state-def)

show ?thesis
proof(cases frs'')
  case Nil thus ?thesis using  $tconf'\ hconf'$  by(simp add: correct-state-def)
next
  case (Cons f'' Frs'')
  obtain  $stk''\ loc''\ C0''\ M0''\ pc''$ 
    where  $f'' = (stk'', loc'', C0'', M0'', pc'')$ 
    by(cases f'', blast)
  with  $\langle frs'' = f'' \# Frs'' \rangle\ cs''$ 
  obtain  $Ts''\ T''\ mxs''\ mxl_0''\ ins''\ xt''\ ST''\ LT''$ 
    where  $hconf\ h$ 
    and  $sees'': P \vdash C0''\ sees\ M0'': Ts'' \rightarrow T'' = \lfloor (mxs'', mxl_0'', ins'', xt'') \rfloor\ in\ C0''$ 
    and  $\Phi\ C0''\ M0''! pc'' = \lfloor (ST'', LT'') \rfloor$ 
    and  $conf\text{-}f\ P\ h\ (ST'', LT'')\ ins''\ (stk'', loc'', C0'', M0'', pc'')$ 
    and  $conf\text{-}fs\ P\ h\ \Phi\ M0''\ (length\ Ts'')\ T''\ Frs''$ 
    by(fastforce simp add: correct-state-def)

  show ?thesis using Cons  $\langle \Phi \vdash t'': (xcp'', h, frs'') \checkmark \rangle\ \langle hext\ h\ h' \rangle\ hconf'\ tconf'$ 
    apply(cases xcp'')
    apply(auto simp add: correct-state-def)
    apply(blast dest: hext-objD intro: conf-fs-hext conf-f-hext)
    done
  qed
next
  case (Some a)
  with  $\langle P, t \vdash Normal\ (xcp, h, frs) \rightarrow ta\text{-}jvmd \rightarrow Normal\ (xcp', h', frs') \rangle$ 
  have  $h = h'$  by(auto elim!: jvmd-NormalE)
  with  $\langle \Phi \vdash t'': (xcp'', h, frs'') \checkmark \rangle$  show ?thesis by simp
qed

lemma lifting-wf-correct-state-d:
   $wf\text{-}jvm\text{-}prog_{\Phi}\ P \implies lifting\text{-}wf\ JVM\text{-}final\ (mexecd\ P)\ (\lambda t\ (xcp, frs)\ h.\ \Phi \vdash t: (xcp, h, frs) \checkmark)$ 
by(unfold-locales)(auto intro: BV-correct-d-1 correct-state-new-thread correct-state-heap-change)

lemma lifting-wf-correct-state:
  assumes  $wf: wf\text{-}jvm\text{-}prog_{\Phi}\ P$ 
  shows  $lifting\text{-}wf\ JVM\text{-}final\ (mexec\ P)\ (\lambda t\ (xcp, frs)\ h.\ \Phi \vdash t: (xcp, h, frs) \checkmark)$ 
proof(unfold-locales)
  fix  $t\ x\ m\ ta\ x'\ m'$ 
  assume  $mexec\ P\ t\ (x, m)\ ta\ (x', m')$ 
  and  $(\lambda(xcp, frs)\ h.\ \Phi \vdash t: (xcp, h, frs) \checkmark)\ x\ m$ 
  with  $wf$  show  $(\lambda(xcp, frs)\ h.\ \Phi \vdash t: (xcp, h, frs) \checkmark)\ x'\ m'$ 
  by(cases x)(cases x', simp add: welltyped-commute[symmetric, OF  $\langle wf\text{-}jvm\text{-}prog_{\Phi}\ P \rangle]$ , rule BV-correct-d-1)
next
  fix  $t\ x\ m\ ta\ x'\ m'\ t''\ x''$ 
  assume  $mexec\ P\ t\ (x, m)\ ta\ (x', m')$ 
  and  $(\lambda(xcp, frs)\ h.\ \Phi \vdash t: (xcp, h, frs) \checkmark)\ x\ m$ 
  and  $NewThread\ t''\ x''\ m' \in set\ \{ta\}_t$ 
  with  $wf$  show  $(\lambda(xcp, frs)\ h.\ \Phi \vdash t'': (xcp, h, frs) \checkmark)\ x''\ m'$ 
  apply(cases x, cases x', cases x'', clarify, unfold welltyped-commute[symmetric, OF  $\langle wf\text{-}jvm\text{-}prog_{\Phi}\ P \rangle]$ )

```

```

    by(rule correct-state-new-thread)
next
  fix t x m ta x' m' t'' x''
  assume mexec P t (x, m) ta (x', m')
    and ( $\lambda(xcp, frs) h. \Phi \vdash t: (xcp, h, frs) \checkmark$ ) x m
    and ( $\lambda(xcp, frs) h. \Phi \vdash t'': (xcp, h, frs) \checkmark$ ) x'' m
  with wf show ( $\lambda(xcp, frs) h. \Phi \vdash t'': (xcp, h, frs) \checkmark$ ) x'' m'
    by(cases x)(cases x', cases x'', clarify, unfold welltyped-commute[symmetric, OF  $\langle wf-jvm-prog_{\Phi}$ 
P $\rangle$ ], rule correct-state-heap-change)
qed

lemmas preserves-correct-state = FWLiftingSem.lifting-wf.RedT-preserves[OF lifting-wf-correct-state]
lemmas preserves-correct-state-d = FWLiftingSem.lifting-wf.RedT-preserves[OF lifting-wf-correct-state-d]

end

context JVM-heap-conf-base begin

definition correct-jvm-state ::  $typ \Rightarrow ('addr, 'thread-id, 'addr jvm-thread-state, 'heap, 'addr) state set$ 
where
  correct-jvm-state  $\Phi$ 
    = {s. correct-state-ts  $\Phi$  (thr s) (shr s)  $\wedge$  lock-thread-ok (locks s) (thr s)}

end

context JVM-heap-conf begin

lemma correct-jvm-state-initial:
  assumes wf: wf-jvm-prog $\Phi$  P
  and wf-start: wf-start-state P C M vs
  shows JVM-start-state P C M vs  $\in$  correct-jvm-state  $\Phi$ 
proof -
  from wf-start obtain Ts T m D
    where start-heap-ok and  $P \vdash C$  sees  $M: Ts \rightarrow T = \lfloor m \rfloor$  in D
    and P, start-heap  $\vdash vs$  [ $\leq$ ] Ts by cases
  with wf BV-correct-initial[OF wf this] show ?thesis
    by(cases m)(auto simp add: correct-jvm-state-def start-state-def JVM-start-state'-def intro: lock-thread-okI
ts-okI split: if-split-asm)
qed

end

context JVM-conf-read begin

lemma invariant3p-correct-jvm-state-mexecdT:
  assumes wf: wf-jvm-prog $\Phi$  P
  shows invariant3p (mexecdT P) (correct-jvm-state  $\Phi$ )
unfolding correct-jvm-state-def
apply(rule invariant3pI)
apply safe
  apply(erule (1) lifting-wf.redT-preserves[OF lifting-wf-correct-state-d[OF wf]])
  apply(erule (1) execd-mthr.redT-preserves-lock-thread-ok)
done

```

```

lemma invariant3p-correct-jvm-state-mexecT:
  assumes wf: wf-jvm-prog $\Phi$  P
  shows invariant3p (mexecT P) (correct-jvm-state  $\Phi$ )
unfolding correct-jvm-state-def
apply(rule invariant3pI)
apply safe
  apply(erule (1) lifting-wf.redT-preserves[OF lifting-wf-correct-state[OF wf]])
apply(erule (1) exec-mthr.redT-preserves-lock-thread-ok)
done

lemma correct-jvm-state-preserved:
  assumes wf: wf-jvm-prog $\Phi$  P
  and correct: s  $\in$  correct-jvm-state  $\Phi$ 
  and red: P  $\vdash$  s  $\rightarrow_{ttas}^{jvm*}$  s'
  shows s'  $\in$  correct-jvm-state  $\Phi$ 
using wf red correct unfolding exec-mthr.RedT-def
by(rule invariant3p-rtrancl3p[OF invariant3p-correct-jvm-state-mexecT])

theorem jvm-typesafe:
  assumes wf: wf-jvm-prog $\Phi$  P
  and start: wf-start-state P C M vs
  and exec: P  $\vdash$  JVM-start-state P C M vs  $\rightarrow_{ttas}^{jvm*}$  s'
  shows s'  $\in$  correct-jvm-state  $\Phi$ 
by(rule correct-jvm-state-preserved[OF wf - exec])(rule correct-jvm-state-initial[OF wf start])

end

declare (in JVM-typesafe) split-paired-Ex [simp del]

context JVM-heap-conf-base' begin

lemma execd-NewThread-Thread-Object:
  assumes wf: wf-jvm-prog $\Phi$  P
  and conf:  $\Phi \vdash t': \sigma \checkmark$ 
  and red: P, t'  $\vdash$  Normal  $\sigma \rightarrow_{ta-jvmd}$  Normal  $\sigma'$ 
  and nt: NewThread t x m  $\in$  set  $\{ta\}_t$ 
  shows  $\exists C. \text{typeof-addr} (\text{fst} (\text{snd } \sigma')) (\text{thread-id2addr } t) = \lfloor \text{Class-type } C \rfloor \wedge P \vdash \text{Class } C \leq \text{Class Thread}$ 
proof –
  from wf obtain wfmd where wfp: wf-prog wfmd P by(blast dest: wt-jvm-progD)
  from red obtain h f Frs xcp
  where check: check P  $\sigma$ 
  and exec: (ta,  $\sigma'$ )  $\in$  exec P t'  $\sigma$ 
  and [simp]:  $\sigma = (xcp, h, f \# Frs)$ 
  by(rule jvmd-NormalE)
  obtain xcp' h' frs' where [simp]:  $\sigma' = (xcp', h', frs')$  by(cases  $\sigma'$ , auto)
  obtain stk loc C M pc where [simp]: f = (stk, loc, C, M, pc) by(cases f, blast)
  from exec nt have [simp]: xcp = None by(cases xcp, auto)
  from  $\langle \Phi \vdash t': \sigma \checkmark \rangle$  obtain Ts T mxs mxl0 ins xt ST LT
  where hconf h
  and P, h  $\vdash t' \checkmark$ 
  and sees: P  $\vdash C$  sees M: Ts  $\rightarrow T = \lfloor (mxs, mxl0, ins, xt) \rfloor$  in C
  and  $\Phi C M ! pc = \lfloor (ST, LT) \rfloor$ 

```

```

and conf-f  $P\ h\ (ST, LT)\ ins\ (stk, loc, C, M, pc)$ 
and conf-fs  $P\ h\ \Phi\ M\ (length\ Ts)\ T\ Frs$ 
by(fastforce simp add: correct-state-def)
from wf red conf nt
obtain  $h\ frs\ a\ stk\ loc\ C\ M\ pc\ M'\ n\ ta'\ va\ h'$ 
  where  $ha: typeof\_addr\ h\ a \neq None$  and  $ta: ta = extTA2JVM\ P\ ta'$ 
  and  $\sigma': \sigma' = extRet2JVM\ n\ h'\ stk\ loc\ C\ M\ pc\ frs\ va$ 
  and  $rel: (ta', va, h') \in red\_external\_aggr\ P\ t'\ a\ M'\ (rev\ (take\ n\ stk))\ h$ 
  by  $-(erule\ (2)\ exec\_new\_threadE, fastforce+)$ 
from nt ta obtain  $x'$  where  $NewThread\ t\ x'\ m \in set\ \{\!\{ta'\}\!\}_t$  by auto
from red-external-aggr-new-thread-exists-thread-object[OF rel ha this]  $\sigma'$ 
show ?thesis by(cases va) auto
qed

```

**lemma** *mexecdT-NewThread-Thread-Object*:

```

 $\llbracket wf\_jvm\_prog\ \Phi\ P; correct\_state\_ts\ \Phi\ (thr\ s)\ (shr\ s); P \vdash s -t' \triangleright ta \rightarrow_{jvmd}\ s'; NewThread\ t\ x\ m \in set\ \{\!\{ta'\}\!\}_t \rrbracket$ 
 $\implies \exists C. typeof\_addr\ (shr\ s')\ (thread\_id2addr\ t) = \lfloor Class\_type\ C \rfloor \wedge P \vdash C \preceq^* Thread$ 
apply(frule correct-state-ts-thread-conf)
apply(erule execd-mthr.redT.cases)
apply(hypsubst)
apply(frule (2) execd-tconf.redT-updTs-preserves[where  $ln' = redT\_updLns\ (locks\ s)\ t'\ no\_wait\_locks\ \{\!\{ta'\}\!\}_t$ ])
apply clarsimp
apply(clarsimp)
apply(drule execd-NewThread-Thread-Object)
apply(drule (1) ts-okD)
apply(fastforce)
apply(assumption)
apply(fastforce)
apply(clarsimp)
apply(simp)
done

```

**end**

**context** *JVM-heap* **begin**

**lemma** *exec-ta-satisfiable*:

```

assumes  $P, t \vdash s -ta-jvm \rightarrow s'$ 
shows  $\exists s. exec\_mthr.actions-ok\ s\ t\ ta$ 
proof -
obtain  $xcp\ h\ frs$  where  $[simp]: s = (xcp, h, frs)$  by(cases s)
from assms obtain  $stk\ loc\ C\ M\ pc\ frs'$  where  $[simp]: frs = (stk, loc, C, M, pc) \# frs'$ 
  by(cases frs)(auto simp add: exec-1-iff)
show ?thesis
proof(cases xcp)
  case Some with assms show ?thesis by(auto simp add: exec-1-iff lock-ok-las-def finfun-upd-apply split-paired-Ex)
next
  case None
with assms show ?thesis
  apply(cases instrs-of  $P\ C\ M\ !\ pc$ )
  apply(auto simp add: exec-1-iff lock-ok-las-def finfun-upd-apply split-beta final-thread.actions-ok-iff)

```

```

split: if-split-asm dest: red-external-aggr-ta-satisfiable[where final=JVM-final])
  apply(fastforce simp add: final-thread.actions-ok-iff lock-ok-las-def dest: red-external-aggr-ta-satisfiable[where
final=JVM-final])
    apply(fastforce simp add: finfun-upd-apply intro: exI[where x=K$ None] exI[where x=K$  $\lfloor(t,$ 
0)]]) may-lock.intros)+
  done
qed
qed

end

context JVM-typesafe begin

lemma execd-wf-progress:
  assumes wf: wf-jvm-prog $\Phi$  P
  shows progress JVM-final (mexecd P) (execd-mthr.wset-Suspend-ok P (correct-jvm-state  $\Phi$ ))
  (is progress - - ?wf-state)
proof
{
  fix s t x ta x' m' w
  assume mexecd: mexecd P t (x, shr s) ta (x', m')
  and Suspend: Suspend w  $\in$  set  $\{ta\}_w$ 
  from mexecd-Suspend-Invoke[OF mexecd Suspend]
  show  $\neg$  JVM-final x' by auto
}
note Suspend-final = this
{
  fix s
  assume s: s  $\in$  ?wf-state
  hence lock-thread-ok (locks s) (thr s)
  by(auto dest: execd-mthr.wset-Suspend-okD1 simp add: correct-jvm-state-def)
  moreover
  have exec-mthr.wset-final-ok (wset s) (thr s)
  proof(rule exec-mthr.wset-final-okI)
  fix t w
  assume wset s t =  $\lfloor w \rfloor$ 
  from execd-mthr.wset-Suspend-okD2[OF s this]
  obtain x0 ta x m1 w' ln'' and s0 :: ('addr, 'thread-id, 'addr option  $\times$  'addr frame list, 'heap,
'addr) state
  where mexecd: mexecd P t (x0, shr s0) ta (x, m1)
  and Suspend: Suspend w'  $\in$  set  $\{ta\}_w$ 
  and tst: thr s t =  $\lfloor(x, ln'')\rfloor$  by blast
  from Suspend-final[OF mexecd Suspend] tst
  show  $\exists x ln. thr s t = \lfloor(x, ln)\rfloor \wedge \neg$  JVM-final x by blast
  qed
  ultimately show lock-thread-ok (locks s) (thr s)  $\wedge$  exec-mthr.wset-final-ok (wset s) (thr s) ..
}
next
fix s t x ta x' m'
assume wfs: s  $\in$  ?wf-state
  and thr s t =  $\lfloor(x, no-wait-locks)\rfloor$ 
  and mexecd P t (x, shr s) ta (x', m')
  and wait:  $\neg$  waiting (wset s t)
  moreover obtain ls ts h ws is where s [simp]: s = (ls, (ts, h), ws, is) by(cases s) fastforce

```

```

ultimately have  $ts\ t = \lfloor (x, \text{no-wait-locks}) \rfloor$   $\text{mexecd } P\ t\ (x, h)\ ta\ (x', m')$  by auto
from wfs have correct-state-ts  $\Phi\ ts\ h$  by (auto dest: execd-mthr.wset-Suspend-okD1 simp add: correct-jvm-state-def)
from wf obtain wfmd where wfp: wf-prog wfmd P by (auto dest: wt-jvm-progD)

from  $\langle ts\ t = \lfloor (x, \text{no-wait-locks}) \rfloor \rangle$   $\langle \text{mexecd } P\ t\ (x, h)\ ta\ (x', m') \rangle$ 
obtain xcp frs xcp' frs'
  where  $P, t \vdash \text{Normal } (xcp, h, frs) \text{ --ta--jvmd--> Normal } (xcp', m', frs')$ 
  and  $[simp]: x = (xcp, frs)\ x' = (xcp', frs')$ 
  by (cases x, auto)
then obtain f Frs
  where check: check P (xcp, h, f # Frs)
  and  $[simp]: frs = f \# Frs$ 
  and exec:  $(ta, xcp', m', frs') \in \text{exec } P\ t\ (xcp, h, f \# Frs)$ 
  by (auto elim: jvmd-NormalE)
with  $\langle ts\ t = \lfloor (x, \text{no-wait-locks}) \rfloor \rangle$   $\langle \text{correct-state-ts } \Phi\ ts\ h \rangle$ 
have correct:  $\Phi \vdash t: (xcp, h, f \# Frs) \checkmark$  by (auto dest: ts-okD)
obtain stk loc C M pc where  $f [simp]: f = (stk, loc, C, M, pc)$  by (cases f)
from correct obtain Ts T mxs mxl0 ins xt ST LT
  where hconf: hconf h
  and tconf:  $P, h \vdash t \checkmark$ 
  and sees:  $P \vdash C \text{ sees } M: Ts \rightarrow T = \lfloor (mxs, mxl0, ins, xt) \rfloor \text{ in } C$ 
  and wt:  $\Phi\ C\ M\ !\ pc = \lfloor (ST, LT) \rfloor$ 
  and conf-f: conf-f P h (ST, LT) ins (stk, loc, C, M, pc)
  and confs: conf-fs P h  $\Phi\ M$  (length Ts) T Frs
  and confxcp: conf-xcp P h xcp (ins ! pc)
  and preh: preallocated h
  by (fastforce simp add: correct-state-def)

have  $\exists ta' \sigma'. P, t \vdash \text{Normal } (xcp, h, (stk, loc, C, M, pc) \# Frs) \text{ --ta'--jvmd--> Normal } \sigma' \wedge$ 
   $(\text{final-thread.actions-ok JVM-final } (ls, (ts, h), ws, is)\ t\ ta' \vee$ 
   $\text{final-thread.actions-ok}' (ls, (ts, h), ws, is)\ t\ ta' \wedge \text{final-thread.actions-subset } ta' ta)$ 
proof (cases final-thread.actions-ok' (ls, (ts, h), ws, is) t ta)
  case True
    have final-thread.actions-subset ta ta ..
    with True  $\langle P, t \vdash \text{Normal } (xcp, h, frs) \text{ --ta--jvmd--> Normal } (xcp', m', frs') \rangle$ 
    show ?thesis by auto
  next
    case False
    note naok = this
    have ws: wset s t = None  $\vee$ 
       $(\exists n\ a\ T\ w. ins\ !\ pc = \text{Invoke wait } n \wedge stk\ !\ n = \text{Addr } a \wedge \text{typeof-addr } h\ a = \lfloor T \rfloor \wedge \text{is-native } P\ T\ wait \wedge wset\ s\ t = \lfloor \text{PostWS } w \rfloor \wedge xcp = \text{None})$ 
    proof (cases wset s t)
      case None thus ?thesis ..
    next
      case (Some w)
      from execd-mthr.wset-Suspend-okD2 [OF wfs this]  $\langle ts\ t = \lfloor (x, \text{no-wait-locks}) \rfloor \rangle$ 
      obtain xcp0 frs0 h0 ta0 w' s1 tta1
        where red0:  $\text{mexecd } P\ t\ ((xcp0, frs0), h0)\ ta0\ ((xcp, frs), shr\ s1)$ 
        and Suspend:  $\text{Suspend } w' \in \text{set } \{\{ta0\}\}_w$ 
        and s1:  $P \vdash s1 \text{ --> tta1 --> jvmd* } s$ 
        by auto
      from mexecd-Suspend-Invoke [OF red0 Suspend] sees

```

**obtain**  $n$   $a$   $T$  **where**  $[simp]: ins ! pc = Invoke\ wait\ n\ xcp = None\ stk ! n = Addr\ a$   
**and**  $type: typeof\text{-}addr\ h0\ a = \lfloor T \rfloor$   
**and**  $iec: is\text{-}native\ P\ T\ wait$   
**by**( $auto\ simp\ add: is\text{-}native.simps$ ) *blast*

**from**  $red0$  **have**  $h0 \sqsubseteq shr\ s1$  **by**( $auto\ dest: exec\text{-}1\text{-}d\text{-}hext$ )  
**also from**  $s1$  **have**  $shr\ s1 \sqsubseteq shr\ s$  **by**( $rule\ Execd\text{-}hext$ )  
**finally have**  $typeof\text{-}addr\ (shr\ s)\ a = \lfloor T \rfloor$  **using**  $type$   
**by**( $rule\ typeof\text{-}addr\ hext\text{-}mono$ )  
**moreover from**  $Some\ wait\ s$  **obtain**  $w'$  **where**  $ws\ t = \lfloor PostWS\ w' \rfloor$   
**by**( $auto\ simp\ add: not\text{-}waiting\text{-}iff$ )  
**ultimately show**  $?thesis$  **using**  $iec\ s$  **by**  $auto$   
**qed**

**from**  $ws\ naok\ exec\ sees$   
**show**  $?thesis$

**proof**( $cases\ ins ! pc$ )

**case** ( $Invoke\ M'\ n$ )

**from**  $ws\ Invoke\ check\ exec\ sees\ naok$  **obtain**  $a\ Ts\ U\ Ta\ Us\ D\ D'$

**where**  $a: stk ! n = Addr\ a$

**and**  $n: n < length\ stk$

**and**  $Ta: typeof\text{-}addr\ h\ a = \lfloor Ta \rfloor$

**and**  $wtext: P \vdash class\text{-}type\text{-}of\ Ta\ sees\ M': Us \rightarrow U = Native\ in\ D'\ D'.M'(Us)::U$

**and**  $sub: P \vdash Ts \sqsubseteq Us$

**and**  $Ts: map\ typeof_h\ (rev\ (take\ n\ stk)) = map\ Some\ Ts$

**and**  $[simp]: xcp = None$

**apply**( $cases\ xcp$ )

**apply**( $simp\ add: is\text{-}Ref\text{-}def\ has\text{-}method\text{-}def\ external\text{-}WT'\text{-}iff\ check\text{-}def\ lock\text{-}ok\text{-}las'\text{-}def\ confs\text{-}conv\text{-}map$   
 $split\text{-}beta\ split: if\text{-}split\text{-}asm\ option.splits$ )

**apply**( $auto\ simp\ add: lock\text{-}ok\text{-}las'\text{-}def$ )[2]

**apply**( $fastforce\ simp\ add: is\text{-}native.simps\ lock\text{-}ok\text{-}las'\text{-}def\ dest: sees\text{-}method\text{-}fun$ ) +  
**done**

**from**  $exec\ Ta\ n\ a\ sees\ Invoke\ wtext$  **obtain**  $ta'\ va\ m''$

**where**  $exec': (ta', va, m'') \in red\text{-}external\text{-}aggr\ P\ t\ a\ M'\ (rev\ (take\ n\ stk))\ h$

**and**  $ta: ta = extTA2JVM\ P\ ta'$

**and**  $va: (xcp', m', frs') = extRet2JVM\ n\ m''\ stk\ loc\ C\ M\ pc\ Frs\ va$

**by**( $auto$ )

**from**  $va$  **have**  $[simp]: m'' = m'$  **by**( $cases\ va$ )  $simp\text{-}all$

**from**  $Ta\ Ts\ wtext\ sub$  **have**  $wtext': P, h \vdash a.M'(rev\ (take\ n\ stk)) : U$

**by**( $auto\ intro!: external\text{-}WT'.intros\ simp\ add: is\text{-}native.simps$ )

**with**  $wfp\ exec'\ tconf$  **have**  $red: P, t \vdash \langle a.M'(rev\ (take\ n\ stk)), h \rangle - ta' \rightarrow ext\ \langle va, m' \rangle$

**by**( $simp\ add: WT\text{-}red\text{-}external\text{-}list\text{-}conv$ )

**from**  $ws\ Invoke$  **have**  $wset\ s\ t = None \vee M' = wait \wedge (\exists w. wset\ s\ t = \lfloor PostWS\ w \rfloor)$  **by**  $auto$

**with**  $wfp\ red\ tconf\ hconf$  **obtain**  $ta''\ va'\ h''$

**where**  $red': P, t \vdash \langle a.M'(rev\ (take\ n\ stk)), h \rangle - ta'' \rightarrow ext\ \langle va', h' \rangle$

**and**  $ok': final\text{-}thread.actions\text{-}ok\ JVM\text{-}final\ s\ t\ ta'' \vee final\text{-}thread.actions\text{-}ok'\ s\ t\ ta'' \wedge final\text{-}thread.actions\text{-}subset\ ta''\ ta'$

**by**( $rule\ red\text{-}external\text{-}wf\text{-}red$ )

**from**  $red'\ a\ n\ Ta\ Invoke\ sees\ wtext$

**have**  $(extTA2JVM\ P\ ta'', extRet2JVM\ n\ h''\ stk\ loc\ C\ M\ pc\ Frs\ va') \in exec\ P\ t\ (xcp, h, f \# Frs)$

**by**( $auto\ intro: red\text{-}external\text{-}imp\text{-}red\text{-}external\text{-}aggr$ )

**with**  $check$  **have**  $P, t \vdash Normal\ (xcp, h, (stk, loc, C, M, pc) \# Frs) - extTA2JVM\ P\ ta'' - jvmd \rightarrow$   
 $Normal\ (extRet2JVM\ n\ h''\ stk\ loc\ C\ M\ pc\ Frs\ va')$

**by**  $-(rule\ exec\text{-}1\text{-}d.exec\text{-}1\text{-}d\text{-}NormalI, auto\ simp\ add: exec\text{-}d\text{-}def)$

```

moreover from  $ok' ta$ 
have  $final-thread.actions-ok JVM-final (ls, (ts, h), ws, is) t (extTA2JVM P ta'') \vee$ 
 $final-thread.actions-ok' (ls, (ts, h), ws, is) t (extTA2JVM P ta'') \wedge final-thread.actions-subset$ 
 $(extTA2JVM P ta'') ta$ 
by( $auto simp add: final-thread.actions-ok'-convert-extTA elim: final-thread.actions-subset.cases$ 
 $del: subsetI$ )
ultimately show  $?thesis$  by  $blast$ 
next
case  $MEnter$ 
with  $exec sees naok ws$  have  $False$ 
by( $cases xcp$ )( $auto split: if-split-asm simp add: lock-ok-las'-def finfun-upd-apply ta-upd-simps$ )
thus  $?thesis ..$ 
next
case  $MExit$ 
with  $exec sees False check ws$  obtain  $a$  where  $[simp]: hd stk = Addr a xcp = None ws t = None$ 
and  $ta: ta = \{\!| Unlock \rightarrow a, SyncUnlock a \!\} \vee ta = \{\!| UnlockFail \rightarrow a \!\}$ 
by( $cases xcp$ )( $fastforce split: if-split-asm simp add: lock-ok-las'-def finfun-upd-apply is-Ref-def$ 
 $check-def$ ) $+$ 
from  $ta$  show  $?thesis$ 
proof( $rule disjE$ )
assume  $ta: ta = \{\!| Unlock \rightarrow a, SyncUnlock a \!\}$ 
let  $?ta' = \{\!| UnlockFail \rightarrow a \!\}$ 
from  $ta$   $exec sees MExit$  obtain  $\sigma'$ 
where  $(?ta', \sigma') \in exec P t (xcp, h, f \# Frs)$  by  $auto$ 
with  $check$  have  $P, t \vdash Normal (xcp, h, (stk, loc, C, M, pc) \# Frs) - ?ta' - jvmd \rightarrow Normal \sigma'$ 
by  $-(rule exec-1-d.exec-1-d-NormalI, auto simp add: exec-d-def)$ 
moreover from  $False ta$  have  $has-locks (ls \$ a) t = 0$ 
by( $auto simp add: lock-ok-las'-def finfun-upd-apply ta-upd-simps$ )
hence  $final-thread.actions-ok' (ls, (ts, h), ws, is) t ?ta'$ 
by( $auto simp add: lock-ok-las'-def finfun-upd-apply ta-upd-simps$ )
moreover from  $ta$  have  $final-thread.actions-subset ?ta' ta$ 
by( $auto simp add: final-thread.actions-subset-iff collect-locks'-def finfun-upd-apply ta-upd-simps$ )
ultimately show  $?thesis$  by( $fastforce simp add: ta-upd-simps$ )
next
assume  $ta: ta = \{\!| UnlockFail \rightarrow a \!\}$ 
let  $?ta' = \{\!| Unlock \rightarrow a, SyncUnlock a \!\}$ 
from  $ta$   $exec sees MExit$  obtain  $\sigma'$ 
where  $(?ta', \sigma') \in exec P t (xcp, h, f \# Frs)$  by  $auto$ 
with  $check$  have  $P, t \vdash Normal (xcp, h, (stk, loc, C, M, pc) \# Frs) - ?ta' - jvmd \rightarrow Normal \sigma'$ 
by  $-(rule exec-1-d.exec-1-d-NormalI, auto simp add: exec-d-def)$ 
moreover from  $False ta$  have  $has-lock (ls \$ a) t$ 
by( $auto simp add: lock-ok-las'-def finfun-upd-apply ta-upd-simps$ )
hence  $final-thread.actions-ok' (ls, (ts, h), ws, is) t ?ta'$ 
by( $auto simp add: lock-ok-las'-def finfun-upd-apply ta-upd-simps$ )
moreover from  $ta$  have  $final-thread.actions-subset ?ta' ta$ 
by( $auto simp add: final-thread.actions-subset-iff collect-locks'-def finfun-upd-apply ta-upd-simps$ )
ultimately show  $?thesis$  by( $fastforce simp add: ta-upd-simps$ )
qed
qed( $case-tac [!]$   $xcp, auto simp add: split-beta lock-ok-las'-def split: if-split-asm$ )
qed
thus  $\exists ta' x' m'. mexecd P t (x, shr s) ta' (x', m') \wedge$ 
 $(final-thread.actions-ok JVM-final s t ta' \vee$ 
 $final-thread.actions-ok' s t ta' \wedge final-thread.actions-subset ta' ta)$ 
by  $fastforce$ 

```



```

next
  fix s t x
  assume wfs: s ∈ ?wf-state
    and tst: thr s t = [(x, no-wait-locks)]
    and ¬ JVM-final x
  from wfs have correct: correct-state-ts Φ (thr s) (shr s)
  by(auto dest: execd-mthr.wset-Suspend-okD1 simp add: correct-jvm-state-def)
  obtain xcp frs where x: x = (xcp, frs) by (cases x, auto)
  with ⟨¬ JVM-final x⟩ obtain f Frs where frs = f # Frs
  by(fastforce simp add: neg-Nil-conv)
  with tst correct x have Φ ⊢ t: (xcp, shr s, f # Frs) √ by(auto dest: ts-okD)
  with ⟨wf-jvm-progΦ P⟩
  have exec-d P t (xcp, shr s, f # Frs) ≠ TypeError by(auto dest: no-type-error)
  then obtain Σ where exec-d P t (xcp, shr s, f # Frs) = Normal Σ by(auto)
  hence exec P t (xcp, shr s, f # Frs) = Σ
  by(auto simp add: exec-d-def check-def split: if-split-asm)
  with progress[OF wf ⟨Φ ⊢ t: (xcp, shr s, f # Frs) √⟩]
  obtain ta σ where (ta, σ) ∈ Σ unfolding exec-1-iff by blast
  with ⟨x = (xcp, frs)⟩ ⟨frs = f # Frs⟩ ⟨Φ ⊢ t: (xcp, shr s, f # Frs) √⟩
  ⟨wf-jvm-progΦ P⟩ ⟨exec-d P t (xcp, shr s, f # Frs) = Normal Σ⟩
  show ∃ ta x' m'. mexecd P t (x, shr s) ta (x', m')
  by(cases ta, cases σ)(fastforce simp add: split-paired-Ex intro: exec-1-d-NormalI)
qed(fastforce dest: defensive-imp-aggressive-1 mexec-instr-Wakeup-no-Join exec-ta-satisfiable)+

end

context JVM-conf-read begin

lemma mexecT-eq-mexecdT:
  assumes wf: wf-jvm-progΦ P
  and cs: correct-state-ts Φ (thr s) (shr s)
  shows P ⊢ s -t>ta→jvm s' = P ⊢ s -t>ta→jvmd s'
proof(rule iffI)
  assume P ⊢ s -t>ta→jvm s'
  thus P ⊢ s -t>ta→jvmd s'
proof(cases rule: exec-mthr.redT-elim[consumes 1, case-names normal acquire])
  case (normal x x' m')
  obtain xcp frs where x [simp]: x = (xcp, frs) by(cases x, auto)
  from ⟨thr s t = [(x, no-wait-locks)]⟩ cs
  have Φ ⊢ t: (xcp, shr s, frs) √ by(auto dest: ts-okD)
  from mexec-eq-mexecd[OF wf ⟨Φ ⊢ t: (xcp, shr s, frs) √⟩] ⟨mexec P t (x, shr s) ta (x', m')⟩
  have *: mexecd P t (x, shr s) ta (x', m') by simp
  with lifting-wf.redT-updTs-preserves[OF lifting-wf-correct-state-d[OF wf] cs, OF this ⟨thr s t =
  [(x, no-wait-locks)]⟩] ⟨thread-oks (thr s) {ta}_t⟩
  have correct-state-ts Φ ((redT-updTs (thr s) {ta}_t)(t ↦ (x', redT-updLns (locks s) t no-wait-locks
  {ta}_t))) m' by simp
  with * show ?thesis using normal
  by(cases s')(erule execd-mthr.redT-normal, auto)
next
  case acquire thus ?thesis
  apply(cases s', clarify)
  apply(rule execd-mthr.redT-acquire, assumption+)
  by(auto)
qed

```

next

assume  $P \vdash s \rightarrow_{\text{ta}} \text{jvmd } s'$

thus  $P \vdash s \rightarrow_{\text{ta}} \text{jvm } s'$

proof (cases rule: *execd-mthr.redT-elim*[consumes 1, case-names normal acquire])

case (normal  $x \ x' \ m'$ )

obtain  $xcp \ frs$  where  $x \ [simp]: x = (xcp, frs)$  by (cases  $x$ , auto)

from  $\langle thr \ s \ t = \lfloor (x, no\text{-}wait\text{-}locks) \rfloor \rangle \ cs$

have  $\Phi \vdash t: (xcp, shr \ s, frs) \checkmark$  by (auto dest: *ts-okD*)

from *mexec-eq-mexecd*[*OF wf*  $\langle \Phi \vdash t: (xcp, shr \ s, frs) \checkmark \rangle$ ]  $\langle mexecd \ P \ t \ (x, shr \ s) \ ta \ (x', m') \rangle$

have *mexec*  $P \ t \ (x, shr \ s) \ ta \ (x', m')$  by *simp*

moreover from *lifting-wf.redT-updTs-preserves*[*OF lifting-wf-correct-state-d*[*OF wf*]  $cs$ , *OF*  $\langle mexecd \ P \ t \ (x, shr \ s) \ ta \ (x', m') \rangle \langle thr \ s \ t = \lfloor (x, no\text{-}wait\text{-}locks) \rfloor \rangle \langle thread\text{-}oks \ (thr \ s) \ \{\!\!| \ ta \ |\!\!\}_t \rangle$

have *correct-state-ts*  $\Phi \ ((redT\text{-}updTs \ (thr \ s) \ \{\!\!| \ ta \ |\!\!\}_t)(t \mapsto (x', redT\text{-}updLns \ (locks \ s) \ t \ no\text{-}wait\text{-}locks \ \{\!\!| \ ta \ |\!\!\}_t))) \ m'$  by *simp*

ultimately show ?thesis using normal

by (cases  $s'$ ) (erule *exec-mthr.redT-normal*, auto)

next

case *acquire* thus ?thesis

apply (cases  $s'$ , clarify)

apply (rule *exec-mthr.redT-acquire*, assumption+)

by (auto)

qed

qed

lemma *mExecT-eq-mExecdT*:

assumes *wf*: *wf-jvm-prog* $\Phi \ P$

and *ct*: *correct-state-ts*  $\Phi \ (thr \ s) \ (shr \ s)$

shows  $P \vdash s \rightarrow_{\text{ttas}} \text{jvm}^* s' = P \vdash s \rightarrow_{\text{ttas}} \text{jvmd}^* s'$

proof

assume *Red*:  $P \vdash s \rightarrow_{\text{ttas}} \text{jvm}^* s'$

thus  $P \vdash s \rightarrow_{\text{ttas}} \text{jvmd}^* s'$  using *ct*

proof (induct rule: *exec-mthr.RedT-induct*[consumes 1, case-names refl step])

case *refl* thus ?case by auto

next

case (step  $s \ ttas \ s' \ t \ ta \ s''$ )

hence  $P \vdash s \rightarrow_{\text{ttas}} \text{jvmd}^* s'$  by *blast*

moreover from  $\langle correct\text{-}state\text{-}ts \ \Phi \ (thr \ s) \ (shr \ s) \rangle \langle P \vdash s \rightarrow_{\text{ttas}} \text{jvm}^* s' \rangle$

have *correct-state-ts*  $\Phi \ (thr \ s') \ (shr \ s')$

by (auto dest: *preserves-correct-state*[*OF wf*])

with  $\langle P \vdash s' \rightarrow_{\text{ta}} \text{jvm} \ s'' \rangle$  have  $P \vdash s' \rightarrow_{\text{ta}} \text{jvmd} \ s''$

by (unfold *mexecT-eq-mexecdT*[*OF wf*])

ultimately show ?case

by (blast intro: *execd-mthr.RedTI rtrancl3p-step elim: execd-mthr.RedTE*)

qed

next

assume *Red*:  $P \vdash s \rightarrow_{\text{ttas}} \text{jvmd}^* s'$

thus  $P \vdash s \rightarrow_{\text{ttas}} \text{jvm}^* s'$  using *ct*

proof (induct rule: *execd-mthr.RedT-induct*[consumes 1, case-names refl step])

case *refl* thus ?case by auto

next

case (step  $s \ ttas \ s' \ t \ ta \ s''$ )

hence  $P \vdash s \rightarrow_{\text{ttas}} \text{jvmd}^* s'$  by *blast*

moreover from  $\langle correct\text{-}state\text{-}ts \ \Phi \ (thr \ s) \ (shr \ s) \rangle \langle P \vdash s \rightarrow_{\text{ttas}} \text{jvmd}^* s' \rangle$

```

have correct-state-ts  $\Phi$  (thr s') (shr s')
  by(auto dest: preserves-correct-state-d[OF wf])
with  $\langle P \vdash s' - t \triangleright ta \rightarrow_{jvmd} s'' \rangle$  have  $P \vdash s' - t \triangleright ta \rightarrow_{jvm} s''$ 
  by(unfold mexecT-eq-mexecdT[OF wf])
ultimately show ?case
  by(blast intro: exec-mthr.RedTI rtrancl3p-step elim: exec-mthr.RedTE)
qed
qed

```

**lemma** *mexecT-preserves-thread-conf*:

```

[[ wf-jvm-prog $\Phi$  P; correct-state-ts  $\Phi$  (thr s) (shr s);
  P  $\vdash s - t \triangleright ta \rightarrow_{jvm} s'$ ; thread-conf P (thr s) (shr s) ]]
 $\implies$  thread-conf P (thr s') (shr s')
by(simp only: mexecT-eq-mexecdT)(rule execd-tconf.redT-preserves)

```

**lemma** *mExecT-preserves-thread-conf*:

```

[[ wf-jvm-prog $\Phi$  P; correct-state-ts  $\Phi$  (thr s) (shr s);
  P  $\vdash s - \triangleright tta \rightarrow_{jvm^*} s'$ ; thread-conf P (thr s) (shr s) ]]
 $\implies$  thread-conf P (thr s') (shr s')
by(simp only: mExecT-eq-mExecdT)(rule execd-tconf.RedT-preserves)

```

**lemma** *wset-Suspend-ok-mexecd-mexec*:

```

assumes wf: wf-jvm-prog $\Phi$  P
shows exec-mthr.wset-Suspend-ok P (correct-jvm-state  $\Phi$ ) = execd-mthr.wset-Suspend-ok P (correct-jvm-state  $\Phi$ )
apply(safe)
apply(rule execd-mthr.wset-Suspend-okI)
apply(erule exec-mthr.wset-Suspend-okD1)
apply(drule (1) exec-mthr.wset-Suspend-okD2)
apply(subst (asm) (2) split-paired-Ex)
apply(elim bexE exE conjE)
apply(subst (asm) mexec-eq-mexecdT[OF wf])
apply(simp add: correct-jvm-state-def)
apply(blast dest: ts-okD)
apply(subst (asm) mexecT-eq-mexecdT[OF wf])
apply(simp add: correct-jvm-state-def)
apply(subst (asm) mExecT-eq-mExecdT[OF wf])
apply(simp add: correct-jvm-state-def)
apply(rule bexI exI|erule conjI|assumption)+
apply(rule exec-mthr.wset-Suspend-okI)
apply(erule execd-mthr.wset-Suspend-okD1)
apply(drule (1) execd-mthr.wset-Suspend-okD2)
apply(subst (asm) (2) split-paired-Ex)
apply(elim bexE exE conjE)
apply(subst (asm) mexec-eq-mexecdT[OF wf, symmetric])
apply(simp add: correct-jvm-state-def)
apply(blast dest: ts-okD)
apply(subst (asm) mexecT-eq-mexecdT[OF wf, symmetric])
apply(simp add: correct-jvm-state-def)
apply(subst (asm) mExecT-eq-mExecdT[OF wf, symmetric])
apply(simp add: correct-jvm-state-def)
apply(rule bexI exI|erule conjI|assumption)+
done

```

end

context *JVM-typesafe* begin

lemma *exec-wf-progress*:

assumes *wf*: *wf-jvm-prog* $\Phi$  *P*

shows *progress JVM-final* (*mexec P*) (*exec-mthr.wset-Suspend-ok P* (*correct-jvm-state*  $\Phi$ ))

(is *progress* - - ?*wf-state*)

proof –

interpret *progress*: *progress JVM-final mexecd P convert-RA ?wf-state*

using *assms unfolding wset-Suspend-ok-mexecd-mexec*[*OF wf*] **by**(rule *execd-wf-progress*)

show ?*thesis*

proof(*unfold-locales*)

fix *s*

assume *s*  $\in$  ?*wf-state*

thus *lock-thread-ok* (*locks s*) (*thr s*)  $\wedge$  *exec-mthr.wset-final-ok* (*wset s*) (*thr s*)

**by**(rule *progress.wf-stateD*)

next

fix *s t x ta x' m'*

assume *wfs*: *s*  $\in$  ?*wf-state*

and *tst*: *thr s t* =  $\lfloor (x, \text{no-wait-locks}) \rfloor$

and *exec*: *mexec P t* (*x, shr s*) *ta* (*x', m'*)

and *wait*:  $\neg$  *waiting* (*wset s t*)

from *wfs tst* **have** *correct*:  $\Phi \vdash t$ : (*fst x, shr s, snd x*)  $\checkmark$

**by**(auto *dest!*: *exec-mthr.wset-Suspend-okD1 ts-okD simp add*: *correct-jvm-state-def*)

with *exec* **have** *mexecd P t* (*x, shr s*) *ta* (*x', m'*)

**by**(*cases x*)(*simp only*: *mexec-eq-mexecd*[*OF wf*] *fst-conv snd-conv*)

from *progress.wf-red*[*OF wfs tst this wait*] *correct*

show  $\exists ta' x' m'. \text{mexec } P \text{ } t \text{ } (x, \text{shr } s) \text{ } ta' \text{ } (x', m') \wedge$

$(\text{final-thread.actions-ok JVM-final } s \text{ } t \text{ } ta' \vee$

$\text{final-thread.actions-ok}' s \text{ } t \text{ } ta' \wedge \text{final-thread.actions-subset } ta' \text{ } ta)$

**by**(*cases x*)(*simp only*: *fst-conv snd-conv mexec-eq-mexecd*[*OF wf*])

next

fix *s t x ta x' m' w*

assume *wfs*: *s*  $\in$  ?*wf-state*

and *tst*: *thr s t* =  $\lfloor (x, \text{no-wait-locks}) \rfloor$

and *exec*: *mexec P t* (*x, shr s*) *ta* (*x', m'*)

and *wait*:  $\neg$  *waiting* (*wset s t*)

and *Suspend*: *Suspend w*  $\in$  *set*  $\{ta\}_w$

from *wfs tst* **have** *correct*:  $\Phi \vdash t$ : (*fst x, shr s, snd x*)  $\checkmark$

**by**(auto *dest!*: *exec-mthr.wset-Suspend-okD1 ts-okD simp add*: *correct-jvm-state-def*)

with *exec* **have** *mexecd P t* (*x, shr s*) *ta* (*x', m'*)

**by**(*cases x*)(*simp only*: *mexec-eq-mexecd*[*OF wf*] *fst-conv snd-conv*)

with *wfs tst* **show**  $\neg$  *JVM-final* *x'* **using** *wait Suspend* **by**(rule *progress.red-wait-set-not-final*)

next

fix *s t x*

assume *wfs*: *s*  $\in$  ?*wf-state*

and *tst*: *thr s t* =  $\lfloor (x, \text{no-wait-locks}) \rfloor$

and  $\neg$  *JVM-final* *x*

from *progress.wf-progress*[*OF this*]

show  $\exists ta x' m'. \text{mexec } P \text{ } t \text{ } (x, \text{shr } s) \text{ } ta \text{ } (x', m')$

**by**(auto *dest*: *defensive-imp-aggressive-1 simp add*: *split-beta*)

qed(*fastforce dest*: *mexec-instr-Wakeup-no-Join exec-ta-satisfiable*)+

qed

**theorem** *mexecd-TypeSafety*:

fixes  $ln :: 'addr \Rightarrow f \text{ nat}$   
 assumes  $wf: wf\text{-jvm-prog}_{\Phi} P$   
 and  $s: s \in \text{execd-mthr.wset-Suspend-ok } P \text{ (correct-jvm-state } \Phi)$   
 and  $Exec: P \vdash s \rightarrow_{ttas}^{*} jvmd s'$   
 and  $\neg (\exists t \ ta \ s''. P \vdash s' \rightarrow_{t \triangleright ta}^{*} jvmd s'')$   
 and  $ts't: thr \ s' \ t = \lfloor ((xcp, frs), ln) \rfloor$   
 shows  $frs \neq [] \vee ln \neq \text{no-wait-locks} \implies t \in \text{execd-mthr.deadlocked } P \ s'$   
 and  $\Phi \vdash t: (xcp, shr \ s', frs) \checkmark$   
**proof** –  
 interpret progress JVM-final mexecd P convert-RA execd-mthr.wset-Suspend-ok P (correct-jvm-state  $\Phi$ )  
 by(rule execd-wf-progress) fact+  
 from Exec s have wfs':  $s' \in \text{execd-mthr.wset-Suspend-ok } P \text{ (correct-jvm-state } \Phi)$   
 unfolding execd-mthr.RedT-def  
 by(blast intro: invariant3p-rtrancl3p execd-mthr.invariant3p-wset-Suspend-ok invariant3p-correct-jvm-state-mexecdT[OF wf])  
 with ts't show cst:  $\Phi \vdash t: (xcp, shr \ s', frs) \checkmark$   
 by(auto dest: ts-okD execd-mthr.wset-Suspend-okD1 simp add: correct-jvm-state-def)  
 assume nfin:  $frs \neq [] \vee ln \neq \text{no-wait-locks}$   
 from nfin  $\langle thr \ s' \ t = \lfloor ((xcp, frs), ln) \rfloor \rangle$  have exec-mthr.not-final-thread s' t  
 by(auto simp: exec-mthr.not-final-thread-iff)  
 from  $\langle \neg (\exists t \ ta \ s''. P \vdash s' \rightarrow_{t \triangleright ta}^{*} jvmd s'') \rangle$   
 show  $t \in \text{execd-mthr.deadlocked } P \ s'$   
**proof**(rule contrapos-np)  
 assume  $t \notin \text{execd-mthr.deadlocked } P \ s'$   
 with  $\langle \text{exec-mthr.not-final-thread } s' \ t \rangle$  have  $\neg \text{execd-mthr.deadlocked}' P \ s'$   
 by(auto simp add: execd-mthr.deadlocked'-def)  
 hence  $\neg \text{execd-mthr.deadlock } P \ s'$  unfolding execd-mthr.deadlock-eq-deadlocked'  
 thus  $\exists t \ ta \ s''. P \vdash s' \rightarrow_{t \triangleright ta}^{*} jvmd s''$  by(rule redT-progress[OF wfs'])  
 qed  
 qed

**theorem** *mexec-TypeSafety*:

fixes  $ln :: 'addr \Rightarrow f \text{ nat}$   
 assumes  $wf: wf\text{-jvm-prog}_{\Phi} P$   
 and  $s: s \in \text{exec-mthr.wset-Suspend-ok } P \text{ (correct-jvm-state } \Phi)$   
 and  $Exec: P \vdash s \rightarrow_{ttas}^{*} jvm s'$   
 and  $\neg (\exists t \ ta \ s''. P \vdash s' \rightarrow_{t \triangleright ta}^{*} jvm s'')$   
 and  $ts't: thr \ s' \ t = \lfloor ((xcp, frs), ln) \rfloor$   
 shows  $frs \neq [] \vee ln \neq \text{no-wait-locks} \implies t \in \text{multithreaded-base.deadlocked JVM-final (mexec } P) \ s'$   
 and  $\Phi \vdash t: (xcp, shr \ s', frs) \checkmark$   
**proof** –  
 interpret progress JVM-final mexec P convert-RA exec-mthr.wset-Suspend-ok P (correct-jvm-state  $\Phi$ )  
 by(rule exec-wf-progress) fact+  
 from Exec s have wfs':  $s' \in \text{exec-mthr.wset-Suspend-ok } P \text{ (correct-jvm-state } \Phi)$   
 unfolding exec-mthr.RedT-def  
 by(blast intro: invariant3p-rtrancl3p exec-mthr.invariant3p-wset-Suspend-ok invariant3p-correct-jvm-state-mexecT[OF wf])

**with**  $ts't$  **show**  $cst: \Phi \vdash t: (xcp, shr\ s', frs) \checkmark$   
**by**(*auto dest: ts-okD exec-mthr.wset-Suspend-okD1 simp add: correct-jvm-state-def*)

**assume**  $nfin: frs \neq [] \vee ln \neq no\text{-}wait\text{-}locks$   
**from**  $nfin \langle thr\ s' t = [((xcp, frs), ln)] \rangle$  **have**  $exec\text{-}mthr.\text{not-final-thread}\ s' t$   
**by**(*auto simp: exec-mthr.not-final-thread-iff*)  
**from**  $\langle \neg (\exists t\ ta\ s''. P \vdash s' -t>ta \rightarrow_{jvm} s'') \rangle$   
**show**  $t \in exec\text{-}mthr.\text{deadlocked}\ P\ s'$   
**proof**(*rule contrapos-np*)  
**assume**  $t \notin exec\text{-}mthr.\text{deadlocked}\ P\ s'$   
**with**  $\langle exec\text{-}mthr.\text{not-final-thread}\ s' t \rangle$  **have**  $\neg exec\text{-}mthr.\text{deadlocked}'\ P\ s'$   
**by**(*auto simp add: exec-mthr.deadlocked'-def*)  
**hence**  $\neg exec\text{-}mthr.\text{deadlock}\ P\ s'$  **unfolding**  $exec\text{-}mthr.\text{deadlock-eq-deadlocked}'$ .  
**thus**  $\exists t\ ta\ s''. P \vdash s' -t>ta \rightarrow_{jvm} s''$  **by**(*rule redT-progress[OF wfs<sup>1</sup>]*)  
**qed**  
**qed**

**lemma** *start-mexec-mexecd-commute:*

**assumes**  $wf: wf\text{-}jvm\text{-}prog_{\Phi}\ P$   
**and**  $start: wf\text{-}start\text{-}state\ P\ C\ M\ vs$   
**shows**  $P \vdash JVM\text{-}start\text{-}state\ P\ C\ M\ vs \rightarrow_{ttas} \rightarrow_{jvmd}^* s \longleftrightarrow P \vdash JVM\text{-}start\text{-}state\ P\ C\ M\ vs \rightarrow_{ttas} \rightarrow_{jvm}^* s$   
**using** *correct-jvm-state-initial[OF assms]*  
**by**(*clarsimp simp add: correct-jvm-state-def*)(*rule mExecT-eq-mExecdT[symmetric, OF wf]*)

**theorem** *mRtrancl-eq-mRtranclD:*

**assumes**  $wf: wf\text{-}jvm\text{-}prog_{\Phi}\ P$   
**and**  $ct: correct\text{-}state\text{-}ts\ \Phi\ (thr\ s)\ (shr\ s)$   
**shows**  $exec\text{-}mthr.\text{mthr}.\text{Rtrancl3p}\ P\ s\ ttas \longleftrightarrow execd\text{-}mthr.\text{mthr}.\text{Rtrancl3p}\ P\ s\ ttas$  (**is**  $?lhs \longleftrightarrow ?rhs$ )  
**proof**  
**show**  $?lhs$  **if**  $?rhs$  **using** *that ct*  
**proof**(*coinduction arbitrary: s ttas*)  
**case**  $Rtrancl3p$   
**interpret** *lifting-wf JVM-final mexecd P convert-RA  $\lambda t\ (xcp, frs)\ h. \Phi \vdash t: (xcp, h, frs) \checkmark$*   
**using**  $wf$  **by**(*rule lifting-wf-correct-state-d*)  
**from**  $Rtrancl3p(1)$  **show**  $?case$   
**proof** *cases*  
**case** *stop: Rtrancl3p-stop*  
**then show**  $?thesis$  **using**  $mexecT\text{-}eq\text{-}mexecdT[OF\ wf\ Rtrancl3p(2)]$  **by** *clarsimp*  
**next**  
**case**  $(Rtrancl3p\text{-}into\text{-}Rtrancl3p\ s'\ ttas'\ tta)$   
**then show**  $?thesis$  **using**  $mexecT\text{-}eq\text{-}mexecdT[OF\ wf\ Rtrancl3p(2)]\ Rtrancl3p(2)$   
**by**(*cases tta; cases s'*)(*fastforce simp add: split-paired-Ex dest: redT-preserves*)  
**qed**  
**qed**

**show**  $?rhs$  **if**  $?lhs$  **using** *that ct*

**proof**(*coinduction arbitrary: s ttas*)

**case**  $Rtrancl3p$

**interpret** *lifting-wf JVM-final mexec P convert-RA  $\lambda t\ (xcp, frs)\ h. \Phi \vdash t: (xcp, h, frs) \checkmark$*   
**using**  $wf$  **by**(*rule lifting-wf-correct-state*)

**from**  $Rtrancl3p(1)$  **show**  $?case$

**proof** *cases*

```

  case stop: Rtranc3p-stop
  then show ?thesis using mexecT-eq-mexecdT[OF wf Rtranc3p(2)] by clarsimp
next
  case (Rtranc3p-into-Rtranc3p s' ttas' tta)
  then show ?thesis using mexecT-eq-mexecdT[OF wf Rtranc3p(2)] Rtranc3p(2)
    by(cases tta; cases s')(fastforce simp add: split-paired-Ex dest: redT-preserves)
qed
qed
qed

lemma start-mRtranc1-mRtrancld-commute:
  assumes wf: wf-jvm-progΦ P
  and start: wf-start-state P C M vs
  shows exec-mthr.mthr.Rtranc3p P (JVM-start-state P C M vs) ttas  $\longleftrightarrow$  execd-mthr.mthr.Rtranc3p
    P (JVM-start-state P C M vs) ttas
  using correct-jvm-state-initial[OF assms] by(clarsimp simp add: correct-jvm-state-def mRtranc1-eq-mRtrancld[OF wf])

end

```

### 6.7.1 Determinism

context JVM-heap-conf begin

```

lemma exec-instr-deterministic:
  assumes wf: wf-prog wf-md P
  and det: deterministic-heap-ops
  and exec1: (ta', σ') ∈ exec-instr i P t (shr s) stk loc C M pc frs
  and exec2: (ta'', σ'') ∈ exec-instr i P t (shr s) stk loc C M pc frs
  and check: check-instr i P (shr s) stk loc C M pc frs
  and aok1: final-thread.actions-ok final s t ta'
  and aok2: final-thread.actions-ok final s t ta''
  and tconf: P, shr s ⊢ t √t
  shows ta' = ta'' ∧ σ' = σ''
  using exec1 exec2 aok1 aok2
  proof(cases i)
    case (Invoke M' n)
    { fix T ta''' ta'''' va' va'' h' h''
      assume T: typeof-addr (shr s) (the-Addr (stk ! n)) = ⊥T
      and method: snd (snd (snd (method P (class-type-of T) M'))) = None P ⊢ class-type-of T has
        M'
      and params: P, shr s ⊢ rev (take n stk) [≤] fst (snd (method P (class-type-of T) M'))
      and red1: (ta''', va', h') ∈ red-external-aggr P t (the-Addr (stk ! n)) M' (rev (take n stk)) (shr s)
      and red2: (ta''', va'', h'') ∈ red-external-aggr P t (the-Addr (stk ! n)) M' (rev (take n stk)) (shr
        s)
      and ta': ta' = extTA2JVM P ta'''
      and ta'': ta'' = extTA2JVM P ta''''
      from T method params obtain T' where P, shr s ⊢ the-Addr (stk ! n) · M' (rev (take n stk)) : T'
      by(fastforce simp add: has-method-def confs-conv-map external-WT'-iff)
      hence P, t ⊢ ⟨the-Addr (stk ! n) · M' (rev (take n stk)), shr s⟩ -ta'''→ext ⟨va', h'⟩
      and P, t ⊢ ⟨the-Addr (stk ! n) · M' (rev (take n stk)), shr s⟩ -ta''''→ext ⟨va'', h''⟩
      using red1 red2 tconf
      by-(rule WT-red-external-aggr-imp-red-external[OF wf], assumption+)+
      moreover from aok1 aok2 ta' ta''

```

```

    have final-thread.actions-ok final s t ta'''
    and final-thread.actions-ok final s t ta''''
    by(auto simp add: final-thread.actions-ok-iff)
    ultimately have ta''' = ta'''' ∧ va' = va'' ∧ h' = h''
    by(rule red-external-deterministic[OF det]) }
  with assms Invoke show ?thesis
  by(clarsimp simp add: split-beta split: if-split-asm) blast
next
case MExit
{ assume final-thread.actions-ok final s t {UnlockFail→the-Addr (hd stk)}
  and final-thread.actions-ok final s t {Unlock→the-Addr (hd stk), SyncUnlock (the-Addr (hd stk))}
  hence False
    by(auto simp add: final-thread.actions-ok-iff lock-ok-las-def finfun-upd-apply elim!: allE[where
x=the-Addr (hd stk)]) }
  with assms MExit show ?thesis by(auto split: if-split-asm)
qed(auto simp add: split-beta split: if-split-asm dest: deterministic-heap-ops-readD[OF det] determin-
istic-heap-ops-writeD[OF det] deterministic-heap-ops-allocateD[OF det])

lemma exec-1-deterministic:
  assumes wf: wf-jvm-progΦ P
  and det: deterministic-heap-ops
  and exec1: P, t ⊢ (xcp, shr s, frs) -ta'-jvm→ σ'
  and exec2: P, t ⊢ (xcp, shr s, frs) -ta''-jvm→ σ''
  and aok1: final-thread.actions-ok final s t ta'
  and aok2: final-thread.actions-ok final s t ta''
  and conf: Φ ⊢ t:(xcp, shr s, frs) ✓
  shows ta' = ta'' ∧ σ' = σ''
proof -
  from wf obtain wf-md where wf': wf-prog wf-md P by(blast dest: wt-jvm-progD)
  from conf have tconf: P, shr s ⊢ t ✓t by(simp add: correct-state-def)
  from exec1 conf have P, t ⊢ Normal (xcp, shr s, frs) -ta'-jvmd→ Normal σ'
    by(simp add: welltyped-commute[OF wf])
  hence check P (xcp, shr s, frs) by(rule jvmd-NormalE)
  with exec1 exec2 aok1 aok2 tconf show ?thesis
    by(cases xcp)(case-tac [!], frs, auto elim!: exec-1.cases dest: exec-instr-deterministic[OF wf' det]
simp add: check-def split-beta)
qed

end

context JVM-conf-read begin

lemma invariant3p-correct-state-ts:
  assumes wf-jvm-progΦ P
  shows invariant3p (mexecT P) {s. correct-state-ts Φ (thr s) (shr s)}
using assms by(rule lifting-wf.invariant3p-ts-ok[OF lifting-wf-correct-state])

lemma mexec-deterministic:
  assumes wf: wf-jvm-progΦ P
  and det: deterministic-heap-ops
  shows exec-mthr.deterministic P {s. correct-state-ts Φ (thr s) (shr s)}
proof(rule exec-mthr.deterministicI)
  fix s t x ta' x' m' ta'' x'' m''
  assume tst: thr s t = [(x, no-wait-locks)]

```



```

    and red: mexec P t (x, shr s) ta' (x', m') mexec P t (x, shr s) ta'' (x'', m'')
    and aok: exec-mthr.actions-ok s t ta' exec-mthr.actions-ok s t ta''
    and correct [simplified]: s ∈ {s. correct-state-ts Φ (thr s) (shr s)}
    moreover obtain xcp frs where [simp]: x = (xcp, frs) by (cases x)
    moreover obtain xcp' frs' where [simp]: x' = (xcp', frs') by (cases x')
    moreover obtain xcp'' frs'' where [simp]: x'' = (xcp'', frs'') by (cases x'')
    ultimately have exec1: P, t ⊢ (xcp, shr s, frs) -ta'-jvm→ (xcp', m', frs')
    and exec1: P, t ⊢ (xcp, shr s, frs) -ta''-jvm→ (xcp'', m'', frs'')
    by simp-all
    moreover note aok
    moreover from correct tst have Φ ⊢ t:(xcp, shr s, frs)✓
    by (auto dest: ts-okD)
    ultimately have ta' = ta'' ∧ (xcp', m', frs') = (xcp'', m'', frs'')
    by (rule exec-1-deterministic[OF wf det])
    thus ta' = ta'' ∧ x' = x'' ∧ m' = m'' by simp
qed (rule invariant3p-correct-state-ts[OF wf])

```

end

end

## 6.8 Preservation of deadlock for the JVMs

theory *JVMDeadlocked*

imports

*BVProgressThreaded*

begin

context *JVM-progress* begin

lemma *must-sync-preserved-d*:

assumes wf: wf-jvm-prog<sub>Φ</sub> P

and ml: execd-mthr.must-sync P t (xcp, frs) h

and hext: hext h h'

and hconf': hconf h'

and cs: Φ ⊢ t: (xcp, h, frs) ✓

shows execd-mthr.must-sync P t (xcp, frs) h'

proof (rule execd-mthr.must-syncI)

from ml obtain ta xcp' frs' m'

where red: P, t ⊢ Normal (xcp, h, frs) -ta-jvmd→ Normal (xcp', m', frs')

by (auto elim: execd-mthr.must-syncE)

then obtain f Frs

where check: check P (xcp, h, frs)

and exec: (ta, xcp', m', frs') ∈ exec P t (xcp, h, frs)

and [simp]: frs = f # Frs

by (auto elim: jvmd-NormalE)

from cs hext hconf' have cs': Φ ⊢ t: (xcp, h', frs) ✓

by (rule correct-state-hext-mono)

then obtain ta σ' where exec: P, t ⊢ (xcp, h', frs) -ta-jvm→ σ'

by (auto dest: progress[OF wf])

hence P, t ⊢ Normal (xcp, h', frs) -ta-jvmd→ Normal σ'

unfolding welltyped-commute[OF wf cs'] .

moreover from exec have ∃ s. exec-mthr.actions-ok s t ta by (rule exec-ta-satisfiable)

**ultimately show**  $\exists ta\ x' m' s. mexecd\ P\ t\ ((xcp, frs), h')\ ta\ (x', m') \wedge exec\_mthr.actions-ok\ s\ t\ ta$   
**by**  $(cases\ \sigma')(fastforce\ simp\ del: split-paired-Ex)$   
**qed**

**lemma** *can-sync-devreserp-d*:

**assumes**  $wf: wf\_jvm-prog_{\Phi}\ P$   
**and**  $cl': execd\_mthr.can-sync\ P\ t\ (xcp, frs)\ h'\ L$   
**and**  $cs: \Phi \vdash t: (xcp, h, frs)\ \checkmark$   
**and**  $hext: hext\ h\ h'$   
**and**  $hconf': hconf\ h'$   
**shows**  $\exists L' \subseteq L. execd\_mthr.can-sync\ P\ t\ (xcp, frs)\ h\ L'$

**proof** –

**from**  $cl'$  **obtain**  $ta\ xcp'\ frs'\ m'$   
**where**  $red: P, t \vdash Normal\ (xcp, h', frs) \rightarrow ta-jvmd \rightarrow Normal\ (xcp', m', frs')$   
**and**  $L: L = collect\_locks\ \{\{ta\}\}_l <+> collect\_cond\_actions\ \{\{ta\}\}_c <+> collect\_interrupts\ \{\{ta\}\}_i$   
**by**  $-(erule\ execd\_mthr.can-syncE, auto)$

**then obtain**  $f\ Frs$

**where**  $check: check\ P\ (xcp, h', frs)$   
**and**  $exec: (ta, xcp', m', frs') \in exec\ P\ t\ (xcp, h', frs)$   
**and**  $[simp]: frs = f \# Frs$   
**by**  $(auto\ elim: jvmd-NormalE\ simp\ add: finfun-upd-apply)$

**obtain**  $stk\ loc\ C\ M\ pc$  **where**  $[simp]: f = (stk, loc, C, M, pc)$  **by**  $(cases\ f, blast)$

**from**  $cs$  **obtain**  $ST\ LT\ Ts\ T\ mxs\ mxl\ ins\ xt$  **where**

$hconf: hconf\ h$  **and**  
 $tconf: P, h \vdash t\ \checkmark$  **and**  
 $meth: P \vdash C\ sees\ M: Ts \rightarrow T = \lfloor (mxs, mxl, ins, xt) \rfloor\ in\ C$  **and**  
 $\Phi: \Phi\ C\ M\ !\ pc = Some\ (ST, LT)$  **and**  
 $frame: conf-f\ P\ h\ (ST, LT)\ ins\ (stk, loc, C, M, pc)$  **and**  
 $frames: conf-fs\ P\ h\ \Phi\ M\ (size\ Ts)\ T\ Frs$   
**by**  $(fastforce\ simp\ add: correct-state-def\ dest: sees-method-fun)$

**from**  $cs$  **have**  $exec\ P\ t\ (xcp, h, f \# Frs) \neq \{\}$

**by**  $(auto\ dest!: progress[OF\ wf]\ simp\ add: exec-1-iff)$

**with**  $no-type-error[OF\ wf\ cs]$  **have**  $check': check\ P\ (xcp, h, frs)$

**by**  $(auto\ simp\ add: exec-d-def\ split: if-split-asm)$

**from**  $wf$  **obtain**  $wfmd$  **where**  $wfp: wf-prog\ wfmd\ P$  **by**  $(auto\ dest: wt-jvm-progD)$

**from**  $tconf\ hext$  **have**  $tconf': P, h' \vdash t\ \checkmark$  **by**  $(rule\ tconf-hext-mono)$

**show**  $?thesis$

**proof**  $(cases\ xcp)$

**case**  $[simp]: (Some\ a)$

**with**  $exec$  **have**  $[simp]: m' = h'$  **by**  $(auto)$

**from**  $\langle \Phi \vdash t: (xcp, h, frs)\ \checkmark \rangle$  **obtain**  $D$  **where**  $D: typeof-addr\ h\ a = \lfloor Class-type\ D \rfloor$

**by**  $(auto\ simp\ add: correct-state-def)$

**with**  $hext$  **have**  $cname-of\ h\ a = cname-of\ h'\ a$  **by**  $(auto\ dest: hext-objD\ simp\ add: cname-of-def)$

**with**  $exec$  **have**  $(ta, xcp', h, frs') \in exec\ P\ t\ (xcp, h, frs)$  **by**  $auto$

**moreover from**  $check\ D\ hext$  **have**  $check\ P\ (xcp, h, frs)$

**by**  $(auto\ simp\ add: check-def\ check-xcpt-def\ dest: hext-objD)$

**ultimately have**  $P, t \vdash Normal\ (xcp, h, frs) \rightarrow ta-jvmd \rightarrow Normal\ (xcp', h, frs')$

**by**  $-(rule\ exec-1-d-NormalI, simp\ only: exec-d-def\ if-True)$

**with**  $L$  **have**  $execd\_mthr.can-sync\ P\ t\ (xcp, frs)\ h\ L$

**by**  $(auto\ intro: execd\_mthr.can-syncI)$

**thus**  $?thesis$  **by**  $auto$

**next**

**case**  $[simp]: None$

**note**  $[simp] = \text{defs1 list-all2-Cons2}$

**from** *frame* **have**  $ST: P, h \vdash stk [: \leq] ST$

**and**  $LT: P, h \vdash loc [: \leq \top] LT$

**and**  $pc: pc < \text{length } ins$  **by** *simp-all*

**from** *wf meth*  $pc$  **have**  $wt: P, T, m\!xs, size\ ins, xt \vdash ins!pc, pc :: \Phi\ C\ M$

**by**(*rule wt-jvm-prog-impl-wt-instr*)

**from**  $\langle \Phi \vdash t: (xcp, h, frs) \checkmark \rangle$

**have**  $\exists ta\ \sigma'. P, t \vdash (xcp, h, f \# Frs) -ta-jvm \rightarrow \sigma'$

**by**(*auto dest: progress[OF wf] simp del: correct-state-def split-paired-Ex*)

**with** *exec meth* **have**  $\exists ta'\ \sigma'. (ta', \sigma') \in \text{exec } P\ t\ (xcp, h, frs) \wedge \text{collect-locks } \llbracket ta' \rrbracket_l \subseteq \text{collect-locks } \llbracket ta \rrbracket_l \wedge \text{collect-cond-actions } \llbracket ta' \rrbracket_c \subseteq \text{collect-cond-actions } \llbracket ta \rrbracket_c \wedge \text{collect-interrupts } \llbracket ta' \rrbracket_i \subseteq \text{collect-interrupts } \llbracket ta \rrbracket_i$

**proof**(*cases ins ! pc*)

**case** (*Invoke M' n*)

**show** *?thesis*

**proof**(*cases stk ! n = Null*)

**case** *True* **with** *Invoke exec meth* **show** *?thesis* **by** *simp*

**next**

**case** *False*

**with** *check meth* **obtain** *a* **where**  $a: stk ! n = \text{Addr } a$  **and**  $n: n < \text{length } stk$

**by**(*auto simp add: check-def is-Ref-def Invoke*)

**from** *frame* **have**  $stk: P, h \vdash stk [: \leq] ST$  **by**(*auto simp add: conf-f-def*)

**hence**  $P, h \vdash stk ! n : \leq ST ! n$  **using** *n* **by**(*rule list-all2-nthD*)

**with** *a* **obtain** *ao Ta* **where**  $Ta: \text{typeof-addr } h\ a = \lfloor Ta \rfloor$

**by**(*auto simp add: conf-def*)

**from** *hext Ta* **have**  $Ta': \text{typeof-addr } h'\ a = \lfloor Ta \rfloor$  **by**(*rule typeof-addr-hext-mono*)

**with** *check a meth Invoke False* **obtain** *D Ts' T' meth D'*

**where**  $C: D = \text{class-type-of } Ta$

**and**  $\text{sees'}: P \vdash D \text{ sees } M': Ts' \rightarrow T' = \text{meth in } D'$

**and**  $\text{params}: P, h' \vdash \text{rev } (\text{take } n\ stk) [: \leq] Ts'$

**by**(*auto simp add: check-def has-method-def*)

**show** *?thesis*

**proof**(*cases meth*)

**case** *Some*

**with** *exec meth a Ta Ta' Invoke n sees' C* **show** *?thesis* **by**(*simp add: split-beta*)

**next**

**case** *None*

**with** *exec meth a Ta Ta' Invoke n sees' C*

**obtain**  $ta'\ va\ h''$  **where**  $ta': ta = \text{extTA2JVM } P\ ta'$

**and**  $va: (xcp', m', frs') = \text{extRet2JVM } n\ h''\ stk\ loc\ C\ M\ pc\ Frs\ va$

**and**  $\text{exec'}: (ta', va, h'') \in \text{red-external-aggr } P\ t\ a\ M'(\text{rev } (\text{take } n\ stk))\ h'$

**by**(*fastforce*)

**from** *va* **have**  $[simp]: h'' = m'$  **by**(*cases va*) *simp-all*

**note** *Ta* **moreover** **from** *None sees' wfp* **have**  $D' \cdot M'(Ts') :: T'$  **by**(*auto intro: sees-wf-native*)

**with** *C sees' params Ta' None* **have**  $P, h' \vdash a \cdot M'(\text{rev } (\text{take } n\ stk)) : T'$

**by**(*auto simp add: external-WT'-iff confs-conv-map*)

**with** *wfp exec' tconf'* **have**  $\text{red}: P, t \vdash \langle a \cdot M'(\text{rev } (\text{take } n\ stk)), h' \rangle -ta' \rightarrow \text{ext } \langle va, m' \rangle$

**by**(*simp add: WT-red-external-list-conv*)

**from** *stk* **have**  $P, h \vdash \text{take } n\ stk [: \leq] \text{take } n\ ST$  **by**(*rule list-all2-takeI*)

**then** **obtain** *Ts* **where**  $\text{map } \text{typeof}_h\ (\text{take } n\ stk) = \text{map } \text{Some } Ts$

**by**(*auto simp add: confs-conv-map*)

```

hence map typeofh (rev (take n stk)) = map Some (rev Ts) by (simp only: rev-map[symmetric])
moreover hence map typeofh' (rev (take n stk)) = map Some (rev Ts) using hext by (rule
map-typeof-hext-mono)
with  $\langle P, h' \vdash a \cdot M'(\text{rev } (\text{take } n \text{ stk})) : T' \rangle \langle D' \cdot M'(Ts') :: T' \rangle$  sees' C Ta' Ta
have  $P \vdash \text{rev } Ts \leq Ts'$  by cases (auto dest: sees-method-fun)
ultimately have  $P, h \vdash a \cdot M'(\text{rev } (\text{take } n \text{ stk})) : T'$ 
using Ta C sees' params None  $\langle D' \cdot M'(Ts') :: T' \rangle$ 
by (auto simp add: external-WT'-iff confs-conv-map)
from red-external-wt-hconf-hext[OF wfp red hext this tconf hconf]
obtain ta'' va' h''' where  $P, t \vdash \langle a \cdot M'(\text{rev } (\text{take } n \text{ stk})), h \rangle - \text{ta}'' \rightarrow \text{ext } \langle \text{va}', h''' \rangle$ 
and ta'': collect-locks  $\llbracket \text{ta}'' \rrbracket_l = \text{collect-locks } \llbracket \text{ta}' \rrbracket_l$ 
collect-cond-actions  $\llbracket \text{ta}'' \rrbracket_c = \text{collect-cond-actions } \llbracket \text{ta}' \rrbracket_c$ 
collect-interrupts  $\llbracket \text{ta}'' \rrbracket_i = \text{collect-interrupts } \llbracket \text{ta}' \rrbracket_i$ 
by auto
with None a Ta Invoke meth Ta' n C sees'
have (extTA2JVM P ta'', extRet2JVM n h''' stk loc C M pc Frs va')  $\in \text{exec } P \ t \ (xcp, h, \text{frs})$ 
by (force intro: red-external-imp-red-external-aggr simp del: split-paired-Ex)
with ta'' ta' show ?thesis by (fastforce simp del: split-paired-Ex)
qed
qed
qed(auto 4 4 split: if-split-asm simp add: split-beta ta-upd-simps exec-1-iff intro: rev-image-eqI simp
del: split-paired-Ex)
with check' have  $\exists \text{ta}' \sigma'. P, t \vdash \text{Normal } (xcp, h, \text{frs}) - \text{ta}' - \text{jvmd} \rightarrow \text{Normal } \sigma' \wedge \text{collect-locks}$ 
 $\llbracket \text{ta}' \rrbracket_l \subseteq \text{collect-locks } \llbracket \text{ta} \rrbracket_l \wedge$ 
collect-cond-actions  $\llbracket \text{ta}' \rrbracket_c \subseteq \text{collect-cond-actions } \llbracket \text{ta} \rrbracket_c \wedge \text{collect-interrupts } \llbracket \text{ta}' \rrbracket_i \subseteq \text{collect-interrupts}$ 
 $\llbracket \text{ta} \rrbracket_i$ 
apply clarify
apply (rule exI conjI)+
apply (rule exec-1-d.exec-1-d-NormalI, auto simp add: exec-d-def)
done
with L show ?thesis
apply -
apply (erule exE conjE | rule exI conjI)+
prefer 2
apply (rule tac x'=(fst  $\sigma'$ , snd (snd  $\sigma'$ )) and m'=fst (snd  $\sigma'$ ) in execd-mthr.can-syncI)
apply auto
done
qed
qed
end

context JVM-typesafe begin

lemma execd-preserve-deadlocked:
assumes wf: wf-jvm-prog $\Phi$  P
shows preserve-deadlocked JVM-final (mexecd P) convert-RA (correct-jvm-state  $\Phi$ )
proof (unfold-locales)
show invariant3p (mexecdT P) (correct-jvm-state  $\Phi$ )
by (rule invariant3p-correct-jvm-state-mexecdT[OF wf])
next
fix s t' ta' s' t x ln
assume s: s  $\in \text{correct-jvm-state } \Phi$ 
and red:  $P \vdash s - t' \vdash \text{ta}' \rightarrow_{\text{jvmd}} s'$ 

```

```

  and tst: thr s t = [(x, ln)]
  and execd-mthr.must-sync P t x (shr s)
moreover obtain xcp frs where x [simp]: x = (xcp, frs) by(cases x, auto)
ultimately have ml: execd-mthr.must-sync P t (xcp, frs) (shr s) by simp
moreover from s have cs': correct-state-ts  $\Phi$  (thr s) (shr s) by(simp add: correct-jvm-state-def)
with tst have  $\Phi \vdash t: (xcp, shr s, frs) \checkmark$  by(auto dest: ts-okD)
moreover from red have hext (shr s) (shr s') by(rule execd-hext)
moreover from wf red cs' have correct-state-ts  $\Phi$  (thr s') (shr s')
  by(rule lifting-wf.redT-preserves[OF lifting-wf-correct-state-d])
from red tst have thr s' t  $\neq$  None
  by(cases s)(cases s', rule notI, auto dest: execd-mthr.redT-thread-not-disappear)
with  $\langle \text{correct-state-ts } \Phi \text{ (thr s') (shr s')} \rangle$  have hconf (shr s')
  by(auto dest: ts-okD simp add: correct-state-def)
ultimately have execd-mthr.must-sync P t (xcp, frs) (shr s')
  by-(rule must-sync-preserved-d[OF wf])
thus execd-mthr.must-sync P t x (shr s') by simp
next
fix s t' ta' s' t x ln L
assume s: s  $\in$  correct-jvm-state  $\Phi$ 
  and red: P  $\vdash$  s  $\rightarrow$  ta'  $\rightarrow$  jvmd s'
  and tst: thr s t = [(x, ln)]
  and execd-mthr.can-sync P t x (shr s') L
moreover obtain xcp frs where x [simp]: x = (xcp, frs) by(cases x, auto)
ultimately have ml: execd-mthr.can-sync P t (xcp, frs) (shr s') L by simp
moreover from s have cs': correct-state-ts  $\Phi$  (thr s) (shr s) by(simp add: correct-jvm-state-def)
with tst have  $\Phi \vdash t: (xcp, shr s, frs) \checkmark$  by(auto dest: ts-okD)
moreover from red have hext (shr s) (shr s') by(rule execd-hext)
moreover from red tst have thr s' t  $\neq$  None
  by(cases s)(cases s', rule notI, auto dest: execd-mthr.redT-thread-not-disappear)
from red cs' have correct-state-ts  $\Phi$  (thr s') (shr s')
  by(rule lifting-wf.redT-preserves[OF lifting-wf-correct-state-d[OF wf]])
with  $\langle \text{thr s' t } \neq \text{None} \rangle$  have hconf (shr s')
  by(auto dest: ts-okD simp add: correct-state-def)
ultimately have  $\exists L' \subseteq L. \text{execd-mthr.can-sync } P \text{ t (xcp, frs) (shr s) } L'$ 
  by-(rule can-sync-devreserp-d[OF wf])
thus  $\exists L' \subseteq L. \text{execd-mthr.can-sync } P \text{ t x (shr s) } L'$  by simp
qed
end

```

and now everything again for the aggressive VM

**context** JVM-heap-conf-base' **begin**

**lemma** must-lock-d-eq-must-lock:

```

  [[ wf-jvm-prog $\Phi$  P;  $\Phi \vdash t: (xcp, h, frs) \checkmark$  ]]
   $\impl$  execd-mthr.must-sync P t (xcp, frs) h = exec-mthr.must-sync P t (xcp, frs) h
apply(rule iffI)
apply(rule execd-mthr.must-syncI)
apply(erule execd-mthr.must-syncE)
apply(simp only: mexec-eq-mexecd)
apply(blast)
apply(rule execd-mthr.must-syncI)
apply(erule exec-mthr.must-syncE)
apply(simp only: mexec-eq-mexecd[symmetric])

```

**apply**(*blast*)  
**done**

**lemma** *can-lock-d-eq-can-lock*:

$\llbracket \text{wf-jvm-prog}_{\Phi} P; \Phi \vdash t: (xcp, h, frs) \checkmark \rrbracket$   
 $\implies \text{execd-mthr.can-sync } P \ t \ (xcp, frs) \ h \ L = \text{exec-mthr.can-sync } P \ t \ (xcp, frs) \ h \ L$

**apply**(*rule iffI*)

**apply**(*erule execd-mthr.can-syncE*)

**apply**(*rule exec-mthr.can-syncI*)

**apply**(*simp only: mexec-eq-mexecd*)

**apply**(*assumption*) +

**apply**(*erule exec-mthr.can-syncE*)

**apply**(*rule execd-mthr.can-syncI*)

**by**(*simp only: mexec-eq-mexecd*)

**end**

**context** *JVM-typesafe* **begin**

**lemma** *exec-preserve-deadlocked*:

**assumes** *wf*:  $\text{wf-jvm-prog}_{\Phi} P$

**shows** *preserve-deadlocked JVM-final (mexec P) convert-RA (correct-jvm-state  $\Phi$ )*

**proof** –

**interpret** *preserve-deadlocked JVM-final mexecd P convert-RA correct-jvm-state  $\Phi$*

**by**(*rule execd-preserve-deadlocked*) *fact* +

{ **fix** *s t' ta' s' t x*

**assume** *s*:  $s \in \text{correct-jvm-state } \Phi$

**and** *red*:  $P \vdash s -t' \triangleright ta' \rightarrow_{\text{jvm}} s'$

**and** *tst*:  $\text{thr } s \ t = \llbracket (x, \text{no-wait-locks}) \rrbracket$

**obtain** *xcp frs* **where** *x* [*simp*]:  $x = (xcp, frs)$  **by**(*cases x, auto*)

**from** *s* **have** *css*: *correct-state-ts*  $\Phi$  (*thr s*) (*shr s*) **by**(*simp add: correct-jvm-state-def*)

**with** *red* **have** *redd*:  $P \vdash s -t' \triangleright ta' \rightarrow_{\text{jvmd}} s'$  **by**(*simp add: mexecT-eq-mexecdT[OF wf]*)

**from** *css tst* **have** *cst*:  $\Phi \vdash t: (xcp, \text{shr } s, frs) \checkmark$  **by**(*auto dest: ts-okD*)

**from** *redd* **have** *cst'*:  $\Phi \vdash t: (xcp, \text{shr } s', frs) \checkmark$

**proof**(*cases rule: execd-mthr.redT-elim*s)

**case** *acquire* **with** *cst* **show** *?thesis* **by** *simp*

**next**

**case** (*normal X X' M' ws'*)

**obtain** *XCP FRS* **where** *X* [*simp*]:  $X = (XCP, FRS)$  **by**(*cases X, auto*)

**obtain** *XCP' FRS'* **where** *X'* [*simp*]:  $X' = (XCP', FRS')$  **by**(*cases X', auto*)

**from**  $\langle \text{mexecd } P \ t' \ (X, \text{shr } s) \ ta' \ (X', M') \rangle$

**have**  $P, t' \vdash \text{Normal } (XCP, \text{shr } s, FRS) -ta' -\text{jvmd} \rightarrow \text{Normal } (XCP', M', FRS')$  **by** *simp*

**moreover from**  $\langle \text{thr } s \ t' = \llbracket (X, \text{no-wait-locks}) \rrbracket \rangle$  *css*

**have**  $\Phi \vdash t': (XCP, \text{shr } s, FRS) \checkmark$  **by**(*auto dest: ts-okD*)

**ultimately have**  $\Phi \vdash t': (XCP, M', FRS) \checkmark$  **by**  $-(\text{rule correct-state-heap-change}[OF \text{wf}])$

**moreover from** *lifting-wf.redT-updTs-preserves[OF lifting-wf-correct-state-d[OF wf] css, OF*  
 $\langle \text{mexecd } P \ t' \ (X, \text{shr } s) \ ta' \ (X', M') \rangle \langle \text{thr } s \ t' = \llbracket (X, \text{no-wait-locks}) \rrbracket \rangle$ , *of no-wait-locks*  $\langle \text{thread-oks}$   
 $(\text{thr } s) \ \{\!\!| \ ta' \!\!\}_t \rangle$

**have** *correct-state-ts*  $\Phi$  ( $(\text{redT-updTs } (\text{thr } s) \ \{\!\!| \ ta' \!\!\}_t)(t' \mapsto (X', \text{no-wait-locks}))$ ) *M'* **by** *simp*

**ultimately have** *correct-state-ts*  $\Phi$  ( $\text{redT-updTs } (\text{thr } s) \ \{\!\!| \ ta' \!\!\}_t$ ) *M'*

**using**  $\langle \text{thr } s \ t' = \llbracket (X, \text{no-wait-locks}) \rrbracket \rangle \langle \text{thread-oks } (\text{thr } s) \ \{\!\!| \ ta' \!\!\}_t \rangle$

**apply**(*auto intro!: ts-okI dest: ts-okD*)

**apply**(*case-tac t=t'*)

```

    apply(fastforce dest: redT-updTs-Some)
  apply(drule-tac t=t in ts-okD, fastforce+)
done
hence correct-state-ts  $\Phi$  (redT-updTs (thr s)  $\llbracket ta' \rrbracket_t$ ) (shr s')
  using  $\langle s' = (redT-updLs (locks s) t' \llbracket ta' \rrbracket_t), ((redT-updTs (thr s) \llbracket ta' \rrbracket_t)(t' \mapsto (X', redT-updLns$ 
  (locks s) t' no-wait-locks  $\llbracket ta' \rrbracket_t)$ ),  $M'$ ),  $ws'$ , redT-updIs (interrupts s)  $\llbracket ta' \rrbracket_i$ ) $\rangle$ 
  by simp
  moreover from tst  $\langle thread-oks (thr s) \llbracket ta' \rrbracket_t \rangle$ 
  have redT-updTs (thr s)  $\llbracket ta' \rrbracket_t t = \lfloor (x, no-wait-locks) \rfloor$  by(auto intro: redT-updTs-Some)
  ultimately show ?thesis by(auto dest: ts-okD)
qed
{ assume exec-mthr.must-sync P t x (shr s)
  hence ml: exec-mthr.must-sync P t (xcp, frs) (shr s) by simp
  with cst have execd-mthr.must-sync P t (xcp, frs) (shr s)
    by(auto dest: must-lock-d-eq-must-lock[OF wf])
  with s redd tst have execd-mthr.must-sync P t x (shr s')
    unfolding x by(rule can-lock-preserved)
  with cst' have exec-mthr.must-sync P t x (shr s')
    by(auto dest: must-lock-d-eq-must-lock[OF wf]) }
note ml = this
{ fix L
  assume exec-mthr.can-sync P t x (shr s') L
  hence cl: exec-mthr.can-sync P t (xcp, frs) (shr s') L by simp
  with cst' have execd-mthr.can-sync P t (xcp, frs) (shr s') L
    by(auto dest: can-lock-d-eq-can-lock[OF wf])
  with s redd tst
  have  $\exists L' \subseteq L. execd-mthr.can-sync P t x (shr s) L'$ 
    unfolding x by(rule can-lock-devreserp)
  then obtain L' where execd-mthr.can-sync P t x (shr s) L'
    and L':  $L' \subseteq L$  by blast
  with cst have exec-mthr.can-sync P t x (shr s) L'
    by(auto dest: can-lock-d-eq-can-lock[OF wf])
  with L' have  $\exists L' \subseteq L. execd-mthr.can-sync P t x (shr s) L'$ 
    by(blast) }
note this ml }
moreover have invariant3p (mexecT P) (correct-jvm-state  $\Phi$ ) by(rule invariant3p-correct-jvm-state-mexecT[OF wf])
ultimately show ?thesis by(unfold-locales)
qed

end

end

```

## 6.9 Monotonicity of eff and app

```

theory EffectMono
imports
  Effect
begin

declare not-Err-eq [iff]

```

**declare** *widens-trans*[*trans*]

**lemma** *app<sub>i</sub>-mono*:

**assumes** *wf*: *wf-prog p P*

**assumes** *less*:  $P \vdash \tau \leq_i \tau'$

**shows**  $\text{app}_i(i, P, \text{m}\bar{x}s, \text{m}\bar{p}c, rT, \tau') \implies \text{app}_i(i, P, \text{m}\bar{x}s, \text{m}\bar{p}c, rT, \tau)$

**proof** –

**assume** *app*:  $\text{app}_i(i, P, \text{m}\bar{x}s, \text{m}\bar{p}c, rT, \tau')$

**obtain** *ST LT ST' LT'* **where**

$[simp]: \tau = (ST, LT)$  **and**

$[simp]: \tau' = (ST', LT')$

**by** (*cases*  $\tau$ , *cases*  $\tau'$ )

**from** *less* **have**  $[simp]: \text{size } ST = \text{size } ST'$  **and**  $[simp]: \text{size } LT = \text{size } LT'$

**by** (*auto dest: list-all2-lengthD*)

**note**  $[iff] = \text{list-all2-Cons2 widen-Class}$

**note**  $[simp] = \text{fun-of-def}$

**from** *app less* **show**  $\text{app}_i(i, P, \text{m}\bar{x}s, \text{m}\bar{p}c, rT, \tau)$

**proof** (*cases i*)

**case** *Load*

**with** *app less* **show** *?thesis* **by** (*auto dest!: list-all2-nthD*)

**next**

**case** (*Invoke M n*)

**with** *app* **have**  $n: n < \text{size } ST'$  **by** *simp*

{ **assume**  $ST!n = NT$  **hence** *?thesis* **using**  $n$  *app Invoke* **by** *simp* }

**moreover** {

**assume**  $ST!n = NT$

**moreover with**  $n$  *less* **have**  $ST!n = NT$

**by** (*auto dest: list-all2-nthD*)

**ultimately have** *?thesis* **using**  $n$  *app Invoke* **by** *simp* }

**moreover** {

**assume**  $ST: ST!n \neq NT$  **and**  $ST': ST!n \neq NT$

**from**  $ST'$  *app Invoke*

**obtain**  $D Ts T m C'$

**where**  $D: \text{class-type-of}'(ST'!n) = \lfloor D \rfloor$

**and**  $Ts: P \vdash \text{rev}(\text{take } n ST') \leq Ts$

**and**  $D-M: P \vdash D \text{ sees } M: Ts \rightarrow T = m \text{ in } C'$

**by** *fastforce*

**from** *less* **have**  $P \vdash ST!n \leq ST!n$

**by** (*auto dest: list-all2-nthD2[OF - n]*)

**with**  $D$  **obtain**  $D'$  **where**  $D': \text{class-type-of}'(ST!n) = \lfloor D' \rfloor$

**and**  $D_{\text{sub}C}: P \vdash D' \preceq^* D$

**using**  $ST$  **by** (*rule widen-is-class-type-of*)

**from**  $wf D-M D_{\text{sub}C}$  **obtain**  $Ts' T' m' C''$  **where**

$D'-M: P \vdash D' \text{ sees } M: Ts' \rightarrow T' = m' \text{ in } C''$  **and**

$Ts': P \vdash Ts \leq Ts'$

**by** (*blast dest: sees-method-mono*)

**from** *less* **have**  $P \vdash \text{rev}(\text{take } n ST) \leq \text{rev}(\text{take } n ST')$  **by** *simp*



```

    also note  $T_s$  also note  $T_s'$ 
    finally have  $P \vdash \text{rev } (\text{take } n \text{ } ST) [\leq] T_s'$  .
    with  $D'-M \ D' \text{ app less Invoke } D$  have ?thesis by(auto)
  }
  ultimately show ?thesis by blast
next
case Getfield
with app less show ?thesis
  by(fastforce simp add: sees-field-def widen-Array dest: has-fields-fun)
next
case Putfield
with app less show ?thesis
  by (fastforce intro: widen-trans rtranc1-trans simp add: sees-field-def widen-Array dest: has-fields-fun)
next
case CAS
with app less show ?thesis
  by (fastforce intro: widen-trans rtranc1-trans simp add: sees-field-def widen-Array dest: has-fields-fun)
next
case Return
with app less show ?thesis by (fastforce intro: widen-trans)
next
case ALoad
with app less show ?thesis by(auto simp add: widen-Array)
next
case AStore
with app less show ?thesis by(auto simp add: widen-Array)
next
case ALength
with app less show ?thesis by(auto simp add: widen-Array)
next
case (Checkcast T)
with app less show ?thesis
  by(auto elim!: refTE simp: widen-Array)
next
case (Instanceof T)
with app less show ?thesis
  by(auto elim!: refTE simp: widen-Array)
next
case ThrowExc
with app less show ?thesis
  by(auto elim!: refTE simp: widen-Array)
next
case MEnter
with app less show ?thesis
  by(auto elim!: refTE simp: widen-Array)
next
case MExit
with app less show ?thesis
  by(auto elim!: refTE simp: widen-Array)
next
case (BinOpInstr bop)
with app less show ?thesis by(force dest: WTrt-binop-widen-mono)
next
case Dup

```

```

    with app less show ?thesis
      by(auto dest: list-all2-lengthD)
  next
    case Swap
    with app less show ?thesis
      by(auto dest: list-all2-lengthD)
    qed (auto elim!: refTE not-refTE)
qed

lemma succs-mono:
  assumes wf: wf-prog p P and appi: appi (i,P,mxs,mpc,rT,τ')
  shows P ⊢ τ ≤i τ' ⇒ set (succs i τ pc) ⊆ set (succs i τ' pc)
proof (cases i)
  case (Invoke M n)
  obtain ST LT ST' LT' where
    [simp]: τ = (ST,LT) and [simp]: τ' = (ST',LT') by (cases τ, cases τ')
  assume P ⊢ τ ≤i τ'
  moreover
  with appi Invoke have n < size ST by (auto dest: list-all2-lengthD)
  ultimately
  have P ⊢ ST!n ≤ ST!n by (auto simp add: fun-of-def dest: list-all2-nthD)
  with Invoke show ?thesis by auto
next
  case ALoad
  obtain ST LT ST' LT' where
    [simp]: τ = (ST,LT) and [simp]: τ' = (ST',LT') by (cases τ, cases τ')
  assume P ⊢ τ ≤i τ'
  moreover
  with appi ALoad have 1 < size ST by (auto dest: list-all2-lengthD)
  ultimately
  have P ⊢ ST!1 ≤ ST!1 by (auto simp add: fun-of-def dest: list-all2-nthD)
  with ALoad show ?thesis by auto
next
  case AStore
  obtain ST LT ST' LT' where
    [simp]: τ = (ST,LT) and [simp]: τ' = (ST',LT') by (cases τ, cases τ')
  assume P ⊢ τ ≤i τ'
  moreover
  with appi AStore have 2 < size ST by (auto dest: list-all2-lengthD)
  ultimately
  have P ⊢ ST!2 ≤ ST!2 by (auto simp add: fun-of-def dest: list-all2-nthD)
  with AStore show ?thesis by auto
next
  case ALength
  obtain ST LT ST' LT' where
    [simp]: τ = (ST,LT) and [simp]: τ' = (ST',LT') by (cases τ, cases τ')
  assume P ⊢ τ ≤i τ'
  moreover
  with appi ALength have 0 < size ST by (auto dest: list-all2-lengthD)
  ultimately
  have P ⊢ ST!0 ≤ ST!0 by (auto simp add: fun-of-def dest: list-all2-nthD)
  with ALength show ?thesis by auto
next
  case MEnter

```

**obtain**  $ST\ LT\ ST'\ LT'$  **where**  
 $[simp]: \tau = (ST, LT)$  **and**  $[simp]: \tau' = (ST', LT')$  **by** (*cases*  $\tau$ , *cases*  $\tau'$ )  
**assume**  $P \vdash \tau \leq_i \tau'$   
**moreover**  
**with**  $app_i\ MEnter$  **have**  $0 < size\ ST$  **by** (*auto dest: list-all2-lengthD*)  
**ultimately**  
**have**  $P \vdash ST!0 \leq ST!0$  **by** (*auto simp add: fun-of-def dest: list-all2-nthD*)  
**with**  $MEnter$  **show** *?thesis* **by** *auto*  
**next**  
**case**  $MExit$   
**obtain**  $ST\ LT\ ST'\ LT'$  **where**  
 $[simp]: \tau = (ST, LT)$  **and**  $[simp]: \tau' = (ST', LT')$  **by** (*cases*  $\tau$ , *cases*  $\tau'$ )  
**assume**  $P \vdash \tau \leq_i \tau'$   
**moreover**  
**with**  $app_i\ MExit$  **have**  $0 < size\ ST$  **by** (*auto dest: list-all2-lengthD*)  
**ultimately**  
**have**  $P \vdash ST!0 \leq ST!0$  **by** (*auto simp add: fun-of-def dest: list-all2-nthD*)  
**with**  $MExit$  **show** *?thesis* **by** *auto*  
**qed** *auto*

**lemma** *app-mono*:

**assumes** *wf*: *wf-prog*  $p\ P$   
**assumes** *less'*:  $P \vdash \tau \leq' \tau'$   
**shows**  $app\ i\ P\ m\ rT\ pc\ mpc\ xt\ \tau' \implies app\ i\ P\ m\ rT\ pc\ mpc\ xt\ \tau$   
**proof** (*cases*  $\tau$ )  
**case** *None* **thus** *?thesis* **by** *simp*  
**next**  
**case** (*Some*  $\tau_1$ )  
**moreover**  
**with** *less'* **obtain**  $\tau_2$  **where**  $\tau_2: \tau' = \text{Some } \tau_2$  **by** (*cases*  $\tau'$ ) *auto*  
**ultimately** **have** *less*:  $P \vdash \tau_1 \leq_i \tau_2$  **using** *less'* **by** *simp*

**assume**  $app\ i\ P\ m\ rT\ pc\ mpc\ xt\ \tau'$   
**with** *Some*  $\tau_2$  **obtain**  
 $app_i$ :  $app_i\ (i, P, pc, m, rT, \tau_2)$  **and**  
 $xcpt$ :  $xcpt\text{-}app\ i\ P\ pc\ m\ xt\ \tau_2$  **and**  
*succs*:  $\forall (pc', s') \in set\ (eff\ i\ P\ pc\ xt\ (\text{Some } \tau_2)).\ pc' < mpc$   
**by** (*auto simp add: app-def*)

**from** *wf less*  $app_i$  **have**  $app_i\ (i, P, pc, m, rT, \tau_1)$  **by** (*rule app<sub>i</sub>-mono*)  
**moreover**  
**from** *less* **have**  $size\ (fst\ \tau_1) = size\ (fst\ \tau_2)$   
**by** (*cases*  $\tau_1$ , *cases*  $\tau_2$ ) (*auto dest: list-all2-lengthD*)  
**with**  $xcpt$  **have**  $xcpt\text{-}app\ i\ P\ pc\ m\ xt\ \tau_1$  **by** (*simp add: xcpt-app-def*)  
**moreover**  
**from** *wf app<sub>i</sub> less* **have**  $\forall pc.\ set\ (succs\ i\ \tau_1\ pc) \subseteq set\ (succs\ i\ \tau_2\ pc)$   
**by** (*blast dest: succs-mono*)  
**with** *succs*  
**have**  $\forall (pc', s') \in set\ (eff\ i\ P\ pc\ xt\ (\text{Some } \tau_1)).\ pc' < mpc$   
**by** (*cases*  $\tau_1$ , *cases*  $\tau_2$ )  
(*auto simp add: eff-def norm-eff-def xcpt-eff-def dest: bspec*)  
**ultimately**  
**show** *?thesis* **using** *Some* **by** (*simp add: app-def*)  
**qed**

**lemma** *eff<sub>i</sub>-mono*:

**assumes** *wf*: *wf-prog* *p P*  
**assumes** *less*:  $P \vdash \tau \leq_i \tau'$   
**assumes** *app<sub>i</sub>*: *app* *i P m rT pc mpc xt* (*Some*  $\tau'$ )  
**assumes** *succs*: *succs* *i  $\tau$  pc*  $\neq []$  *succs* *i  $\tau'$  pc*  $\neq []$   
**shows**  $P \vdash \text{eff}_i(i, P, \tau) \leq_i \text{eff}_i(i, P, \tau')$

**proof** –

**obtain** *ST LT ST' LT'* **where**  
 [*simp*]:  $\tau = (ST, LT)$  **and**  
 [*simp*]:  $\tau' = (ST', LT')$   
**by** (*cases*  $\tau$ , *cases*  $\tau'$ )

**note** [*simp*] = *eff-def app-def fun-of-def*

**from** *less* **have**  $P \vdash (\text{Some } \tau) \leq' (\text{Some } \tau')$  **by** *simp*  
**from** *wf this app<sub>i</sub>*  
**have** *app*: *app* *i P m rT pc mpc xt* (*Some*  $\tau$ ) **by** (*rule app-mono*)

**from** *less app app<sub>i</sub>* **show** *?thesis*

**proof** (*cases i*)

**case** *ThrowExc* **with** *succs* **have** *False* **by** *simp*  
**thus** *?thesis* ..

**next**

**case** *Return* **with** *succs* **have** *False* **by** *simp*  
**thus** *?thesis* ..

**next**

**case** (*Load i*)  
**from** *Load app* **obtain** *y* **where**  
*y*:  $i < \text{size } LT \text{ } LT!i = OK \text{ } y$  **by** *clarsimp*  
**from** *Load app<sub>i</sub>* **obtain** *y'* **where**  
*y'*:  $i < \text{size } LT' \text{ } LT'!i = OK \text{ } y'$  **by** *clarsimp*

**from** *less* **have**  $P \vdash LT [\leq_{\top}] LT'$  **by** *simp*  
**with** *y y'* **have**  $P \vdash y \leq y'$  **by** (*auto dest: list-all2-nthD*)  
**with** *Load less y y' app app<sub>i</sub>*  
**show** *?thesis* **by** *auto*

**next**

**case** *Store* **with** *less app app<sub>i</sub>*  
**show** *?thesis* **by** (*auto simp add: list-all2-update-cong*)

**next**

**case** (*Invoke M n*)  
**with** *app<sub>i</sub>* **have**  $n: n < \text{size } ST'$  **by** *simp*  
**from** *less* **have** [*simp*]:  $\text{size } ST = \text{size } ST'$   
**by** (*auto dest: list-all2-lengthD*)

**from** *Invoke succs* **have**  $ST: ST!n \neq NT$  **and**  $ST': ST'!n \neq NT$  **by** (*auto*)

**from**  $ST' \text{ app}_i \text{ Invoke}$  **obtain** *D Ts T m C'*  
**where** *D*: *class-type-of'* ( $ST'!n$ ) =  $\lfloor D \rfloor$   
**and** *Ts*:  $P \vdash \text{rev } (\text{take } n \text{ } ST') [\leq] Ts$   
**and** *D-M*:  $P \vdash D \text{ sees } M: Ts \rightarrow T = m \text{ in } C'$   
**by** *fastforce*

```

from less have  $P \vdash ST!n \leq ST!n$  by (auto dest: list-all2-nthD2 [ $OF - n$ ])
with  $D$  obtain  $D'$  where  $D'$ : class-type-of' ( $ST ! n$ ) =  $\lfloor D' \rfloor$ 
  and  $DsubC$ :  $P \vdash D' \preceq^* D$ 
  using  $ST$  by (rule widen-is-class-type-of)

from  $wf\ D\text{-}M\ DsubC$  obtain  $Ts'\ T'\ m'\ C''$  where
   $D'\text{-}M$ :  $P \vdash D'$  sees  $M$ :  $Ts' \rightarrow T' = m'$  in  $C''$  and
   $Ts'$ :  $P \vdash Ts \leq Ts'$  and  $P \vdash T' \leq T$  by (blast dest: sees-method-mono)

show ?thesis using Invoke n D D' D-M less D'-M Ts'  $\langle P \vdash T' \leq T \rangle$ 
  by (auto intro: list-all2-dropI)
next
  case  $ALoad$  with less app appi succs
  show ?thesis by (auto split: if-split-asm dest: Array-Array-widen)
next
  case  $AStore$  with less app appi succs
  show ?thesis by (auto split: if-split-asm dest: Array-Array-widen)
next
  case ( $BinOpInstr\ bop$ )
  with less app appi succs show ?thesis
    by auto (force dest: WTrt-binop-widen-mono WTrt-binop-fun)
qed auto
qed
end

```

## 6.10 The Typing Framework for the JVM

**theory** *TF-JVM*

**imports**

*../DFA/Typing-Framework-err*

*EffectMono*

*BVSpec*

*../Common/ExternalCallWF*

**begin**

**definition** *exec* :: *'addr jvm-prog*  $\Rightarrow$  *nat*  $\Rightarrow$  *ty*  $\Rightarrow$  *ex-table*  $\Rightarrow$  *'addr instr list*  $\Rightarrow$  *ty<sub>i</sub>' err step-type*

**where**

*exec G mxs rT et bs*  $\equiv$

*err-step* (*size bs*) ( $\lambda pc. app\ (bs!pc)\ G\ mxs\ rT\ pc\ (size\ bs)\ et$ ) ( $\lambda pc. eff\ (bs!pc)\ G\ pc\ et$ )

**locale** *JVM-sl* =

**fixes**  $P :: 'addr\ jvm\text{-}prog$  **and**  $mxs$  **and**  $mxl_0$

**fixes**  $Ts :: ty\ list$  **and**  $is :: 'addr\ instr\ list$  **and**  $xt$  **and**  $T_r$

**fixes**  $mxl$  **and**  $A$  **and**  $r$  **and**  $f$  **and**  $app$  **and**  $eff$  **and**  $step$

**defines** [*simp*]:  $mxl \equiv 1 + size\ Ts + mxl_0$

**defines** [*simp*]:  $A \equiv states\ P\ mxs\ mxl$

**defines** [*simp*]:  $r \equiv JVM\text{-}SemiType.le\ P\ mxs\ mxl$

**defines** [*simp*]:  $f \equiv JVM\text{-}SemiType.sup\ P\ mxs\ mxl$

**defines** [*simp*]:  $app \equiv \lambda pc. Effect.app\ (is!pc)\ P\ mxs\ T_r\ pc\ (size\ is)\ xt$

**defines** [*simp*]:  $eff \equiv \lambda pc. Effect.eff\ (is!pc)\ P\ pc\ xt$

**defines**  $[simp]$ :  $step \equiv err\text{-}step\ (size\ is)\ app\ eff$

**locale**  $start\text{-}context = JVM\text{-}sl +$   
**fixes**  $p$  **and**  $C$   
**assumes**  $wf$ :  $wf\text{-}prog\ p\ P$   
**assumes**  $C$ :  $is\text{-}class\ P\ C$   
**assumes**  $Ts$ :  $set\ Ts \subseteq types\ P$   
  
**fixes**  $first :: ty_i'$  **and**  $start$   
**defines**  $[simp]$ :  
 $first \equiv Some\ ([], OK\ (Class\ C)\ \# \ map\ OK\ Ts\ @\ replicate\ mxl_0\ Err)$   
**defines**  $[simp]$ :  
 $start \equiv OK\ first\ \# \ replicate\ (size\ is - 1)\ (OK\ None)$

### 6.10.1 Connecting JVM and Framework

**lemma** (**in**  $JVM\text{-}sl$ )  $step\text{-}def\text{-}exec$ :  $step \equiv exec\ P\ mxs\ T_r\ xt\ is$   
**by** ( $simp\ add$ :  $exec\text{-}def$ )

**lemma**  $special\text{-}ex\text{-}swap\text{-}lemma\ [iff]$ :  
 $(\exists X. (\exists n. X = A\ n \wedge P\ n) \wedge Q\ X) = (\exists n. Q(A\ n) \wedge P\ n)$   
**by**  $blast$

**lemma**  $ex\text{-}in\text{-}list\ [iff]$ :  
 $(\exists n. ST \in list\ n\ A \wedge n \leq mxs) = (set\ ST \subseteq A \wedge size\ ST \leq mxs)$   
**by** ( $unfold\ list\text{-}def$ )  $auto$

**lemma**  $singleton\text{-}list$ :  
 $(\exists n. [Class\ C] \in list\ n\ (types\ P) \wedge n \leq mxs) = (is\text{-}class\ P\ C \wedge 0 < mxs)$   
**by** ( $auto$ )

**lemma**  $set\text{-}drop\text{-}subset$ :  
 $set\ xs \subseteq A \implies set\ (drop\ n\ xs) \subseteq A$   
**by** ( $auto\ dest$ :  $in\text{-}set\text{-}dropD$ )

**lemma**  $Suc\text{-}minus\text{-}minus\text{-}le$ :  
 $n < mxs \implies Suc\ (n - (n - b)) \leq mxs$   
**by**  $arith$

**lemma**  $in\text{-}listE$ :  
 $\llbracket xs \in list\ n\ A; \llbracket size\ xs = n; set\ xs \subseteq A \rrbracket \implies P \rrbracket \implies P$   
**by** ( $unfold\ list\text{-}def$ )  $blast$

**declare**  $is\text{-}relevant\text{-}entry\text{-}def\ [simp]$   
**declare**  $set\text{-}drop\text{-}subset\ [simp]$

**lemma** (**in**  $start\text{-}context$ )  $[simp, intro!]$ :  $is\text{-}class\ P\ Throwable$   
**apply** ( $rule\ converse\text{-}subcls\text{-}is\text{-}class[OF\ wf]$ )  
**apply** ( $rule\ xcpt\text{-}subcls\text{-}Throwable[OF\ \text{-}\ wf]$ )  
**prefer** 2  
**apply** ( $rule\ is\text{-}class\text{-}xcpt[OF\ \text{-}\ wf]$ )  
**apply** ( $fastforce\ simp\ add$ :  $sys\text{-}xcpts\text{-}def\ sys\text{-}xcpts\text{-}list\text{-}def$ ) +  
**done**

```

declare option.splits[split del]
declare option.case-cong[cong]
declare is-type-array [simp del]

theorem (in start-context) exec-pres-type:
  pres-type step (size is) A
declare option.case-cong-weak[cong]
declare option.splits[split]
declare is-type-array[simp]

declare is-relevant-entry-def [simp del]
declare set-drop-subset [simp del]

lemma lesubstep-type-simple:
   $xs \sqsubseteq_{\text{Product.le } (=) \ r} ys \implies \text{set } xs \ \{\sqsubseteq_r\} \ \text{set } ys$ 
declare is-relevant-entry-def [simp del]

lemma conjI2:  $\llbracket A; A \implies B \rrbracket \implies A \wedge B$  by blast

lemma (in JVM-sl) eff-mono:
   $\llbracket \text{wf-prog } p \ P; \text{pc} < \text{length } is; s \sqsubseteq_{\text{sup-state-opt } P} t; \text{app } pc \ t \rrbracket$ 
   $\implies \text{set } (\text{eff } pc \ s) \ \{\sqsubseteq_{\text{sup-state-opt } P}\} \ \text{set } (\text{eff } pc \ t)$ 
lemma (in JVM-sl) bounded-step: bounded step (size is)
theorem (in JVM-sl) step-mono:
   $\text{wf-prog } wf\text{-mb } P \implies \text{mono } r \ \text{step } (size \ is) \ A$ 

lemma (in start-context) first-in-A [iff]: OK first  $\in A$ 
  using Ts C by (force intro!; list-appendI simp add: JVM-states-unfold)

lemma (in JVM-sl) wt-method-def2:
  wt-method P C' Ts Tr mxs mxl0 is xt  $\tau s$  =
  (is  $\neq []$   $\wedge$ 
   size  $\tau s = size \ is \wedge$ 
   OK 'set  $\tau s \subseteq \text{states } P \ m_{xs} \ m_{xl} \wedge$ 
   wt-start P C' Ts mxl0  $\tau s \wedge$ 
   wt-app-eff (sup-state-opt P) app eff  $\tau s$ )

end

```

## 6.11 LBV for the JVM

```

theory LBVJVM
imports
  ../DFA/Abstract-BV
  TF-JVM
begin

type-synonym prog-cert = cname  $\Rightarrow$  mname  $\Rightarrow$  tyi' err list

definition check-cert :: 'addr jvm-prog  $\Rightarrow$  nat  $\Rightarrow$  nat  $\Rightarrow$  nat  $\Rightarrow$  tyi' err list  $\Rightarrow$  bool'

```

**where**

$check\text{-}cert\ P\ m\!x\!s\ m\!x\!l\ n\ cert \equiv check\text{-}types\ P\ m\!x\!s\ m\!x\!l\ cert \wedge size\ cert = n+1 \wedge$   
 $(\forall i < n. cert!i \neq Err) \wedge cert!n = OK\ None$

**definition**  $lbv_{jvm} :: 'addr\ jvm\text{-}prog \Rightarrow nat \Rightarrow nat \Rightarrow ty \Rightarrow ex\text{-}table \Rightarrow$   
 $ty_i' err\ list \Rightarrow 'addr\ instr\ list \Rightarrow ty_i' err \Rightarrow ty_i' err$

**where**

$lbv_{jvm}\ P\ m\!x\!s\ m\!x\!r\ T_r\ et\ cert\ bs \equiv$   
 $wtl\text{-}inst\text{-}list\ bs\ cert\ (JVM\text{-}SemiType.sup\ P\ m\!x\!s\ m\!x\!r)\ (JVM\text{-}SemiType.le\ P\ m\!x\!s\ m\!x\!r)\ Err\ (OK$   
 $None)\ (exec\ P\ m\!x\!s\ T_r\ et\ bs)\ 0$

**definition**  $wt\text{-}lbv :: 'addr\ jvm\text{-}prog \Rightarrow cname \Rightarrow ty\ list \Rightarrow ty \Rightarrow nat \Rightarrow nat \Rightarrow$   
 $ex\text{-}table \Rightarrow ty_i' err\ list \Rightarrow 'addr\ instr\ list \Rightarrow bool$

**where**

$wt\text{-}lbv\ P\ C\ Ts\ T_r\ m\!x\!s\ m\!x\!l_0\ et\ cert\ ins \equiv$   
 $check\text{-}cert\ P\ m\!x\!s\ (1+size\ Ts+m\!x\!l_0)\ (size\ ins)\ cert \wedge$   
 $0 < size\ ins \wedge$   
 $(let\ start = Some\ ([],(OK\ (Class\ C))\#((map\ OK\ Ts))\@(\replicate\ m\!x\!l_0\ Err));$   
 $result = lbv_{jvm}\ P\ m\!x\!s\ (1+size\ Ts+m\!x\!l_0)\ T_r\ et\ cert\ ins\ (OK\ start)$   
 $in\ result \neq Err)$

**definition**  $wt\text{-}jvm\text{-}prog\text{-}lbv :: 'addr\ jvm\text{-}prog \Rightarrow prog\text{-}cert \Rightarrow bool$

**where**

$wt\text{-}jvm\text{-}prog\text{-}lbv\ P\ cert \equiv$   
 $wf\text{-}prog\ (\lambda P\ C\ (mn,Ts,T_r,(m\!x\!s,m\!x\!l_0,b,et)).\ wt\text{-}lbv\ P\ C\ Ts\ T_r\ m\!x\!s\ m\!x\!l_0\ et\ (cert\ C\ mn)\ b)\ P$

**definition**  $mk\text{-}cert :: 'addr\ jvm\text{-}prog \Rightarrow nat \Rightarrow ty \Rightarrow ex\text{-}table \Rightarrow 'addr\ instr\ list$   
 $\Rightarrow ty_m \Rightarrow ty_i' err\ list$

**where**

$mk\text{-}cert\ P\ m\!x\!s\ T_r\ et\ bs\ phi \equiv make\text{-}cert\ (exec\ P\ m\!x\!s\ T_r\ et\ bs)\ (map\ OK\ phi)\ (OK\ None)$

**definition**  $prg\text{-}cert :: 'addr\ jvm\text{-}prog \Rightarrow ty_P \Rightarrow prog\text{-}cert$

**where**

$prg\text{-}cert\ P\ phi\ C\ mn \equiv let\ (C,Ts,T_r,method) = method\ P\ C\ mn;\ (m\!x\!s,m\!x\!l_0,ins,et) = the\ meth$   
 $in\ mk\text{-}cert\ P\ m\!x\!s\ T_r\ et\ ins\ (phi\ C\ mn)$

**lemma**  $check\text{-}certD\ [intro?]:$

$check\text{-}cert\ P\ m\!x\!s\ m\!x\!l\ n\ cert \implies cert\text{-}ok\ cert\ n\ Err\ (OK\ None)\ (states\ P\ m\!x\!s\ m\!x\!l)$   
**by**  $(unfold\ cert\text{-}ok\text{-}def\ check\text{-}cert\text{-}def\ check\text{-}types\text{-}def)\ auto$

**lemma** **(in**  $start\text{-}context$ **)**  $wt\text{-}lbv\text{-}wt\text{-}step:$

**assumes**  $lbv:$   $wt\text{-}lbv\ P\ C\ Ts\ T_r\ m\!x\!s\ m\!x\!l_0\ xt\ cert\ is$   
**shows**  $\exists \tau s \in list\ (size\ is)\ A.\ wt\text{-}step\ r\ Err\ step\ \tau s \wedge OK\ first \sqsubseteq_r \tau s!0$

**lemma** **(in**  $start\text{-}context$ **)**  $wt\text{-}lbv\text{-}wt\text{-}method:$

**assumes**  $lbv:$   $wt\text{-}lbv\ P\ C\ Ts\ T_r\ m\!x\!s\ m\!x\!l_0\ xt\ cert\ is$   
**shows**  $\exists \tau s.\ wt\text{-}method\ P\ C\ Ts\ T_r\ m\!x\!s\ m\!x\!l_0\ is\ xt\ \tau s$

**lemma** **(in**  $start\text{-}context$ **)**  $wt\text{-}method\text{-}wt\text{-}lbv:$

**assumes**  $wt:$   $wt\text{-}method\ P\ C\ Ts\ T_r\ m\!x\!s\ m\!x\!l_0\ is\ xt\ \tau s$   
**defines**  $[simp]:\ cert \equiv mk\text{-}cert\ P\ m\!x\!s\ T_r\ xt\ is\ \tau s$

**shows**  $wt\text{-}lbv\ P\ C\ Ts\ T_r\ m\!x\!s\ m\!x\!l_0\ xt\ cert\ is$



**theorem** *jvm-lbv-correct*:  
 $wt\text{-}jvm\text{-}prog\text{-}lbv\ P\ Cert \implies wf\text{-}jvm\text{-}prog\ P$   
**theorem** *jvm-lbv-complete*:  
**assumes**  $wt: wf\text{-}jvm\text{-}prog_{\Phi}\ P$   
**shows**  $wt\text{-}jvm\text{-}prog\text{-}lbv\ P\ (prg\text{-}cert\ P\ \Phi)$   
**end**

## 6.12 Kildall for the JVM

**theory** *BVExec*  
**imports**  
 $\dots/DFA/Abstract\text{-}BV$   
 $TF\text{-}JVM$   
**begin**

**definition** *kiljvm* ::  $'addr\ jvm\text{-}prog \Rightarrow nat \Rightarrow nat \Rightarrow ty \Rightarrow$   
 $'addr\ instr\ list \Rightarrow ex\text{-}table \Rightarrow ty_i' err\ list \Rightarrow ty_i' err\ list$

**where**

$kiljvm\ P\ mxs\ mxl\ T_r\ is\ xt \equiv$   
 $kildall\ (JVM\text{-}SemiType.le\ P\ mxs\ mxl)\ (JVM\text{-}SemiType.sup\ P\ mxs\ mxl)$   
 $(exec\ P\ mxs\ T_r\ xt\ is)$

**definition** *wt-kildall* ::  $'addr\ jvm\text{-}prog \Rightarrow cname \Rightarrow ty\ list \Rightarrow ty \Rightarrow nat \Rightarrow nat \Rightarrow$   
 $'addr\ instr\ list \Rightarrow ex\text{-}table \Rightarrow bool$

**where**

$wt\text{-}kildall\ P\ C'\ Ts\ T_r\ mxs\ mxl_0\ is\ xt \equiv$   
 $0 < size\ is \wedge$   
 $(let\ first = Some\ ([], [OK\ (Class\ C')]\ @ (map\ OK\ Ts) @ (replicate\ mxl_0\ Err));$   
 $start = OK\ first \# (replicate\ (size\ is - 1)\ (OK\ None));$   
 $result = kiljvm\ P\ mxs\ (1 + size\ Ts + mxl_0)\ T_r\ is\ xt\ start$   
 $in\ \forall n < size\ is. result!n \neq Err)$

**definition** *wf-jvm-prog<sub>k</sub>* ::  $'addr\ jvm\text{-}prog \Rightarrow bool$

**where**

$wf\text{-}jvm\text{-}prog_k\ P \equiv$   
 $wf\text{-}prog\ (\lambda P\ C'\ (M, Ts, T_r, (mxs, mxl_0, is, xt)). wt\text{-}kildall\ P\ C'\ Ts\ T_r\ mxs\ mxl_0\ is\ xt)\ P$

**theorem** (**in** *start-context*) *is-bcv-kiljvm*:  
 $is\text{-}bcv\ r\ Err\ step\ (size\ is)\ A\ (kiljvm\ P\ mxs\ mxl\ T_r\ is\ xt)$

**lemma** *subset-replicate* [*intro?*]:  $set\ (replicate\ n\ x) \subseteq \{x\}$   
**by** (*induct* *n*) *auto*

**lemma** *in-set-replicate*:

**assumes**  $x \in set\ (replicate\ n\ y)$   
**shows**  $x = y$

**lemma** (**in** *start-context*) *start-in-A* [*intro?*]:  
 $0 < size\ is \implies start \in list\ (size\ is)\ A$   
**using** *Ts C*

**theorem** (**in** *start-context*) *wt-kil-correct*:

**assumes** *wtk*: *wt-kildall* *P C Ts T<sub>r</sub> mxs mxl<sub>0</sub>* is *xt*  
**shows**  $\exists \tau s$ . *wt-method* *P C Ts T<sub>r</sub> mxs mxl<sub>0</sub>* is *xt*  $\tau s$

**theorem** (in *start-context*) *wt-kil-complete*:

**assumes** *wtm*: *wt-method* *P C Ts T<sub>r</sub> mxs mxl<sub>0</sub>* is *xt*  $\tau s$   
**shows** *wt-kildall* *P C Ts T<sub>r</sub> mxs mxl<sub>0</sub>* is *xt*

**theorem** *jvm-kildall-correct*:

*wf-jvm-prog<sub>k</sub>* *P* = *wf-jvm-prog* *P*

**end**

## 6.13 Code generation for the byte code verifier

**theory** *BCVExec*

**imports**

*BVNoTypeError*

*BVExec*

**begin**

**lemmas** [*code-unfold*] = *exec-lub-def*

**lemmas** [*code*] = *JVM-le-unfold*[*THEN meta-eq-to-obj-eq*]

**lemma** *err-code* [*code*]:

*Err.err* *A* = *Collect* (*case-err* *True* ( $\lambda x$ . *x*  $\in$  *A*))

**by**(*auto simp add: err-def split: err.split*)

**lemma** *list-code* [*code*]:

*list* *n* *A* = {*xs*. *size xs* = *n*  $\wedge$  *list-all* ( $\lambda x$ . *x*  $\in$  *A*) *xs*}

**unfolding** *list-def*

**by**(*auto intro!: ext simp add: list-all-iff*)

**lemma** *opt-code* [*code*]:

*opt* *A* = *Collect* (*case-option* *True* ( $\lambda x$ . *x*  $\in$  *A*))

**by**(*auto simp add: opt-def split: option.split*)

**lemma** *Times-code* [*code-unfold*]:

*Sigma* *A* ( $\% \cdot$ . *B*) = {(*a*, *b*). *a*  $\in$  *A*  $\wedge$  *b*  $\in$  *B*}

**by** *auto*

**lemma** *upto-esl-code* [*code*]:

*upto-esl* *m* (*A*, *r*, *f*) = (*Union* (( $\lambda n$ . *list* *n* *A*) ‘ {..*m*}), *Listn.le* *r*, *Listn.sup* *f*)

**by**(*auto simp add: upto-esl-def*)

**lemmas** [*code*] = *lesub-def* *plussub-def*

**lemma** *JVM-sup-unfold* [*code*]:

*JVM-SemiType.sup* *S* *m* *n* =

*lift2* (*Opt.sup* (*Product.sup* (*Listn.sup* (*SemiType.sup* *S*)) ( $\lambda x$  *y*. *OK* (*map2* (*lift2* (*SemiType.sup* *S*)) *x* *y*))))

**unfolding** *JVM-SemiType.sup-def* *JVM-SemiType.sl-def* *Opt.esl-def* *Err.sl-def*

*stk-esl-def* *loc-sl-def* *Product.esl-def* *Listn.sl-def* *upto-esl-def*

*SemiType.esl-def* *Err.esl-def*

**by** *simp*

**declare** *sup-fun-def* [*code*]

**lemma** [*code*]: *states P mxs mxl = fst(sl P mxs mxl)*

**unfolding** *states-def* ..

**lemma** *check-types-code* [*code*]:

*check-types P mxs mxl  $\tau s = (list-all (\lambda x. x \in (states P mxs mxl)) \tau s)$*

**unfolding** *check-types-def* **by**(*auto simp add: list-all-iff*)

**lemma** *wf-jvm-prog-code* [*code-unfold*]:

*wf-jvm-prog = wf-jvm-prog<sub>k</sub>*

**by**(*simp add: fun-eq-iff jvm-kildall-correct*)

**definition** *wf-jvm-prog'* = *wf-jvm-prog*

**ML-val**  $\langle @\{code\ wf-jvm-prog'\} \rangle$

**end**

**theory** *BV-Main*

**imports**

*JVMDeadlocked*

*LBVJVM*

*BCVExec*

**begin**

**end**



## Chapter 7

# Compilation

### 7.1 Method calls in expressions

**theory** *CallExpr* **imports**

*../J/Expr*

**begin**

```
fun inline-call :: ('a,'b,'addr) exp  $\Rightarrow$  ('a,'b,'addr) exp  $\Rightarrow$  ('a,'b,'addr) exp
  and inline-calls :: ('a,'b,'addr) exp  $\Rightarrow$  ('a,'b,'addr) exp list  $\Rightarrow$  ('a,'b,'addr) exp list
where
  inline-call f (new C) = new C
| inline-call f (newA T [e]) = newA T [inline-call f e]
| inline-call f (Cast C e) = Cast C (inline-call f e)
| inline-call f (e instanceof T) = (inline-call f e instanceof T)
| inline-call f (Val v) = Val v
| inline-call f (Var V) = Var V
| inline-call f (V:=e) = V := inline-call f e
| inline-call f (e «bop» e') = (if is-val e then (e «bop» inline-call f e') else (inline-call f e «bop» e'))
| inline-call f (a[i]) = (if is-val a then a[inline-call f i] else (inline-call f a)[i])
| inline-call f (AAss a i e) =
  (if is-val a then if is-val i then AAss a i (inline-call f e) else AAss a (inline-call f i) e
   else AAss (inline-call f a) i e)
| inline-call f (a·length) = inline-call f a·length
| inline-call f (e·F{D}) = inline-call f e·F{D}
| inline-call f (FAss e F D e') = (if is-val e then FAss e F D (inline-call f e') else FAss (inline-call f
e) F D e')
| inline-call f (CompareAndSwap e D F e' e'') =
  (if is-val e then if is-val e' then CompareAndSwap e D F e' (inline-call f e'')
   else CompareAndSwap e D F (inline-call f e') e''
   else CompareAndSwap (inline-call f e) D F e' e'')
| inline-call f (e·M(es)) =
  (if is-val e then if is-vals es  $\wedge$  is-addr e then f else e·M(inline-calls f es) else inline-call f e·M(es))
| inline-call f ({V:T=vo; e}) = {V:T=vo; inline-call f e}
| inline-call f (syncV (o') e) = syncV (inline-call f o') e
| inline-call f (insyncV (a) e) = insyncV (a) (inline-call f e)
| inline-call f (e;;e') = inline-call f e;;e'
| inline-call f (if (b) e else e') = (if (inline-call f b) e else e')
| inline-call f (while (b) e) = while (b) e
| inline-call f (throw e) = throw (inline-call f e)
| inline-call f (try e1 catch(C V) e2) = try inline-call f e1 catch(C V) e2
```

| *inline-calls*  $f \ [] = []$   
 | *inline-calls*  $f (e \# es) = (if \ is-val \ e \ then \ e \ \# \ inline-calls \ f \ es \ else \ inline-call \ f \ e \ \# \ es)$

**fun** *collapse* :: 'addr expr  $\times$  'addr expr list  $\Rightarrow$  'addr expr **where**  
   *collapse* ( $e, []$ ) =  $e$   
 | *collapse* ( $e, (e' \# es)$ ) = *collapse* (*inline-call*  $e \ e'$ ,  $es$ )

**definition** *is-call* :: ('a, 'b, 'addr) exp  $\Rightarrow$  bool  
**where** *is-call*  $e = (call \ e \neq None)$

**definition** *is-calls* :: ('a, 'b, 'addr) exp list  $\Rightarrow$  bool  
**where** *is-calls*  $es = (calls \ es \neq None)$

**lemma** *inline-calls-map-Val-append* [*simp*]:  
   *inline-calls*  $f (map \ Val \ vs \ @ \ es) = map \ Val \ vs \ @ \ inline-calls \ f \ es$   
**by**(*induct vs, auto*)

**lemma** *inline-call-eq-Val-aux*:  
   *inline-call*  $e \ E = Val \ v \Longrightarrow call \ E = \lfloor aMvs \rfloor \Longrightarrow e = Val \ v$   
**by**(*induct E*)(*auto split: if-split-asm*)

**lemmas** *inline-call-eq-Val* [*dest*] = *inline-call-eq-Val-aux inline-call-eq-Val-aux*[*OF sym, THEN sym*]

**lemma** *inline-calls-eq-empty* [*simp*]: *inline-calls*  $e \ es = [] \longleftrightarrow es = []$   
**by**(*cases es, auto*)

**lemma** *inline-calls-map-Val* [*simp*]: *inline-calls*  $e (map \ Val \ vs) = map \ Val \ vs$   
**by**(*induct vs auto*)

**lemma** **fixes**  $E :: ('a, 'b, 'addr) \ exp$  **and**  $Es :: ('a, 'b, 'addr) \ exp \ list$   
**shows** *inline-call-eq-Throw* [*dest*]: *inline-call*  $e \ E = Throw \ a \Longrightarrow call \ E = \lfloor aMvs \rfloor \Longrightarrow e = Throw \ a \vee e = addr \ a$   
**by**(*induct E rule: exp.induct*)(*fastforce split: if-split-asm*)**+**

**lemma** *Throw-eq-inline-call-eq* [*dest*]:  
   *inline-call*  $e \ E = Throw \ a \Longrightarrow call \ E = \lfloor aMvs \rfloor \Longrightarrow Throw \ a = e \vee addr \ a = e$   
**by**(*auto dest: inline-call-eq-Throw*[*OF sym*])

**lemma** *is-vals-inline-calls* [*dest*]:  
    $\llbracket is-vals (inline-calls \ e \ es); calls \ es = \lfloor aMvs \rfloor \rrbracket \Longrightarrow is-val \ e$   
**by**(*induct es, auto split: if-split-asm*)

**lemma** [*dest*]:  $\llbracket inline-calls \ e \ es = map \ Val \ vs; calls \ es = \lfloor aMvs \rfloor \rrbracket \Longrightarrow is-val \ e$   
    $\llbracket map \ Val \ vs = inline-calls \ e \ es; calls \ es = \lfloor aMvs \rfloor \rrbracket \Longrightarrow is-val \ e$   
**by**(*fastforce intro!: is-vals-inline-calls del: is-val.intros simp add: is-vals-conv elim: sym*)**+**

**lemma** *inline-calls-eq-Val-Throw* [*dest*]:  
    $\llbracket inline-calls \ e \ es = map \ Val \ vs \ @ \ Throw \ a \ \# \ es'; calls \ es = \lfloor aMvs \rfloor \rrbracket \Longrightarrow e = Throw \ a \vee is-val \ e$   
**apply**(*induct es arbitrary: vs a es'*)  
**apply**(*auto simp add: Cons-eq-append-conv split: if-split-asm*)  
**done**

**lemma** *Val-Throw-eq-inline-calls* [dest]:

$\llbracket \text{map Val vs @ Throw a \# es' = inline-calls e es; calls es = [aMvs]} \rrbracket \implies \text{Throw a} = e \vee \text{is-val e}$   
**by**(*auto dest: inline-calls-eq-Val-Throw[OF sym]*)

**declare** *option.split* [split del] *if-split-asm* [split] *if-split* [split del]

**lemma** *call-inline-call* [simp]:

$\text{call e} = [aMvs] \implies \text{call (inline-call \{v:T=vo; e'\} e)} = \text{call e'}$

$\text{calls es} = [aMvs] \implies \text{calls (inline-calls \{v:T=vo; e'\} es)} = \text{call e'}$

**apply**(*induct e and es rule: call.induct calls.induct*)

**apply**(*fastforce*)

**apply**(*fastforce*)

**apply**(*fastforce*)

**apply**(*fastforce*)

**apply**(*fastforce*)

**apply**(*fastforce split: if-split*)

**apply**(*fastforce*)

**apply**(*fastforce*)

**apply**(*fastforce split: if-split*)

**apply**(*clarsimp*)

**apply**(*fastforce split: if-split*)

**apply**(*fastforce split: if-split*)

**apply**(*fastforce*)

**apply**(*fastforce*)

**apply**(*fastforce split: if-split*)

**apply**(*fastforce split: if-split*)

**apply**(*fastforce split: if-split*)

**apply**(*fastforce*)

**apply**(*fastforce*)

**apply**(*fastforce*)

**apply**(*fastforce*)

**apply**(*fastforce*)

**apply**(*fastforce*)

**apply**(*fastforce*)

**apply**(*fastforce*)

**apply**(*fastforce*)

**apply**(*fastforce split: if-split*)

**done**

**declare** *option.split* [split] *if-split* [split] *if-split-asm* [split del]

**lemma** *fv-inline-call*:  $\text{fv (inline-call e' e)} \subseteq \text{fv e} \cup \text{fv e'}$

**and** *fvs-inline-calls*:  $\text{fvs (inline-calls e' es)} \subseteq \text{fvs es} \cup \text{fv e'}$

**by**(*induct e and es rule: call.induct calls.induct*)(*fastforce split: if-split-asm*)+

**lemma** *contains-insync-inline-call-conv*:

$\text{contains-insync (inline-call e e')} \longleftrightarrow \text{contains-insync e} \wedge \text{call e'} \neq \text{None} \vee \text{contains-insync e'}$

**and** *contains-insyncs-inline-calls-conv*:

$\text{contains-insyncs (inline-calls e es')} \longleftrightarrow \text{contains-insync e} \wedge \text{calls es'} \neq \text{None} \vee \text{contains-insyncs es'}$

**by**(*induct e' and es' rule: call.induct calls.induct*)(*auto split: if-split-asm simp add: is-vals-conv*)

**lemma** *contains-insync-inline-call* [simp]:

$\text{call e' = [aMvs]} \implies \text{contains-insync (inline-call e e')} \longleftrightarrow \text{contains-insync e} \vee \text{contains-insync e'}$

**and** *contains-insyncs-inline-calls* [*simp*]:  
 $\text{calls } es' = \lfloor aMvs \rfloor \implies \text{contains-insyncs } (\text{inline-calls } e \text{ } es') \longleftrightarrow \text{contains-insync } e \vee \text{contains-insyncs } es'$

**by**(*simp-all add: contains-insync-inline-call-conv contains-insyncs-inline-calls-conv*)

**lemma** *collapse-append* [*simp*]:  
 $\text{collapse } (e, es @ es') = \text{collapse } (\text{collapse } (e, es), es')$   
**by**(*induct es arbitrary: e, auto*)

**lemma** *collapse-conv-foldl*:  
 $\text{collapse } (e, es) = \text{foldl inline-call } e \text{ } es$   
**by**(*induct es arbitrary: e simp-all*)

**lemma** *fv-collapse*:  $\forall e \in \text{set } es. \text{is-call } e \implies \text{fv } (\text{collapse } (e, es)) \subseteq \text{fvs } (e \# es)$   
**apply**(*induct es arbitrary: e*)  
**apply**(*insert fv-inline-call*)  
**apply**(*fastforce dest: subsetD*)  
**done**

**lemma** *final-inline-callD*:  $\llbracket \text{final } (\text{inline-call } E \text{ } e); \text{is-call } e \rrbracket \implies \text{final } E$   
**by**(*induct e(auto simp add: is-call-def split: if-split-asm)*)

**lemma** *collapse-finalD*:  $\llbracket \text{final } (\text{collapse } (e, es)); \forall e \in \text{set } es. \text{is-call } e \rrbracket \implies \text{final } e$   
**by**(*induct es arbitrary: e(auto dest: final-inline-callD)*)

**context** *heap-base* **begin**

**definition** *synthesized-call* ::  $'m \text{ prog} \Rightarrow 'heap \Rightarrow ('addr \times mname \times 'addr \text{ val list}) \Rightarrow \text{bool}$   
**where**  
 $\text{synthesized-call } P \text{ } h =$   
 $(\lambda(a, M, vs). \exists T \text{ } Ts \text{ } Tr \text{ } D. \text{typeof-addr } h \text{ } a = \lfloor T \rfloor \wedge P \vdash \text{class-type-of } T \text{ sees } M:Ts \rightarrow Tr = \text{Native in } D)$

**lemma** *synthesized-call-conv*:  
 $\text{synthesized-call } P \text{ } h \text{ } (a, M, vs) =$   
 $(\exists T \text{ } Ts \text{ } Tr \text{ } D. \text{typeof-addr } h \text{ } a = \lfloor T \rfloor \wedge P \vdash \text{class-type-of } T \text{ sees } M:Ts \rightarrow Tr = \text{Native in } D)$   
**by**(*simp add: synthesized-call-def*)

**end**

**end**

## 7.2 The JinjaThreads source language with explicit call stacks

**theory** *J0* **imports**  
 $\dots/J/WWellForm$   
 $\dots/J/WellType$   
 $\dots/J/Threaded$   
 $\dots/Framework/FWBisimulation$   
 $CallExpr$   
**begin**

**declare** *widen-refT* [*elim*]



**abbreviation**  $final\text{-}expr0 :: 'addr\ expr \times 'addr\ expr\ list \Rightarrow bool$  **where**  
 $final\text{-}expr0 \equiv \lambda(e, es). final\ e \wedge es = []$

**type-synonym**

$(addr, thread\text{-}id, heap)\ J0\text{-}thread\text{-}action =$   
 $(addr, thread\text{-}id, 'addr\ expr \times 'addr\ expr\ list, heap)\ Jinja\text{-}thread\text{-}action$

**type-synonym**

$(addr, thread\text{-}id, heap)\ J0\text{-}state = (addr, thread\text{-}id, 'addr\ expr \times 'addr\ expr\ list, heap, addr)\ state$

**print-translation**  $\langle$

```

let
  fun tr'
    [a1, t
    , Const (@{type-syntax prod}, -) $
      (Const (@{type-syntax exp}, -) $
        Const (@{type-syntax String.literal}, -) $ Const (@{type-syntax unit}, -) $ a2) $
      (Const (@{type-syntax list}, -) $
        (Const (@{type-syntax exp}, -) $
          Const (@{type-syntax String.literal}, -) $
          Const (@{type-syntax unit}, -) $ a3))
    , h] =
    if a1 = a2 andalso a2 = a3 then Syntax.const @{type-syntax J0-thread-action} $ a1 $ t $ h
    else raise Match;
  in [(@{type-syntax Jinja-thread-action}, K tr')]
end

```

**typ**  $(addr, thread\text{-}id, heap)\ J0\text{-}thread\text{-}action$

**print-translation**  $\langle$

```

let
  fun tr'
    [a1, t
    , Const (@{type-syntax prod}, -) $
      (Const (@{type-syntax exp}, -) $
        Const (@{type-syntax String.literal}, -) $ Const (@{type-syntax unit}, -) $ a2) $
      (Const (@{type-syntax list}, -) $
        (Const (@{type-syntax exp}, -) $
          Const (@{type-syntax String.literal}, -) $
          Const (@{type-syntax unit}, -) $ a3))
    , h, a4] =
    if a1 = a2 andalso a2 = a3 then Syntax.const @{type-syntax J0-state} $ a1 $ t $ h
    else raise Match;
  in [(@{type-syntax state}, K tr')]
end

```

**typ**  $(addr, thread\text{-}id, heap)\ J0\text{-}state$

**definition**  $extNTA2J0 :: 'addr\ J\text{-}prog \Rightarrow (cname \times mname \times 'addr) \Rightarrow ('addr\ expr \times 'addr\ expr\ list)$   
**where**

$extNTA2J0\ P = (\lambda(C, M, a). let\ (D, -, -, meth) = method\ P\ C\ M; (-, body) = the\ meth$

$in (\{this:Class D=[Addr a]; body\}, [])$

**lemma** *extNTA2J0-iff* [simp]:

$extNTA2J0 P (C, M, a) =$

$(\{this:Class (fst (method P C M))=[Addr a]; snd (the (snd (snd (snd (method P C M))))\}, [])$

**by**(simp add: extNTA2J0-def split-def)

**abbreviation** *extTA2J0* ::

$'addr J\text{-}prog \Rightarrow ('addr, 'thread\text{-}id, 'heap) \text{ external-thread-action} \Rightarrow ('addr, 'thread\text{-}id, 'heap) J0\text{-thread-action}$

**where**  $extTA2J0 P \equiv convert\text{-}extTA (extNTA2J0 P)$

**lemma** *obs-a-extTA2J-eq-obs-a-extTA2J0* [simp]:  $\{\!\!| extTA2J P ta \!\!\}_o = \{\!\!| extTA2J0 P ta \!\!\}_o$

**by**(cases ta)(simp add: ta-upd-simps)

**lemma** *extTA2J0-ε*:  $extTA2J0 P \varepsilon = \varepsilon$

**by**(simp)

**context** *J-heap-base* **begin**

**definition** *no-call* ::  $'m prog \Rightarrow 'heap \Rightarrow ('a, 'b, 'addr) exp \Rightarrow bool$

**where**  $no\text{-}call P h e = (\forall aMvs. call e = [aMvs] \longrightarrow synthesized\text{-}call P h aMvs)$

**definition** *no-calls* ::  $'m prog \Rightarrow 'heap \Rightarrow ('a, 'b, 'addr) exp list \Rightarrow bool$

**where**  $no\text{-}calls P h es = (\forall aMvs. calls es = [aMvs] \longrightarrow synthesized\text{-}call P h aMvs)$

**inductive** *red0* ::

$'addr J\text{-}prog \Rightarrow 'thread\text{-}id \Rightarrow 'addr expr \Rightarrow 'addr expr list \Rightarrow 'heap$

$\Rightarrow ('addr, 'thread\text{-}id, 'heap) J0\text{-thread-action} \Rightarrow 'addr expr \Rightarrow 'addr expr list \Rightarrow 'heap \Rightarrow bool$

$(-, \vdash 0 ((1 \langle \cdot / \cdot, \cdot \rangle) \dashrightarrow / (1 \langle \cdot / \cdot, \cdot \rangle))) [51, 0, 0, 0, 0, 0, 0, 0] 81)$

**for**  $P :: 'addr J\text{-}prog$  **and**  $t :: 'thread\text{-}id$

**where**

*red0Red*:

$\llbracket extTA2J0 P, P, t \vdash \langle e, (h, Map.empty) \rangle -ta \rightarrow \langle e', (h', xs') \rangle;$

$\forall aMvs. call e = [aMvs] \longrightarrow synthesized\text{-}call P h aMvs \rrbracket$

$\implies P, t \vdash 0 \langle e/es, h \rangle -ta \rightarrow \langle e'/es, h' \rangle$

| *red0Call*:

$\llbracket call e = [(a, M, vs)]; type\text{-}of\text{-}addr h a = [U];$

$P \vdash class\text{-}type\text{-}of U \text{ sees } M:Ts \rightarrow T = [(pns, body)] \text{ in } D;$

$size vs = size pns; size Ts = size pns \rrbracket$

$\implies P, t \vdash 0 \langle e/es, h \rangle -\varepsilon \rightarrow \langle blocks (this \# pns) (Class D \# Ts) (Addr a \# vs) body/e\#es, h \rangle$

| *red0Return*:

$final e' \implies P, t \vdash 0 \langle e'/e\#es, h \rangle -\varepsilon \rightarrow \langle inline\text{-}call e' e/es, h \rangle$

**abbreviation** *J0-start-state* ::  $'addr J\text{-}prog \Rightarrow cname \Rightarrow mname \Rightarrow 'addr val list \Rightarrow ('addr, 'thread\text{-}id,$

$'heap) J0\text{-state}$

**where**

$J0\text{-start-state} \equiv$

$start\text{-}state (\lambda C M Ts T (pns, body) vs. (blocks (this \# pns) (Class C \# Ts) (Null \# vs) body, []))$

**abbreviation** *mred0* ::

$'addr J\text{-}prog \Rightarrow ('addr, 'thread\text{-}id, 'addr expr \times 'addr expr list, 'heap, 'addr, ('addr, 'thread\text{-}id) obs\text{-}event)$

*semantics*

**where**  $mred0\ P \equiv (\lambda t\ ((e, es), h)\ ta\ ((e', es'), h')).\ red0\ P\ t\ e\ es\ h\ ta\ e'\ es'\ h')$

**end**

**declare**  $domIff[iff, simp\ del]$

**context**  $J\text{-heap-base}$  **begin**

**lemma** **assumes**  $wf: wwf\text{-}J\text{-prog}\ P$

**shows**  $red\text{-}fv\text{-}subset: extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies fv\ e' \subseteq fv\ e$

**and**  $reds\text{-}fvs\text{-}subset: extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies fvs\ es' \subseteq fvs\ es$

**proof**(*induct rule: red-reds.inducts*)

**case** ( $RedCall\ s\ a\ U\ M\ Ts\ T\ pns\ body\ D\ vs$ )

**hence**  $fv\ body \subseteq \{this\} \cup set\ pns$

**using**  $wf$  **by**( $fastforce\ dest!: sees\text{-}wf\text{-}mdecl\ simp: wf\text{-}mdecl\text{-}def$ )

**with**  $RedCall$  **show**  $?case$  **by**  $fastforce$

**next**

**case**  $RedCallExternal$  **thus**  $?case$  **by**( $auto\ simp\ add: extRet2J\text{-}def\ split: extCallRet.split\text{-}asm$ )

**qed**( $fastforce$ )**+**

**end**

**declare**  $domIff[iff\ del]$

**context**  $J\text{-heap-base}$  **begin**

**lemma** **assumes**  $wf: wwf\text{-}J\text{-prog}\ P$

**shows**  $red\text{-}fv\text{-}ok: \llbracket extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; fv\ e \subseteq dom\ (lcl\ s) \rrbracket \implies fv\ e' \subseteq dom\ (lcl\ s')$

**and**  $reds\text{-}fvs\text{-}ok: \llbracket extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; fvs\ es \subseteq dom\ (lcl\ s) \rrbracket \implies fvs\ es' \subseteq dom\ (lcl\ s')$

**proof**(*induct rule: red-reds.inducts*)

**case** ( $RedCall\ s\ a\ U\ M\ Ts\ T\ pns\ body\ D\ vs$ )

**from**  $\langle P \vdash class\text{-}type\text{-}of\ U\ sees\ M: Ts \rightarrow T = \lfloor(pns, body)\rfloor\ in\ D \rangle$  **have**  $wwf\text{-}J\text{-mdecl}\ P\ D\ (M, Ts, T, pns, body)$

**by**( $auto\ dest!: sees\text{-}wf\text{-}mdecl[OF\ wwf]\ simp\ add: wf\text{-}mdecl\text{-}def$ )

**with**  $RedCall$  **show**  $?case$  **by**( $auto$ )

**next**

**case**  $RedCallExternal$  **thus**  $?case$  **by**( $auto\ simp\ add: extRet2J\text{-}def\ split: extCallRet.split\text{-}asm$ )

**next**

**case** ( $BlockRed\ e\ h\ x\ V\ vo\ ta\ e'\ h'\ x'\ T$ )

**note**  $red = \langle extTA, P, t \vdash \langle e, (h, x(V := vo)) \rangle -ta \rightarrow \langle e', (h', x') \rangle \rangle$

**hence**  $fv\ e' \subseteq fv\ e$  **by**( $auto\ dest: red\text{-}fv\text{-}subset[OF\ wwf]\ del: subsetI$ )

**moreover** **from**  $\langle fv\ \{V: T=vo; e\} \subseteq dom\ (lcl\ (h, x)) \rangle$

**have**  $fv\ e - \{V\} \subseteq dom\ x$  **by**( $simp$ )

**ultimately** **have**  $fv\ e' - \{V\} \subseteq dom\ x - \{V\}$  **by**( $auto$ )

**moreover** **from**  $red$  **have**  $dom\ (x(V := vo)) \subseteq dom\ x'$

**by**( $auto\ dest: red\text{-}lcl\text{-}incr\ del: subsetI$ )

**ultimately** **have**  $fv\ e' - \{V\} \subseteq dom\ x' - \{V\}$

**by**( $auto\ split: if\text{-}split\text{-}asm$ )

**thus**  $?case$  **by**( $auto\ simp\ del: fun\text{-}upd\text{-}apply$ )

**qed**( $fastforce\ dest: red\text{-}lcl\text{-}incr\ del: subsetI$ )**+**

**lemma** *is-call-red-state-unchanged:*

$\llbracket extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; call\ e = \lfloor aMvs \rfloor; \neg synthesized\text{-}call\ P\ (hp\ s)\ aMvs \rrbracket \implies s' = s$

$\wedge ta = \varepsilon$

**and** *is-calls-reds-state-unchanged*:  
 $\llbracket extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; calls\ es = \lfloor aMvs \rfloor; \neg synthesized-call\ P\ (hp\ s)\ aMvs \rrbracket \implies s' = s \wedge ta = \varepsilon$   
**apply**(*induct rule: red-reds.inducts*)  
**apply**(*fastforce split: if-split-asm simp add: synthesized-call-def*) +  
**done**

**lemma** *called-methodD*:

$\llbracket extTA, P, t \vdash \langle e, s \rangle [-ta \rightarrow] \langle e', s' \rangle; call\ e = \lfloor (a, M, vs) \rfloor; \neg synthesized-call\ P\ (hp\ s)\ (a, M, vs) \rrbracket$   
 $\implies \exists hT\ D\ Us\ U\ pns\ body. hp\ s' = hp\ s \wedge typeof-addr\ (hp\ s)\ a = \lfloor hT \rfloor \wedge$   
 $P \vdash class-type-of\ hT\ sees\ M: Us \rightarrow U = \lfloor (pns, body) \rfloor\ in\ D \wedge$   
 $length\ vs = length\ pns \wedge length\ Us = length\ pns$

**and** *called-methodsD*:  
 $\llbracket extTA, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; calls\ es = \lfloor (a, M, vs) \rfloor; \neg synthesized-call\ P\ (hp\ s)\ (a, M, vs) \rrbracket$   
 $\implies \exists hT\ D\ Us\ U\ pns\ body. hp\ s' = hp\ s \wedge typeof-addr\ (hp\ s)\ a = \lfloor hT \rfloor \wedge$   
 $P \vdash class-type-of\ hT\ sees\ M: Us \rightarrow U = \lfloor (pns, body) \rfloor\ in\ D \wedge$   
 $length\ vs = length\ pns \wedge length\ Us = length\ pns$   
**apply**(*induct rule: red-reds.inducts*)  
**apply**(*auto split: if-split-asm simp add: synthesized-call-def*)  
**apply**(*fastforce*)  
**done**

## 7.2.1 Silent moves

**primrec**  $\tau move0 :: 'm\ prog \Rightarrow 'heap \Rightarrow ('a, 'b, 'addr)\ exp \Rightarrow bool$   
**and**  $\tau moves0 :: 'm\ prog \Rightarrow 'heap \Rightarrow ('a, 'b, 'addr)\ exp\ list \Rightarrow bool$   
**where**  
 $\tau move0\ P\ h\ (new\ C) \longleftrightarrow False$   
 $\tau move0\ P\ h\ (newA\ T[e]) \longleftrightarrow \tau move0\ P\ h\ e \vee (\exists a. e = Throw\ a)$   
 $\tau move0\ P\ h\ (Cast\ U\ e) \longleftrightarrow \tau move0\ P\ h\ e \vee (\exists a. e = Throw\ a) \vee (\exists v. e = Val\ v)$   
 $\tau move0\ P\ h\ (e\ instanceof\ T) \longleftrightarrow \tau move0\ P\ h\ e \vee (\exists a. e = Throw\ a) \vee (\exists v. e = Val\ v)$   
 $\tau move0\ P\ h\ (e\ \ll bop \gg e') \longleftrightarrow \tau move0\ P\ h\ e \vee (\exists a. e = Throw\ a) \vee (\exists v. e = Val\ v \wedge$   
 $(\tau move0\ P\ h\ e' \vee (\exists a. e' = Throw\ a) \vee (\exists v. e' = Val\ v)))$   
 $\tau move0\ P\ h\ (Val\ v) \longleftrightarrow False$   
 $\tau move0\ P\ h\ (Var\ V) \longleftrightarrow True$   
 $\tau move0\ P\ h\ (V := e) \longleftrightarrow \tau move0\ P\ h\ e \vee (\exists a. e = Throw\ a) \vee (\exists v. e = Val\ v)$   
 $\tau move0\ P\ h\ (a[i]) \longleftrightarrow \tau move0\ P\ h\ a \vee (\exists ad. a = Throw\ ad) \vee (\exists v. a = Val\ v \wedge (\tau move0\ P\ h\ i$   
 $\vee (\exists a. i = Throw\ a)))$   
 $\tau move0\ P\ h\ (AAss\ a\ i\ e) \longleftrightarrow \tau move0\ P\ h\ a \vee (\exists ad. a = Throw\ ad) \vee (\exists v. a = Val\ v \wedge$   
 $(\tau move0\ P\ h\ i \vee (\exists a. i = Throw\ a) \vee (\exists v. i = Val\ v \wedge (\tau move0\ P\ h\ e \vee (\exists a. e = Throw\ a)))))$   
 $\tau move0\ P\ h\ (a.length) \longleftrightarrow \tau move0\ P\ h\ a \vee (\exists ad. a = Throw\ ad)$   
 $\tau move0\ P\ h\ (e.F\{D\}) \longleftrightarrow \tau move0\ P\ h\ e \vee (\exists a. e = Throw\ a)$   
 $\tau move0\ P\ h\ (FAss\ e\ F\ D\ e') \longleftrightarrow \tau move0\ P\ h\ e \vee (\exists a. e = Throw\ a) \vee (\exists v. e = Val\ v \wedge (\tau move0\ P\ h\ e' \vee (\exists a. e' = Throw\ a)))$   
 $\tau move0\ P\ h\ (e.compareAndSwap(D.F, e', e'')) \longleftrightarrow \tau move0\ P\ h\ e \vee (\exists a. e = Throw\ a) \vee (\exists v. e = Val\ v \wedge$   
 $(\tau move0\ P\ h\ e' \vee (\exists a. e' = Throw\ a) \vee (\exists v. e' = Val\ v \wedge (\tau move0\ P\ h\ e'' \vee (\exists a. e'' = Throw\ a)))))$   
 $\tau move0\ P\ h\ (e.M(es)) \longleftrightarrow \tau move0\ P\ h\ e \vee (\exists a. e = Throw\ a) \vee (\exists v. e = Val\ v \wedge$   
 $((\tau moves0\ P\ h\ es \vee (\exists vs\ a\ es'. es = map\ Val\ vs\ @\ Throw\ a\ \# es')) \vee$

$(\exists vs. es = \text{map Val } vs \wedge (v = \text{Null} \vee (\forall T C Ts Tr D. \text{typeof}_h v = \lfloor T \rfloor \longrightarrow \text{class-type-of}' T = \lfloor C \rfloor \longrightarrow P \vdash C \text{ sees } M: Ts \rightarrow Tr = \text{Native in } D \longrightarrow \tau \text{external-defs } D M))))$   
 $\mid \tau \text{move0 } P h (\{V:T=vo; e\}) \longleftrightarrow \tau \text{move0 } P h e \vee ((\exists a. e = \text{Throw } a) \vee (\exists v. e = \text{Val } v))$   
 $\mid \tau \text{move0 } P h (\text{sync}_{V'}(e) e') \longleftrightarrow \tau \text{move0 } P h e \vee (\exists a. e = \text{Throw } a)$   
 $\mid \tau \text{move0 } P h (\text{insync}_{V'}(ad) e) \longleftrightarrow \tau \text{move0 } P h e$   
 $\mid \tau \text{move0 } P h (e;;e') \longleftrightarrow \tau \text{move0 } P h e \vee (\exists a. e = \text{Throw } a) \vee (\exists v. e = \text{Val } v)$   
 $\mid \tau \text{move0 } P h (\text{if } (e) e' \text{ else } e'') \longleftrightarrow \tau \text{move0 } P h e \vee (\exists a. e = \text{Throw } a) \vee (\exists v. e = \text{Val } v)$   
 $\mid \tau \text{move0 } P h (\text{while } (e) e') = \text{True}$   
 $\mid \text{--- Throw } a \text{ is no } \tau \text{move0} \text{ because there is no reduction for it. If it were, most defining equations would be simpler. However, } \text{insync}_{V'}(ad) \text{ Throw } ad \text{ must not be a } \tau \text{move0, but would be if } \text{Throw } a \text{ was.}$

$\tau \text{move0 } P h (\text{throw } e) \longleftrightarrow \tau \text{move0 } P h e \vee (\exists a. e = \text{Throw } a) \vee e = \text{null}$   
 $\mid \tau \text{move0 } P h (\text{try } e \text{ catch } (C V) e') \longleftrightarrow \tau \text{move0 } P h e \vee (\exists a. e = \text{Throw } a) \vee (\exists v. e = \text{Val } v)$

$\mid \tau \text{moves0 } P h [] \longleftrightarrow \text{False}$   
 $\mid \tau \text{moves0 } P h (e \# es) \longleftrightarrow \tau \text{move0 } P h e \vee (\exists v. e = \text{Val } v \wedge \tau \text{moves0 } P h es)$

**abbreviation**  $\tau \text{MOVE} :: 'm \text{ prog} \Rightarrow (('addr \text{ expr} \times 'addr \text{ locals}) \times 'heap, ('addr, 'thread-id, 'heap) J\text{-thread-action}) \text{ trsys}$

**where**  $\tau \text{MOVE} \equiv \lambda P ((e, x), h) ta s'. \tau \text{move0 } P h e \wedge ta = \varepsilon$

**primrec**  $\tau \text{Move0} :: 'm \text{ prog} \Rightarrow 'heap \Rightarrow ('addr \text{ expr} \times 'addr \text{ expr list}) \Rightarrow \text{bool}$

**where**

$\tau \text{Move0 } P h (e, es) = (\tau \text{move0 } P h e \vee \text{final } e)$

**abbreviation**  $\tau \text{MOVE0} :: 'm \text{ prog} \Rightarrow (('addr \text{ expr} \times 'addr \text{ expr list}) \times 'heap, ('addr, 'thread-id, 'heap) J0\text{-thread-action}) \text{ trsys}$

**where**  $\tau \text{MOVE0} \equiv \lambda P (es, h) ta s. \tau \text{Move0 } P h es \wedge ta = \varepsilon$

**definition**  $\tau \text{red0} ::$

$((('addr, 'thread-id, 'heap) \text{ external-thread-action} \Rightarrow ('addr, 'thread-id, 'x, 'heap) \text{ Jinja-thread-action})$   
 $\Rightarrow 'addr J\text{-prog} \Rightarrow 'thread-id \Rightarrow 'heap \Rightarrow ('addr \text{ expr} \times 'addr \text{ locals}) \Rightarrow ('addr \text{ expr} \times 'addr \text{ locals})$   
 $\Rightarrow \text{bool}$

**where**

$\tau \text{red0 extTA } P t h es e'xs' =$   
 $(\text{extTA}, P, t \vdash \langle \text{fst } es, (h, \text{snd } es) \rangle \rightarrow \varepsilon \rightarrow \langle \text{fst } e'xs', (h, \text{snd } e'xs') \rangle \wedge \tau \text{move0 } P h (\text{fst } es) \wedge \text{no-call } P h (\text{fst } es))$

**definition**  $\tau \text{reds0} ::$

$((('addr, 'thread-id, 'heap) \text{ external-thread-action} \Rightarrow ('addr, 'thread-id, 'x, 'heap) \text{ Jinja-thread-action})$   
 $\Rightarrow 'addr J\text{-prog} \Rightarrow 'thread-id \Rightarrow 'heap \Rightarrow ('addr \text{ expr list} \times 'addr \text{ locals}) \Rightarrow ('addr \text{ expr list} \times 'addr \text{ locals})$   
 $\Rightarrow \text{bool}$

**where**

$\tau \text{reds0 extTA } P t h esxs es'xs' =$   
 $(\text{extTA}, P, t \vdash \langle \text{fst } esxs, (h, \text{snd } esxs) \rangle \rightarrow \varepsilon \rightarrow \langle \text{fst } es'xs', (h, \text{snd } es'xs') \rangle \wedge \tau \text{moves0 } P h (\text{fst } esxs) \wedge \text{no-calls } P h (\text{fst } esxs))$

**abbreviation**  $\tau \text{red0t} ::$

$((('addr, 'thread-id, 'heap) \text{ external-thread-action} \Rightarrow ('addr, 'thread-id, 'x, 'heap) \text{ Jinja-thread-action})$   
 $\Rightarrow 'addr J\text{-prog} \Rightarrow 'thread-id \Rightarrow 'heap \Rightarrow ('addr \text{ expr} \times 'addr \text{ locals}) \Rightarrow ('addr \text{ expr} \times 'addr \text{ locals})$   
 $\Rightarrow \text{bool}$

**where**  $\tau \text{red0t extTA } P t h \equiv (\tau \text{red0 extTA } P t h)^{+++}$

**abbreviation**  $\tau \text{reds0t} ::$

$((\text{'addr}, \text{'thread-id}, \text{'heap}) \text{ external-thread-action} \Rightarrow (\text{'addr}, \text{'thread-id}, \text{'x}, \text{'heap}) \text{ Jinja-thread-action})$   
 $\Rightarrow \text{'addr } J\text{-prog} \Rightarrow \text{'thread-id} \Rightarrow \text{'heap} \Rightarrow (\text{'addr expr list} \times \text{'addr locals}) \Rightarrow (\text{'addr expr list} \times \text{'addr locals}) \Rightarrow \text{bool}$

**where**  $\tau\text{reds0t extTA } P \text{ } t \text{ } h \equiv (\tau\text{reds0 extTA } P \text{ } t \text{ } h)^{\wedge++}$

**abbreviation**  $\tau\text{red0r} ::$

$((\text{'addr}, \text{'thread-id}, \text{'heap}) \text{ external-thread-action} \Rightarrow (\text{'addr}, \text{'thread-id}, \text{'x}, \text{'heap}) \text{ Jinja-thread-action})$   
 $\Rightarrow \text{'addr } J\text{-prog} \Rightarrow \text{'thread-id} \Rightarrow \text{'heap} \Rightarrow (\text{'addr expr} \times \text{'addr locals}) \Rightarrow (\text{'addr expr} \times \text{'addr locals}) \Rightarrow \text{bool}$

**where**  $\tau\text{red0r extTA } P \text{ } t \text{ } h \equiv (\tau\text{red0 extTA } P \text{ } t \text{ } h)^{\wedge**}$

**abbreviation**  $\tau\text{reds0r} ::$

$((\text{'addr}, \text{'thread-id}, \text{'heap}) \text{ external-thread-action} \Rightarrow (\text{'addr}, \text{'thread-id}, \text{'x}, \text{'heap}) \text{ Jinja-thread-action})$   
 $\Rightarrow \text{'addr } J\text{-prog} \Rightarrow \text{'thread-id} \Rightarrow \text{'heap} \Rightarrow (\text{'addr expr list} \times \text{'addr locals}) \Rightarrow (\text{'addr expr list} \times \text{'addr locals}) \Rightarrow \text{bool}$

**where**  $\tau\text{reds0r extTA } P \text{ } t \text{ } h \equiv (\tau\text{reds0 extTA } P \text{ } t \text{ } h)^{\wedge**}$

**definition**  $\tau\text{Red0} ::$

$\text{'addr } J\text{-prog} \Rightarrow \text{'thread-id} \Rightarrow \text{'heap} \Rightarrow (\text{'addr expr} \times \text{'addr expr list}) \Rightarrow (\text{'addr expr} \times \text{'addr expr list}) \Rightarrow \text{bool}$

**where**  $\tau\text{Red0 } P \text{ } t \text{ } h \text{ } ees \text{ } e'es' = (P, t \vdash 0 \langle \text{fst } ees / \text{snd } ees, h \rangle - \varepsilon \rightarrow \langle \text{fst } e'es' / \text{snd } e'es', h \rangle) \wedge \tau\text{Move0 } P \text{ } h \text{ } ees)$

**abbreviation**  $\tau\text{Red0r} ::$

$\text{'addr } J\text{-prog} \Rightarrow \text{'thread-id} \Rightarrow \text{'heap} \Rightarrow (\text{'addr expr} \times \text{'addr expr list}) \Rightarrow (\text{'addr expr} \times \text{'addr expr list}) \Rightarrow \text{bool}$

**where**  $\tau\text{Red0r } P \text{ } t \text{ } h \equiv (\tau\text{Red0 } P \text{ } t \text{ } h)^{\wedge**}$

**abbreviation**  $\tau\text{Red0t} ::$

$\text{'addr } J\text{-prog} \Rightarrow \text{'thread-id} \Rightarrow \text{'heap} \Rightarrow (\text{'addr expr} \times \text{'addr expr list}) \Rightarrow (\text{'addr expr} \times \text{'addr expr list}) \Rightarrow \text{bool}$

**where**  $\tau\text{Red0t } P \text{ } t \text{ } h \equiv (\tau\text{Red0 } P \text{ } t \text{ } h)^{\wedge++}$

**lemma**  $\tau\text{move0-}\tau\text{moves0-intros}:$

**fixes**  $e \text{ } e1 \text{ } e2 \text{ } e' :: (\text{'a}, \text{'b}, \text{'addr}) \text{ exp}$  **and**  $es :: (\text{'a}, \text{'b}, \text{'addr}) \text{ exp list}$   
**shows**  $\tau\text{move0NewArray}: \tau\text{move0 } P \text{ } h \text{ } e \Longrightarrow \tau\text{move0 } P \text{ } h \text{ } (\text{newA } T[e])$   
**and**  $\tau\text{move0Cast}: \tau\text{move0 } P \text{ } h \text{ } e \Longrightarrow \tau\text{move0 } P \text{ } h \text{ } (\text{Cast } U \text{ } e)$   
**and**  $\tau\text{move0CastRed}: \tau\text{move0 } P \text{ } h \text{ } (\text{Cast } U \text{ } (\text{Val } v))$   
**and**  $\tau\text{move0InstanceOf}: \tau\text{move0 } P \text{ } h \text{ } e \Longrightarrow \tau\text{move0 } P \text{ } h \text{ } (e \text{ instanceof } T)$   
**and**  $\tau\text{move0InstanceOfRed}: \tau\text{move0 } P \text{ } h \text{ } ((\text{Val } v) \text{ instanceof } T)$   
**and**  $\tau\text{move0BinOp1}: \tau\text{move0 } P \text{ } h \text{ } e \Longrightarrow \tau\text{move0 } P \text{ } h \text{ } (e \ll \text{bop} \gg e')$   
**and**  $\tau\text{move0BinOp2}: \tau\text{move0 } P \text{ } h \text{ } e \Longrightarrow \tau\text{move0 } P \text{ } h \text{ } (\text{Val } v \ll \text{bop} \gg e)$   
**and**  $\tau\text{move0BinOp}: \tau\text{move0 } P \text{ } h \text{ } (\text{Val } v \ll \text{bop} \gg \text{Val } v')$   
**and**  $\tau\text{move0Var}: \tau\text{move0 } P \text{ } h \text{ } (\text{Var } V)$   
**and**  $\tau\text{move0LAss}: \tau\text{move0 } P \text{ } h \text{ } e \Longrightarrow \tau\text{move0 } P \text{ } h \text{ } (V := e)$   
**and**  $\tau\text{move0LAssRed}: \tau\text{move0 } P \text{ } h \text{ } (V := \text{Val } v)$   
**and**  $\tau\text{move0AAcc1}: \tau\text{move0 } P \text{ } h \text{ } e \Longrightarrow \tau\text{move0 } P \text{ } h \text{ } (e[e'])$   
**and**  $\tau\text{move0AAcc2}: \tau\text{move0 } P \text{ } h \text{ } e \Longrightarrow \tau\text{move0 } P \text{ } h \text{ } (\text{Val } v[e])$   
**and**  $\tau\text{move0AAss1}: \tau\text{move0 } P \text{ } h \text{ } e \Longrightarrow \tau\text{move0 } P \text{ } h \text{ } (e[e1] := e2)$   
**and**  $\tau\text{move0AAss2}: \tau\text{move0 } P \text{ } h \text{ } e \Longrightarrow \tau\text{move0 } P \text{ } h \text{ } (\text{Val } v[e] := e')$   
**and**  $\tau\text{move0AAss3}: \tau\text{move0 } P \text{ } h \text{ } e \Longrightarrow \tau\text{move0 } P \text{ } h \text{ } (\text{Val } v[\text{Val } v'] := e)$   
**and**  $\tau\text{move0ALength}: \tau\text{move0 } P \text{ } h \text{ } e \Longrightarrow \tau\text{move0 } P \text{ } h \text{ } (e.\text{length})$   
**and**  $\tau\text{move0FAcc}: \tau\text{move0 } P \text{ } h \text{ } e \Longrightarrow \tau\text{move0 } P \text{ } h \text{ } (e.F\{D\})$   
**and**  $\tau\text{move0FAss1}: \tau\text{move0 } P \text{ } h \text{ } e \Longrightarrow \tau\text{move0 } P \text{ } h \text{ } (\text{FAss } e \text{ } F \text{ } D \text{ } e')$

**and**  $\tau\text{move0FAss2}: \tau\text{move0 } P \ h \ e \implies \tau\text{move0 } P \ h \ (\text{Val } v \cdot F\{D\} := e)$   
**and**  $\tau\text{move0CAS1}: \tau\text{move0 } P \ h \ e \implies \tau\text{move0 } P \ h \ (e \cdot \text{compareAndSwap}(D \cdot F, e', e''))$   
**and**  $\tau\text{move0CAS2}: \tau\text{move0 } P \ h \ e' \implies \tau\text{move0 } P \ h \ (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, e', e''))$   
**and**  $\tau\text{move0CAS3}: \tau\text{move0 } P \ h \ e'' \implies \tau\text{move0 } P \ h \ (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', e''))$   
**and**  $\tau\text{move0CallObj}: \tau\text{move0 } P \ h \ e \implies \tau\text{move0 } P \ h \ (e \cdot M(es))$   
**and**  $\tau\text{move0CallParams}: \tau\text{moves0 } P \ h \ es \implies \tau\text{move0 } P \ h \ (\text{Val } v \cdot M(es))$   
**and**  $\tau\text{move0Call}: (\bigwedge T \ C \ Ts \ Tr \ D. \llbracket \text{typeof}_h v = \lfloor T \rfloor; \text{class-type-of}' T = \lfloor C \rfloor; P \vdash C \text{ sees } M: Ts \rightarrow Tr = \text{Native in } D \rrbracket \implies \tau\text{external-defs } D \ M) \implies \tau\text{move0 } P \ h \ (\text{Val } v \cdot M(\text{map Val } vs))$   
**and**  $\tau\text{move0Block}: \tau\text{move0 } P \ h \ e \implies \tau\text{move0 } P \ h \ \{V:T=vo; e\}$   
**and**  $\tau\text{move0BlockRed}: \tau\text{move0 } P \ h \ \{V:T=vo; \text{Val } v\}$   
**and**  $\tau\text{move0Sync}: \tau\text{move0 } P \ h \ e \implies \tau\text{move0 } P \ h \ (\text{sync}_{V'}(e) \ e')$   
**and**  $\tau\text{move0InSync}: \tau\text{move0 } P \ h \ e \implies \tau\text{move0 } P \ h \ (\text{insync}_{V'}(a) \ e)$   
**and**  $\tau\text{move0Seq}: \tau\text{move0 } P \ h \ e \implies \tau\text{move0 } P \ h \ (e;;e')$   
**and**  $\tau\text{move0SeqRed}: \tau\text{move0 } P \ h \ (\text{Val } v;; e')$   
**and**  $\tau\text{move0Cond}: \tau\text{move0 } P \ h \ e \implies \tau\text{move0 } P \ h \ (\text{if } (e) \ e1 \text{ else } e2)$   
**and**  $\tau\text{move0CondRed}: \tau\text{move0 } P \ h \ (\text{if } (\text{Val } v) \ e1 \text{ else } e2)$   
**and**  $\tau\text{move0WhileRed}: \tau\text{move0 } P \ h \ (\text{while } (e) \ e')$   
**and**  $\tau\text{move0Throw}: \tau\text{move0 } P \ h \ e \implies \tau\text{move0 } P \ h \ (\text{throw } e)$   
**and**  $\tau\text{move0ThrowNull}: \tau\text{move0 } P \ h \ (\text{throw null})$   
**and**  $\tau\text{move0Try}: \tau\text{move0 } P \ h \ e \implies \tau\text{move0 } P \ h \ (\text{try } e \text{ catch } (C \ V) \ e')$   
**and**  $\tau\text{move0TryRed}: \tau\text{move0 } P \ h \ (\text{try } \text{Val } v \text{ catch } (C \ V) \ e)$   
**and**  $\tau\text{move0TryThrow}: \tau\text{move0 } P \ h \ (\text{try } \text{Throw } a \text{ catch } (C \ V) \ e)$   
**and**  $\tau\text{move0NewArrayThrow}: \tau\text{move0 } P \ h \ (\text{newA } T \lfloor \text{Throw } a \rfloor)$   
**and**  $\tau\text{move0CastThrow}: \tau\text{move0 } P \ h \ (\text{Cast } T \ (\text{Throw } a))$   
**and**  $\tau\text{move0CInstanceOfThrow}: \tau\text{move0 } P \ h \ ((\text{Throw } a) \text{ instanceof } T)$   
**and**  $\tau\text{move0BinOpThrow1}: \tau\text{move0 } P \ h \ (\text{Throw } a \ \langle\langle \text{bop} \rangle\rangle \ e')$   
**and**  $\tau\text{move0BinOpThrow2}: \tau\text{move0 } P \ h \ (\text{Val } v \ \langle\langle \text{bop} \rangle\rangle \ \text{Throw } a)$   
**and**  $\tau\text{move0LAssThrow}: \tau\text{move0 } P \ h \ (V := (\text{Throw } a))$   
**and**  $\tau\text{move0AAccThrow1}: \tau\text{move0 } P \ h \ (\text{Throw } a \lfloor e \rfloor)$   
**and**  $\tau\text{move0AAccThrow2}: \tau\text{move0 } P \ h \ (\text{Val } v \lfloor \text{Throw } a \rfloor)$   
**and**  $\tau\text{move0AAssThrow1}: \tau\text{move0 } P \ h \ (\text{AAss } (\text{Throw } a) \ e \ e')$   
**and**  $\tau\text{move0AAssThrow2}: \tau\text{move0 } P \ h \ (\text{AAss } (\text{Val } v) \ (\text{Throw } a) \ e')$   
**and**  $\tau\text{move0AAssThrow3}: \tau\text{move0 } P \ h \ (\text{AAss } (\text{Val } v) \ (\text{Val } v') \ (\text{Throw } a))$   
**and**  $\tau\text{move0ALengthThrow}: \tau\text{move0 } P \ h \ (\text{Throw } a \cdot \text{length})$   
**and**  $\tau\text{move0FAccThrow}: \tau\text{move0 } P \ h \ (\text{Throw } a \cdot F\{D\})$   
**and**  $\tau\text{move0FAssThrow1}: \tau\text{move0 } P \ h \ (\text{Throw } a \cdot F\{D\} := e)$   
**and**  $\tau\text{move0FAssThrow2}: \tau\text{move0 } P \ h \ (F\text{Ass } (\text{Val } v) \ F \ D \ (\text{Throw } a))$   
**and**  $\tau\text{move0CallThrowObj}: \tau\text{move0 } P \ h \ (\text{Throw } a \cdot M(es))$   
**and**  $\tau\text{move0CallThrowParams}: \tau\text{move0 } P \ h \ (\text{Val } v \cdot M(\text{map Val } vs \ @ \ \text{Throw } a \ \# \ es))$   
**and**  $\tau\text{move0BlockThrow}: \tau\text{move0 } P \ h \ \{V:T=vo; \text{Throw } a\}$   
**and**  $\tau\text{move0SyncThrow}: \tau\text{move0 } P \ h \ (\text{sync}_{V'}(\text{Throw } a) \ e)$   
**and**  $\tau\text{move0SeqThrow}: \tau\text{move0 } P \ h \ (\text{Throw } a;;e)$   
**and**  $\tau\text{move0CondThrow}: \tau\text{move0 } P \ h \ (\text{if } (\text{Throw } a) \ e1 \text{ else } e2)$   
**and**  $\tau\text{move0ThrowThrow}: \tau\text{move0 } P \ h \ (\text{throw } (\text{Throw } a))$

**and**  $\tau\text{moves0Hd}: \tau\text{move0 } P \ h \ e \implies \tau\text{moves0 } P \ h \ (e \ \# \ es)$   
**and**  $\tau\text{moves0Tl}: \tau\text{moves0 } P \ h \ es \implies \tau\text{moves0 } P \ h \ (\text{Val } v \ \# \ es)$

**by** *auto*

**lemma**  $\tau\text{moves0-map-Val} \ [iff]:$

$\neg \tau\text{moves0 } P \ h \ (\text{map Val } vs)$

**by**(*induct vs auto*)

**lemma**  $\tau\text{moves0-map-Val-append} \ [simp]:$

$\tau \text{moves0 } P \ h \ (\text{map } \text{Val } \text{vs} \ @ \ \text{es}) = \tau \text{moves0 } P \ h \ \text{es}$   
**by**(*induct vs*)(*auto*)

**lemma** *no-reds-map-Val-Throw* [*simp*]:

$\text{extTA}, P, t \vdash \langle \text{map } \text{Val } \text{vs} \ @ \ \text{Throw } a \ \# \ \text{es}, s \rangle \ [-ta \rightarrow] \langle \text{es}', s' \rangle = \text{False}$   
**by**(*induct vs arbitrary: es'*)(*auto elim: reds.cases*)

**lemma** *assumes* [*simp*]:  $\text{extTA } \varepsilon = \varepsilon$

**shows** *red- $\tau$ -taD*:  $\llbracket \text{extTA}, P, t \vdash \langle e, s \rangle \ [-ta \rightarrow] \langle e', s' \rangle; \tau \text{move0 } P \ (hp \ s) \ e \rrbracket \Longrightarrow ta = \varepsilon$

**and** *reds- $\tau$ -taD*:  $\llbracket \text{extTA}, P, t \vdash \langle \text{es}, s \rangle \ [-ta \rightarrow] \langle \text{es}', s' \rangle; \tau \text{moves0 } P \ (hp \ s) \ \text{es} \rrbracket \Longrightarrow ta = \varepsilon$

**apply**(*induct rule: red-reds.inducts*)

**apply**(*fastforce simp add: map-eq-append-conv  $\tau$ external'-def  $\tau$ external-def dest:  $\tau$ external'-red-external-TA-empty*)  
**done**

**lemma**  *$\tau$ move0-heap-unchanged*:  $\llbracket \text{extTA}, P, t \vdash \langle e, s \rangle \ [-ta \rightarrow] \langle e', s' \rangle; \tau \text{move0 } P \ (hp \ s) \ e \rrbracket \Longrightarrow hp \ s' = hp \ s$

**and**  *$\tau$ moves0-heap-unchanged*:  $\llbracket \text{extTA}, P, t \vdash \langle \text{es}, s \rangle \ [-ta \rightarrow] \langle \text{es}', s' \rangle; \tau \text{moves0 } P \ (hp \ s) \ \text{es} \rrbracket \Longrightarrow hp \ s' = hp \ s$

**apply**(*induct rule: red-reds.inducts*)

**apply**(*auto*)

**apply**(*fastforce simp add: map-eq-append-conv  $\tau$ external'-def  $\tau$ external-def dest:  $\tau$ external'-red-external-heap-unch*)  
**done**

**lemma**  *$\tau$ Move0-iff*:

$\tau \text{Move0 } P \ h \ \text{ees} \longleftrightarrow (\text{let } (e, -) = \text{ees in } \tau \text{move0 } P \ h \ e \vee \text{final } e)$

**by**(*cases ees*)(*simp*)

**lemma** *no-call-simps* [*simp*]:

*no-call*  $P \ h \ (\text{new } C) = \text{True}$

*no-call*  $P \ h \ (\text{newA } T[e]) = \text{no-call } P \ h \ e$

*no-call*  $P \ h \ (\text{Cast } T \ e) = \text{no-call } P \ h \ e$

*no-call*  $P \ h \ (e \ \text{instanceof } T) = \text{no-call } P \ h \ e$

*no-call*  $P \ h \ (\text{Val } v) = \text{True}$

*no-call*  $P \ h \ (\text{Var } V) = \text{True}$

*no-call*  $P \ h \ (V := e) = \text{no-call } P \ h \ e$

*no-call*  $P \ h \ (e \ \ll \text{bop} \ e') = (\text{if is-val } e \ \text{then } \text{no-call } P \ h \ e' \ \text{else } \text{no-call } P \ h \ e)$

*no-call*  $P \ h \ (a[i]) = (\text{if is-val } a \ \text{then } \text{no-call } P \ h \ i \ \text{else } \text{no-call } P \ h \ a)$

*no-call*  $P \ h \ (AAss \ a \ i \ e) = (\text{if is-val } a \ \text{then } (\text{if is-val } i \ \text{then } \text{no-call } P \ h \ e \ \text{else } \text{no-call } P \ h \ i) \ \text{else } \text{no-call } P \ h \ a)$

*no-call*  $P \ h \ (a \cdot \text{length}) = \text{no-call } P \ h \ a$

*no-call*  $P \ h \ (e \cdot F\{D\}) = \text{no-call } P \ h \ e$

*no-call*  $P \ h \ (FAss \ e \ F \ D \ e') = (\text{if is-val } e \ \text{then } \text{no-call } P \ h \ e' \ \text{else } \text{no-call } P \ h \ e)$

*no-call*  $P \ h \ (e \cdot \text{compareAndSwap}(D \cdot F, e', e'')) = (\text{if is-val } e \ \text{then } (\text{if is-val } e' \ \text{then } \text{no-call } P \ h \ e'' \ \text{else } \text{no-call } P \ h \ e') \ \text{else } \text{no-call } P \ h \ e)$

*no-call*  $P \ h \ (e \cdot M(\text{es})) = (\text{if is-val } e \ \text{then } (\text{if is-vals } \text{es} \wedge \text{is-addr } e \ \text{then } \text{synthesized-call } P \ h \ (\text{THE } a. e = \text{addr } a, M, \text{THE } \text{vs. } \text{es} = \text{map } \text{Val } \text{vs}) \ \text{else } \text{no-calls } P \ h \ \text{es}) \ \text{else } \text{no-call } P \ h \ e)$

*no-call*  $P \ h \ (\{V:T=v_0; e\}) = \text{no-call } P \ h \ e$

*no-call*  $P \ h \ (\text{sync}_{V'}(e) \ e') = \text{no-call } P \ h \ e$

*no-call*  $P \ h \ (\text{insync}_{V'}(ad) \ e) = \text{no-call } P \ h \ e$

*no-call*  $P \ h \ (e;;e') = \text{no-call } P \ h \ e$

*no-call*  $P \ h \ (\text{if } (e) \ e1 \ \text{else } e2) = \text{no-call } P \ h \ e$

*no-call*  $P \ h \ (\text{while}(e) \ e') = \text{True}$

*no-call*  $P \ h \ (\text{throw } e) = \text{no-call } P \ h \ e$

*no-call*  $P \ h \ (\text{try } e \ \text{catch}(C \ V) \ e') = \text{no-call } P \ h \ e$



**by**(*auto simp add: no-call-def no-calls-def*)

**lemma** *no-calls-simps* [*simp*]:

*no-calls P h [] = True*

*no-calls P h (e # es) = (if is-val e then no-calls P h es else no-call P h e)*

**by**(*simp-all add: no-call-def no-calls-def*)

**lemma** *no-calls-map-Val* [*simp*]:

*no-calls P h (map Val vs)*

**by**(*induct vs*) *simp-all*

**lemma** *assumes nfin*:  $\neg \text{final } e'$

**shows** *inline-call- $\tau$ move0-inv*:  $\text{call } e = \lfloor aMvs \rfloor \implies \tau \text{move0 } P \ h \ (\text{inline-call } e' \ e) = \tau \text{move0 } P \ h \ e'$   
**and** *inline-calls- $\tau$ moves0-inv*:  $\text{calls } es = \lfloor aMvs \rfloor \implies \tau \text{moves0 } P \ h \ (\text{inline-calls } e' \ es) = \tau \text{move0 } P \ h \ e'$

**apply**(*induct e and es rule:  $\tau \text{move0.induct } \tau \text{moves0.induct}$* )

**apply**(*insert nfin*)

**apply** *simp-all*

**apply** *auto*

**done**

**lemma**  *$\tau \text{red0-iff}$*  [*iff*]:

$\tau \text{red0 } \text{extTA } P \ t \ h \ (e, \text{xs}) \ (e', \text{xs}') = (\text{extTA}, P, t \vdash \langle e, (h, \text{xs}) \rangle \rightarrow \langle e', (h, \text{xs}') \rangle \wedge \tau \text{move0 } P \ h \ e \wedge \text{no-call } P \ h \ e)$

**by**(*simp add:  $\tau \text{red0-def}$* )

**lemma**  *$\tau \text{reds0-iff}$*  [*iff*]:

$\tau \text{reds0 } \text{extTA } P \ t \ h \ (es, \text{xs}) \ (es', \text{xs}') =$

$(\text{extTA}, P, t \vdash \langle es, (h, \text{xs}) \rangle \rightarrow \langle es', (h, \text{xs}') \rangle \wedge \tau \text{moves0 } P \ h \ es \wedge \text{no-calls } P \ h \ es)$

**by**(*simp add:  $\tau \text{reds0-def}$* )

**lemma**  *$\tau \text{red0t-1step}$* :

$\llbracket \text{extTA}, P, t \vdash \langle e, (h, \text{xs}) \rangle \rightarrow \langle e', (h, \text{xs}') \rangle; \tau \text{move0 } P \ h \ e; \text{no-call } P \ h \ e \rrbracket$

$\implies \tau \text{red0t } \text{extTA } P \ t \ h \ (e, \text{xs}) \ (e', \text{xs}')$

**by**(*blast intro: tranclp.r-into-trancl*)

**lemma**  *$\tau \text{red0t-2step}$* :

$\llbracket \text{extTA}, P, t \vdash \langle e, (h, \text{xs}) \rangle \rightarrow \langle e', (h, \text{xs}') \rangle; \tau \text{move0 } P \ h \ e; \text{no-call } P \ h \ e; \text{extTA}, P, t \vdash \langle e', (h, \text{xs}') \rangle \rightarrow \langle e'', (h, \text{xs}'') \rangle; \tau \text{move0 } P \ h \ e'; \text{no-call } P \ h \ e' \rrbracket$

$\implies \tau \text{red0t } \text{extTA } P \ t \ h \ (e, \text{xs}) \ (e'', \text{xs}'')$

**by**(*blast intro: tranclp.trancl-into-trancl[OF  $\tau \text{red0t-1step}$ ]*)

**lemma**  *$\tau \text{red1t-3step}$* :

$\llbracket \text{extTA}, P, t \vdash \langle e, (h, \text{xs}) \rangle \rightarrow \langle e', (h, \text{xs}') \rangle; \tau \text{move0 } P \ h \ e; \text{no-call } P \ h \ e; \text{extTA}, P, t \vdash \langle e', (h, \text{xs}') \rangle \rightarrow \langle e'', (h, \text{xs}'') \rangle; \tau \text{move0 } P \ h \ e'; \text{no-call } P \ h \ e'; \text{extTA}, P, t \vdash \langle e'', (h, \text{xs}'') \rangle \rightarrow \langle e''', (h, \text{xs}''') \rangle; \tau \text{move0 } P \ h \ e''; \text{no-call } P \ h \ e'' \rrbracket$

$\implies \tau \text{red0t } \text{extTA } P \ t \ h \ (e, \text{xs}) \ (e''', \text{xs}''')$

**by**(*blast intro: tranclp.trancl-into-trancl[OF  $\tau \text{red0t-2step}$ ]*)

**lemma**  *$\tau \text{reds0t-1step}$* :

$\llbracket \text{extTA}, P, t \vdash \langle es, (h, \text{xs}) \rangle \rightarrow \langle es', (h, \text{xs}') \rangle; \tau \text{moves0 } P \ h \ es; \text{no-calls } P \ h \ es \rrbracket$

$\implies \tau \text{reds0t } \text{extTA } P \ t \ h \ (es, \text{xs}) \ (es', \text{xs}')$

**by**(*blast intro: tranclp.r-into-trancl*)

**lemma**  $\tau\text{reds0t-2step}$ :

$\llbracket \text{extTA}, P, t \vdash \langle es, (h, xs) \rangle [-\varepsilon \rightarrow] \langle es', (h, xs') \rangle; \tau\text{moves0 } P \text{ h } es; \text{no-calls } P \text{ h } es;$   
 $\text{extTA}, P, t \vdash \langle es', (h, xs') \rangle [-\varepsilon \rightarrow] \langle es'', (h, xs'') \rangle; \tau\text{moves0 } P \text{ h } es'; \text{no-calls } P \text{ h } es' \rrbracket$   
 $\implies \tau\text{reds0t extTA } P \text{ t h } (es, xs) (es'', xs'')$

**by**(blast intro: tranclp.trancl-into-trancl[OF  $\tau\text{reds0t-1step}$ ])

**lemma**  $\tau\text{reds0t-3step}$ :

$\llbracket \text{extTA}, P, t \vdash \langle es, (h, xs) \rangle [-\varepsilon \rightarrow] \langle es', (h, xs') \rangle; \tau\text{moves0 } P \text{ h } es; \text{no-calls } P \text{ h } es;$   
 $\text{extTA}, P, t \vdash \langle es', (h, xs') \rangle [-\varepsilon \rightarrow] \langle es'', (h, xs'') \rangle; \tau\text{moves0 } P \text{ h } es'; \text{no-calls } P \text{ h } es';$   
 $\text{extTA}, P, t \vdash \langle es'', (h, xs'') \rangle [-\varepsilon \rightarrow] \langle es''', (h, xs''') \rangle; \tau\text{moves0 } P \text{ h } es''; \text{no-calls } P \text{ h } es'' \rrbracket$   
 $\implies \tau\text{reds0t extTA } P \text{ t h } (es, xs) (es''', xs''')$

**by**(blast intro: tranclp.trancl-into-trancl[OF  $\tau\text{reds0t-2step}$ ])

**lemma**  $\tau\text{red0r-1step}$ :

$\llbracket \text{extTA}, P, t \vdash \langle e, (h, xs) \rangle -\varepsilon \rightarrow \langle e', (h, xs') \rangle; \tau\text{move0 } P \text{ h } e; \text{no-call } P \text{ h } e \rrbracket$   
 $\implies \tau\text{red0r extTA } P \text{ t h } (e, xs) (e', xs')$

**by**(blast intro: r-into-rtranclp)

**lemma**  $\tau\text{red0r-2step}$ :

$\llbracket \text{extTA}, P, t \vdash \langle e, (h, xs) \rangle -\varepsilon \rightarrow \langle e', (h, xs') \rangle; \tau\text{move0 } P \text{ h } e; \text{no-call } P \text{ h } e;$   
 $\text{extTA}, P, t \vdash \langle e', (h, xs') \rangle -\varepsilon \rightarrow \langle e'', (h, xs'') \rangle; \tau\text{move0 } P \text{ h } e'; \text{no-call } P \text{ h } e' \rrbracket$   
 $\implies \tau\text{red0r extTA } P \text{ t h } (e, xs) (e'', xs'')$

**by**(blast intro: rtranclp.rtrancl-into-rtrancl[OF  $\tau\text{red0r-1step}$ ])

**lemma**  $\tau\text{red0r-3step}$ :

$\llbracket \text{extTA}, P, t \vdash \langle e, (h, xs) \rangle -\varepsilon \rightarrow \langle e', (h, xs') \rangle; \tau\text{move0 } P \text{ h } e; \text{no-call } P \text{ h } e;$   
 $\text{extTA}, P, t \vdash \langle e', (h, xs') \rangle -\varepsilon \rightarrow \langle e'', (h, xs'') \rangle; \tau\text{move0 } P \text{ h } e'; \text{no-call } P \text{ h } e';$   
 $\text{extTA}, P, t \vdash \langle e'', (h, xs'') \rangle -\varepsilon \rightarrow \langle e''', (h, xs''') \rangle; \tau\text{move0 } P \text{ h } e''; \text{no-call } P \text{ h } e'' \rrbracket$   
 $\implies \tau\text{red0r extTA } P \text{ t h } (e, xs) (e''', xs''')$

**by**(blast intro: rtranclp.rtrancl-into-rtrancl[OF  $\tau\text{red0r-2step}$ ])

**lemma**  $\tau\text{reds0r-1step}$ :

$\llbracket \text{extTA}, P, t \vdash \langle es, (h, xs) \rangle [-\varepsilon \rightarrow] \langle es', (h, xs') \rangle; \tau\text{moves0 } P \text{ h } es; \text{no-calls } P \text{ h } es \rrbracket$   
 $\implies \tau\text{reds0r extTA } P \text{ t h } (es, xs) (es', xs')$

**by**(blast intro: r-into-rtranclp)

**lemma**  $\tau\text{reds0r-2step}$ :

$\llbracket \text{extTA}, P, t \vdash \langle es, (h, xs) \rangle [-\varepsilon \rightarrow] \langle es', (h, xs') \rangle; \tau\text{moves0 } P \text{ h } es; \text{no-calls } P \text{ h } es;$   
 $\text{extTA}, P, t \vdash \langle es', (h, xs') \rangle [-\varepsilon \rightarrow] \langle es'', (h, xs'') \rangle; \tau\text{moves0 } P \text{ h } es'; \text{no-calls } P \text{ h } es' \rrbracket$   
 $\implies \tau\text{reds0r extTA } P \text{ t h } (es, xs) (es'', xs'')$

**by**(blast intro: rtranclp.rtrancl-into-rtrancl[OF  $\tau\text{reds0r-1step}$ ])

**lemma**  $\tau\text{reds0r-3step}$ :

$\llbracket \text{extTA}, P, t \vdash \langle es, (h, xs) \rangle [-\varepsilon \rightarrow] \langle es', (h, xs') \rangle; \tau\text{moves0 } P \text{ h } es; \text{no-calls } P \text{ h } es;$   
 $\text{extTA}, P, t \vdash \langle es', (h, xs') \rangle [-\varepsilon \rightarrow] \langle es'', (h, xs'') \rangle; \tau\text{moves0 } P \text{ h } es'; \text{no-calls } P \text{ h } es';$   
 $\text{extTA}, P, t \vdash \langle es'', (h, xs'') \rangle [-\varepsilon \rightarrow] \langle es''', (h, xs''') \rangle; \tau\text{moves0 } P \text{ h } es''; \text{no-calls } P \text{ h } es'' \rrbracket$   
 $\implies \tau\text{reds0r extTA } P \text{ t h } (es, xs) (es''', xs''')$

**by**(blast intro: rtranclp.rtrancl-into-rtrancl[OF  $\tau\text{reds0r-2step}$ ])

**lemma**  $\tau\text{red0t-inj-}\tau\text{reds0t}$ :

$\tau\text{red0t extTA } P \text{ t h } (e, xs) (e', xs')$   
 $\implies \tau\text{reds0t extTA } P \text{ t h } (e \# es, xs) (e' \# es, xs')$

**by**(induct rule: tranclp-induct2)(auto intro: tranclp.trancl-into-trancl ListRed1)

**lemma**  $\tau\text{reds0t-cons-}\tau\text{reds0t}$ :

$$\tau\text{reds0t extTA } P \text{ t h } (es, xs) (es', xs') \\ \implies \tau\text{reds0t extTA } P \text{ t h } (Val \ v \ \# \ es, xs) (Val \ v \ \# \ es', xs')$$

**by**(*induct rule*:  $\text{trancpl-induct2}$ )(*auto intro*:  $\text{trancpl.trancpl-into-trancpl ListRed2}$ )

**lemma**  $\tau\text{red0r-inj-}\tau\text{reds0r}$ :

$$\tau\text{red0r extTA } P \text{ t h } (e, xs) (e', xs') \\ \implies \tau\text{reds0r extTA } P \text{ t h } (e \ \# \ es, xs) (e' \ \# \ es, xs')$$

**by**(*induct rule*:  $\text{rtrancpl-induct2}$ )(*auto intro*:  $\text{rtrancpl.rtrancpl-into-rtrancpl ListRed1}$ )

**lemma**  $\tau\text{reds0r-cons-}\tau\text{reds0r}$ :

$$\tau\text{reds0r extTA } P \text{ t h } (es, xs) (es', xs') \\ \implies \tau\text{reds0r extTA } P \text{ t h } (Val \ v \ \# \ es, xs) (Val \ v \ \# \ es', xs')$$

**by**(*induct rule*:  $\text{rtrancpl-induct2}$ )(*auto intro*:  $\text{rtrancpl.rtrancpl-into-rtrancpl ListRed2}$ )

**lemma**  $\text{NewArray-}\tau\text{red0t-xt}$ :

$$\tau\text{red0t extTA } P \text{ t h } (e, xs) (e', xs') \\ \implies \tau\text{red0t extTA } P \text{ t h } (\text{newA } T[e], xs) (\text{newA } T[e'], xs')$$

**by**(*induct rule*:  $\text{trancpl-induct2}$ )(*auto intro*:  $\text{trancpl.trancpl-into-trancpl NewArrayRed}$ )

**lemma**  $\text{Cast-}\tau\text{red0t-xt}$ :

$$\tau\text{red0t extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA } P \text{ t h } (\text{Cast } T \ e, xs) (\text{Cast } T \ e', xs')$$

**by**(*induct rule*:  $\text{trancpl-induct2}$ )(*auto intro*:  $\text{trancpl.trancpl-into-trancpl CastRed}$ )

**lemma**  $\text{InstanceOf-}\tau\text{red0t-xt}$ :

$$\tau\text{red0t extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA } P \text{ t h } (e \text{ instanceof } T, xs) (e' \text{ instanceof } T, xs')$$

**by**(*induct rule*:  $\text{trancpl-induct2}$ )(*auto intro*:  $\text{trancpl.trancpl-into-trancpl InstanceOfRed}$ )

**lemma**  $\text{BinOp-}\tau\text{red0t-xt1}$ :

$$\tau\text{red0t extTA } P \text{ t h } (e1, xs) (e1', xs') \implies \tau\text{red0t extTA } P \text{ t h } (e1 \ \llbracket \text{bop} \rrbracket \ e2, xs) (e1' \ \llbracket \text{bop} \rrbracket \ e2, xs')$$

**by**(*induct rule*:  $\text{trancpl-induct2}$ )(*auto intro*:  $\text{trancpl.trancpl-into-trancpl BinOpRed1}$ )

**lemma**  $\text{BinOp-}\tau\text{red0t-xt2}$ :

$$\tau\text{red0t extTA } P \text{ t h } (e2, xs) (e2', xs') \implies \tau\text{red0t extTA } P \text{ t h } (Val \ v \ \llbracket \text{bop} \rrbracket \ e2, xs) (Val \ v \ \llbracket \text{bop} \rrbracket \ e2', xs')$$

**by**(*induct rule*:  $\text{trancpl-induct2}$ )(*auto intro*:  $\text{trancpl.trancpl-into-trancpl BinOpRed2}$ )

**lemma**  $\text{LAss-}\tau\text{red0t}$ :

$$\tau\text{red0t extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA } P \text{ t h } (V := e, xs) (V := e', xs')$$

**by**(*induct rule*:  $\text{trancpl-induct2}$ )(*auto intro*:  $\text{trancpl.trancpl-into-trancpl LAssRed}$ )

**lemma**  $\text{AAcc-}\tau\text{red0t-xt1}$ :

$$\tau\text{red0t extTA } P \text{ t h } (a, xs) (a', xs') \implies \tau\text{red0t extTA } P \text{ t h } (a[i], xs) (a'[i], xs')$$

**by**(*induct rule*:  $\text{trancpl-induct2}$ )(*auto intro*:  $\text{trancpl.trancpl-into-trancpl AAccRed1}$ )

**lemma**  $\text{AAcc-}\tau\text{red0t-xt2}$ :

$$\tau\text{red0t extTA } P \text{ t h } (i, xs) (i', xs') \implies \tau\text{red0t extTA } P \text{ t h } (Val \ a[i], xs) (Val \ a[i'], xs')$$

**by**(*induct rule*:  $\text{trancpl-induct2}$ )(*auto intro*:  $\text{trancpl.trancpl-into-trancpl AAccRed2}$ )

**lemma**  $\text{AAss-}\tau\text{red0t-xt1}$ :

$$\tau\text{red0t extTA } P \text{ t h } (a, xs) (a', xs') \implies \tau\text{red0t extTA } P \text{ t h } (a[i] := e, xs) (a'[i] := e, xs')$$

**by**(*induct rule*:  $\text{trancpl-induct2}$ )(*auto intro*:  $\text{trancpl.trancpl-into-trancpl AAssRed1}$ )

**lemma**  $\text{AAss-}\tau\text{red0t-xt2}$ :

$\tau\text{red0t extTA } P \text{ t h } (i, xs) (i', xs') \implies \tau\text{red0t extTA } P \text{ t h } (Val\ a[i] := e, xs) (Val\ a[i'] := e, xs')$   
**by**(*induct rule*: *trancpl-induct2*)(*auto intro*: *trancpl.trancpl-into-trancpl AAssRed2*)

**lemma** *AAss- $\tau\text{red0t-xt3}$* :  
 $\tau\text{red0t extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA } P \text{ t h } (Val\ a[Val\ i] := e, xs) (Val\ a[Val\ i'] := e', xs')$   
**by**(*induct rule*: *trancpl-induct2*)(*auto intro*: *trancpl.trancpl-into-trancpl AAssRed3*)

**lemma** *ALength- $\tau\text{red0t-xt}$* :  
 $\tau\text{red0t extTA } P \text{ t h } (a, xs) (a', xs') \implies \tau\text{red0t extTA } P \text{ t h } (a.\text{length}, xs) (a'.\text{length}, xs')$   
**by**(*induct rule*: *trancpl-induct2*)(*auto intro*: *trancpl.trancpl-into-trancpl ALengthRed*)

**lemma** *FAcc- $\tau\text{red0t-xt}$* :  
 $\tau\text{red0t extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA } P \text{ t h } (e.F\{D\}, xs) (e'.F\{D\}, xs')$   
**by**(*induct rule*: *trancpl-induct2*)(*auto intro*: *trancpl.trancpl-into-trancpl FAccRed*)

**lemma** *FAss- $\tau\text{red0t-xt1}$* :  
 $\tau\text{red0t extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA } P \text{ t h } (e.F\{D\} := e2, xs) (e'.F\{D\} := e2, xs')$   
**by**(*induct rule*: *trancpl-induct2*)(*auto intro*: *trancpl.trancpl-into-trancpl FAssRed1*)

**lemma** *FAss- $\tau\text{red0t-xt2}$* :  
 $\tau\text{red0t extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA } P \text{ t h } (Val\ v.F\{D\} := e, xs) (Val\ v.F\{D\} := e', xs')$   
**by**(*induct rule*: *trancpl-induct2*)(*auto intro*: *trancpl.trancpl-into-trancpl FAssRed2*)

**lemma** *CAS- $\tau\text{red0t-xt1}$* :  
 $\tau\text{red0t extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA } P \text{ t h } (e.\text{compareAndSwap}(D.F, e2, e3), xs) (e'.\text{compareAndSwap}(D.F, e2, e3), xs')$   
**by**(*induct rule*: *trancpl-induct2*)(*auto intro*: *trancpl.trancpl-into-trancpl CASRed1*)

**lemma** *CAS- $\tau\text{red0t-xt2}$* :  
 $\tau\text{red0t extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA } P \text{ t h } (Val\ v.\text{compareAndSwap}(D.F, e, e3), xs) (Val\ v.\text{compareAndSwap}(D.F, e', e3), xs')$   
**by**(*induct rule*: *trancpl-induct2*)(*auto intro*: *trancpl.trancpl-into-trancpl CASRed2*)

**lemma** *CAS- $\tau\text{red0t-xt3}$* :  
 $\tau\text{red0t extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA } P \text{ t h } (Val\ v.\text{compareAndSwap}(D.F, Val\ v', e), xs) (Val\ v.\text{compareAndSwap}(D.F, Val\ v', e'), xs')$   
**by**(*induct rule*: *trancpl-induct2*)(*auto intro*: *trancpl.trancpl-into-trancpl CASRed3*)

**lemma** *Call- $\tau\text{red0t-obj}$* :  
 $\tau\text{red0t extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0t extTA } P \text{ t h } (e.M(ps), xs) (e'.M(ps), xs')$   
**by**(*induct rule*: *trancpl-induct2*)(*auto intro*: *trancpl.trancpl-into-trancpl CallObj*)

**lemma** *Call- $\tau\text{red0t-param}$* :  
 $\tau\text{red0t extTA } P \text{ t h } (es, xs) (es', xs') \implies \tau\text{red0t extTA } P \text{ t h } (Val\ v.M(es), xs) (Val\ v.M(es'), xs')$   
**by**(*induct rule*: *trancpl-induct2*)(*fastforce intro*: *trancpl.trancpl-into-trancpl CallParams*)+

**lemma** *Block- $\tau\text{red0t-xt}$* :  
 $\tau\text{red0t extTA } P \text{ t h } (e, xs(V := vo)) (e', xs') \implies \tau\text{red0t extTA } P \text{ t h } (\{V:T=vo; e\}, xs) (\{V:T=xs' V; e'\}, xs'(V := xs\ V))$   
**proof**(*induct rule*: *trancpl-induct2*)  
**case base thus** ?*case* **by**(*auto intro*: *BlockRed simp del: fun-upd-apply*)  
**next**

**case** (*step*  $e' \ xs' \ e'' \ xs''$ )  
**from**  $\langle \tau red0 \ extTA \ P \ t \ h \ (e', \ xs') \ (e'', \ xs'') \rangle$   
**have**  $extTA, P, t \vdash \langle e', (h, \ xs') \rangle -\varepsilon \rightarrow \langle e'', (h, \ xs'') \rangle \ \tau move0 \ P \ h \ e' \ no-call \ P \ h \ e' \ \text{by} \ auto$   
**hence**  $extTA, P, t \vdash \langle e', (h, \ xs'(V := xs \ V, \ V := xs' \ V)) \rangle -\varepsilon \rightarrow \langle e'', (h, \ xs'') \rangle \ \text{by} \ simp$   
**from**  $BlockRed[OF \ this, \ of \ T] \ \langle \tau move0 \ P \ h \ e' \ \rangle \ \langle no-call \ P \ h \ e' \ \rangle$   
**have**  $\tau red0 \ extTA \ P \ t \ h \ (\{V:T=xs' \ V; \ e'\}, \ xs'(V := xs \ V)) \ (\{V:T=xs'' \ V; \ e''\}, \ xs''(V := xs \ V))$   
**by** (*auto*)  
**with**  $\langle \tau red0t \ extTA \ P \ t \ h \ (\{V:T=vo; \ e\}, \ xs) \ (\{V:T=xs' \ V; \ e'\}, \ xs'(V := xs \ V)) \rangle \ \text{show} \ ?case \ ..$   
**qed**

**lemma** *Sync- $\tau red0t$ -xt:*

$\tau red0t \ extTA \ P \ t \ h \ (e, \ xs) \ (e', \ xs') \implies \tau red0t \ extTA \ P \ t \ h \ (sync_V \ (e) \ e2, \ xs) \ (sync_V \ (e') \ e2, \ xs')$   
**by** (*induct rule: tranclp-induct2*) (*auto intro: tranclp.trancl-into-trancl SynchronizedRed1*)

**lemma** *InSync- $\tau red0t$ -xt:*

$\tau red0t \ extTA \ P \ t \ h \ (e, \ xs) \ (e', \ xs') \implies \tau red0t \ extTA \ P \ t \ h \ (insync_V \ (a) \ e, \ xs) \ (insync_V \ (a) \ e', \ xs')$   
**by** (*induct rule: tranclp-induct2*) (*auto intro: tranclp.trancl-into-trancl SynchronizedRed2*)

**lemma** *Seq- $\tau red0t$ -xt:*

$\tau red0t \ extTA \ P \ t \ h \ (e, \ xs) \ (e', \ xs') \implies \tau red0t \ extTA \ P \ t \ h \ (e;;e2, \ xs) \ (e';;e2, \ xs')$   
**by** (*induct rule: tranclp-induct2*) (*auto intro: tranclp.trancl-into-trancl SeqRed*)

**lemma** *Cond- $\tau red0t$ -xt:*

$\tau red0t \ extTA \ P \ t \ h \ (e, \ xs) \ (e', \ xs') \implies \tau red0t \ extTA \ P \ t \ h \ (if \ (e) \ e1 \ else \ e2, \ xs) \ (if \ (e') \ e1 \ else \ e2, \ xs')$   
**by** (*induct rule: tranclp-induct2*) (*auto intro: tranclp.trancl-into-trancl CondRed*)

**lemma** *Throw- $\tau red0t$ -xt:*

$\tau red0t \ extTA \ P \ t \ h \ (e, \ xs) \ (e', \ xs') \implies \tau red0t \ extTA \ P \ t \ h \ (throw \ e, \ xs) \ (throw \ e', \ xs')$   
**by** (*induct rule: tranclp-induct2*) (*auto intro: tranclp.trancl-into-trancl ThrowRed*)

**lemma** *Try- $\tau red0t$ -xt:*

$\tau red0t \ extTA \ P \ t \ h \ (e, \ xs) \ (e', \ xs') \implies \tau red0t \ extTA \ P \ t \ h \ (try \ e \ catch \ (C \ V) \ e2, \ xs) \ (try \ e' \ catch \ (C \ V) \ e2, \ xs')$   
**by** (*induct rule: tranclp-induct2*) (*auto intro: tranclp.trancl-into-trancl TryRed*)

**lemma** *NewArray- $\tau red0r$ -xt:*

$\tau red0r \ extTA \ P \ t \ h \ (e, \ xs) \ (e', \ xs') \implies \tau red0r \ extTA \ P \ t \ h \ (newA \ T[e], \ xs) \ (newA \ T[e'], \ xs')$   
**by** (*induct rule: rtranclp-induct2*) (*auto intro: rtranclp.rtrancl-into-rtrancl NewArrayRed*)

**lemma** *Cast- $\tau red0r$ -xt:*

$\tau red0r \ extTA \ P \ t \ h \ (e, \ xs) \ (e', \ xs') \implies \tau red0r \ extTA \ P \ t \ h \ (Cast \ T \ e, \ xs) \ (Cast \ T \ e', \ xs')$   
**by** (*induct rule: rtranclp-induct2*) (*auto intro: rtranclp.rtrancl-into-rtrancl CastRed*)

**lemma** *InstanceOf- $\tau red0r$ -xt:*

$\tau red0r \ extTA \ P \ t \ h \ (e, \ xs) \ (e', \ xs') \implies \tau red0r \ extTA \ P \ t \ h \ (e \ instanceof \ T, \ xs) \ (e' \ instanceof \ T, \ xs')$   
**by** (*induct rule: rtranclp-induct2*) (*auto intro: rtranclp.rtrancl-into-rtrancl InstanceOfRed*)

**lemma** *BinOp- $\tau red0r$ -xt1:*

$\tau red0r \ extTA \ P \ t \ h \ (e1, \ xs) \ (e1', \ xs') \implies \tau red0r \ extTA \ P \ t \ h \ (e1 \ \langle\!\langle bop \!\rangle\!\rangle \ e2, \ xs) \ (e1' \ \langle\!\langle bop \!\rangle\!\rangle \ e2, \ xs')$   
**by** (*induct rule: rtranclp-induct2*) (*auto intro: rtranclp.rtrancl-into-rtrancl BinOpRed1*)

**lemma** *BinOp- $\tau red0r$ -xt2:*

$\tau red0r \ extTA \ P \ t \ h \ (e2, \ xs) \ (e2', \ xs') \implies \tau red0r \ extTA \ P \ t \ h \ (Val \ v \ \langle\!\langle bop \!\rangle\!\rangle \ e2, \ xs) \ (Val \ v \ \langle\!\langle bop \!\rangle\!\rangle \ e2', \ xs')$

$e2', xs')$

**by**(*induct rule*: *rtranclp-induct2*)(*auto intro*: *rtranclp.rtrancl-into-rtrancl BinOpRed2*)

**lemma** *LAss- $\tau$ red0r*:

$\tau\text{red0r extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0r extTA } P \text{ t h } (V := e, xs) (V := e', xs')$

**by**(*induct rule*: *rtranclp-induct2*)(*auto intro*: *rtranclp.rtrancl-into-rtrancl LAssRed*)

**lemma** *AAcc- $\tau$ red0r-xt1*:

$\tau\text{red0r extTA } P \text{ t h } (a, xs) (a', xs') \implies \tau\text{red0r extTA } P \text{ t h } (a[i], xs) (a'[i], xs')$

**by**(*induct rule*: *rtranclp-induct2*)(*auto intro*: *rtranclp.rtrancl-into-rtrancl AAccRed1*)

**lemma** *AAcc- $\tau$ red0r-xt2*:

$\tau\text{red0r extTA } P \text{ t h } (i, xs) (i', xs') \implies \tau\text{red0r extTA } P \text{ t h } (Val a[i], xs) (Val a[i'], xs')$

**by**(*induct rule*: *rtranclp-induct2*)(*auto intro*: *rtranclp.rtrancl-into-rtrancl AAccRed2*)

**lemma** *AAss- $\tau$ red0r-xt1*:

$\tau\text{red0r extTA } P \text{ t h } (a, xs) (a', xs') \implies \tau\text{red0r extTA } P \text{ t h } (a[i] := e, xs) (a'[i] := e, xs')$

**by**(*induct rule*: *rtranclp-induct2*)(*auto intro*: *rtranclp.rtrancl-into-rtrancl AAssRed1*)

**lemma** *AAss- $\tau$ red0r-xt2*:

$\tau\text{red0r extTA } P \text{ t h } (i, xs) (i', xs') \implies \tau\text{red0r extTA } P \text{ t h } (Val a[i] := e, xs) (Val a[i'] := e, xs')$

**by**(*induct rule*: *rtranclp-induct2*)(*auto intro*: *rtranclp.rtrancl-into-rtrancl AAssRed2*)

**lemma** *AAss- $\tau$ red0r-xt3*:

$\tau\text{red0r extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0r extTA } P \text{ t h } (Val a[Val i] := e, xs) (Val a[Val i'] := e', xs')$

**by**(*induct rule*: *rtranclp-induct2*)(*auto intro*: *rtranclp.rtrancl-into-rtrancl AAssRed3*)

**lemma** *ALength- $\tau$ red0r-xt*:

$\tau\text{red0r extTA } P \text{ t h } (a, xs) (a', xs') \implies \tau\text{red0r extTA } P \text{ t h } (a.\text{length}, xs) (a'.\text{length}, xs')$

**by**(*induct rule*: *rtranclp-induct2*)(*auto intro*: *rtranclp.rtrancl-into-rtrancl ALengthRed*)

**lemma** *FAcc- $\tau$ red0r-xt*:

$\tau\text{red0r extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0r extTA } P \text{ t h } (e.F\{D\}, xs) (e'.F\{D\}, xs')$

**by**(*induct rule*: *rtranclp-induct2*)(*auto intro*: *rtranclp.rtrancl-into-rtrancl FAccRed*)

**lemma** *FAss- $\tau$ red0r-xt1*:

$\tau\text{red0r extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0r extTA } P \text{ t h } (e.F\{D\} := e2, xs) (e'.F\{D\} := e2, xs')$

**by**(*induct rule*: *rtranclp-induct2*)(*auto intro*: *rtranclp.rtrancl-into-rtrancl FAssRed1*)

**lemma** *FAss- $\tau$ red0r-xt2*:

$\tau\text{red0r extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0r extTA } P \text{ t h } (Val v.F\{D\} := e, xs) (Val v.F\{D\} := e', xs')$

**by**(*induct rule*: *rtranclp-induct2*)(*auto intro*: *rtranclp.rtrancl-into-rtrancl FAssRed2*)

**lemma** *CAS- $\tau$ red0r-xt1*:

$\tau\text{red0r extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0r extTA } P \text{ t h } (e.\text{compareAndSwap}(D.F, e2, e3), xs) (e'.\text{compareAndSwap}(D.F, e2, e3), xs')$

**by**(*induct rule*: *rtranclp-induct2*)(*auto intro*: *rtranclp.rtrancl-into-rtrancl CASRed1*)

**lemma** *CAS- $\tau$ red0r-xt2*:

$\tau\text{red0r extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0r extTA } P \text{ t h } (Val v.\text{compareAndSwap}(D.F, e, e3), xs) (Val v.\text{compareAndSwap}(D.F, e', e3), xs')$

**by**(*induct rule*: *rtranclp-induct2*)(*auto intro*: *rtranclp.rtrancl-into-rtrancl CASRed2*)

**lemma** *CAS- $\tau$ red0r-xt3*:

$\tau\text{red0r extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0r extTA } P \text{ t h } (Val\ v \cdot compareAndSwap(D \cdot F, Val\ v', e), xs) (Val\ v \cdot compareAndSwap(D \cdot F, Val\ v', e'), xs')$

**by**(*induct rule*: rtrancpl-induct2)(*auto intro*: rtrancpl.rtranc1-into-rtranc1 CASRed3)

**lemma** *Call- $\tau$ red0r-obj*:

$\tau\text{red0r extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0r extTA } P \text{ t h } (e \cdot M(ps), xs) (e' \cdot M(ps), xs')$

**by**(*induct rule*: rtrancpl-induct2)(*auto intro*: rtrancpl.rtranc1-into-rtranc1 CallObj)

**lemma** *Call- $\tau$ red0r-param*:

$\tau\text{red0r extTA } P \text{ t h } (es, xs) (es', xs') \implies \tau\text{red0r extTA } P \text{ t h } (Val\ v \cdot M(es), xs) (Val\ v \cdot M(es'), xs')$

**by**(*induct rule*: rtrancpl-induct2)(*fastforce intro*: rtrancpl.rtranc1-into-rtranc1 CallParams)+

**lemma** *Block- $\tau$ red0r-xt*:

$\tau\text{red0r extTA } P \text{ t h } (e, xs(V := vo)) (e', xs') \implies \tau\text{red0r extTA } P \text{ t h } (\{V:T=vo; e\}, xs) (\{V:T=xs' V; e'\}, xs'(V := xs V))$

**proof**(*induct rule*: rtrancpl-induct2)

**case refl thus** ?case **by**(*simp del*: fun-upd-apply)(*auto simp add*: fun-upd-apply)

**next**

**case** (*step*  $e' xs' e'' xs''$ )

**from**  $\langle \tau\text{red0 extTA } P \text{ t h } (e', xs') (e'', xs'') \rangle$

**have**  $\text{extTA}, P, t \vdash \langle e', (h, xs') \rangle -\varepsilon \rightarrow \langle e'', (h, xs'') \rangle \tau\text{move0 } P \text{ h } e' \text{ no-call } P \text{ h } e' \text{ by auto}$

**hence**  $\text{extTA}, P, t \vdash \langle e', (h, xs'(V := xs V, V := xs' V)) \rangle -\varepsilon \rightarrow \langle e'', (h, xs'') \rangle \text{ by simp}$

**from** *BlockRed[OF this, of T]*  $\langle \tau\text{move0 } P \text{ h } e' \rangle \langle \text{no-call } P \text{ h } e' \rangle$

**have**  $\tau\text{red0 extTA } P \text{ t h } (\{V:T=xs' V; e'\}, xs'(V := xs V)) (\{V:T=xs'' V; e''\}, xs''(V := xs V))$

**by auto**

**with**  $\langle \tau\text{red0r extTA } P \text{ t h } (\{V:T=vo; e\}, xs) (\{V:T=xs' V; e'\}, xs'(V := xs V)) \rangle$  **show** ?case ..

**qed**

**lemma** *Sync- $\tau$ red0r-xt*:

$\tau\text{red0r extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0r extTA } P \text{ t h } (\text{sync}_V(e) e2, xs) (\text{sync}_V(e') e2, xs')$

**by**(*induct rule*: rtrancpl-induct2)(*auto intro*: rtrancpl.rtranc1-into-rtranc1 SynchronizedRed1)

**lemma** *InSync- $\tau$ red0r-xt*:

$\tau\text{red0r extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0r extTA } P \text{ t h } (\text{insync}_V(a) e, xs) (\text{insync}_V(a) e', xs')$

**by**(*induct rule*: rtrancpl-induct2)(*auto intro*: rtrancpl.rtranc1-into-rtranc1 SynchronizedRed2)

**lemma** *Seq- $\tau$ red0r-xt*:

$\tau\text{red0r extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0r extTA } P \text{ t h } (e;;e2, xs) (e';;e2, xs')$

**by**(*induct rule*: rtrancpl-induct2)(*auto intro*: rtrancpl.rtranc1-into-rtranc1 SeqRed)

**lemma** *Cond- $\tau$ red0r-xt*:

$\tau\text{red0r extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0r extTA } P \text{ t h } (\text{if } (e) e1 \text{ else } e2, xs) (\text{if } (e') e1 \text{ else } e2, xs')$

**by**(*induct rule*: rtrancpl-induct2)(*auto intro*: rtrancpl.rtranc1-into-rtranc1 CondRed)

**lemma** *Throw- $\tau$ red0r-xt*:

$\tau\text{red0r extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0r extTA } P \text{ t h } (\text{throw } e, xs) (\text{throw } e', xs')$

**by**(*induct rule*: rtrancpl-induct2)(*auto intro*: rtrancpl.rtranc1-into-rtranc1 ThrowRed)

**lemma** *Try- $\tau$ red0r-xt*:

$\tau\text{red0r extTA } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red0r extTA } P \text{ t h } (\text{try } e \text{ catch } (C\ V) e2, xs) (\text{try } e' \text{ catch } (C\ V) e2, xs')$

**by**(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl TryRed*)

**lemma**  $\tau\text{Red0-conv}$  [*iff*]:

$\tau\text{Red0 } P \ t \ h \ (e, es) \ (e', es') = (P, t \vdash 0 \ \langle e/es, h \rangle \ -\varepsilon \rightarrow \langle e'/es', h \rangle \wedge \tau\text{Move0 } P \ h \ (e, es))$

**by**(*simp add:  $\tau\text{Red0-def}$* )

**lemma**  $\tau\text{red0r-lcl-incr}$ :

$\tau\text{red0r extTA } P \ t \ h \ (e, xs) \ (e', xs') \implies \text{dom } xs \subseteq \text{dom } xs'$

**by**(*induct rule: rtranclp-induct2*)(*auto dest: red-lcl-incr del: subsetI*)

**lemma**  $\tau\text{red0t-lcl-incr}$ :

$\tau\text{red0t extTA } P \ t \ h \ (e, xs) \ (e', xs') \implies \text{dom } xs \subseteq \text{dom } xs'$

**by**(*rule  $\tau\text{red0r-lcl-incr}$* )(*rule tranclp-into-rtranclp*)

**lemma**  $\tau\text{red0r-dom-lcl}$ :

**assumes** *wwf: wwf-J-prog P*

**shows**  $\tau\text{red0r extTA } P \ t \ h \ (e, xs) \ (e', xs') \implies \text{dom } xs' \subseteq \text{dom } xs \cup \text{fv } e$

**apply**(*induct rule: converse-rtranclp-induct2*)

**apply** *blast*

**apply**(*clarisimp del: subsetI*)

**apply**(*frule red-dom-lcl*)

**apply**(*drule red-fv-subset[OF wwf]*)

**apply** *auto*

**done**

**lemma**  $\tau\text{red0t-dom-lcl}$ :

**assumes** *wwf: wwf-J-prog P*

**shows**  $\tau\text{red0t extTA } P \ t \ h \ (e, xs) \ (e', xs') \implies \text{dom } xs' \subseteq \text{dom } xs \cup \text{fv } e$

**by**(*rule  $\tau\text{red0r-dom-lcl}[OF wwf]$* )(*rule tranclp-into-rtranclp*)

**lemma**  $\tau\text{red0r-fv-subset}$ :

**assumes** *wwf: wwf-J-prog P*

**shows**  $\tau\text{red0r extTA } P \ t \ h \ (e, xs) \ (e', xs') \implies \text{fv } e' \subseteq \text{fv } e$

**by**(*induct rule: converse-rtranclp-induct2*)(*auto dest: red-fv-subset[OF wwf]*)

**lemma**  $\tau\text{red0t-fv-subset}$ :

**assumes** *wwf: wwf-J-prog P*

**shows**  $\tau\text{red0t extTA } P \ t \ h \ (e, xs) \ (e', xs') \implies \text{fv } e' \subseteq \text{fv } e$

**by**(*rule  $\tau\text{red0r-fv-subset}[OF wwf]$* )(*rule tranclp-into-rtranclp*)

**lemma** **fixes**  $e :: ('a, 'b, 'addr) \text{ exp}$  **and**  $es :: ('a, 'b, 'addr) \text{ exp list}$

**shows**  $\tau\text{move0-callD: call } e = \lfloor (a, M, vs) \rfloor \implies \tau\text{move0 } P \ h \ e \longleftrightarrow (\text{synthesized-call } P \ h \ (a, M, vs) \longrightarrow \tau\text{external}' P \ h \ a \ M)$

**and**  $\tau\text{moves0-callsD: calls } es = \lfloor (a, M, vs) \rfloor \implies \tau\text{moves0 } P \ h \ es \longleftrightarrow (\text{synthesized-call } P \ h \ (a, M, vs) \longrightarrow \tau\text{external}' P \ h \ a \ M)$

**apply**(*induct e and es rule: call.induct calls.induct*)

**apply**(*auto split: if-split-asm simp add: is-vals-conv*)

**apply**(*fastforce simp add: synthesized-call-def map-eq-append-conv  $\tau\text{external}'\text{-def}$   $\tau\text{external-def}$  *dest: sees-method-fun*)*

**done**

**lemma** **fixes**  $e :: ('a, 'b, 'addr) \text{ exp}$  **and**  $es :: ('a, 'b, 'addr) \text{ exp list}$

**shows**  $\tau\text{move0-not-call: } \llbracket \tau\text{move0 } P \ h \ e; \text{ call } e = \lfloor (a, M, vs) \rfloor; \text{ synthesized-call } P \ h \ (a, M, vs) \rrbracket \implies \tau\text{external}' P \ h \ a \ M$



**and**  $\tau\text{moves0-not-calls}$ :  $\llbracket \tau\text{moves0 } P \ h \ es; \text{calls } es = \lfloor (a, M, vs) \rfloor; \text{synthesized-call } P \ h \ (a, M, vs) \rrbracket$   
 $\implies \tau\text{external}' P \ h \ a \ M$

**apply**(*drule*  $\tau\text{move0-callD}$ [**where**  $P=P$  **and**  $h=h$ ], *simp*)  
**apply**(*drule*  $\tau\text{moves0-callsD}$ [**where**  $P=P$  **and**  $h=h$ ], *simp*)  
**done**

**lemma**  $\tau\text{red0-into-}\tau\text{Red0}$ :

**assumes**  $\text{red}$ :  $\tau\text{red0 } (\text{extTA2J0 } P) \ P \ t \ h \ (e, \text{Map.empty}) \ (e', xs')$   
**shows**  $\tau\text{Red0 } P \ t \ h \ (e, es) \ (e', es)$

**proof** –

**from**  $\text{red}$  **have**  $\text{red}$ :  $\text{extTA2J0 } P, P, t \vdash \langle e, (h, \text{Map.empty}) \rangle -\varepsilon \rightarrow \langle e', (h, xs') \rangle$   
**and**  $\tau\text{move0 } P \ h \ e$  **and**  $\text{no-call } P \ h \ e$  **by** *auto*  
**hence**  $P, t \vdash 0 \ \langle e/es, h \rangle -\varepsilon \rightarrow \langle e'/es, h \rangle$   
**by**–(*erule red0Red, auto simp add: no-call-def*)  
**thus** *?thesis* **using**  $\langle \tau\text{move0 } P \ h \ e \rangle$  **by**(*auto*)

**qed**

**lemma**  $\tau\text{red0r-into-}\tau\text{Red0r}$ :

**assumes**  $wf$ :  $wf\text{-J-prog } P$

**shows**

$\llbracket \tau\text{red0r } (\text{extTA2J0 } P) \ P \ t \ h \ (e, \text{Map.empty}) \ (e'', \text{Map.empty}); \text{fv } e = \{\} \rrbracket$   
 $\implies \tau\text{Red0r } P \ t \ h \ (e, es) \ (e'', es)$

**proof**(*induct e xs*  $\equiv \text{Map.empty} :: \text{'addr locals rule: converse-rtranclp-induct2}$ )

**case refl** **show** *?case* **by** *blast*

**next**

**case** (*step e e' xs'*)  
**from**  $\langle \tau\text{red0 } (\text{extTA2J0 } P) \ P \ t \ h \ (e, \text{Map.empty}) \ (e', xs') \rangle$   
**have**  $\text{red}$ :  $\text{extTA2J0 } P, P, t \vdash \langle e, (h, \text{Map.empty}) \rangle -\varepsilon \rightarrow \langle e', (h, xs') \rangle$   
**and**  $\tau\text{move0 } P \ h \ e$  **and**  $\text{no-call } P \ h \ e$  **by** *auto*  
**from**  $\text{red-dom-lcl}[\text{OF } \text{red}] \ \langle \text{fv } e = \{\} \rangle$   
**have**  $\text{dom } xs' = \{\}$  **by**(*auto split:if-split-asm*)  
**hence**  $xs' = \text{Map.empty}$  **by**(*auto*)  
**moreover**  
**from**  $wf \ \text{red}$  **have**  $\text{fv } e' \subseteq \text{fv } e$  **by**(*rule red-fv-subset*)  
**with**  $\langle \text{fv } e = \{\} \rangle$  **have**  $\text{fv } e' = \{\}$  **by** *blast*  
**ultimately** **have**  $\tau\text{Red0r } P \ t \ h \ (e', es) \ (e'', es)$  **by**(*rule step*)  
**moreover** **from**  $\text{red} \ \langle \tau\text{move0 } P \ h \ e \rangle \ \langle xs' = \text{Map.empty} \rangle \ \langle \text{no-call } P \ h \ e \rangle$   
**have**  $\tau\text{Red0 } P \ t \ h \ (e, es) \ (e', es)$  **by**(*auto simp add: no-call-def intro!: red0Red*)  
**ultimately** **show** *?case* **by**(*blast intro: converse-rtranclp-into-rtranclp*)

**qed**

**lemma**  $\tau\text{red0t-into-}\tau\text{Red0t}$ :

**assumes**  $wf$ :  $wf\text{-J-prog } P$

**shows**

$\llbracket \tau\text{red0t } (\text{extTA2J0 } P) \ P \ t \ h \ (e, \text{Map.empty}) \ (e'', \text{Map.empty}); \text{fv } e = \{\} \rrbracket$   
 $\implies \tau\text{Red0t } P \ t \ h \ (e, es) \ (e'', es)$

**proof**(*induct e xs*  $\equiv \text{Map.empty} :: \text{'addr locals rule: converse-tranclp-induct2}$ )

**case base** **thus** *?case*

**by**(*blast intro!: tranclp.r-into-tranclp  $\tau\text{red0-into-}\tau\text{Red0}$* )

**next**

**case** (*step e e' xs'*)  
**from**  $\langle \tau\text{red0 } (\text{extTA2J0 } P) \ P \ t \ h \ (e, \text{Map.empty}) \ (e', xs') \rangle$   
**have**  $\text{red}$ :  $\text{extTA2J0 } P, P, t \vdash \langle e, (h, \text{Map.empty}) \rangle -\varepsilon \rightarrow \langle e', (h, xs') \rangle$  **and**  $\tau\text{move0 } P \ h \ e$  **and**  $\text{no-call}$

$P \ h \ e$  **by** *auto*  
**from** *red-dom-lcl*[*OF red*]  $\langle fv \ e = \{\} \rangle$   
**have**  $dom \ xs' = \{\}$  **by** (*auto split:if-split-asm*)  
**hence**  $xs' = Map.empty$  **by** *auto*  
**moreover from** *wwf red* **have**  $fv \ e' \subseteq fv \ e$  **by** (*rule red-fv-subset*)  
**with**  $\langle fv \ e = \{\} \rangle$  **have**  $fv \ e' = \{\}$  **by** *blast*  
**ultimately have**  $\tau Red0t \ P \ t \ h \ (e', es) \ (e'', es)$  **by** (*rule step*)  
**moreover from** *red*  $\langle \tau move0 \ P \ h \ e \rangle \ \langle xs' = Map.empty \rangle \ \langle no-call \ P \ h \ e \rangle$   
**have**  $\tau Red0 \ P \ t \ h \ (e, es) \ (e', es)$  **by** (*auto simp add: no-call-def intro!: red0Red*)  
**ultimately show**  $?case$  **by** (*blast intro: tranclp-into-tranclp2*)  
**qed**

**lemma**  $\tau red0r\text{-}Val$ :  
 $\tau red0r \ extTA \ P \ t \ h \ (Val \ v, xs) \ s' \longleftrightarrow s' = (Val \ v, xs)$   
**proof**  
**assume**  $\tau red0r \ extTA \ P \ t \ h \ (Val \ v, xs) \ s'$   
**thus**  $s' = (Val \ v, xs)$  **by** *induct(auto)*  
**qed** *auto*

**lemma**  $\tau red0t\text{-}Val$ :  
 $\tau red0t \ extTA \ P \ t \ h \ (Val \ v, xs) \ s' \longleftrightarrow False$   
**proof**  
**assume**  $\tau red0t \ extTA \ P \ t \ h \ (Val \ v, xs) \ s'$   
**thus** *False* **by** *induct auto*  
**qed** *auto*

**lemma**  $\tau reds0r\text{-}map\text{-}Val$ :  
 $\tau reds0r \ extTA \ P \ t \ h \ (map \ Val \ vs, xs) \ s' \longleftrightarrow s' = (map \ Val \ vs, xs)$   
**proof**  
**assume**  $\tau reds0r \ extTA \ P \ t \ h \ (map \ Val \ vs, xs) \ s'$   
**thus**  $s' = (map \ Val \ vs, xs)$  **by** *induct auto*  
**qed** *auto*

**lemma**  $\tau reds0t\text{-}map\text{-}Val$ :  
 $\tau reds0t \ extTA \ P \ t \ h \ (map \ Val \ vs, xs) \ s' \longleftrightarrow False$   
**proof**  
**assume**  $\tau reds0t \ extTA \ P \ t \ h \ (map \ Val \ vs, xs) \ s'$   
**thus** *False* **by** *induct auto*  
**qed** *auto*

**lemma** *Red-Suspend-is-call*:  
 $\llbracket P, t \vdash 0 \ \langle e/exs, h \rangle -ta \rightarrow \langle e'/exs', h' \rangle; Suspend \ w \in set \ \{\!|ta|\!\}_w \rrbracket \implies is-call \ e'$   
**by** (*auto elim!: red0.cases dest: red-Suspend-is-call simp add: is-call-def*)

**lemma** *red0-mthr*: *multithreaded final-expr0* (*mred0 P*)  
**by** (*unfold-locales*)(*auto elim!: red0.cases dest: red-new-thread-heap*)

**lemma** *red0- $\tau$ mthr-wf*:  $\tau multithreaded\text{-}wf \ final\text{-}expr0 \ (mred0 \ P) \ (\tau MOVE0 \ P)$   
**proof** –  
**interpret** *multithreaded final-expr0 mred0 P* **by** (*rule red0-mthr*)  
**show**  $?thesis$   
**proof**  
**fix**  $x1 \ m1 \ t \ ta1 \ x1' \ m1'$

```

    assume mred0 P t (x1, m1) ta1 (x1', m1')  $\tau$ MOVE0 P (x1, m1) ta1 (x1', m1')
    thus m1 = m1' by(cases x1)(fastforce elim!: red0.cases dest:  $\tau$ move0-heap-unchanged)
qed(simp add: split-beta)
qed

```

**lemma** *red- $\tau$ mthr-wf*:  $\tau$ multithreaded-wf final-expr (mred P) ( $\tau$ MOVE P)

**proof**

```

  fix x1 m1 t ta1 x1' m1'
  assume mred P t (x1, m1) ta1 (x1', m1')  $\tau$ MOVE P (x1, m1) ta1 (x1', m1')
  thus m1 = m1' by(auto dest:  $\tau$ move0-heap-unchanged simp add: split-def)
qed(simp add: split-beta)

```

**end**

**sublocale** *J-heap-base* < *red-mthr*:

```

   $\tau$ multithreaded-wf
  final-expr
  mred P
  convert-RA
   $\tau$ MOVE P
  for P
by(rule red- $\tau$ mthr-wf)

```

**sublocale** *J-heap-base* < *red0-mthr*:

```

   $\tau$ multithreaded-wf
  final-expr0
  mred0 P
  convert-RA
   $\tau$ MOVE0 P
  for P
by(rule red0- $\tau$ mthr-wf)

```

**context** *J-heap-base* **begin**

**lemma**  *$\tau$ Red0r-into-red0- $\tau$ mthr-silent-moves*:

```

   $\tau$ Red0r P t h (e, es) (e'', es'')  $\implies$  red0-mthr.silent-moves P t ((e, es), h) ((e'', es''), h)
apply(induct rule: rtranclp-induct2)
apply blast
apply(erule rtranclp.rtrancl-into-rtrancl)
apply(simp add: red0-mthr.silent-move-iff)
done

```

**lemma**  *$\tau$ Red0t-into-red0- $\tau$ mthr-silent-movet*:

```

   $\tau$ Red0t P t h (e, es) (e'', es'')  $\implies$  red0-mthr.silent-movet P t ((e, es), h) ((e'', es''), h)
apply(induct rule: tranclp-induct2)
apply(fastforce simp add: red0-mthr.silent-move-iff elim: tranclp.trancl-into-trancl)+
done

```

**end**

**end**

### 7.3 Bisimulation proof for between source code small step semantics with and without callstacks for single threads

**theory** *J0Bisim* **imports**

*J0*  
*../J/JWellForm*  
*../Common/ExternalCallWF*

**begin**

**inductive** *wf-state* :: '*addr expr* × '*addr expr list* ⇒ *bool*

**where**

$\llbracket fvs\ (e \# es) = \{\}; \forall e \in set\ es.\ is-call\ e \rrbracket$   
 $\implies wf-state\ (e, es)$

**inductive** *bisim-red-red0* :: ('*addr expr* × '*addr locals*) × '*heap* ⇒ ('*addr expr* × '*addr expr list*) × '*heap* ⇒ *bool*

**where**

$wf-state\ ees \implies bisim-red-red0\ ((collapse\ ees, Map.empty), h)\ (ees, h)$

**abbreviation** *ta-bisim0* :: ('*addr*, '*thread-id*, '*heap*) *J-thread-action* ⇒ ('*addr*, '*thread-id*, '*heap*) *J0-thread-action* ⇒ *bool*

**where**  $ta-bisim0 \equiv ta-bisim\ (\lambda t.\ bisim-red-red0)$

**lemma** *wf-state-iff* [*simp*, *code*]:

$wf-state\ (e, es) \longleftrightarrow fvs\ (e \# es) = \{\} \wedge (\forall e \in set\ es.\ is-call\ e)$

**by**(*simp add: wf-state.simps*)

**lemma** *bisim-red-red0I* [*intro*]:

$\llbracket e' = collapse\ ees; xs = Map.empty; h' = h; wf-state\ ees \rrbracket \implies bisim-red-red0\ ((e', xs), h')\ (ees, h)$

**by**(*simp add: bisim-red-red0.simps del: split-paired-Ex*)

**lemma** *bisim-red-red0-final0D*:

$\llbracket bisim-red-red0\ (x1, m1)\ (x2, m2); final-expr0\ x2 \rrbracket \implies final-expr\ x1$

**by**(*erule bisim-red-red0.cases auto*)

**context** *J-heap-base* **begin**

**lemma** *red0-preserves-wf-state*:

**assumes** *wf*: *wf-J-prog P*

**and** *red*:  $P, t \vdash 0\ \langle e / es, h \rangle \rightarrow ta \rightarrow \langle e' / es', h' \rangle$

**and** *wf-state*: *wf-state* (*e*, *es*)

**shows** *wf-state* (*e'*, *es'*)

**using** *wf-state*

**proof**(*cases*)

**assume** *fvs* (*e* # *es*) = {} **and** *icl*:  $\forall e \in set\ es.\ is-call\ e$

**hence** *fv*: *fv* *e* = {} *fvs* *es* = {} **by** *auto*

**show** *?thesis*

**proof**

**from** *red* **show** *fvs* (*e'* # *es'*) = {}

**proof** *cases*

**case** (*red0Red* *xs'*)

**hence** [*simp*]: *es'* = *es*

**and** *red*:  $extTA2J0\ P, P, t \vdash \langle e, (h, Map.empty) \rangle \rightarrow ta \rightarrow \langle e', (h', xs') \rangle$  **by** *auto*

**from** *red-fv-subset[OF wf red]* *fv* *e'* = {} **by** *auto*

```

  with fv show ?thesis by simp
next
  case (red0Call a M vs U Ts T pns body D)
  hence [simp]: ta = ε
    e' = blocks (this # pns) (Class D # Ts) (Addr a # vs) body
    es' = e # es h' = h
    and sees: P ⊢ class-type-of U sees M: Ts → T = [(pns, body)] in D by auto
  from sees-wf-mdecl[OF wf sees]
  have fv body ⊆ insert this (set pns) length Ts = length pns by (simp-all add: wf-mdecl-def)
  thus ?thesis using fv ⟨length vs = length pns⟩ by auto
next
  case (red0Return E)
  with fv-inline-call[of e E] show ?thesis using fv by auto
qed
next
  from red icl show ∀ e ∈ set es'. is-call e
    by cases (simp-all add: is-call-def)
qed
qed

lemma new-thread-bisim0-extNTA2J-extNTA2J0:
  assumes wf: wwf-J-prog P
  and red: P, t ⊢ ⟨a' · M'(vs), h⟩ -ta→ext ⟨va, h'⟩
  and nt: NewThread t' CMa m ∈ set {ta}_t
  shows bisim-red-red0 (extNTA2J P CMa, m) (extNTA2J0 P CMa, m)
proof -
  obtain C M a where CMa [simp]: CMa = (C, M, a) by (cases CMa)
  from red nt have [simp]: m = h' by (rule red-ext-new-thread-heap)
  from red-external-new-thread-sees[OF wf red nt [unfolded CMa]]
  obtain T pns body D where h'a: type-of-addr h' a = [Class-type C]
    and sees: P ⊢ C sees M: [] → T = [(pns, body)] in D by auto
  from sees-wf-mdecl[OF wf sees] have fv body ⊆ {this} by (auto simp add: wf-mdecl-def)
  with red nt h'a sees show ?thesis by (fastforce simp add: is-call-def intro: bisim-red-red0.intros)
qed

lemma ta-bisim0-extNTA2J-extNTA2J0:
  [[ wwf-J-prog P; P, t ⊢ ⟨a' · M'(vs), h⟩ -ta→ext ⟨va, h'⟩ ]]
  ⇒ ta-bisim0 (extTA2J P ta) (extTA2J0 P ta)
apply (auto simp add: ta-bisim-def intro!: list-all2-all-nthI)
apply (case-tac {ta}_t ! n)
apply (simp-all)
apply (erule (1) new-thread-bisim0-extNTA2J-extNTA2J0)
apply (auto simp add: in-set-conv-nth)
done

lemma assumes wf: wwf-J-prog P
  shows red-red0-tabisim0:
    P, t ⊢ ⟨e, s⟩ -ta→ ⟨e', s'⟩ ⇒ ∃ ta'. extTA2J0 P, P, t ⊢ ⟨e, s⟩ -ta'→ ⟨e', s'⟩ ∧ ta-bisim0 ta ta'
  and reds-reds0-tabisim0:
    P, t ⊢ ⟨es, s⟩ [-ta→] ⟨es', s'⟩ ⇒ ∃ ta'. extTA2J0 P, P, t ⊢ ⟨es, s⟩ [-ta'→] ⟨es', s'⟩ ∧ ta-bisim0 ta ta'
proof (induct rule: red-reds.inducts)
  case (RedCallExternal s a T M Ts Tr D vs ta va h' ta' e' s')
  note red = ⟨P, t ⊢ ⟨a · M(vs), hp s⟩ -ta→ext ⟨va, h'⟩⟩
  note T = ⟨type-of-addr (hp s) a = [T]⟩

```

```

from  $T \langle P \vdash \text{class-type-of } T \text{ sees } M: Ts \rightarrow Tr = \text{Native in } D \rangle \text{ red}$ 
have  $\text{extTA2J0 } P, P, t \vdash \langle \text{addr } a \cdot M(\text{map Val vs}), s \rangle - \text{extTA2J0 } P \text{ ta} \rightarrow \langle e', (h', \text{lcl } s) \rangle$ 
  by (rule red-reds.RedCallExternal)(simp-all add:  $\langle e' = \text{extRet2J } (\text{addr } a \cdot M(\text{map Val vs})) \text{ va} \rangle$ )
moreover from  $\langle \text{ta}' = \text{extTA2J } P \text{ ta} \rangle \text{ } T \text{ red wf}$ 
have  $\text{ta-bisim0 } \text{ta}' (\text{extTA2J0 } P \text{ ta})$  by (auto intro: ta-bisim0-extNTA2J-extNTA2J0)
ultimately show  $?case \text{unfolding } \langle s' = (h', \text{lcl } s) \rangle$  by blast
next
  case RedTryFail thus  $?case$  by (force intro: red-reds.RedTryFail)
qed (fastforce intro: red-reds.intros simp add: ta-bisim-def ta-upd-simps)+

lemma assumes wf: wuf-J-prog P
shows red0-red-tabisim0:
   $\text{extTA2J0 } P, P, t \vdash \langle e, s \rangle - \text{ta} \rightarrow \langle e', s' \rangle \implies \exists \text{ta}'. P, t \vdash \langle e, s \rangle - \text{ta}' \rightarrow \langle e', s' \rangle \wedge \text{ta-bisim0 } \text{ta}' \text{ta}$ 
and reds0-reds-tabisim0:
   $\text{extTA2J0 } P, P, t \vdash \langle es, s \rangle [-\text{ta} \rightarrow] \langle es', s' \rangle \implies \exists \text{ta}'. P, t \vdash \langle es, s \rangle [-\text{ta}' \rightarrow] \langle es', s' \rangle \wedge \text{ta-bisim0 } \text{ta}' \text{ta}$ 
proof (induct rule: red-reds.inducts)
case (RedCallExternal s a T M Ts Tr D vs ta va h' ta' e' s')
note  $\text{red} = \langle P, t \vdash \langle a \cdot M(vs), \text{hp } s \rangle - \text{ta} \rightarrow \text{ext } \langle \text{va}, h' \rangle \rangle$ 
note  $T = \langle \text{typeof-addr } (\text{hp } s) \text{ } a = \lfloor T \rfloor \rangle$ 
from  $T \langle P \vdash \text{class-type-of } T \text{ sees } M: Ts \rightarrow Tr = \text{Native in } D \rangle \text{ red}$ 
have  $P, t \vdash \langle \text{addr } a \cdot M(\text{map Val vs}), s \rangle - \text{extTA2J } P \text{ ta} \rightarrow \langle e', (h', \text{lcl } s) \rangle$ 
  by (rule red-reds.RedCallExternal)(simp-all add:  $\langle e' = \text{extRet2J } (\text{addr } a \cdot M(\text{map Val vs})) \text{ va} \rangle$ )
moreover from  $\langle \text{ta}' = \text{extTA2J0 } P \text{ ta} \rangle \text{ } T \text{ red wf}$ 
have  $\text{ta-bisim0 } (\text{extTA2J } P \text{ ta}) \text{ta}'$  by (auto intro: ta-bisim0-extNTA2J-extNTA2J0)
ultimately show  $?case \text{unfolding } \langle s' = (h', \text{lcl } s) \rangle$  by blast
next
  case RedTryFail thus  $?case$  by (force intro: red-reds.RedTryFail)
qed (fastforce intro: red-reds.intros simp add: ta-bisim-def ta-upd-simps)+

lemma red-inline-call-red:
assumes  $\text{red}: P, t \vdash \langle e, (h, \text{Map.empty}) \rangle - \text{ta} \rightarrow \langle e', (h', \text{Map.empty}) \rangle$ 
shows  $\text{call } E = \lfloor aMvs \rfloor \implies P, t \vdash \langle \text{inline-call } e \text{ } E, (h, x) \rangle - \text{ta} \rightarrow \langle \text{inline-call } e' \text{ } E, (h', x) \rangle$ 
(is -  $\implies$  ?concl E x)

and
   $\text{calls } Es = \lfloor aMvs \rfloor \implies P, t \vdash \langle \text{inline-calls } e \text{ } Es, (h, x) \rangle [-\text{ta} \rightarrow] \langle \text{inline-calls } e' \text{ } Es, (h', x) \rangle$ 
(is -  $\implies$  ?concls Es x)
proof (induct E and Es arbitrary: x and x rule: call.induct calls.induct)
case (Call obj M pns x)
note  $\text{IHobj} = \langle \bigwedge x. \text{call obj} = \lfloor aMvs \rfloor \implies ?concl \text{obj } x \rangle$ 
note  $\text{IHpns} = \langle \bigwedge x. \text{calls pns} = \lfloor aMvs \rfloor \implies ?concls \text{pns } x \rangle$ 
obtain a M' vs where [simp]:  $aMvs = (a, M', vs)$  by (cases aMvs, auto)
from  $\langle \text{call } (\text{obj} \cdot M(\text{pns})) = \lfloor aMvs \rfloor \rangle$  have  $\text{call } (\text{obj} \cdot M(\text{pns})) = \lfloor (a, M', vs) \rfloor$  by simp
thus  $?case$ 
proof (induct rule: call-callE)
case CallObj
  with  $\text{IHobj}[\text{of } x]$  show  $?case$  by (fastforce intro: red-reds.CallObj)
next
  case (CallParams v'')
  with  $\text{IHpns}[\text{of } x]$  show  $?case$  by (fastforce intro: red-reds.CallParams)
next
  case Call
  from  $\text{red-lcl-add}[OF \text{red}, \text{where } ?l0.0=x]$ 
  have  $P, t \vdash \langle e, (h, x) \rangle - \text{ta} \rightarrow \langle e', (h', x) \rangle$  by simp

```

```

  with Call show ?case by(fastforce dest: BlockRed)
qed
next
case (Block V T' vo exp x)
note IH =  $\langle \bigwedge x. \text{call } \text{exp} = \lfloor aMvs \rfloor \implies ?\text{concl } \text{exp } x \rangle$ 
from IH[of x (V := vo)]  $\langle \text{call } \{ V:T'=vo; \text{exp} \} = \lfloor aMvs \rfloor \rangle$ 
show ?case by(clarsimp simp del: fun-upd-apply)(drule BlockRed, auto)
next
case (Cons-exp exp exps x)
show ?case
proof(cases is-val exp)
  case True
  with  $\langle \text{calls } (\text{exp} \# \text{exps}) = \lfloor aMvs \rfloor \rangle$  have calls exps =  $\lfloor aMvs \rfloor$  by auto
  with  $\langle \text{calls } \text{exps} = \lfloor aMvs \rfloor \implies ?\text{concl } \text{exps } x \rangle$  True
  show ?thesis by(fastforce intro: ListRed2)
next
  case False
  with  $\langle \text{calls } (\text{exp} \# \text{exps}) = \lfloor aMvs \rfloor \rangle$  have call exp =  $\lfloor aMvs \rfloor$  by auto
  with  $\langle \text{call } \text{exp} = \lfloor aMvs \rfloor \implies ?\text{concl } \text{exp } x \rangle$ 
  show ?thesis by(fastforce intro: ListRed1)
qed
qed(fastforce intro: red-reds.intros)+

lemma
  assumes P  $\vdash$  class-type-of T sees M:Us  $\rightarrow$  U =  $\lfloor (\text{pns}, \text{body}) \rfloor$  in D length vs = length pns length Us
  = length pns
  shows is-call-red-inline-call:
     $\llbracket \text{call } e = \lfloor (a, M, \text{vs}) \rfloor; \text{typeof-addr } (\text{hp } s) \ a = \lfloor T \rfloor \rrbracket$ 
 $\implies P, t \vdash \langle e, s \rangle \rightarrow \langle \text{inline-call } (\text{blocks } (\text{this} \# \text{pns}) (\text{Class } D \# \text{Us}) (\text{Addr } a \# \text{vs}) \text{body}) \ e, s \rangle$ 
    (is  $\rightarrow \implies ?\text{red } e \ s$ )
  and is-calls-reds-inline-calls:
     $\llbracket \text{calls } \text{es} = \lfloor (a, M, \text{vs}) \rfloor; \text{typeof-addr } (\text{hp } s) \ a = \lfloor T \rfloor \rrbracket$ 
 $\implies P, t \vdash \langle \text{es}, s \rangle \rightarrow \langle \text{inline-calls } (\text{blocks } (\text{this} \# \text{pns}) (\text{Class } D \# \text{Us}) (\text{Addr } a \# \text{vs}) \text{body}) \ \text{es}, s \rangle$ 
    (is  $\rightarrow \implies ?\text{reds } \text{es } s$ )
proof(induct e and es arbitrary: s and s rule: call.induct calls.induct)
  case (Call obj M' params s)
  note IHObj =  $\langle \bigwedge s. \llbracket \text{call } \text{obj} = \lfloor (a, M, \text{vs}) \rfloor; \text{typeof-addr } (\text{hp } s) \ a = \lfloor T \rfloor \rrbracket \implies ?\text{red } \text{obj } s \rangle$ 
  note IHParams =  $\langle \bigwedge s. \llbracket \text{calls } \text{params} = \lfloor (a, M, \text{vs}) \rfloor; \text{typeof-addr } (\text{hp } s) \ a = \lfloor T \rfloor \rrbracket \implies ?\text{reds } \text{params}$ 
 $s \rangle$ 
  from  $\langle \text{call } (\text{obj} \cdot M'(\text{params})) = \lfloor (a, M, \text{vs}) \rfloor \rangle$ 
  show ?case
  proof(induct rule: call-callE)
    case CallObj
    from IHObj[OF CallObj]  $\langle \text{typeof-addr } (\text{hp } s) \ a = \lfloor T \rfloor \rangle$  have ?red obj s by blast
    moreover from CallObj have  $\neg$  is-val obj by auto
    ultimately show ?case by(auto intro: red-reds.CallObj)
  next
    case CallParams v
    from IHParams[OF  $\langle \text{calls } \text{params} = \lfloor (a, M, \text{vs}) \rfloor \rangle$ ]  $\langle \text{typeof-addr } (\text{hp } s) \ a = \lfloor T \rfloor \rangle$ 
    have ?reds params s by blast
    moreover from CallParams have  $\neg$  is-vals params by auto
    ultimately show ?case using  $\langle \text{obj} = \text{Val } v \rangle$  by(auto intro: red-reds.CallParams)
  next
    case Call

```

**with** RedCall[**where**  $s=s$ , *simplified*, *OF*  $\langle \text{typeof-addr } (hp\ s)\ a = \lfloor T \rfloor \rangle \langle P \vdash \text{class-type-of } T \text{ sees } M:Us \rightarrow U = \lfloor (pns, \text{body}) \rfloor \text{ in } D \rangle \langle \text{length } vs = \text{length } pns \rangle \langle \text{length } Us = \text{length } pns \rangle$   
**show** *?thesis* **by**(*simp*)  
**qed**  
**next**  
**case** (*Block*  $V\ ty\ vo\ exp\ s$ )  
**note**  $IH = \langle \bigwedge s. \llbracket \text{call } exp = \lfloor (a, M, vs) \rfloor; \text{typeof-addr } (hp\ s)\ a = \lfloor T \rfloor \rrbracket \implies ?red\ exp\ s \rangle$   
**from**  $\langle \text{call } \{V:ty=vo; exp\} = \lfloor (a, M, vs) \rfloor \rangle IH[\text{of } (hp\ s, (lcl\ s)(V := vo))] \langle \text{typeof-addr } (hp\ s)\ a = \lfloor T \rfloor \rangle$   
**show** *?case* **by**(*cases*  $s$ , *simp* *del: fun-upd-apply*)(*drule* *red-reds*.*BlockRed*, *simp*)  
**qed**(*fastforce* *intro: red-reds.intros*)+

**lemma** *red-inline-call-red'*:

**assumes** *fv: fv ee = {}*  
**and** *ee fin:  $\neg \text{final } ee$*   
**shows**  $\llbracket \text{call } E = \lfloor aMvs \rfloor; P, t \vdash \langle \text{inline-call } ee\ E, (h, x) \rangle -ta \rightarrow \langle E', (h', x') \rangle \rrbracket$   
 $\implies \exists ee'. E' = \text{inline-call } ee'\ E \wedge P, t \vdash \langle ee, (h, \text{Map.empty}) \rangle -ta \rightarrow \langle ee', (h', \text{Map.empty}) \rangle \wedge$   
 $x = x'$   
**(is**  $\llbracket -; - \rrbracket \implies ?concl\ E\ E'\ x\ x'$ **)**  
**and**  $\llbracket \text{calls } Es = \lfloor aMvs \rfloor; P, t \vdash \langle \text{inline-calls } ee\ Es, (h, x) \rangle [-ta \rightarrow] \langle Es', (h', x') \rangle \rrbracket$   
 $\implies \exists ee'. Es' = \text{inline-calls } ee'\ Es \wedge P, t \vdash \langle ee, (h, \text{Map.empty}) \rangle -ta \rightarrow \langle ee', (h', \text{Map.empty}) \rangle$   
 $\wedge x = x'$   
**(is**  $\llbracket -; - \rrbracket \implies ?concls\ Es\ Es'\ x\ x'$ **)**  
**proof**(*induct*  $E$  **and**  $Es$  *arbitrary:  $E' x x'$  and  $Es' x x'$*  *rule: call.induct calls.induct*)  
**case** *new* **thus** *?case* **by** *simp*  
**next**  
**case** (*newArray*  $T\ exp\ E'\ x\ x'$ )  
**thus** *?case* **using** *ee fin* **by**(*auto* *elim!:* *red-cases*)  
**next**  
**case** *Cast* **thus** *?case* **using** *ee fin* **by**(*auto* *elim!:* *red-cases*)  
**next**  
**case** *InstanceOf* **thus** *?case* **using** *ee fin* **by**(*auto* *elim!:* *red-cases*)  
**next**  
**case** *Val* **thus** *?case* **by** *simp*  
**next**  
**case** *Var* **thus** *?case* **by** *simp*  
**next**  
**case** *LAss*  
**thus** *?case* **using** *ee fin* **by**(*auto* *elim!:* *red-cases*)  
**next**  
**case** *BinOp*  
**thus** *?case* **using** *ee fin* **by**(*auto* *elim!:* *red-cases* *split: if-split-asm*)  
**next**  
**case** *AAcc*  
**thus** *?case* **using** *ee fin* **by**(*auto* *elim!:* *red-cases* *split: if-split-asm*)  
**next**  
**case** *AAss* **thus** *?case* **using** *ee fin* **by**(*auto* *elim!:* *red-cases* *split: if-split-asm*)  
**next**  
**case** *ALen* **thus** *?case* **using** *ee fin* **by**(*auto* *elim!:* *red-cases* *split: if-split-asm*)  
**next**  
**case** *FAcc* **thus** *?case* **using** *ee fin* **by**(*auto* *elim!:* *red-cases*)  
**next**  
**case** *FAss* **thus** *?case* **using** *ee fin* **by**(*auto* *elim!:* *red-cases* *split: if-split-asm*)  
**next**



```

case CompareAndSwap thus ?case using eefin by(auto elim!: red-cases split: if-split-asm)
next
case (Call obj M pns E' x x')
note IHobj =  $\langle \bigwedge x E' x'. \llbracket \text{call obj} = \lfloor aMvs \rfloor; P, t \vdash \langle \text{inline-call ee obj}, (h, x) \rangle -ta\rightarrow \langle E', (h', x') \rangle \rrbracket$ 
 $\implies ?\text{concl obj } E' x x'$ 
note IHpns =  $\langle \bigwedge Es' x x'. \llbracket \text{calls pns} = \lfloor aMvs \rfloor; P, t \vdash \langle \text{inline-calls ee pns}, (h, x) \rangle [-ta\rightarrow] \langle Es', (h', x') \rangle \rrbracket$ 
 $\implies ?\text{concls pns } Es' x x'$ 
note red =  $\langle P, t \vdash \langle \text{inline-call ee } (obj \cdot M(pns)), (h, x) \rangle -ta\rightarrow \langle E', (h', x') \rangle \rangle$ 
obtain a M' vs where [simp]: aMvs = (a, M', vs) by(cases aMvs, auto)
from  $\langle \text{call } (obj \cdot M(pns)) = \lfloor aMvs \rfloor \rangle$  have  $\text{call } (obj \cdot M(pns)) = \lfloor (a, M', vs) \rfloor$  by simp
thus ?case
proof(cases rule: call-callE)
case CallObj
hence  $\neg \text{is-val obj}$  by auto
with red CallObj eefin obtain obj' where  $E' = obj' \cdot M(pns)$ 
and red':  $P, t \vdash \langle \text{inline-call ee obj}, (h, x) \rangle -ta\rightarrow \langle obj', (h', x') \rangle$ 
by(auto elim!: red-cases)
from IHobj[OF - red'] CallObj obtain ee'
where  $\text{inline-call ee' obj} = obj' x = x'$ 
and  $P, t \vdash \langle ee, (h, \text{Map.empty}) \rangle -ta\rightarrow \langle ee', (h', \text{Map.empty}) \rangle$  by(auto simp del: fun-upd-apply)
with  $\langle E' = obj' \cdot M(pns) \rangle$  CallObj red' show ?thesis by(fastforce simp del: fun-upd-apply)
next
case (CallParams v')
hence  $\neg \text{is-vals pns}$  by auto
with red CallParams eefin obtain pns' where  $E' = obj \cdot M(pns')$ 
and red':  $P, t \vdash \langle \text{inline-calls ee pns}, (h, x) \rangle [-ta\rightarrow] \langle pns', (h', x') \rangle$ 
by(auto elim!: red-cases)
from IHpns[OF - red'] CallParams obtain ee'
where  $\text{inline-calls ee' pns} = pns' x = x'$ 
and  $P, t \vdash \langle ee, (h, \text{Map.empty}) \rangle -ta\rightarrow \langle ee', (h', \text{Map.empty}) \rangle$ 
by(auto simp del: fun-upd-apply)
with  $\langle E' = obj \cdot M(pns') \rangle$  CallParams red'  $\langle \neg \text{is-vals pns} \rangle$ 
show ?thesis by(auto simp del: fun-upd-apply)
next
case Call
with red have red':  $P, t \vdash \langle ee, (h, x) \rangle -ta\rightarrow \langle E', (h', x') \rangle$  by(auto)
from red-lcl-sub[OF red', of {}] fv
have  $P, t \vdash \langle ee, (h, \text{Map.empty}) \rangle -ta\rightarrow \langle E', (h', \text{Map.empty}) \rangle$  by simp
moreover have  $x' = x$ 
proof(rule ext)
fix V
from red-notfree-unchanged[OF red', of V] fv
show  $x' V = x V$  by simp
qed
ultimately show ?thesis using Call by simp
qed
next
case (Block V ty voo exp E' x x')
note IH =  $\langle \bigwedge x E' x'. \llbracket \text{call exp} = \lfloor aMvs \rfloor; P, t \vdash \langle \text{inline-call ee exp}, (h, x) \rangle -ta\rightarrow \langle E', (h', x') \rangle \rrbracket$ 
 $\implies ?\text{concl exp } E' x x'$ 
from  $\langle \text{call } \{ V:ty=voo; exp \} = \lfloor aMvs \rfloor \rangle$  have ic:  $\text{call exp} = \lfloor aMvs \rfloor$  by simp
note red =  $\langle P, t \vdash \langle \text{inline-call ee } \{ V:ty=voo; exp \}, (h, x) \rangle -ta\rightarrow \langle E', (h', x') \rangle \rangle$ 
hence  $P, t \vdash \langle \{ V:ty=voo; \text{inline-call ee exp} \}, (h, x) \rangle -ta\rightarrow \langle E', (h', x') \rangle$  by simp

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with ic eefin obtain  $\exp' x''$  where  $E' = \{V:ty=x'' V; \exp'\}$ 
  and  $\text{red}' : P, t \vdash \langle \text{inline-call } ee \exp, (h, \text{fun-upd } x V \text{voo}) \rangle \text{--ta--} \langle \exp', (h', x'') \rangle$ 
  and  $x' = \text{fun-upd } x'' V (x V)$ 
  by  $-(\text{erule red.cases}, \text{auto dest: inline-call-eq-Val})$ 
from  $IH[OF \text{ic red}']$  obtain  $ee' \text{vo}'$ 
  where  $\text{icl: inline-call } ee' \exp = \exp' x'' = \text{fun-upd } x V \text{voo}$ 
  and  $\text{red}'' : P, t \vdash \langle ee, (h, \text{Map.empty}) \rangle \text{--ta--} \langle ee', (h', \text{Map.empty}) \rangle$  by blast
from  $\langle x'' = \text{fun-upd } x V \text{voo} \rangle$  have  $x'' V = \text{voo}$  by  $(\text{simp add: fun-eq-iff})$ 
with  $\text{icl red}'' \langle E' = \{V:ty=x'' V; \exp'\} \rangle \langle x' = \text{fun-upd } x'' V (x V) \rangle \text{red}'$ 
show  $?case$  by  $(\text{auto simp del: fun-upd-apply})$ 
next
  case Synchronized thus  $?case$  using eefin by  $(\text{auto elim!: red-cases})$ 
next
  case InSynchronized thus  $?case$  using eefin by  $(\text{auto elim!: red-cases})$ 
next
  case Seq
  thus  $?case$  using eefin by  $(\text{auto elim!: red-cases})$ 
next
  case Cond thus  $?case$  using eefin by  $(\text{auto elim!: red-cases})$ 
next
  case While thus  $?case$  by simp
next
  case throw
  thus  $?case$  using eefin by  $(\text{auto elim!: red-cases})$ 
next
  case TryCatch
  thus  $?case$  using eefin by  $(\text{auto elim!: red-cases})$ 
next
  case Nil-exp thus  $?case$  by simp
next
  case Cons-exp
  thus  $?case$  using eefin by  $(\text{auto elim!: reds-cases split: if-split-asm})$ 
qed

lemma assumes sees:  $P \vdash \text{class-type-of } T \text{ sees } M:Us \rightarrow U = \lfloor (pns, \text{body}) \rfloor$  in  $D$ 
shows is-call-red-inline-callD:
 $\llbracket P, t \vdash \langle e, s \rangle \text{--ta--} \langle e', s' \rangle; \text{call } e = \lfloor (a, M, \text{vs}) \rfloor; \text{typeof-addr } (hp \text{ } s) \text{ } a = \lfloor T \rfloor \rrbracket$ 
 $\implies e' = \text{inline-call } (\text{blocks } (\text{this} \# pns) (\text{Class } D \# Us) (\text{Addr } a \# \text{vs}) \text{body}) \text{ } e$ 
and is-calls-reds-inline-callsD:
 $\llbracket P, t \vdash \langle es, s \rangle \text{--ta--} \langle es', s' \rangle; \text{calls } es = \lfloor (a, M, \text{vs}) \rfloor; \text{typeof-addr } (hp \text{ } s) \text{ } a = \lfloor T \rfloor \rrbracket$ 
 $\implies es' = \text{inline-calls } (\text{blocks } (\text{this} \# pns) (\text{Class } D \# Us) (\text{Addr } a \# \text{vs}) \text{body}) \text{ } es$ 
proof  $(\text{induct rule: red-reds.inducts})$ 
  case RedCall with sees show  $?case$  by  $(\text{auto dest: sees-method-fun})$ 
next
  case RedCallExternal
  with sees show  $?case$  by  $(\text{auto dest: sees-method-fun})$ 
next
  case  $(\text{BlockRed } e \text{ } h \text{ } x \text{ } V \text{ } \text{vo} \text{ } \text{ta} \text{ } e' \text{ } h' \text{ } x' \text{ } T')$ 
from  $\langle \text{call } \{V:T'=vo; e\} = \lfloor (a, M, \text{vs}) \rfloor \rangle \langle \text{typeof-addr } (hp (h, x)) \text{ } a = \lfloor T \rfloor \rangle \text{sees}$ 
have  $\text{call } e = \lfloor (a, M, \text{vs}) \rfloor$  and  $\neg \text{synthesized-call } P \text{ } h \text{ } (a, M, \text{vs})$ 
  by  $(\text{auto simp add: synthesized-call-conv dest: sees-method-fun})$ 
with  $\langle P, t \vdash \langle e, (h, x(V := vo)) \rangle \text{--ta--} \langle e', (h', x') \rangle \rangle$ 
have  $x(V := vo) = x'$  by  $(\text{auto dest: is-call-red-state-unchanged})$ 
hence  $x' V = vo$  by auto

```

**with** *BlockRed* **show** ?case **by** (*simp*)  
**qed**(*fastforce split: if-split-asm*)**+**

**lemma** (**in**  $-$ ) *wf-state-ConsD*:  $wf\text{-}state\ (e, e' \# es) \implies wf\text{-}state\ (e', es)$   
**by** (*simp*)

**lemma** *red-fold-exs*:

$\llbracket P, t \vdash \langle e, (h, Map.empty) \rangle -ta\rightarrow \langle e', (h', Map.empty) \rangle; wf\text{-}state\ (e, es) \rrbracket$   
 $\implies P, t \vdash \langle collapse\ (e, es), (h, Map.empty) \rangle -ta\rightarrow \langle collapse\ (e', es), (h', Map.empty) \rangle$   
 (**is**  $\llbracket -; - \rrbracket \implies ?concl\ e\ e'$ )  
**proof**(*induction es arbitrary: e e'*)  
**case** *Nil* **thus** ?case **by** *simp*  
**next**  
**case** (*Cons E es*)  
**note**  $wf = \langle wf\text{-}state\ (e, E \# es) \rangle$   
**note**  $red = \langle P, t \vdash \langle e, (h, Map.empty) \rangle -ta\rightarrow \langle e', (h', Map.empty) \rangle \rangle$   
**from** *wf* **obtain** *a M vs* **where** *call*:  $call\ E = \lfloor (a, M, vs) \rfloor$   
**by**(*auto simp add: is-call-def*)  
**from** *red call* **have**  $P, t \vdash \langle inline\text{-}call\ e\ E, (h, Map.empty) \rangle -ta\rightarrow \langle inline\text{-}call\ e'\ E, (h', Map.empty) \rangle$   
**by**(*rule red-inline-call-red*)  
**hence**  $P, t \vdash \langle collapse\ (inline\text{-}call\ e\ E, es), (h, Map.empty) \rangle -ta\rightarrow \langle collapse\ (inline\text{-}call\ e'\ E, es), (h', Map.empty) \rangle$   
**proof**(*rule Cons.IH*)  
**from** *wf* **have**  $fv\ E = \{\}$   $fv\ e = \{\}$  **by** *auto*  
**with**  $fv\text{-}inline\text{-}call[of\ e\ E]$  **have**  $fv\ (inline\text{-}call\ e\ E) = \{\}$  **by** *auto*  
**thus**  $wf\text{-}state\ (inline\text{-}call\ e\ E, es)$  **using** *wf* **by** *auto*  
**qed**  
**thus** ?case **by** *simp*  
**qed**

**lemma** *red-fold-exs'*:

$\llbracket P, t \vdash \langle collapse\ (e, es), (h, Map.empty) \rangle -ta\rightarrow \langle e', (h', x') \rangle; wf\text{-}state\ (e, es); \neg final\ e \rrbracket$   
 $\implies \exists E'. e' = collapse\ (E', es) \wedge P, t \vdash \langle e, (h, Map.empty) \rangle -ta\rightarrow \langle E', (h', Map.empty) \rangle$   
 (**is**  $\llbracket -; -; - \rrbracket \implies ?concl\ e\ es$ )  
**proof**(*induction es arbitrary: e*)  
**case** *Nil*  
**hence**  $red': P, t \vdash \langle e, (h, Map.empty) \rangle -ta\rightarrow \langle e', (h', x') \rangle$  **by** *simp*  
**with**  $red\text{-}dom\text{-}lcl[OF\ this]\ \langle wf\text{-}state\ (e, []) \rangle$  **show** ?case **by** *auto*  
**next**  
**case** (*Cons E es*)  
**note**  $wf = \langle wf\text{-}state\ (e, E \# es) \rangle$   
**note**  $nfin = \langle \neg final\ e \rangle$   
**from** *wf* **have**  $fv\ e = \{\}$  **by** *simp*  
**from** *wf* **obtain** *a M vs* **where** *call*:  $call\ E = \lfloor (a, M, vs) \rfloor$  **by**(*auto simp add: is-call-def*)  
**from**  $\langle P, t \vdash \langle collapse\ (e, E \# es), (h, Map.empty) \rangle -ta\rightarrow \langle e', (h', x') \rangle \rangle$   
**have**  $P, t \vdash \langle collapse\ (inline\text{-}call\ e\ E, es), (h, Map.empty) \rangle -ta\rightarrow \langle e', (h', x') \rangle$  **by** *simp*  
**moreover from**  $wf\text{-}fv\text{-}inline\text{-}call[of\ e\ E]$  **have**  $wf\text{-}state\ (inline\text{-}call\ e\ E, es)$  **by** *auto*  
**moreover from** *nfin call* **have**  $\neg final\ (inline\text{-}call\ e\ E)$  **by**(*auto elim!: final.cases*)  
**ultimately have** ?concl  $(inline\text{-}call\ e\ E)\ es$  **by**(*rule Cons.IH*)  
**then obtain**  $E'$  **where**  $e' = collapse\ (E', es)$   
**and**  $red': P, t \vdash \langle inline\text{-}call\ e\ E, (h, Map.empty) \rangle -ta\rightarrow \langle E', (h', Map.empty) \rangle$  **by** *blast*  
**from**  $red\text{-}inline\text{-}call\text{-}red'(1)[OF\ \langle fv\ e = \{\} \rangle\ nfin\ \langle call\ E = \lfloor (a, M, vs) \rfloor \rangle\ red']$   
**obtain**  $e'$  **where**  $E' = inline\text{-}call\ e'\ E$   $P, t \vdash \langle e, (h, Map.empty) \rangle -ta\rightarrow \langle e', (h', Map.empty) \rangle$  **by** *auto*  
**thus** ?case **using**  $e'$  **by** *auto*

qed

**lemma**  $\tau\text{Red0r-inline-call-not-final}$ :

$\exists e' es'. \tau\text{Red0r } P \text{ t h } (e, es) (e', es') \wedge (\text{final } e' \longrightarrow es' = []) \wedge \text{collapse } (e, es) = \text{collapse } (e', es')$

**proof**(*induct es arbitrary: e*)

**case** *Nil* **thus** ?*case* **by** *blast*

**next**

**case** (*Cons e es E*) **show** ?*case*

**proof**(*cases final E*)

**case** *True*

**hence**  $\tau\text{Red0 } P \text{ t h } (E, e \# es) (\text{inline-call } E \text{ e, es})$  **by**(*auto intro: red0Return*)

**moreover from** *Cons*[*of inline-call E e*] **obtain**  $e' es'$

**where**  $\tau\text{Red0r } P \text{ t h } (\text{inline-call } E \text{ e, es}) (e', es') \text{ final } e' \longrightarrow es' = []$

$\text{collapse } (\text{inline-call } E \text{ e, es}) = \text{collapse } (e', es')$  **by** *blast*

**ultimately show** ?*thesis* **unfolding** *collapse.simps* **by**(*blast intro: converse-rtrancpl-into-rtrancpl*)

qed *blast*

qed

**lemma**  $\tau\text{Red0-preserves-wf-state}$ :

$\llbracket \text{wf-J-prog } P; \tau\text{Red0 } P \text{ t h } (e, es) (e', es'); \text{wf-state } (e, es) \rrbracket \Longrightarrow \text{wf-state } (e', es')$

**by**(*auto simp del: wf-state-iff intro: red0-preserves-wf-state*)

**lemma**  $\tau\text{Red0r-preserves-wf-state}$ :

**assumes** *wf: wf-J-prog P*

**shows**  $\llbracket \tau\text{Red0r } P \text{ t h } (e, es) (e', es'); \text{wf-state } (e, es) \rrbracket \Longrightarrow \text{wf-state } (e', es')$

**by**(*induct rule: rtrancpl-induct2*)(*blast intro:  $\tau\text{Red0-preserves-wf-state}[OF wf]$* )+

**lemma**  $\tau\text{Red0t-preserves-wf-state}$ :

**assumes** *wf: wf-J-prog P*

**shows**  $\llbracket \tau\text{Red0t } P \text{ t h } (e, es) (e', es'); \text{wf-state } (e, es) \rrbracket \Longrightarrow \text{wf-state } (e', es')$

**by**(*induct rule: trancpl-induct2*)(*blast intro:  $\tau\text{Red0-preserves-wf-state}[OF wf]$* )+

**lemma** *collapse- $\tau\text{move0-inv}$* :

$\llbracket \forall e \in \text{set } es. \text{is-call } e; \neg \text{final } e \rrbracket \Longrightarrow \tau\text{move0 } P \text{ h } (\text{collapse } (e, es)) = \tau\text{move0 } P \text{ h } e$

**proof**(*induction es arbitrary: e*)

**case** *Nil* **thus** ?*case* **by** *clarsimp*

**next**

**case** (*Cons e es e''*)

**from**  $\langle \forall a \in \text{set } (e \# es). \text{is-call } a \rangle$  **obtain** *aMvs* **where** *calls:  $\forall e \in \text{set } es. \text{is-call } e$*

**and** *call: call e = [aMvs]* **by**(*auto simp add: is-call-def*)

**note** *calls moreover*

**from**  $\langle \neg \text{final } e'' \rangle$  *call* **have**  $\neg \text{final } (\text{inline-call } e'' e)$  **by**(*auto simp add: final-iff*)

**ultimately have**  $\tau\text{move0 } P \text{ h } (\text{collapse } (\text{inline-call } e'' e, es)) = \tau\text{move0 } P \text{ h } (\text{inline-call } e'' e)$

**by**(*rule Cons.IH*)

**also from** *call*  $\langle \neg \text{final } e'' \rangle$  **have**  $\dots = \tau\text{move0 } P \text{ h } e''$  **by**(*auto simp add: inline-call- $\tau\text{move0-inv}$* )

**finally show** ?*case* **by** *simp*

qed

**lemma**  $\tau\text{Red0r-into-silent-moves}$ :

$\tau\text{Red0r } P \text{ t h } (e, es) (e', es') \Longrightarrow \text{red0-mthr.silent-moves } P \text{ t } ((e, es), h) ((e', es'), h)$

**by**(*induct rule: rtrancpl-induct2*)(*fastforce intro:  $\tau\text{rsys.silent-move.intros elim!:$  rtrancpl.rtrancpl-into-rtrancpl*)+

**lemma**  $\tau\text{Red0t-into-silent-movet}$ :

$\tau\text{Red0t } P \text{ t h } (e, es) (e', es') \Longrightarrow \text{red0-mthr.silent-movet } P \text{ t } ((e, es), h) ((e', es'), h)$

by(induct rule: tranclp-induct2)(fastforce intro:  $\tau$ trsys.silent-move.intros elim!: tranclp.trancl-into-trancl)+

lemma red-simulates-red0:

assumes wwf: wwf-J-prog P

and sim: bisim-red-red0 s1 s2 mred0 P t s2 ta2 s2'  $\neg \tau$ MOVE0 P s2 ta2 s2'

shows  $\exists s1' ta1. mred P t s1 ta1 s1' \wedge \neg \tau$ MOVE P s1 ta1 s1'  $\wedge$  bisim-red-red0 s1' s2'  $\wedge$  ta-bisim0 ta1 ta2

proof –

note sim

moreover obtain e1 h1 x1 where s1: s1 = ((e1, x1), h1) by(cases s1, auto)

moreover obtain e' es' h2' where s2': s2' = ((e', es'), h2') by(cases s2', auto)

moreover obtain e es h2 where s2: s2 = ((e, es), h2) by(cases s2, auto)

ultimately have bisim: bisim-red-red0 ((e1, x1), h1) ((e, es), h2)

and red: P, t  $\vdash 0 \langle e/es, h2 \rangle -ta2 \rightarrow \langle e'/es', h2' \rangle$

and  $\tau: \neg \tau$ move0 P h2 e  $\wedge \neg$  final e  $\vee$  ta2  $\neq \varepsilon$  by auto

from red  $\tau$  have  $\tau: \neg \tau$ move0 P h2 e and nfin:  $\neg$  final e

by(cases, auto dest: red- $\tau$ -taD[where extTA=extTA2J0 P, OF extTA2J0- $\varepsilon$ ])+

from bisim have heap: h1 = h2 and fold: e1 = collapse (e, es)

and x1: x1 = Map.empty and wf-state: wf-state (e, es)

by(auto elim!: bisim-red-red0.cases)

from red wf-state have wf-state': wf-state (e', es') by(rule red0-preserves-wf-state[OF wwf])

from red show ?thesis

proof(cases)

case (red0Red xs')

hence [simp]: es' = es

and extTA2J0 P, P, t  $\vdash \langle e, (h2, \text{Map.empty}) \rangle -ta2 \rightarrow \langle e', (h2', xs') \rangle$  by auto

from red0-red-tabisim0[OF wwf this(2)] obtain ta1

where red': P, t  $\vdash \langle e, (h2, \text{Map.empty}) \rangle -ta1 \rightarrow \langle e', (h2', xs') \rangle$

and tasim: ta-bisim0 ta1 ta2 by auto

moreover from wf-state have fv e = {} by auto

with red-dom-lcl[OF red'] red-fv-subset[OF wwf red'] have xs' = Map.empty by auto

ultimately have P, t  $\vdash \langle e, (h2, \text{Map.empty}) \rangle -ta1 \rightarrow \langle e', (h2', \text{Map.empty}) \rangle$  by simp

with wf-state have P, t  $\vdash \langle \text{collapse } (e, es), (h2, \text{Map.empty}) \rangle -ta1 \rightarrow \langle \text{collapse } (e', es), (h2', \text{Map.empty}) \rangle$

by -(erule red-fold-exs, auto)

moreover from  $\tau$  wf-state fold nfin have  $\neg \tau$ move0 P h2 e1 by(auto simp add: collapse- $\tau$ move0-inv)

hence  $\neg \tau$ MOVE P ((collapse (e, es), Map.empty), h2) ta1 ((collapse (e', es), Map.empty), h2')

unfolding fold by auto

moreover from wf-state' have bisim-red-red0 ((collapse (e', es), Map.empty), h2') s2'

unfolding s2' by(auto)

ultimately show ?thesis unfolding heap s1 s2 s2' fold x1

using tasim by(fastforce intro!: exI rtranclp.rtrancl-refl)

next

case red0Call

with  $\tau$  have False

by(auto simp add: synthesized-call-def  $\tau$ external'-def dest!:  $\tau$ move0-callD[where P=P and h=h2]

dest: sees-method-fun)

thus ?thesis ..

next

case red0Return with nfin show ?thesis by simp

qed

qed

lemma delay-bisimulation-measure-red-red0:

```

assumes wf: wwf-J-prog P
shows delay-bisimulation-measure (mred P t) (mred0 P t) bisim-red-red0 ta-bisim0 (τMOVE P)
(τMOVE0 P) (λe e'. False) (λ((e, es), h) ((e, es'), h). length es < length es')
proof
  show wfP (λe e'. False) by auto
next
  have wf {(x :: nat, y). x < y} by(rule wf-less)
  hence wf (inv-image {(x :: nat, y). x < y} (length o snd o fst)) by(rule wf-inv-image)
  also have inv-image {(x :: nat, y). x < y} (length o snd o fst) = {(x, y). (λ((e, es), h) ((e, es'), h).
length es < length es') x y} by auto
  finally show wfP (λ((e, es), h) ((e, es'), h). length es < length es')
    unfolding wfP-def .
next
  fix s1 s2 s1'
  assume bisim-red-red0 s1 s2 and red-mthr.silent-move P t s1 s1'
  moreover obtain e1 h1 x1 where s1: s1 = ((e1, x1), h1) by(cases s1, auto)
  moreover obtain e1' h1' x1' where s1': s1' = ((e1', x1'), h1') by(cases s1', auto)
  moreover obtain e es h2 where s2: s2 = ((e, es), h2) by(cases s2, auto)
  ultimately have bisim: bisim-red-red0 ((e1, x1), h1) ((e, es), h2)
    and red: P, t ⊢ ⟨e1, (h1, x1)⟩ -ε→ ⟨e1', (h1', x1')⟩
    and τ: τmove0 P h1 e1 by(auto elim: τtrsys.silent-move.cases)
  from bisim have heap: h1 = h2
    and fold: e1 = collapse (e, es)
    and x1: x1 = Map.empty
    and wf-state: wf-state (e, es)
    by(auto elim!: bisim-red-red0.cases)
  from τRed0r-inline-call-not-final[of P t h1 e es]
  obtain e' es' where red1: τRed0r P t h1 (e, es) (e', es')
    and final e' ⇒ es' = []
    and feq: collapse (e, es) = collapse (e', es') by blast
  have nfin: ¬ final e'
  proof
    assume fin: final e'
    hence es' = [] by(rule ⟨final e' ⇒ es' = []⟩)
    with fold fin feq have final e1 by simp
    with red show False by auto
  qed
  from red1 wf-state have wf-state': wf-state (e', es') by(rule τRed0r-preserves-wf-state[OF wf])
  hence fv: fvs (e' # es') = {} and icl: ∀ e ∈ set es'. is-call e by auto
  from red-fold-exs'[OF red[unfolded fold x1 feq] wf-state' nfin]
  obtain E' where e1': e1' = collapse (E', es')
    and red': P, t ⊢ ⟨e', (h1, Map.empty)⟩ -ε→ ⟨E', (h1', Map.empty)⟩ by auto
  from fv fv-collapse[of es e] wf-state fold feq have fv e1 = {} by(auto)
  with red-dom-lcl[OF red] x1 have x1': x1' = Map.empty by simp
  from red-red0-tabisim0[OF wf red]
  have red'': extTA2J0 P, P, t ⊢ ⟨e', (h1, Map.empty)⟩ -ε→ ⟨E', (h1', Map.empty)⟩ by simp
  show bisim-red-red0 s1' s2 ∧ (λe e'. False)++ s1' s1 ∨
    (∃ s2'. red0-mthr.silent-movet P t s2 s2' ∧ bisim-red-red0 s1' s2')
  proof(cases no-call P h1 e')
    case True
    with red'' have P, t ⊢ 0 ⟨e'/es', h1⟩ -ε→ ⟨E'/es', h1'⟩ unfolding no-call-def by(rule red0Red)
    moreover from red τ have [simp]: h1' = h1 by(auto dest: τmove0-heap-unchanged)
    moreover from τ fold feq icl nfin have τmove0 P h1 e' by(simp add: collapse-τmove0-inv)
    ultimately have τRed0 P t h1 (e', es') (E', es') using ⟨τmove0 P h1 e'⟩ by auto

```

with  $red1$  have  $\tau Red0t P t h1 (e, es) (E', es')$  **by**(rule  $rtrancplp\text{-}into\text{-}trancplp1$ )  
 moreover hence  $wf\text{-}state (E', es')$  **using**  $wf\text{-}state$  **by**(rule  $\tau Red0t\text{-}preserves\text{-}wf\text{-}state[OF\ wf]$ )  
 hence  $bisim\text{-}red\text{-}red0 ((e1', x1'), h1) ((E', es'), h1)$  **unfolding**  $x1' e1'$  **by**(auto)  
 ultimately show  $?thesis$  **using**  $s1 s1' s2 heap$  **by**  $simp(blast\ intro: \tau Red0t\text{-}into\text{-}silent\text{-}movet)$   
 next  
 case  $False$   
 then obtain  $a M vs$  **where**  $call: call\ e' = \lfloor (a, M, vs) \rfloor$   
 and  $notsynth: \neg synthesized\text{-}call\ P\ h1\ (a, M, vs)$  **by**(auto  $simp\ add: no\text{-}call\text{-}def$ )  
 from  $notsynth$  called-methodD[ $OF\ red''\ call$ ] **obtain**  $T D Us U pns body$   
 where  $h1' = h1$   
 and  $ha: typeof\text{-}addr\ h1\ a = \lfloor T \rfloor$   
 and  $sees: P \vdash class\text{-}type\text{-}of\ T\ sees\ M: Us \rightarrow U = \lfloor (pns, body) \rfloor$  in  $D$   
 and  $length: length\ vs = length\ pns\ length\ Us = length\ pns$   
**by**(auto)  
 let  $?e = blocks\ (this \# pns)\ (Class\ D \# Us)\ (Addr\ a \# vs)\ body$   
 from  $call\ ha$  have  $P, t \vdash 0 \langle e'/es', h1 \rangle -\varepsilon \rightarrow \langle ?e/e' \# es', h1 \rangle$   
**using**  $sees\ length$  **by**(rule  $red0Call$ )  
 moreover from  $\tau\ fold\ feq\ icl\ nfin\ False$  have  $\tau move0\ P\ h1\ e'$  **by**( $simp\ add: collapse\text{-}\tau move0\text{-}inv$ )  
 ultimately have  $\tau Red0\ P\ t\ h1\ (e', es')\ (?e, e' \# es')$  **by** auto  
 with  $red1$  have  $\tau Red0t\ P\ t\ h1\ (e, es)\ (?e, e' \# es')$  **by**(rule  $rtrancplp\text{-}into\text{-}trancplp1$ )  
 moreover {  
 from  $\langle P, t \vdash 0 \langle e'/es', h1 \rangle -\varepsilon \rightarrow \langle ?e/e' \# es', h1 \rangle \rangle$  have  $wf\text{-}state\ (?e, e' \# es')$   
**using**  $wf\text{-}state'$  **by**(rule  $red0\text{-}preserves\text{-}wf\text{-}state[OF\ wf]$ )  
 moreover from  $is\text{-}call\text{-}red\text{-}inline\text{-}callD[OF\ sees\ red'\ call]\ ha$   
 have  $E' = inline\text{-}call\ ?e\ e'$  **by** auto  
 ultimately have  $bisim\text{-}red\text{-}red0\ s1'\ ((?e, e' \# es'), h1')$  **unfolding**  $s1' e1' x1'$   
**by**(auto  $del: wf\text{-}state.cases\ wf\text{-}state.intros$ ) }  
 moreover from  $red'\ call\ notsynth$  have  $h1 = h1'$   
**by**(auto  $dest: is\text{-}call\text{-}red\text{-}state\text{-}unchanged$ )  
 ultimately show  $?thesis$  **unfolding**  $heap\ x1' x1\ s2\ s1' \langle h1' = h1 \rangle$   
**by**(blast  $intro: \tau Red0t\text{-}into\text{-}silent\text{-}movet$ )  
 qed  
 next  
 fix  $s1\ s2\ s2'$   
 assume  $bisim\text{-}red\text{-}red0\ s1\ s2$  and  $red0\text{-}mthr.silent\text{-}move\ P\ t\ s2\ s2'$   
 moreover obtain  $e1\ h1\ x1$  **where**  $s1: s1 = ((e1, x1), h1)$  **by**(cases  $s1$ , auto)  
 moreover obtain  $e' es' h2'$  **where**  $s2': s2' = ((e', es'), h2')$  **by**(cases  $s2'$ , auto)  
 moreover obtain  $e es h2$  **where**  $s2: s2 = ((e, es), h2)$  **by**(cases  $s2$ , auto)  
 ultimately have  $bisim: bisim\text{-}red\text{-}red0\ ((e1, x1), h1)\ ((e, es), h2)$   
 and  $red: P, t \vdash 0 \langle e/es, h2 \rangle -\varepsilon \rightarrow \langle e'/es', h2' \rangle$   
 and  $\tau: \tau move0\ P\ h2\ e \vee final\ e$  **by**(auto  $elim: \tau trsys.silent\text{-}move.cases$ )  
 from  $bisim$  have  $heap: h1 = h2$   
 and  $fold: e1 = collapse\ (e, es)$   
 and  $x1: x1 = Map.empty$  and  $wf\text{-}state: wf\text{-}state\ (e, es)$   
**by**(auto  $elim!: bisim\text{-}red\text{-}red0.cases$ )  
 from  $red\ wf\text{-}state$  have  $wf\text{-}state': wf\text{-}state\ (e', es')$  **by**(rule  $red0\text{-}preserves\text{-}wf\text{-}state[OF\ wf]$ )  
 from  $red$  show  $bisim\text{-}red\text{-}red0\ s1\ s2' \wedge (\lambda((e, es), h) ((e, es'), h). length\ es < length\ es')^{++} s2' s2$   
 $\vee$   
 ( $\exists s1'. red\text{-}mthr.silent\text{-}movet\ P\ t\ s1\ s1' \wedge bisim\text{-}red\text{-}red0\ s1' s2'$ )  
 proof cases  
 case ( $red0Red\ xs'$ )  
 hence  $[simp]: es' = es$   
 and  $extTA2J0\ P, P, t \vdash \langle e, (h2, Map.empty) \rangle -\varepsilon \rightarrow \langle e', (h2', xs') \rangle$  **by** auto  
 from  $red0\text{-}red\text{-}tabisim0[OF\ wf\ this(2)]$  have  $red': P, t \vdash \langle e, (h2, Map.empty) \rangle -\varepsilon \rightarrow \langle e', (h2', xs') \rangle$

by *auto*

moreover from *wf-state* have  $fv\ e = \{\}$  by *auto*  
 with *red-dom-lcl*[*OF red'*] *red-fv-subset*[*OF wf red'*] have  $xs' = Map.empty$  by *auto*  
 ultimately have  $P, t \vdash \langle e, (h2, Map.empty) \rangle -\varepsilon \rightarrow \langle e', (h2', Map.empty) \rangle$  by *simp*  
 hence  $P, t \vdash \langle collapse\ (e, es), (h2, Map.empty) \rangle -\varepsilon \rightarrow \langle collapse\ (e', es), (h2', Map.empty) \rangle$   
 using *wf-state* by (*rule red-fold-exs*)  
 moreover from *red'* have  $\neg final\ e$  by *auto*  
 with  $\tau\ wf\text{-}state\ fold$  have  $\tau move0\ P\ h2\ e1$  by (*auto simp add: collapse- $\tau move0$ -inv*)  
 ultimately have *red-mthr.silent-movet*  $P\ t\ s1\ ((collapse\ (e', es), Map.empty), h2')$   
 using *s1 fold  $\tau\ x1\ heap$*  by (*auto intro:  $\tau trsys.silent-move.intros$* )  
 moreover from *wf-state'* have *bisim-red-red0*  $((collapse\ (e', es), Map.empty), h2')\ s2'$   
 unfolding *s2'* by (*auto*)  
 ultimately show *?thesis* by *blast*

next

case (*red0Call a M vs U Ts T pns body D*)  
 hence [*simp*]:  $es' = e \# es\ h2' = h2\ e' = blocks\ (this\ \# pns)\ (Class\ D\ \# Ts)\ (Addr\ a\ \# vs)\ body$   
 and *call*:  $call\ e = [(a, M, vs)]$   
 and *ha*: *typeof-addr*  $h2\ a = [U]$   
 and *sees*:  $P \vdash class\text{-}type\text{-}of\ U\ sees\ M: Ts \rightarrow T = [(pns, body)]$  in *D*  
 and *len*:  $length\ vs = length\ pns\ length\ Ts = length\ pns$  by *auto*  
 from *is-call-red-inline-call*(1)[*OF sees len call, of (h2, Map.empty)*] *ha*  
 have  $P, t \vdash \langle e, (h2, Map.empty) \rangle -\varepsilon \rightarrow \langle inline\text{-}call\ e'\ e, (h2, Map.empty) \rangle$  by *simp*  
 hence  $P, t \vdash \langle collapse\ (e, es), (h2, Map.empty) \rangle -\varepsilon \rightarrow \langle collapse\ (inline\text{-}call\ e'\ e, es), (h2, Map.empty) \rangle$   
 using *wf-state* by (*rule red-fold-exs*)  
 moreover from *call ha wf-state  $\tau$*  have  $\tau move0\ P\ h2\ (collapse\ (e, es))$   
 by (*subst collapse- $\tau move0$ -inv*) *auto*  
 hence  $\tau MOVE\ P\ ((collapse\ (e, es), Map.empty), h2)\ \varepsilon\ ((collapse\ (inline\text{-}call\ e'\ e, es), Map.empty), h2)$  by *auto*  
 moreover from *wf-state'*  
 have *bisim-red-red0*  $((collapse\ (inline\text{-}call\ e'\ e, es), Map.empty), h2)\ ((e', es'), h2')$   
 by (*auto*)  
 ultimately show *?thesis* unfolding *s1 s2 s2' fold heap x1* by (*fastforce*)

next

case (*red0Return E*)  
 hence [*simp*]:  $es = E \# es'\ e' = inline\text{-}call\ e\ E\ h2' = h2$  by *auto*  
 from *fold wf-state'*  
 have *bisim-red-red0*  $((e1, Map.empty), h1)\ ((inline\text{-}call\ e\ E, es'), h2)$   
 unfolding *heap* by (*auto*)  
 thus *?thesis* using *s1 s2' s2 x1* by *auto*

qed

next

fix *s1 s2 ta1 s1'*

assume *bisim-red-red0 s1 s2* and *mred P t s1 ta1 s1'* and  $\neg \tau MOVE\ P\ s1\ ta1\ s1'$   
 moreover obtain *e1 h1 x1* where *s1*:  $s1 = ((e1, x1), h1)$  by (*cases s1, auto*)  
 moreover obtain *e1' h1' x1'* where *s1'*:  $s1' = ((e1', x1'), h1')$  by (*cases s1', auto*)  
 moreover obtain *e es h2* where *s2*:  $s2 = ((e, es), h2)$  by (*cases s2, auto*)  
 ultimately have *bisim*: *bisim-red-red0*  $((e1, x1), h1)\ ((e, es), h2)$   
 and *red*:  $P, t \vdash \langle e1, (h1, x1) \rangle -ta1 \rightarrow \langle e1', (h1', x1') \rangle$   
 and  $\tau$ :  $\neg \tau move0\ P\ h1\ e1$  by (*auto dest: red- $\tau$ -taD*[*where extTA=extTA2J P, OF extTA2J- $\varepsilon$* ])  
 from *bisim* have *heap*:  $h1 = h2$   
 and *fold*:  $e1 = collapse\ (e, es)$   
 and *x1*:  $x1 = Map.empty$   
 and *wf-state*: *wf-state*  $(e, es)$



```

  by(auto elim!: bisim-red-red0.cases)
from  $\tau\text{Red0r-inline-call-not-final}[of\ P\ t\ h1\ e\ es]$ 
obtain  $e'\ es'$  where  $red1: \tau\text{Red0r}\ P\ t\ h1\ (e, es)\ (e', es')$ 
  and  $final\ e' \implies es' = []$  and  $feq: collapse\ (e, es) = collapse\ (e', es')$  by blast
hence  $red1': red0\text{-mthr.silent-moves}\ P\ t\ ((e, es), h2)\ ((e', es'), h2)$ 
  unfolding heap by  $-(rule\ \tau\text{Red0r-into-silent-moves})$ 
have  $nfin: \neg final\ e'$ 
proof
  assume  $fin: final\ e'$ 
  hence  $es' = []$  by  $(rule\ \langle final\ e' \implies es' = [] \rangle)$ 
  with fold fin feq have final e1 by simp
  with red show False by auto
qed
from  $red1\ wf\text{-state}$  have  $wf\text{-state}': wf\text{-state}\ (e', es')$  by  $(rule\ \tau\text{Red0r-preserves-wf-state}[OF\ wf])$ 
hence  $fv: fvs\ (e' \# es') = \{\}$  and  $icl: \forall e \in set\ es'. is\text{-call}\ e$  by auto
from  $red\text{-fold-exs}'[OF\ red[unfolded\ fold\ x1\ feq]\ wf\text{-state}'\ nfin]$ 
obtain  $E'$  where  $e1': e1' = collapse\ (E', es')$ 
  and  $red': P, t \vdash \langle e', (h1, Map.empty) \rangle -ta1 \rightarrow \langle E', (h1', Map.empty) \rangle$  by auto
from  $fv\ fv\text{-collapse}[OF\ icl, of\ e']\ fold\ feq$  have  $fv\ e1 = \{\}$  by(auto)
with  $red\text{-dom-lcl}[OF\ red]\ x1$  have  $x1': x1' = Map.empty$  by simp
from  $red\text{-red0-tabisim0}[OF\ wf\ red']$  obtain  $ta2$ 
  where  $red'': extTA2JO\ P, P, t \vdash \langle e', (h1, Map.empty) \rangle -ta2 \rightarrow \langle E', (h1', Map.empty) \rangle$ 
  and  $tasim: ta\text{-bisim0}\ ta1\ ta2$  by auto
from  $\tau\ fold\ feq\ icl\ nfin$  have  $\neg \tau\text{move0}\ P\ h1\ e'$  by  $(simp\ add: collapse\text{-}\tau\text{move0-inv})$ 
hence  $\forall aMvs. call\ e' = [aMvs] \longrightarrow synthesized\text{-call}\ P\ h1\ aMvs$ 
  by  $(auto\ dest: \tau\text{move0-callID})$ 
with  $red''$  have  $red''': P, t \vdash 0\ \langle e'/es', h1 \rangle -ta2 \rightarrow \langle E'/es', h1' \rangle$  by  $(rule\ red0Red)$ 
moreover from  $\tau\ fold\ feq\ icl\ nfin$  have  $\neg \tau\text{move0}\ P\ h1\ e'$  by  $(simp\ add: collapse\text{-}\tau\text{move0-inv})$ 
hence  $\neg \tau\text{MOVE0}\ P\ ((e', es'), h1)\ ta2\ ((E', es'), h1')$  using nfin by auto
moreover from  $red''' wf\text{-state}'$  have  $wf\text{-state}\ (E', es')$  by  $(rule\ red0\text{-preserves-wf-state}[OF\ wf])$ 
hence  $bisim\text{-red-red0}\ s1'\ ((E', es'), h1')$  unfolding  $s1'\ e1'\ x1'$  by(auto)
ultimately show  $\exists s2'\ s2''\ ta2. red0\text{-mthr.silent-moves}\ P\ t\ s2\ s2' \wedge mred0\ P\ t\ s2'\ ta2\ s2'' \wedge$ 
   $\neg \tau\text{MOVE0}\ P\ s2'\ ta2\ s2'' \wedge bisim\text{-red-red0}\ s1'\ s2'' \wedge ta\text{-bisim0}\ ta1\ ta2$ 
  using  $tasim\ red1'\ heap\ unfolding\ s1'\ s2$  by  $-(rule\ exI\ conjI|assumption|auto)+$ 
next
  fix  $s1\ s2\ ta2\ s2'$ 
  assume  $bisim\text{-red-red0}\ s1\ s2$  and  $mred0\ P\ t\ s2\ ta2\ s2' \neg \tau\text{MOVE0}\ P\ s2\ ta2\ s2'$ 
  from  $red\text{-simulates-red0}[OF\ wf\ this]$ 
  show  $\exists s1'\ s1''\ ta1. red\text{-mthr.silent-moves}\ P\ t\ s1\ s1' \wedge mred\ P\ t\ s1'\ ta1\ s1'' \wedge$ 
     $\neg \tau\text{MOVE}\ P\ s1'\ ta1\ s1'' \wedge bisim\text{-red-red0}\ s1''\ s2' \wedge ta\text{-bisim0}\ ta1\ ta2$ 
    by  $(blast\ intro: rtranclp.rtrancl\ refl)$ 
qed
lemma delay-bisimulation-diverge-red-red0:
  assumes  $wf\text{-J-prog}\ P$ 
  shows  $delay\text{-bisimulation-diverge}\ (mred\ P\ t)\ (mred0\ P\ t)\ bisim\text{-red-red0}\ ta\text{-bisim0}\ (\tau\text{MOVE}\ P)$ 
   $(\tau\text{MOVE0}\ P)$ 
proof -
  interpret  $delay\text{-bisimulation-measure}$ 
   $mred\ P\ t\ mred0\ P\ t\ bisim\text{-red-red0}\ ta\text{-bisim0}\ \tau\text{MOVE}\ P\ \tau\text{MOVE0}\ P$ 
   $\lambda e\ e'. False\ \lambda((e, es), h)\ ((e, es'), h). length\ es < length\ es'$ 
  using  $assms$  by  $(rule\ delay\text{-bisimulation-measure-red-red0})$ 
  show ?thesis by  $unfold\ locales$ 
qed

```

**lemma** *bisim-red-red0-finalD*:

**assumes** *bisim*: *bisim-red-red0* (*x1*, *m1*) (*x2*, *m2*)  
**and** *final-expr* *x1*  
**shows**  $\exists x2'. \text{red0-mthr.silent-moves } P \ t \ (x2, m2) \ (x2', m2) \wedge \text{bisim-red-red0} \ (x1, m1) \ (x2', m2)$   
 $\wedge \text{final-expr0 } x2'$   
**proof** –  
**from** *bisim*  
**obtain** *e' e es* **where** *wf-state*: *wf-state* (*e*, *es*)  
**and** [*simp*]: *x1* = (*e'*, *Map.empty*) *x2* = (*e*, *es*) *e'* = *collapse* (*e*, *es*) *m2* = *m1*  
**by** *cases*(*cases* *x2*, *auto*)  
**from**  $\langle \text{final-expr } x1 \rangle$  **have** *final* (*collapse* (*e*, *es*)) **by** *simp*  
**moreover from** *wf-state* **have**  $\forall e \in \text{set } es. \text{is-call } e$  **by** *auto*  
**ultimately have** *red0-mthr.silent-moves* *P t* ((*e*, *es*), *m1*) ((*collapse* (*e*, *es*), []), *m1*)  
**proof**(*induction es arbitrary: e*)  
**case** *Nil* **thus** ?*case* **by** *simp*  
**next**  
**case** (*Cons e' es*)  
**from**  $\langle \text{final } (\text{collapse } (e, e' \# es)) \rangle$  **have** *final* (*collapse* (*inline-call e e'*, *es*)) **by** *simp*  
**moreover from**  $\langle \forall e \in \text{set } (e' \# es). \text{is-call } e \rangle$  **have**  $\forall e \in \text{set } es. \text{is-call } e$  **by** *simp*  
**ultimately have** *red0-mthr.silent-moves* *P t* ((*inline-call e e'*, *es*), *m1*) ((*collapse* (*inline-call e e'*, *es*), []), *m1*)  
**by**(*rule Cons.IH*)  
**moreover from**  $\langle \text{final } (\text{collapse } (e, e' \# es)) \rangle$   $\langle \forall e \in \text{set } (e' \# es). \text{is-call } e \rangle$   
**have** *final e* **by**(*rule collapse-finalD*)  
**hence** *P, t*  $\vdash 0 \langle e/e' \# es, m1 \rangle -\varepsilon \rightarrow \langle \text{inline-call } e \ e'/es, m1 \rangle$  **by**(*rule red0Return*)  
**with**  $\langle \text{final } e \rangle$  **have** *red0-mthr.silent-move* *P t* ((*e*, *e' # es*), *m1*) ((*inline-call e e'*, *es*), *m1*) **by** *auto*  
**ultimately show** ?*case* **by** –(*erule converse-rtranclp-into-rtranclp, simp*)  
**qed**  
**moreover have** *bisim-red-red0* ((*collapse* (*e*, *es*), *Map.empty*), *m1*) ((*collapse* (*e*, *es*), []), *m1*)  
**using**  $\langle \text{final } (\text{collapse } (e, es)) \rangle$  **by**(*auto intro!: bisim-red-red0I*)  
**ultimately show** ?*thesis* **using**  $\langle \text{final } (\text{collapse } (e, es)) \rangle$  **by** *auto*  
**qed**

**lemma** *red0-simulates-red-not-final*:

**assumes** *wuf*: *wuf-J-prog P*  
**assumes** *bisim*: *bisim-red-red0* ((*e*, *xs*), *h*) ((*e0*, *es0*), *h0*)  
**and** *red*: *P, t*  $\vdash \langle e, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle$   
**and** *fin*:  $\neg \text{final } e0$   
**and** *nτ*:  $\neg \tau \text{move0 } P \ h \ e$   
**shows**  $\exists e0' \ ta0. P, t \vdash 0 \langle e0/es0, h \rangle -ta0 \rightarrow \langle e0'/es0, h' \rangle \wedge \text{bisim-red-red0} \ ((e', xs'), h') \ ((e0', es0), h') \wedge \text{ta-bisim0 } ta \ ta0$   
**proof** –  
**from** *bisim* **have** [*simp*]: *xs* = *Map.empty* *h0* = *h* **and** *e*: *e* = *collapse* (*e0*, *es0*)  
**and** *wfs*: *wf-state* (*e0*, *es0*) **by**(*auto elim!: bisim-red-red0.cases*)  
**with** *red* **have** *P, t*  $\vdash \langle \text{collapse } (e0, es0), (h, \text{Map.empty}) \rangle -ta \rightarrow \langle e', (h', xs') \rangle$  **by** *simp*  
**from** *wfs red-fold-exs'[OF this]* *fin* **obtain** *e0'* **where** *e'*: *e'* = *collapse* (*e0'*, *es0*)  
**and** *red'*: *P, t*  $\vdash \langle e0, (h, \text{Map.empty}) \rangle -ta \rightarrow \langle e0', (h', \text{Map.empty}) \rangle$  **by**(*auto*)  
**from** *wfs fv-collapse[of es0, of e0]* *e* **have** *fv e* = {} **by**(*auto*)  
**with** *red-dom-lcl[OF red]* **have** [*simp*]: *xs'* = *Map.empty* **by** *simp*  
**from** *red-red0-tabisim0[OF wuf red]* **obtain** *ta0*  
**where** *red''*: *extTA2J0 P, P, t*  $\vdash \langle e0, (h, \text{Map.empty}) \rangle -ta0 \rightarrow \langle e0', (h', \text{Map.empty}) \rangle$   
**and** *tasim*: *ta-bisim0 ta ta0* **by** *auto*  
**from** *nτ e wfs fin* **have**  $\neg \tau \text{move0 } P \ h \ e0$  **by**(*auto simp add: collapse-τmove0-inv*)

hence  $\forall aMvs. \text{call } e0 = \lfloor aMvs \rfloor \longrightarrow \text{synthesized-call } P \ h \ aMvs$   
 by(auto dest:  $\tau\text{move0-callD}$ )  
 with  $\text{red}''$  have  $\text{red}''' : P, t \vdash 0 \langle e0/es0, h \rangle -ta0 \rightarrow \langle e0'/es0, h' \rangle$  by(rule  $\text{red0Red}$ )  
 moreover from  $\text{red}'''$  wfs have  $\text{wf-state } (e0', es0)$  by(rule  $\text{red0-preserves-wf-state}[OF \text{ wwf}]$ )  
 hence  $\text{bisim-red-red0 } ((e', xs'), h') ((e0', es0), h')$  unfolding  $e'$  by(auto)  
 ultimately show ?thesis using  $\text{tasim}$  by(auto simp del:  $\text{split-paired-Ex}$ )  
 qed

**lemma**  $\text{red-red0-FWbisim}$ :  
 assumes  $\text{wf: wwf-J-prog } P$   
 shows  $\text{FWdelay-bisimulation-diverge final-expr (mred } P) \text{ final-expr0 (mred0 } P)$   
 $(\lambda t. \text{bisim-red-red0}) (\lambda xes (e0, es0). \neg \text{final } e0) (\tau\text{MOVE } P) (\tau\text{MOVE0 } P)$   
**proof** –  
 interpret  $\text{delay-bisimulation-diverge mred } P \ t \ \text{mred0 } P \ t \ \text{bisim-red-red0 } ta\text{-bisim0 } \tau\text{MOVE } P \ \tau\text{MOVE0 } P$   
 for  $t$  by(rule  $\text{delay-bisimulation-diverge-red-red0}[OF \text{ wf}]$ )  
 show ?thesis  
**proof**  
 fix  $t$  and  $s1 :: (('addr \text{expr} \times 'addr \text{locals}) \times 'heap)$  and  $s2 :: (('addr \text{expr} \times 'addr \text{expr list}) \times 'heap)$   
 assume  $\text{bisim-red-red0 } s1 \ s2 \ (\lambda(x1, m). \text{final-expr } x1) \ s1$   
 moreover obtain  $x1 \ m1$  where  $[simp]: s1 = (x1, m1)$  by(cases  $s1$ )  
 moreover obtain  $x2 \ m2$  where  $[simp]: s2 = (x2, m2)$  by(cases  $s2$ )  
 ultimately have  $\text{bisim-red-red0 } (x1, m1) (x2, m2) \text{final-expr } x1$  by simp-all  
 from  $\text{bisim-red-red0-finalD}[OF \text{ this, of } P \ t]$   
 show  $\exists s2'. \text{red0-mthr.silent-moves } P \ t \ s2 \ s2' \wedge \text{bisim-red-red0 } s1 \ s2' \wedge (\lambda(x2, m). \text{final-expr0 } x2) \ s2'$  by auto  
 next  
 fix  $t$  and  $s1 :: (('addr \text{expr} \times 'addr \text{locals}) \times 'heap)$  and  $s2 :: (('addr \text{expr} \times 'addr \text{expr list}) \times 'heap)$   
 assume  $\text{bisim-red-red0 } s1 \ s2 \ (\lambda(x2, m). \text{final-expr0 } x2) \ s2$   
 moreover obtain  $x1 \ m1$  where  $[simp]: s1 = (x1, m1)$  by(cases  $s1$ )  
 moreover obtain  $x2 \ m2$  where  $[simp]: s2 = (x2, m2)$  by(cases  $s2$ )  
 ultimately have  $\text{bisim-red-red0 } (x1, m1) (x2, m2) \text{final-expr0 } x2$  by simp-all  
 moreover hence  $\text{final-expr } x1$  by(rule  $\text{bisim-red-red0-final0D}$ )  
 ultimately show  $\exists s1'. \text{red-mthr.silent-moves } P \ t \ s1 \ s1' \wedge \text{bisim-red-red0 } s1' \ s2 \wedge (\lambda(x1, m). \text{final-expr } x1) \ s1'$  by auto  
 next  
 fix  $t' \ x \ m1 \ xx \ m2 \ t \ x1 \ x2 \ x1' \ ta1 \ x1'' \ m1' \ x2' \ ta2 \ x2'' \ m2'$   
 assume  $b: \text{bisim-red-red0 } (x, m1) (xx, m2)$  and  $bo: \text{bisim-red-red0 } (x1, m1) (x2, m2)$   
 and  $\text{red-mthr.silent-moves } P \ t \ (x1, m1) (x1', m1)$   
 and  $\text{red1: mred } P \ t \ (x1', m1) \ ta1 \ (x1'', m1')$  and  $\neg \tau\text{MOVE } P \ (x1', m1) \ ta1 \ (x1'', m1')$   
 and  $\text{red0-mthr.silent-moves } P \ t \ (x2, m2) (x2', m2)$   
 and  $\text{red2: mred0 } P \ t \ (x2', m2) \ ta2 \ (x2'', m2')$  and  $\neg \tau\text{MOVE0 } P \ (x2', m2) \ ta2 \ (x2'', m2')$   
 and  $bo': \text{bisim-red-red0 } (x1'', m1') (x2'', m2')$   
 and  $tb: ta\text{-bisim0 } ta1 \ ta2$   
 from  $b$  have  $m1 = m2$  by(auto elim:  $\text{bisim-red-red0.cases}$ )  
 moreover from  $bo'$  have  $m1' = m2'$  by(auto elim:  $\text{bisim-red-red0.cases}$ )  
 ultimately show  $\text{bisim-red-red0 } (x, m1') (xx, m2')$  using  $b$   
 by(auto elim:  $\text{bisim-red-red0.cases}$ )  
 next  
 fix  $t \ x1 \ m1 \ x2 \ m2 \ x1' \ ta1 \ x1'' \ m1' \ x2' \ ta2 \ x2'' \ m2' \ w$   
 assume  $b: \text{bisim-red-red0 } (x1, m1) (x2, m2)$

```

and red-mthr.silent-moves  $P\ t\ (x1, m1)\ (x1', m1)$ 
and red1:  $mred\ P\ t\ (x1', m1)\ ta1\ (x1'', m1')$  and  $\neg \tau MOVE\ P\ (x1', m1)\ ta1\ (x1'', m1')$ 
and red0-mthr.silent-moves  $P\ t\ (x2, m2)\ (x2', m2)$ 
and red2:  $mred0\ P\ t\ (x2', m2)\ ta2\ (x2'', m2')$  and  $\neg \tau MOVE0\ P\ (x2', m2)\ ta2\ (x2'', m2')$ 
and b': bisim-red-red0  $(x1'', m1')\ (x2'', m2')$  and ta-bisim0  $ta1\ ta2$ 
and Suspend:  $Suspend\ w \in set\ \{\!\{ta1\}\!\}_w\ Suspend\ w \in set\ \{\!\{ta2\}\!\}_w$ 
hence  $(\lambda exs\ (e0, es0). is-call\ e0)\ x1''\ x2''$ 
by(cases  $x1'$ )(cases  $x2'$ , auto dest: Red-Suspend-is-call simp add: final-iff)
thus  $(\lambda exs\ (e0, es0). \neg final\ e0)\ x1''\ x2''$  by(auto simp add: final-iff is-call-def)
next
fix  $t\ x1\ m1\ x2\ m2\ ta1\ x1'\ m1'$ 
assume b: bisim-red-red0  $(x1, m1)\ (x2, m2)$ 
and c:  $(\lambda(e0, es0). \neg final\ e0)\ x2$ 
and red1:  $mred\ P\ t\ (x1, m1)\ ta1\ (x1', m1')$ 
and wakeup:  $Notified \in set\ \{\!\{ta1\}\!\}_w \vee WokenUp \in set\ \{\!\{ta1\}\!\}_w$ 
from c have  $\neg final\ (fst\ x2)$  by(auto simp add: is-call-def)
moreover from red1 wakeup have  $\neg \tau move0\ P\ m1\ (fst\ x1)$ 
by(cases  $x1$ )(auto dest: red- $\tau$ -taD[where  $extTA=extTA2J\ P$ , simplified] simp add: ta-upd-simps)
moreover from b have  $m2 = m1$  by(cases) auto
ultimately obtain  $e0'\ ta0$  where  $P, t \vdash 0\ \langle fst\ x2 / snd\ x2, m2 \rangle - ta0 \rightarrow \langle e0' / snd\ x2, m1' \rangle$ 
bisim-red-red0  $((fst\ x1', snd\ x1'), m1')\ ((e0', snd\ x2), m1')$  ta-bisim0  $ta1\ ta0$ 
using red0-simulates-red-not-final[OF wf, of  $fst\ x1\ snd\ x1\ m1\ fst\ x2\ snd\ x2\ m2\ t\ ta1\ fst\ x1'\ m1'\$ 
snd  $x1'$ ]
using b red1 by(auto simp add: split-beta)
thus  $\exists ta2\ x2'\ m2'. mred\ P\ t\ (x2, m2)\ ta2\ (x2', m2') \wedge bisim-red-red0\ (x1', m1')\ (x2', m2') \wedge$ 
ta-bisim0  $ta1\ ta2$ 
by(cases  $ta0$ )(fastforce simp add: split-beta)
next
fix  $t\ x1\ m1\ x2\ m2\ ta2\ x2'\ m2'$ 
assume b: bisim-red-red0  $(x1, m1)\ (x2, m2)$ 
and c:  $(\lambda(e0, es0). \neg final\ e0)\ x2$ 
and red2:  $mred0\ P\ t\ (x2, m2)\ ta2\ (x2', m2')$ 
and wakeup:  $Notified \in set\ \{\!\{ta2\}\!\}_w \vee WokenUp \in set\ \{\!\{ta2\}\!\}_w$ 
from b have [simp]:  $m1 = m2$  by cases auto
with red-simulates-red0[OF wf b red2] wakeup obtain  $s1'\ ta1$ 
where  $mred\ P\ t\ (x1, m1)\ ta1\ s1'\ bisim-red-red0\ s1'\ (x2', m2')\ ta-bisim0\ ta1\ ta2$ 
by(fastforce simp add: split-paired-Ex)
moreover from  $\langle bisim-red-red0\ s1'\ (x2', m2') \rangle$  have  $m2' = snd\ s1'$  by cases auto
ultimately
show  $\exists ta1\ x1'\ m1'. mred\ P\ t\ (x1, m1)\ ta1\ (x1', m1') \wedge bisim-red-red0\ (x1', m1')\ (x2', m2') \wedge$ 
ta-bisim0  $ta1\ ta2$ 
by(cases  $ta1$ )(fastforce simp add: split-beta)
next
show  $(\exists x. final-expr\ x) \longleftrightarrow (\exists x. final-expr0\ x)$ 
by(auto simp add: final-iff)
qed
qed
end

sublocale J-heap-base < red-red0:
FWdelay-bisimulation-base
final-expr
mred P

```

```

    final-expr0
    mred0 P
    convert-RA
    λt. bisim-red-red0
    λexs (e0, es0). ¬ final e0
    τMOVE P τMOVE0 P
  for P
by(unfold-locales)

context J-heap-base begin

lemma bisim-J-J0-start:
  assumes wf: wwf-J-prog P
  and wf-start: wf-start-state P C M vs
  shows red-red0.mbisim (J-start-state P C M vs) (J0-start-state P C M vs)
proof -
  from wf-start obtain Ts T pns body D
  where sees: P ⊢ C sees M:Ts→T=[(pns,body)] in D
  and conf: P,start-heap ⊢ vs [≤] Ts
  by cases auto

  from conf have vs: length vs = length Ts by(rule list-all2-lengthD)
  from sees-wf-mdecl[OF wf sees]
  have wwfCM: wwf-J-mdecl P D (M, Ts, T, pns, body)
  and len: length pns = length Ts by(auto simp add: wf-mdecl-def)
  from wwfCM have ftbody: fv body ⊆ {this} ∪ set pns
  and pns: length pns = length Ts by simp-all
  with vs len have fv: fv (blocks pns Ts vs body) ⊆ {this} by auto
  with len vs sees show ?thesis unfolding start-state-def
    by(auto intro!: red-red0.mbisimI)(auto intro!: bisim-red-red0.intros wset-thread-okI simp add:
is-call-def split: if-split-asm)
qed

end

end

```

## 7.4 The intermediate language J1

```

theory J1State imports
  ../J/State
  CallExpr
begin

type-synonym
  'addr expr1 = (nat, nat, 'addr) exp

type-synonym
  'addr J1-prog = 'addr expr1 prog

type-synonym
  'addr locals1 = 'addr val list

```

**translations**

(type) 'addr expr1 <= (type) (nat, nat, 'addr) exp  
 (type) 'addr J1-prog <= (type) 'addr expr1 prog

**type-synonym**

'addr J1state = ('addr expr1 × 'addr locals1) list

**type-synonym**

('addr, 'thread-id, 'heap) J1-thread-action =  
 ('addr, 'thread-id, ('addr expr1 × 'addr locals1) × ('addr expr1 × 'addr locals1) list, 'heap) Jinja-thread-action

**type-synonym**

('addr, 'thread-id, 'heap) J1-state =  
 ('addr, 'thread-id, ('addr expr1 × 'addr locals1) × ('addr expr1 × 'addr locals1) list, 'heap, 'addr) state

**print-translation** <

```

let
  fun tr'
    [a1, t
    , Const (@{type-syntax prod}, -) $
      (Const (@{type-syntax prod}, -) $
        (Const (@{type-syntax exp}, -) $ Const (@{type-syntax nat}, -) $ Const (@{type-syntax
nat}, -) $ a2) $
        (Const (@{type-syntax list}, -) $ (Const (@{type-syntax val}, -) $ a3))) $
        (Const (@{type-syntax list}, -) $
          (Const (@{type-syntax prod}, -) $
            (Const (@{type-syntax exp}, -) $ Const (@{type-syntax nat}, -) $ Const (@{type-syntax
nat}, -) $ a4) $
            (Const (@{type-syntax list}, -) $ (Const (@{type-syntax val}, -) $ a5))))
        , h] =
      if a1 = a2 andalso a2 = a3 andalso a3 = a4 andalso a4 = a5
      then Syntax.const @{type-syntax J1-thread-action} $ a1 $ t $ h
      else raise Match;
    in [(@{type-syntax Jinja-thread-action}, K tr')]
  end
>
typ ('addr, 'thread-id, 'heap) J1-thread-action

```

**print-translation** <

```

let
  fun tr'
    [a1, t
    , Const (@{type-syntax prod}, -) $
      (Const (@{type-syntax prod}, -) $
        (Const (@{type-syntax exp}, -) $ Const (@{type-syntax nat}, -) $ Const (@{type-syntax
nat}, -) $ a2) $
        (Const (@{type-syntax list}, -) $ (Const (@{type-syntax val}, -) $ a3))) $
        (Const (@{type-syntax list}, -) $
          (Const (@{type-syntax prod}, -) $
            (Const (@{type-syntax exp}, -) $ Const (@{type-syntax nat}, -) $ Const (@{type-syntax
nat}, -) $ a4) $
            (Const (@{type-syntax list}, -) $ (Const (@{type-syntax val}, -) $ a5))))
        , h] =
      if a1 = a2 andalso a2 = a3 andalso a3 = a4 andalso a4 = a5
      then Syntax.const @{type-syntax J1-thread-action} $ a1 $ t $ h
      else raise Match;
    in [(@{type-syntax Jinja-thread-action}, K tr')]
  end
>
typ ('addr, 'thread-id, 'heap) J1-thread-action

```

```

    , h, a6] =
    if a1 = a2 andalso a2 = a3 andalso a3 = a4 andalso a4 = a5 andalso a5 = a6
    then Syntax.const @{\type-syntax J1-state} $ a1 $ t $ h
    else raise Match;
    in [(@{\type-syntax state}, K tr')]
  end
>
typ ('addr, 'thread-id, 'heap) J1-state

fun blocks1 :: nat  $\Rightarrow$  ty list  $\Rightarrow$  (nat,'b,'addr) exp  $\Rightarrow$  (nat,'b,'addr) exp
where
  blocks1 n [] e = e
| blocks1 n (T#Ts) e = {n:T=None; blocks1 (Suc n) Ts e}

primrec max-vars:: ('a,'b,'addr) exp  $\Rightarrow$  nat
and max-varss:: ('a,'b,'addr) exp list  $\Rightarrow$  nat
where
  max-vars (new C) = 0
| max-vars (newA T[e]) = max-vars e
| max-vars (Cast C e) = max-vars e
| max-vars (e instanceof T) = max-vars e
| max-vars (Val v) = 0
| max-vars (e «bop» e') = max (max-vars e) (max-vars e')
| max-vars (Var V) = 0
| max-vars (V:=e) = max-vars e
| max-vars (a[i]) = max (max-vars a) (max-vars i)
| max-vars (AAss a i e) = max (max (max-vars a) (max-vars i)) (max-vars e)
| max-vars (a.length) = max-vars a
| max-vars (e.F{D}) = max-vars e
| max-vars (FAss e1 F D e2) = max (max-vars e1) (max-vars e2)
| max-vars (e.compareAndSwap(D.F, e', e'')) = max (max (max-vars e) (max-vars e')) (max-vars e'')
| max-vars (e.M(es)) = max (max-vars e) (max-varss es)
| max-vars ({V:T=vo; e}) = max-vars e + 1
— sync and insync will need an extra local variable when compiling to bytecode to store the object
that is being synchronized on until its release
| max-vars (syncV (e') e) = max (max-vars e') (max-vars e + 1)
| max-vars (insyncV (a) e) = max-vars e + 1
| max-vars (e1;;e2) = max (max-vars e1) (max-vars e2)
| max-vars (if (e) e1 else e2) =
  max (max-vars e) (max (max-vars e1) (max-vars e2))
| max-vars (while (b) e) = max (max-vars b) (max-vars e)
| max-vars (throw e) = max-vars e
| max-vars (try e1 catch(C V) e2) = max (max-vars e1) (max-vars e2 + 1)

| max-varss [] = 0
| max-varss (e#es) = max (max-vars e) (max-varss es)

```

— Indices in blocks increase by 1

```

primrec B :: 'addr expr1  $\Rightarrow$  nat  $\Rightarrow$  bool
and Bs :: 'addr expr1 list  $\Rightarrow$  nat  $\Rightarrow$  bool
where
  B (new C) i = True
| B (newA T[e]) i = B e i

```

```

|  $\mathcal{B} \text{ (Cast } C \text{ } e) \text{ } i = \mathcal{B} \text{ } e \text{ } i$ 
|  $\mathcal{B} \text{ (e instanceof } T) \text{ } i = \mathcal{B} \text{ } e \text{ } i$ 
|  $\mathcal{B} \text{ (Val } v) \text{ } i = \text{True}$ 
|  $\mathcal{B} \text{ (e1 «bop» } e2) \text{ } i = (\mathcal{B} \text{ } e1 \text{ } i \wedge \mathcal{B} \text{ } e2 \text{ } i)$ 
|  $\mathcal{B} \text{ (Var } j) \text{ } i = \text{True}$ 
|  $\mathcal{B} \text{ (j:=e) } i = \mathcal{B} \text{ } e \text{ } i$ 
|  $\mathcal{B} \text{ (a[j]) } i = (\mathcal{B} \text{ } a \text{ } i \wedge \mathcal{B} \text{ } j \text{ } i)$ 
|  $\mathcal{B} \text{ (a[j]:=e) } i = (\mathcal{B} \text{ } a \text{ } i \wedge \mathcal{B} \text{ } j \text{ } i \wedge \mathcal{B} \text{ } e \text{ } i)$ 
|  $\mathcal{B} \text{ (a.length) } i = \mathcal{B} \text{ } a \text{ } i$ 
|  $\mathcal{B} \text{ (e.F\{D\}) } i = \mathcal{B} \text{ } e \text{ } i$ 
|  $\mathcal{B} \text{ (e1.F\{D\} := e2) } i = (\mathcal{B} \text{ } e1 \text{ } i \wedge \mathcal{B} \text{ } e2 \text{ } i)$ 
|  $\mathcal{B} \text{ (e.compareAndSwap(D.F, e', e'')) } i = (\mathcal{B} \text{ } e \text{ } i \wedge \mathcal{B} \text{ } e' \text{ } i \wedge \mathcal{B} \text{ } e'' \text{ } i)$ 
|  $\mathcal{B} \text{ (e.M(es)) } i = (\mathcal{B} \text{ } e \text{ } i \wedge \mathcal{B} \text{ } s \text{ } es \text{ } i)$ 
|  $\mathcal{B} \text{ (\{j:T=vo; e\}) } i = (i = j \wedge \mathcal{B} \text{ } e \text{ } (i+1))$ 
|  $\mathcal{B} \text{ (sync}_V \text{ (o') } e) \text{ } i = (i = V \wedge \mathcal{B} \text{ } o' \text{ } i \wedge \mathcal{B} \text{ } e \text{ } (i+1))$ 
|  $\mathcal{B} \text{ (insync}_V \text{ (a) } e) \text{ } i = (i = V \wedge \mathcal{B} \text{ } e \text{ } (i+1))$ 
|  $\mathcal{B} \text{ (e1;;e2) } i = (\mathcal{B} \text{ } e1 \text{ } i \wedge \mathcal{B} \text{ } e2 \text{ } i)$ 
|  $\mathcal{B} \text{ (if (e) e1 else e2) } i = (\mathcal{B} \text{ } e \text{ } i \wedge \mathcal{B} \text{ } e1 \text{ } i \wedge \mathcal{B} \text{ } e2 \text{ } i)$ 
|  $\mathcal{B} \text{ (throw } e) \text{ } i = \mathcal{B} \text{ } e \text{ } i$ 
|  $\mathcal{B} \text{ (while (e) c) } i = (\mathcal{B} \text{ } e \text{ } i \wedge \mathcal{B} \text{ } c \text{ } i)$ 
|  $\mathcal{B} \text{ (try e1 catch(C j) } e2) \text{ } i = (\mathcal{B} \text{ } e1 \text{ } i \wedge i=j \wedge \mathcal{B} \text{ } e2 \text{ } (i+1))$ 

|  $\mathcal{B} s \text{ [] } i = \text{True}$ 
|  $\mathcal{B} s \text{ (e\#es) } i = (\mathcal{B} \text{ } e \text{ } i \wedge \mathcal{B} s \text{ } es \text{ } i)$ 

```

Variables for monitor addresses do not occur freely in synchronization blocks

```

primrec syncvars :: ('a, 'a, 'addr) exp  $\Rightarrow$  bool
and syncvarss :: ('a, 'a, 'addr) exp list  $\Rightarrow$  bool
where
  syncvars (new C) = True
| syncvars (newA T[e]) = syncvars e
| syncvars (Cast T e) = syncvars e
| syncvars (e instanceof T) = syncvars e
| syncvars (Val v) = True
| syncvars (e1 «bop» e2) = (syncvars e1  $\wedge$  syncvars e2)
| syncvars (Var V) = True
| syncvars (V:=e) = syncvars e
| syncvars (a[i]) = (syncvars a  $\wedge$  syncvars i)
| syncvars (a[i] := e) = (syncvars a  $\wedge$  syncvars i  $\wedge$  syncvars e)
| syncvars (a.length) = syncvars a
| syncvars (e.F{D}) = syncvars e
| syncvars (e.F{D} := e2) = (syncvars e  $\wedge$  syncvars e2)
| syncvars (e.compareAndSwap(D.F, e', e'')) = (syncvars e  $\wedge$  syncvars e'  $\wedge$  syncvars e'')
| syncvars (e.M(es)) = (syncvars e  $\wedge$  syncvarss es)
| syncvars {V:T=vo;e} = syncvars e
| syncvars (syncV (e1) e2) = (syncvars e1  $\wedge$  syncvars e2  $\wedge$  V  $\notin$  fv e2)
| syncvars (insyncV (a) e) = (syncvars e  $\wedge$  V  $\notin$  fv e)
| syncvars (e1;;e2) = (syncvars e1  $\wedge$  syncvars e2)
| syncvars (if (b) e1 else e2) = (syncvars b  $\wedge$  syncvars e1  $\wedge$  syncvars e2)
| syncvars (while (b) c) = (syncvars b  $\wedge$  syncvars c)
| syncvars (throw e) = syncvars e
| syncvars (try e1 catch(C V) e2) = (syncvars e1  $\wedge$  syncvars e2)

| syncvarss [] = True

```



|  $\text{syncvarss } (e\#es) = (\text{syncvars } e \wedge \text{syncvarss } es)$

**definition**  $\text{bsok} :: 'a, 'b, 'addr \Rightarrow \text{nat} \Rightarrow \text{bool}$   
**where**  $\text{bsok } e \ n \equiv \mathcal{B} \ e \ n \wedge \text{expr-locks } e = (\lambda ad. \ 0)$

**definition**  $\text{bsoks} :: 'a, 'b, 'addr \Rightarrow \text{nat} \Rightarrow \text{bool}$   
**where**  $\text{bsoks } es \ n \equiv \mathcal{B} s \ es \ n \wedge \text{expr-lockss } es = (\lambda ad. \ 0)$

**primrec**  $\text{call1} :: ('a, 'b, 'addr) \text{ exp} \Rightarrow ('addr \times \text{mname} \times 'addr \text{ val list}) \text{ option}$   
**and**  $\text{calls1} :: ('a, 'b, 'addr) \text{ exp list} \Rightarrow ('addr \times \text{mname} \times 'addr \text{ val list}) \text{ option}$

**where**

$\text{call1 } (\text{new } C) = \text{None}$   
 $\text{call1 } (\text{newA } T[e]) = \text{call1 } e$   
 $\text{call1 } (\text{Cast } C \ e) = \text{call1 } e$   
 $\text{call1 } (e \text{ instanceof } T) = \text{call1 } e$   
 $\text{call1 } (\text{Val } v) = \text{None}$   
 $\text{call1 } (\text{Var } V) = \text{None}$   
 $\text{call1 } (V := e) = \text{call1 } e$   
 $\text{call1 } (e \ll \text{bop} \gg e') = (\text{if is-val } e \text{ then call1 } e' \text{ else call1 } e)$   
 $\text{call1 } (a[i]) = (\text{if is-val } a \text{ then call1 } i \text{ else call1 } a)$   
 $\text{call1 } (\text{AAss } a \ i \ e) = (\text{if is-val } a \text{ then } (\text{if is-val } i \text{ then call1 } e \text{ else call1 } i) \text{ else call1 } a)$   
 $\text{call1 } (a \cdot \text{length}) = \text{call1 } a$   
 $\text{call1 } (e \cdot F\{D\}) = \text{call1 } e$   
 $\text{call1 } (\text{FAss } e \ F \ D \ e') = (\text{if is-val } e \text{ then call1 } e' \text{ else call1 } e)$   
 $\text{call1 } (\text{CompareAndSwap } e \ D \ F \ e' \ e'') = (\text{if is-val } e \text{ then } (\text{if is-val } e' \text{ then call1 } e'' \text{ else call1 } e') \text{ else call1 } e)$   
 $\text{call1 } (e \cdot M(es)) = (\text{if is-val } e \text{ then } (\text{if is-vals } es \wedge \text{is-addr } e \text{ then } [(THE \ a. \ e = \text{addr } a, \ M, \ THE \ vs. \ es = \text{map Val } vs)]) \text{ else calls1 } es)$   
 $\text{call1 } (e) = \text{call1 } e$   
 $\text{call1 } (\{V:T=vo; e\}) = (\text{case } vo \text{ of } \text{None} \Rightarrow \text{call1 } e \mid \text{Some } v \Rightarrow \text{None})$   
 $\text{call1 } (\text{sync}_V(o') \ e) = \text{call1 } o'$   
 $\text{call1 } (\text{insync}_V(a) \ e) = \text{call1 } e$   
 $\text{call1 } (e;;e') = \text{call1 } e$   
 $\text{call1 } (\text{if } (e) \ e1 \text{ else } e2) = \text{call1 } e$   
 $\text{call1 } (\text{while}(b) \ e) = \text{None}$   
 $\text{call1 } (\text{throw } e) = \text{call1 } e$   
 $\text{call1 } (\text{try } e1 \text{ catch } (C \ V) \ e2) = \text{call1 } e1$   
 $\text{calls1 } [] = \text{None}$   
 $\text{calls1 } (e\#es) = (\text{if is-val } e \text{ then calls1 } es \text{ else call1 } e)$

**lemma**  $\text{expr-locks-blocks1} \ [simp]:$   
 $\text{expr-locks } (\text{blocks1 } n \ Ts \ e) = \text{expr-locks } e$   
**by**(*induct*  $n \ Ts \ e$  *rule*:  $\text{blocks1.induct}$ ) *simp-all*

**lemma**  $\text{max-varss-append} \ [simp]:$   
 $\text{max-varss } (es \ @ \ es') = \text{max } (\text{max-varss } es) \ (\text{max-varss } es')$   
**by**(*induct*  $es$ , *auto*)

**lemma**  $\text{max-varss-map-Val} \ [simp]: \text{max-varss } (\text{map Val } vs) = 0$   
**by**(*induct*  $vs$ ) *auto*

**lemma** *blocks1-max-vars*:

$\text{max-vars } (\text{blocks1 } n \text{ } Ts \text{ } e) = \text{max-vars } e + \text{length } Ts$

**by**(*induct*  $n \text{ } Ts \text{ } e$  *rule*: *blocks1.induct*)(*auto*)

**lemma** *blocks-max-vars*:

$\llbracket \text{length } vs = \text{length } pns; \text{length } Ts = \text{length } pns \rrbracket$

$\implies \text{max-vars } (\text{blocks } pns \text{ } Ts \text{ } vs \text{ } e) = \text{max-vars } e + \text{length } pns$

**by**(*induct*  $pns \text{ } Ts \text{ } vs \text{ } e$  *rule*: *blocks.induct*)(*auto*)

**lemma** *Bs-append* [*simp*]:  $\mathcal{B}s \text{ } (es \text{ } @ \text{ } es') \text{ } n \longleftrightarrow \mathcal{B}s \text{ } es \text{ } n \wedge \mathcal{B}s \text{ } es' \text{ } n$

**by**(*induct*  $es$ ) *auto*

**lemma** *Bs-map-Val* [*simp*]:  $\mathcal{B}s \text{ } (\text{map } Val \text{ } vs) \text{ } n$

**by**(*induct*  $vs$ ) *auto*

**lemma** *B-blocks1* [*intro*]:  $\mathcal{B} \text{ } body \text{ } (n + \text{length } Ts) \implies \mathcal{B} \text{ } (\text{blocks1 } n \text{ } Ts \text{ } body) \text{ } n$

**by**(*induct*  $n \text{ } Ts \text{ } body$  *rule*: *blocks1.induct*)(*auto*)

**lemma** *B-extRet2J* [*simp*]:  $\mathcal{B} \text{ } e \text{ } n \implies \mathcal{B} \text{ } (\text{extRet2J } e \text{ } va) \text{ } n$

**by**(*cases*  $va$ ) *auto*

**lemma** *B-inline-call*:  $\llbracket \mathcal{B} \text{ } e \text{ } n; \bigwedge n. \mathcal{B} \text{ } e' \text{ } n \rrbracket \implies \mathcal{B} \text{ } (\text{inline-call } e' \text{ } e) \text{ } n$

**and** *Bs-inline-calls*:  $\llbracket \mathcal{B}s \text{ } es \text{ } n; \bigwedge n. \mathcal{B} \text{ } e' \text{ } n \rrbracket \implies \mathcal{B}s \text{ } (\text{inline-calls } e' \text{ } es) \text{ } n$

**by**(*induct*  $e$  **and**  $es$  *arbitrary*:  $n$  **and**  $n$  *rule*: *call.induct* *calls.induct*) *auto*

**lemma** *syncvarss-append* [*simp*]:  $\text{syncvarss } (es \text{ } @ \text{ } es') \longleftrightarrow \text{syncvarss } es \wedge \text{syncvarss } es'$

**by**(*induct*  $es$ ) *auto*

**lemma** *syncvarss-map-Val* [*simp*]:  $\text{syncvarss } (\text{map } Val \text{ } vs)$

**by**(*induct*  $vs$ ) *auto*

**lemma** *bsok-simps* [*simp*]:

$bsok \text{ } (\text{new } C) \text{ } n = \text{True}$

$bsok \text{ } (\text{newA } T[e]) \text{ } n = bsok \text{ } e \text{ } n$

$bsok \text{ } (\text{Cast } T \text{ } e) \text{ } n = bsok \text{ } e \text{ } n$

$bsok \text{ } (e \text{ } \text{instanceof } T) \text{ } n = bsok \text{ } e \text{ } n$

$bsok \text{ } (e1 \text{ } \llbracket \text{bop} \rrbracket \text{ } e2) \text{ } n = (bsok \text{ } e1 \text{ } n \wedge bsok \text{ } e2 \text{ } n)$

$bsok \text{ } (\text{Var } V) \text{ } n = \text{True}$

$bsok \text{ } (\text{Val } v) \text{ } n = \text{True}$

$bsok \text{ } (V := e) \text{ } n = bsok \text{ } e \text{ } n$

$bsok \text{ } (a[i]) \text{ } n = (bsok \text{ } a \text{ } n \wedge bsok \text{ } i \text{ } n)$

$bsok \text{ } (a[i] := e) \text{ } n = (bsok \text{ } a \text{ } n \wedge bsok \text{ } i \text{ } n \wedge bsok \text{ } e \text{ } n)$

$bsok \text{ } (a \cdot \text{length}) \text{ } n = bsok \text{ } a \text{ } n$

$bsok \text{ } (e \cdot F\{D\}) \text{ } n = bsok \text{ } e \text{ } n$

$bsok \text{ } (e \cdot F\{D\} := e') \text{ } n = (bsok \text{ } e \text{ } n \wedge bsok \text{ } e' \text{ } n)$

$bsok \text{ } (e \cdot \text{compareAndSwap}(D \cdot F, e', e'')) \text{ } n = (bsok \text{ } e \text{ } n \wedge bsok \text{ } e' \text{ } n \wedge bsok \text{ } e'' \text{ } n)$

$bsok \text{ } (e \cdot M(ps)) \text{ } n = (bsok \text{ } e \text{ } n \wedge bsoks \text{ } ps \text{ } n)$

$bsok \text{ } \{V:T=vo; e\} \text{ } n = (bsok \text{ } e \text{ } (Suc \text{ } n) \wedge V = n)$

$bsok \text{ } (\text{sync}_V \text{ } (e) \text{ } e') \text{ } n = (bsok \text{ } e \text{ } n \wedge bsok \text{ } e' \text{ } (Suc \text{ } n) \wedge V = n)$

$bsok \text{ } (\text{insync}_V \text{ } (ad) \text{ } e) \text{ } n = \text{False}$

$bsok \text{ } (e;; e') \text{ } n = (bsok \text{ } e \text{ } n \wedge bsok \text{ } e' \text{ } n)$

$bsok \text{ } (\text{if } (e) \text{ } e1 \text{ } \text{else } e2) \text{ } n = (bsok \text{ } e \text{ } n \wedge bsok \text{ } e1 \text{ } n \wedge bsok \text{ } e2 \text{ } n)$

$bsok \text{ } (\text{while } (b) \text{ } c) \text{ } n = (bsok \text{ } b \text{ } n \wedge bsok \text{ } c \text{ } n)$

$bsok \text{ } (\text{throw } e) \text{ } n = bsok \text{ } e \text{ } n$

```

  bsok (try e catch (C V) e') n = (bsok e n ∧ bsok e' (Suc n) ∧ V = n)
and bsoks-simps [simp]:
  bsoks [] n = True
  bsoks (e # es) n = (bsok e n ∧ bsoks es n)
by(auto simp add: bsok-def bsoks-def fun-eq-iff)

```

```

lemma call1-callE:
  assumes call1 (obj·M(pns)) = [(a, M', vs)]
  obtains (CallObj) call1 obj = [(a, M', vs)]
  | (CallParams) v where obj = Val v calls1 pns = [(a, M', vs)]
  | (Call) obj = addr a pns = map Val vs M = M'
using assms by(auto split: if-split-asm simp add: is-vals-conv)

```

```

lemma calls1-map-Val-append [simp]:
  calls1 (map Val vs @ es) = calls1 es
by(induct vs) simp-all

```

```

lemma calls1-map-Val [simp]:
  calls1 (map Val vs) = None
by(induct vs) simp-all

```

```

lemma fixes e :: ('a, 'b, 'addr) exp and es :: ('a, 'b, 'addr) exp list
  shows call1-imp-call: call1 e = [aMvs]  $\implies$  call e = [aMvs]
  and calls1-imp-calls: calls1 es = [aMvs]  $\implies$  calls es = [aMvs]
by(induct e and es rule: call1.induct calls1.induct) auto

```

```

lemma max-vars-inline-call: max-vars (inline-call e' e) ≤ max-vars e + max-vars e'
  and max-varss-inline-calls: max-varss (inline-calls e' es) ≤ max-varss es + max-vars e'
by(induct e and es rule: call1.induct calls1.induct) auto

```

```

lemmas inline-call-max-vars1 = max-vars-inline-call
lemmas inline-calls-max-varss1 = max-varss-inline-calls

```

```

end

```

## 7.5 Abstract heap locales for J1 programs

```

theory J1Heap imports

```

```

  J1State

```

```

  ../Common/Conform

```

```

begin

```

```

locale J1-heap-base = heap-base +
  constrains addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
  and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
  and sc-spurious-wakeups :: bool
  and empty-heap :: 'heap
  and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap × 'addr) set
  and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype
  and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
  and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool

```

```

locale J1-heap = heap +

```

```

constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and sc-spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and P :: 'addr J1-prog

```

**sublocale** *J1-heap* < *J1-heap-base* .

```

locale J1-heap-conf-base = heap-conf-base +
  constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
  and thread-id2addr :: 'thread-id ⇒ 'addr
  and sc-spurious-wakeups :: bool
  and empty-heap :: 'heap
  and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
  and typeof-addr :: 'heap ⇒ 'addr → htype
  and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
  and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
  and hconf :: 'heap ⇒ bool
  and P :: 'addr J1-prog

```

**sublocale** *J1-heap-conf-base* < *J1-heap-base* .

```

locale J1-heap-conf =
  J1-heap-conf-base +
  heap-conf +
  constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
  and thread-id2addr :: 'thread-id ⇒ 'addr
  and sc-spurious-wakeups :: bool
  and empty-heap :: 'heap
  and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
  and typeof-addr :: 'heap ⇒ 'addr → htype
  and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
  and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
  and hconf :: 'heap ⇒ bool
  and P :: 'addr J1-prog

```

**sublocale** *J1-heap-conf* < *J1-heap* **by**(*unfold-locales*)

```

locale J1-conf-read =
  J1-heap-conf +
  heap-conf-read +
  constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
  and thread-id2addr :: 'thread-id ⇒ 'addr
  and sc-spurious-wakeups :: bool
  and empty-heap :: 'heap
  and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
  and typeof-addr :: 'heap ⇒ 'addr → htype
  and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
  and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
  and hconf :: 'heap ⇒ bool

```

and  $P :: 'addr\ J1\text{-}prog$

end

## 7.6 Semantics of the intermediate language

**theory**  $J1$  imports

$J1State$

$J1Heap$

$../Framework/FWBisimulation$

**begin**

**abbreviation**  $final\text{-}expr1 :: ('addr\ expr1 \times 'addr\ locals1) \times ('addr\ expr1 \times 'addr\ locals1)\ list \Rightarrow bool$

**where**

$final\text{-}expr1 \equiv \lambda(ex, eks). final\ (fst\ ex) \wedge eks = []$

**definition**  $extNTA2J1 ::$

$'addr\ J1\text{-}prog \Rightarrow (cname \times mname \times 'addr) \Rightarrow (('addr\ expr1 \times 'addr\ locals1) \times ('addr\ expr1 \times 'addr\ locals1)\ list)$

**where**

$extNTA2J1\ P = (\lambda(C, M, a). let\ (D, -, -, meth) = method\ P\ C\ M; body = the\ meth$   
 $in\ ((\{0:Class\ D=None; body\}, Addr\ a\ \# replicate\ (max\text{-}vars\ body)$   
 $undefined\text{-}value), []))$

**lemma**  $extNTA2J1\text{-}iff\ [simp]:$

$extNTA2J1\ P\ (C, M, a) = ((\{0:Class\ (fst\ (method\ P\ C\ M))=None; the\ (snd\ (snd\ (snd\ (method\ P\ C\ M))))\}, Addr\ a\ \# replicate\ (max\text{-}vars\ (the\ (snd\ (snd\ (snd\ (method\ P\ C\ M))))))\ undefined\text{-}value), [])$

**by**( $simp\ add: extNTA2J1\text{-}def\ split\text{-}beta$ )

**abbreviation**  $extTA2J1 ::$

$'addr\ J1\text{-}prog \Rightarrow ('addr, 'thread\text{-}id, 'heap)\ external\text{-}thread\text{-}action \Rightarrow ('addr, 'thread\text{-}id, 'heap)\ J1\text{-}thread\text{-}action$

**where**  $extTA2J1\ P \equiv convert\text{-}extTA\ (extNTA2J1\ P)$

**abbreviation** (*input*)  $extRet2J1 :: 'addr\ expr1 \Rightarrow 'addr\ extCallRet \Rightarrow 'addr\ expr1$

**where**  $extRet2J1 \equiv extRet2J$

**lemma**  $max\text{-}vars\text{-}extRet2J1\ [simp]:$

$max\text{-}vars\ e = 0 \implies max\text{-}vars\ (extRet2J1\ e\ va) = 0$

**by**( $cases\ va\ simp\text{-}all$ )

**context**  $J1\text{-}heap\text{-}base$  **begin**

**abbreviation**  $J1\text{-}start\text{-}state :: 'addr\ J1\text{-}prog \Rightarrow cname \Rightarrow mname \Rightarrow 'addr\ val\ list \Rightarrow ('addr, 'thread\text{-}id, 'heap)\ J1\text{-}state$

**where**

$J1\text{-}start\text{-}state \equiv$   
 $start\text{-}state\ (\lambda C\ M\ Ts\ T\ body\ vs. ((blocks1\ 0\ (Class\ C\ \# Ts)\ body, Null\ \# vs\ @ replicate\ (max\text{-}vars\ body)\ undefined\text{-}value), []))$

**inductive**  $red1 ::$

$bool \Rightarrow 'addr\ J1\text{-}prog \Rightarrow 'thread\text{-}id \Rightarrow 'addr\ expr1 \Rightarrow 'heap \times 'addr\ locals1$

$\Rightarrow ('addr, 'thread\text{-}id, 'heap)\ external\text{-}thread\text{-}action \Rightarrow 'addr\ expr1 \Rightarrow 'heap \times 'addr\ locals1 \Rightarrow bool$

$(-, -, \vdash 1 \ ((1\langle -, / - \rangle) \dashrightarrow / (1\langle -, / - \rangle)) \ [51, 51, 0, 0, 0, 0, 0] \ 81)$   
**and**  $reds1 ::$   
 $bool \Rightarrow 'addr \ J1\text{-}prog \Rightarrow 'thread\text{-}id \Rightarrow 'addr \ expr1 \ list \Rightarrow 'heap \times 'addr \ locals1$   
 $\Rightarrow ('addr, 'thread\text{-}id, 'heap) \ external\text{-}thread\text{-}action \Rightarrow 'addr \ expr1 \ list \Rightarrow 'heap \times 'addr \ locals1 \Rightarrow$   
 $bool$   
 $(-, -, \vdash 1 \ ((1\langle -, / - \rangle) \dashrightarrow / (1\langle -, / - \rangle)) \ [51, 51, 0, 0, 0, 0, 0] \ 81)$   
**for**  $uf :: bool$  **and**  $P :: 'addr \ J1\text{-}prog$  **and**  $t :: 'thread\text{-}id$   
**where**  
*Red1New:*  
 $(h', a) \in allocate \ h \ (Class\text{-}type \ C)$   
 $\Rightarrow uf, P, t \vdash 1 \ \langle new \ C, (h, l) \rangle -\llbracket NewHeapElem \ a \ (Class\text{-}type \ C) \rrbracket \rightarrow \langle addr \ a, (h', l) \rangle$   
  
 $|$  *Red1NewFail:*  
 $allocate \ h \ (Class\text{-}type \ C) = \{\}$   
 $\Rightarrow uf, P, t \vdash 1 \ \langle new \ C, (h, l) \rangle -\varepsilon \rightarrow \langle THROW \ OutOfMemory, (h, l) \rangle$   
  
 $|$  *New1ArrayRed:*  
 $uf, P, t \vdash 1 \ \langle e, s \rangle -ta \rightarrow \langle e', s^\wedge \rangle$   
 $\Rightarrow uf, P, t \vdash 1 \ \langle newA \ T[e], s \rangle -ta \rightarrow \langle newA \ T[e^\wedge], s^\wedge \rangle$   
  
 $|$  *Red1NewArray:*  
 $\llbracket 0 \leq s \ i; (h', a) \in allocate \ h \ (Array\text{-}type \ T \ (nat \ (sint \ i))) \rrbracket$   
 $\Rightarrow uf, P, t \vdash 1 \ \langle newA \ T[Val \ (Intg \ i)], (h, l) \rangle -\llbracket NewHeapElem \ a \ (Array\text{-}type \ T \ (nat \ (sint \ i))) \rrbracket \rightarrow$   
 $\langle addr \ a, (h', l) \rangle$   
  
 $|$  *Red1NewArrayNegative:*  
 $i < s \ 0 \Rightarrow uf, P, t \vdash 1 \ \langle newA \ T[Val \ (Intg \ i)], s \rangle -\varepsilon \rightarrow \langle THROW \ NegativeArraySize, s \rangle$   
  
 $|$  *Red1NewArrayFail:*  
 $\llbracket 0 \leq s \ i; allocate \ h \ (Array\text{-}type \ T \ (nat \ (sint \ i))) = \{\} \rrbracket$   
 $\Rightarrow uf, P, t \vdash 1 \ \langle newA \ T[Val \ (Intg \ i)], (h, l) \rangle -\varepsilon \rightarrow \langle THROW \ OutOfMemory, (h, l) \rangle$   
  
 $|$  *Cast1Red:*  
 $uf, P, t \vdash 1 \ \langle e, s \rangle -ta \rightarrow \langle e', s^\wedge \rangle$   
 $\Rightarrow uf, P, t \vdash 1 \ \langle Cast \ C \ e, s \rangle -ta \rightarrow \langle Cast \ C \ e', s^\wedge \rangle$   
  
 $|$  *Red1Cast:*  
 $\llbracket typeof_{hp} \ s \ v = \lfloor U \rfloor; P \vdash U \leq T \rrbracket$   
 $\Rightarrow uf, P, t \vdash 1 \ \langle Cast \ T \ (Val \ v), s \rangle -\varepsilon \rightarrow \langle Val \ v, s \rangle$   
  
 $|$  *Red1CastFail:*  
 $\llbracket typeof_{hp} \ s \ v = \lfloor U \rfloor; \neg P \vdash U \leq T \rrbracket$   
 $\Rightarrow uf, P, t \vdash 1 \ \langle Cast \ T \ (Val \ v), s \rangle -\varepsilon \rightarrow \langle THROW \ ClassCast, s \rangle$   
  
 $|$  *InstanceOf1Red:*  
 $uf, P, t \vdash 1 \ \langle e, s \rangle -ta \rightarrow \langle e', s^\wedge \rangle \Rightarrow uf, P, t \vdash 1 \ \langle e \ instanceof \ T, s \rangle -ta \rightarrow \langle e' \ instanceof \ T, s^\wedge \rangle$   
  
 $|$  *Red1InstanceOf:*  
 $\llbracket typeof_{hp} \ s \ v = \lfloor U \rfloor; b \longleftrightarrow v \neq Null \wedge P \vdash U \leq T \rrbracket$   
 $\Rightarrow uf, P, t \vdash 1 \ \langle (Val \ v) \ instanceof \ T, s \rangle -\varepsilon \rightarrow \langle Val \ (Bool \ b), s \rangle$   
  
 $|$  *Bin1OpRed1:*  
 $uf, P, t \vdash 1 \ \langle e, s \rangle -ta \rightarrow \langle e', s^\wedge \rangle \Rightarrow uf, P, t \vdash 1 \ \langle e \ \langle\!\langle bop \!\rangle\!\rangle \ e2, s \rangle -ta \rightarrow \langle e' \ \langle\!\langle bop \!\rangle\!\rangle \ e2, s^\wedge \rangle$

- | *Bin1OpRed2*:  
 $uf, P, t \vdash 1 \langle e, s \rangle -ta\rightarrow \langle e', s' \rangle \implies uf, P, t \vdash 1 \langle (Val\ v) \ll bop \gg e, s \rangle -ta\rightarrow \langle (Val\ v) \ll bop \gg e', s' \rangle$
- | *Red1BinOp*:  
 $binop\ bop\ v1\ v2 = Some\ (Inl\ v) \implies$   
 $uf, P, t \vdash 1 \langle (Val\ v1) \ll bop \gg (Val\ v2), s \rangle -\varepsilon\rightarrow \langle Val\ v, s \rangle$
- | *Red1BinOpFail*:  
 $binop\ bop\ v1\ v2 = Some\ (Inr\ a) \implies$   
 $uf, P, t \vdash 1 \langle (Val\ v1) \ll bop \gg (Val\ v2), s \rangle -\varepsilon\rightarrow \langle Throw\ a, s \rangle$
- | *Red1Var*:  
 $\ll (lcl\ s)!V = v; V < size\ (lcl\ s) \gg$   
 $\implies uf, P, t \vdash 1 \langle Var\ V, s \rangle -\varepsilon\rightarrow \langle Val\ v, s \rangle$
- | *LAss1Red*:  
 $uf, P, t \vdash 1 \langle e, s \rangle -ta\rightarrow \langle e', s' \rangle$   
 $\implies uf, P, t \vdash 1 \langle V:=e, s \rangle -ta\rightarrow \langle V:=e', s' \rangle$
- | *Red1LAss*:  
 $V < size\ l$   
 $\implies uf, P, t \vdash 1 \langle V:=(Val\ v), (h, l) \rangle -\varepsilon\rightarrow \langle unit, (h, l[V := v]) \rangle$
- | *AAcc1Red1*:  
 $uf, P, t \vdash 1 \langle a, s \rangle -ta\rightarrow \langle a', s' \rangle \implies uf, P, t \vdash 1 \langle a[i], s \rangle -ta\rightarrow \langle a'[i], s' \rangle$
- | *AAcc1Red2*:  
 $uf, P, t \vdash 1 \langle i, s \rangle -ta\rightarrow \langle i', s' \rangle \implies uf, P, t \vdash 1 \langle (Val\ a)[i], s \rangle -ta\rightarrow \langle (Val\ a)[i'], s' \rangle$
- | *Red1AAccNull*:  
 $uf, P, t \vdash 1 \langle null[Val\ i], s \rangle -\varepsilon\rightarrow \langle THROW\ NullPointer, s \rangle$
- | *Red1AAccBounds*:  
 $\ll typeof\_addr\ (hp\ s)\ a = \lfloor Array\_type\ T\ n \rfloor; i < s\ 0 \vee sint\ i \geq int\ n \gg$   
 $\implies uf, P, t \vdash 1 \langle (addr\ a)[Val\ (Intg\ i)], s \rangle -\varepsilon\rightarrow \langle THROW\ ArrayIndexOutOfBounds, s \rangle$
- | *Red1AAcc*:  
 $\ll typeof\_addr\ h\ a = \lfloor Array\_type\ T\ n \rfloor; 0 \leq s\ i; sint\ i < int\ n;$   
 $heap\_read\ h\ a\ (ACell\ (nat\ (sint\ i)))\ v \gg$   
 $\implies uf, P, t \vdash 1 \langle (addr\ a)[Val\ (Intg\ i)], (h, xs) \rangle -\{\!| ReadMem\ a\ (ACell\ (nat\ (sint\ i)))\ v \!\}\rightarrow \langle Val\ v,$   
 $(h, xs) \rangle$
- | *AAss1Red1*:  
 $uf, P, t \vdash 1 \langle a, s \rangle -ta\rightarrow \langle a', s' \rangle \implies uf, P, t \vdash 1 \langle a[i] := e, s \rangle -ta\rightarrow \langle a'[i] := e, s' \rangle$
- | *AAss1Red2*:  
 $uf, P, t \vdash 1 \langle i, s \rangle -ta\rightarrow \langle i', s' \rangle \implies uf, P, t \vdash 1 \langle (Val\ a)[i] := e, s \rangle -ta\rightarrow \langle (Val\ a)[i'] := e, s' \rangle$
- | *AAss1Red3*:  
 $uf, P, t \vdash 1 \langle e, s \rangle -ta\rightarrow \langle e', s' \rangle \implies uf, P, t \vdash 1 \langle AAss\ (Val\ a)\ (Val\ i)\ e, s \rangle -ta\rightarrow \langle (Val\ a)[Val\ i] :=$   
 $e', s' \rangle$
- | *Red1AAssNull*:  
 $uf, P, t \vdash 1 \langle AAss\ null\ (Val\ i)\ (Val\ e), s \rangle -\varepsilon\rightarrow \langle THROW\ NullPointer, s \rangle$

- | *Red1AssBounds*:  

$$\llbracket \text{typeof-addr } (hp\ s)\ a = \lfloor \text{Array-type } T\ n \rfloor; i <_s 0 \vee \text{sint } i \geq \text{int } n \rrbracket$$

$$\implies uf, P, t \vdash 1 \langle AAss\ (\text{addr } a)\ (\text{Val } (\text{Intg } i))\ (\text{Val } e), s \rangle -\varepsilon \rightarrow \langle \text{THROW } \text{ArrayIndexOutOfBounds}, s \rangle$$
- | *Red1AssStore*:  

$$\llbracket \text{typeof-addr } (hp\ s)\ a = \lfloor \text{Array-type } T\ n \rfloor; 0 \leq_s i; \text{sint } i < \text{int } n;$$

$$\text{typeof}_{hp\ s}\ w = \lfloor U \rfloor; \neg (P \vdash U \leq T) \rrbracket$$

$$\implies uf, P, t \vdash 1 \langle AAss\ (\text{addr } a)\ (\text{Val } (\text{Intg } i))\ (\text{Val } w), s \rangle -\varepsilon \rightarrow \langle \text{THROW } \text{ArrayStore}, s \rangle$$
- | *Red1Ass*:  

$$\llbracket \text{typeof-addr } h\ a = \lfloor \text{Array-type } T\ n \rfloor; 0 \leq_s i; \text{sint } i < \text{int } n; \text{typeof}_h\ w = \text{Some } U; P \vdash U \leq T;$$

$$\text{heap-write } h\ a\ (\text{ACell } (\text{nat } (\text{sint } i)))\ w\ h' \rrbracket$$

$$\implies uf, P, t \vdash 1 \langle AAss\ (\text{addr } a)\ (\text{Val } (\text{Intg } i))\ (\text{Val } w), (h, l) \rangle -\llbracket \text{WriteMem } a\ (\text{ACell } (\text{nat } (\text{sint } i))) \rrbracket$$

$$w \rrbracket \rightarrow \langle \text{unit}, (h', l) \rangle$$
- | *ALength1Red*:  

$$uf, P, t \vdash 1 \langle a, s \rangle -ta \rightarrow \langle a', s' \rangle \implies uf, P, t \vdash 1 \langle a \cdot \text{length}, s \rangle -ta \rightarrow \langle a' \cdot \text{length}, s' \rangle$$
- | *Red1ALength*:  

$$\text{typeof-addr } h\ a = \lfloor \text{Array-type } T\ n \rfloor$$

$$\implies uf, P, t \vdash 1 \langle \text{addr } a \cdot \text{length}, (h, xs) \rangle -\varepsilon \rightarrow \langle \text{Val } (\text{Intg } (\text{word-of-nat } n)), (h, xs) \rangle$$
- | *Red1ALengthNull*:  

$$uf, P, t \vdash 1 \langle \text{null} \cdot \text{length}, s \rangle -\varepsilon \rightarrow \langle \text{THROW } \text{NullPointer}, s \rangle$$
- | *FAcc1Red*:  

$$uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies uf, P, t \vdash 1 \langle e \cdot F\{D\}, s \rangle -ta \rightarrow \langle e' \cdot F\{D\}, s' \rangle$$
- | *Red1FAcc*:  

$$\text{heap-read } h\ a\ (\text{CField } D\ F)\ v$$

$$\implies uf, P, t \vdash 1 \langle (\text{addr } a) \cdot F\{D\}, (h, xs) \rangle -\llbracket \text{ReadMem } a\ (\text{CField } D\ F)\ v \rrbracket \rightarrow \langle \text{Val } v, (h, xs) \rangle$$
- | *Red1FAccNull*:  

$$uf, P, t \vdash 1 \langle \text{null} \cdot F\{D\}, s \rangle -\varepsilon \rightarrow \langle \text{THROW } \text{NullPointer}, s \rangle$$
- | *FAss1Red1*:  

$$uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies uf, P, t \vdash 1 \langle e \cdot F\{D\} := e2, s \rangle -ta \rightarrow \langle e' \cdot F\{D\} := e2, s' \rangle$$
- | *FAss1Red2*:  

$$uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies uf, P, t \vdash 1 \langle FAss\ (\text{Val } v)\ F\ D\ e, s \rangle -ta \rightarrow \langle \text{Val } v \cdot F\{D\} := e', s' \rangle$$
- | *Red1FAss*:  

$$\text{heap-write } h\ a\ (\text{CField } D\ F)\ v\ h' \implies$$

$$uf, P, t \vdash 1 \langle FAss\ (\text{addr } a)\ F\ D\ (\text{Val } v), (h, l) \rangle -\llbracket \text{WriteMem } a\ (\text{CField } D\ F)\ v \rrbracket \rightarrow \langle \text{unit}, (h', l) \rangle$$
- | *Red1FAssNull*:  

$$uf, P, t \vdash 1 \langle FAss\ \text{null}\ F\ D\ (\text{Val } v), s \rangle -\varepsilon \rightarrow \langle \text{THROW } \text{NullPointer}, s \rangle$$
- | *CAS1Red1*:  

$$uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies$$

$$uf, P, t \vdash 1 \langle e \cdot \text{compareAndSwap}(D \cdot F, e2, e3), s \rangle -ta \rightarrow \langle e' \cdot \text{compareAndSwap}(D \cdot F, e2, e3), s' \rangle$$
- | *CAS1Red2*:



- $uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies$   
 $uf, P, t \vdash 1 \langle \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, e, e3), s \rangle -ta \rightarrow \langle \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, e', e3), s' \rangle$
- | *CAS1Red3*:  
 $uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies$   
 $uf, P, t \vdash 1 \langle \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', e), s \rangle -ta \rightarrow \langle \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', e'), s' \rangle$
- | *CAS1Null*:  
 $uf, P, t \vdash 1 \langle \text{null} \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v, \text{Val } v'), s \rangle -\varepsilon \rightarrow \langle \text{THROW NullPointer}, s \rangle$
- | *Red1CASSucceed*:  
 $\llbracket \text{heap-read } h \ a \ (CField \ D \ F) \ v; \text{heap-write } h \ a \ (CField \ D \ F) \ v' \ h' \rrbracket \implies$   
 $uf, P, t \vdash 1 \langle \text{addr } a \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v, \text{Val } v'), (h, l) \rangle$   
 $-\llbracket \text{ReadMem } a \ (CField \ D \ F) \ v, \text{WriteMem } a \ (CField \ D \ F) \ v' \rrbracket \rightarrow$   
 $\langle \text{true}, (h', l) \rangle$
- | *Red1CASFail*:  
 $\llbracket \text{heap-read } h \ a \ (CField \ D \ F) \ v''; \ v \neq v'' \rrbracket \implies$   
 $uf, P, t \vdash 1 \langle \text{addr } a \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v, \text{Val } v'), (h, l) \rangle$   
 $-\llbracket \text{ReadMem } a \ (CField \ D \ F) \ v'' \rrbracket \rightarrow$   
 $\langle \text{false}, (h, l) \rangle$
- | *Call1Obj*:  
 $uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies uf, P, t \vdash 1 \langle e \cdot M(es), s \rangle -ta \rightarrow \langle e' \cdot M(es), s' \rangle$
- | *Call1Params*:  
 $uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies$   
 $uf, P, t \vdash 1 \langle (\text{Val } v) \cdot M(es), s \rangle -ta \rightarrow \langle (\text{Val } v) \cdot M(es'), s' \rangle$
- | *Red1CallExternal*:  
 $\llbracket \text{typeof-addr } (hp \ s) \ a = \lfloor T \rfloor; \ P \vdash \text{class-type-of } T \text{ sees } M:Ts \rightarrow Tr = \text{Native in } D; \ P, t \vdash \langle a \cdot M(vs), hp \ s \rangle -ta \rightarrow ext \langle va, h' \rangle;$   
 $e' = \text{extRet2J1 } ((\text{addr } a) \cdot M(\text{map } \text{Val } vs)) \ va; \ s' = (h', \text{lcl } s) \rrbracket$   
 $\implies uf, P, t \vdash 1 \langle (\text{addr } a) \cdot M(\text{map } \text{Val } vs), s \rangle -ta \rightarrow \langle e', s' \rangle$
- | *Red1CallNull*:  
 $uf, P, t \vdash 1 \langle \text{null} \cdot M(\text{map } \text{Val } vs), s \rangle -\varepsilon \rightarrow \langle \text{THROW NullPointer}, s \rangle$
- | *Block1Some*:  
 $V < \text{length } x \implies uf, P, t \vdash 1 \langle \{V:T=\lfloor v \rfloor; e\}, (h, x) \rangle -\varepsilon \rightarrow \langle \{V:T=None; e\}, (h, x[V := v]) \rangle$
- | *Block1Red*:  
 $uf, P, t \vdash 1 \langle e, (h, x) \rangle -ta \rightarrow \langle e', (h', x') \rangle$   
 $\implies uf, P, t \vdash 1 \langle \{V:T=None; e\}, (h, x) \rangle -ta \rightarrow \langle \{V:T=None; e'\}, (h', x') \rangle$
- | *Red1Block*:  
 $uf, P, t \vdash 1 \langle \{V:T=None; \text{Val } u\}, s \rangle -\varepsilon \rightarrow \langle \text{Val } u, s \rangle$
- | *Synchronized1Red1*:  
 $uf, P, t \vdash 1 \langle o', s \rangle -ta \rightarrow \langle o'', s' \rangle \implies uf, P, t \vdash 1 \langle \text{sync}_V(o') \ e, s \rangle -ta \rightarrow \langle \text{sync}_V(o'') \ e, s' \rangle$
- | *Synchronized1Null*:  
 $V < \text{length } xs \implies uf, P, t \vdash 1 \langle \text{sync}_V(\text{null}) \ e, (h, xs) \rangle -\varepsilon \rightarrow \langle \text{THROW NullPointer}, (h, xs[V :=$

$Null])\rangle$

| *Lock1Synchronized*:

$V < \text{length } xs \implies uf, P, t \vdash 1 \langle \text{sync}_V(\text{addr } a) \ e, (h, xs) \rangle -\llbracket \text{Lock} \rightarrow a, \text{SyncLock } a \rrbracket \rightarrow \langle \text{insync}_V(a) \ e, (h, xs[V := \text{Addr } a]) \rangle$

| *Synchronized1Red2*:

$uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies uf, P, t \vdash 1 \langle \text{insync}_V(a) \ e, s \rangle -ta \rightarrow \langle \text{insync}_V(a) \ e', s' \rangle$

| *Unlock1Synchronized*:

$\llbracket xs ! V = \text{Addr } a'; V < \text{length } xs \rrbracket \implies uf, P, t \vdash 1 \langle \text{insync}_V(a) \ (\text{Val } v), (h, xs) \rangle -\llbracket \text{Unlock} \rightarrow a', \text{SyncUnlock } a' \rrbracket \rightarrow \langle \text{Val } v, (h, xs) \rangle$

| *Unlock1SynchronizedNull*:

$\llbracket xs ! V = \text{Null}; V < \text{length } xs \rrbracket \implies uf, P, t \vdash 1 \langle \text{insync}_V(a) \ (\text{Val } v), (h, xs) \rangle -\varepsilon \rightarrow \langle \text{THROW } \text{NullPointer}, (h, xs) \rangle$

| *Unlock1SynchronizedFail*:

$\llbracket uf; xs ! V = \text{Addr } a'; V < \text{length } xs \rrbracket \implies uf, P, t \vdash 1 \langle \text{insync}_V(a) \ (\text{Val } v), (h, xs) \rangle -\llbracket \text{UnlockFail} \rightarrow a' \rrbracket \rightarrow \langle \text{THROW } \text{IllegalMonitorState}, (h, xs) \rangle$

| *Seq1Red*:

$uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies uf, P, t \vdash 1 \langle e;;e2, s \rangle -ta \rightarrow \langle e';;e2, s' \rangle$

| *Red1Seq*:

$uf, P, t \vdash 1 \langle \text{Seq } (\text{Val } v) \ e, s \rangle -\varepsilon \rightarrow \langle e, s \rangle$

| *Cond1Red*:

$uf, P, t \vdash 1 \langle b, s \rangle -ta \rightarrow \langle b', s' \rangle \implies uf, P, t \vdash 1 \langle \text{if } (b) \ e1 \ \text{else } e2, s \rangle -ta \rightarrow \langle \text{if } (b') \ e1 \ \text{else } e2, s' \rangle$

| *Red1CondT*:

$uf, P, t \vdash 1 \langle \text{if } (\text{true}) \ e1 \ \text{else } e2, s \rangle -\varepsilon \rightarrow \langle e1, s \rangle$

| *Red1CondF*:

$uf, P, t \vdash 1 \langle \text{if } (\text{false}) \ e1 \ \text{else } e2, s \rangle -\varepsilon \rightarrow \langle e2, s \rangle$

| *Red1While*:

$uf, P, t \vdash 1 \langle \text{while}(b) \ c, s \rangle -\varepsilon \rightarrow \langle \text{if } (b) \ (c;;\text{while}(b) \ c) \ \text{else } \text{unit}, s \rangle$

| *Throw1Red*:

$uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies uf, P, t \vdash 1 \langle \text{throw } e, s \rangle -ta \rightarrow \langle \text{throw } e', s' \rangle$

| *Red1ThrowNull*:

$uf, P, t \vdash 1 \langle \text{throw null}, s \rangle -\varepsilon \rightarrow \langle \text{THROW } \text{NullPointer}, s \rangle$

| *Try1Red*:

$uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies uf, P, t \vdash 1 \langle \text{try } e \ \text{catch}(C \ V) \ e2, s \rangle -ta \rightarrow \langle \text{try } e' \ \text{catch}(C \ V) \ e2, s' \rangle$

| *Red1Try*:

$uf, P, t \vdash 1 \langle \text{try } (\text{Val } v) \ \text{catch}(C \ V) \ e2, s \rangle -\varepsilon \rightarrow \langle \text{Val } v, s \rangle$

| *Red1TryCatch*:

$\llbracket \text{typeof-addr } h \ a = \lfloor \text{Class-type } D \rfloor; P \vdash D \preceq^* C; V < \text{length } x \rrbracket$   
 $\implies uf, P, t \vdash 1 \langle \text{try } (\text{Throw } a) \text{ catch}(C \ V) \ e2, (h, x) \rangle -\varepsilon \rightarrow \langle \{ V:\text{Class } C=\text{None}; e2 \}, (h, x[V := \text{Addr } a]) \rangle$

| *Red1TryFail*:

$\llbracket \text{typeof-addr } (hp \ s) \ a = \lfloor \text{Class-type } D \rfloor; \neg P \vdash D \preceq^* C \rrbracket$   
 $\implies uf, P, t \vdash 1 \langle \text{try } (\text{Throw } a) \text{ catch}(C \ V) \ e2, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$

| *List1Red1*:

$uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies$   
 $uf, P, t \vdash 1 \langle e \# es, s \rangle [-ta \rightarrow] \langle e' \# es, s' \rangle$

| *List1Red2*:

$uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies$   
 $uf, P, t \vdash 1 \langle \text{Val } v \ \# \ es, s \rangle [-ta \rightarrow] \langle \text{Val } v \ \# \ es', s' \rangle$

| *New1ArrayThrow*:  $uf, P, t \vdash 1 \langle \text{newA } T \lfloor \text{Throw } a \rfloor, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
| *Cast1Throw*:  $uf, P, t \vdash 1 \langle \text{Cast } C \ (\text{Throw } a), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
| *InstanceOf1Throw*:  $uf, P, t \vdash 1 \langle (\text{Throw } a) \text{ instanceof } T, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
| *Bin1OpThrow1*:  $uf, P, t \vdash 1 \langle (\text{Throw } a) \ \llbracket \text{bop} \rrbracket \ e2, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
| *Bin1OpThrow2*:  $uf, P, t \vdash 1 \langle (\text{Val } v_1) \ \llbracket \text{bop} \rrbracket \ (\text{Throw } a), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
| *LAss1Throw*:  $uf, P, t \vdash 1 \langle V := (\text{Throw } a), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
| *AAcc1Throw1*:  $uf, P, t \vdash 1 \langle (\text{Throw } a)[i], s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
| *AAcc1Throw2*:  $uf, P, t \vdash 1 \langle (\text{Val } v) \lfloor \text{Throw } a \rfloor, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
| *AAss1Throw1*:  $uf, P, t \vdash 1 \langle (\text{Throw } a)[i] := e, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
| *AAss1Throw2*:  $uf, P, t \vdash 1 \langle (\text{Val } v) \lfloor \text{Throw } a \rfloor := e, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
| *AAss1Throw3*:  $uf, P, t \vdash 1 \langle \text{AAss } (\text{Val } v) \ (\text{Val } i) \ (\text{Throw } a), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
| *ALength1Throw*:  $uf, P, t \vdash 1 \langle (\text{Throw } a) \cdot \text{length}, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
| *FAcc1Throw*:  $uf, P, t \vdash 1 \langle (\text{Throw } a) \cdot F\{D\}, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
| *FAss1Throw1*:  $uf, P, t \vdash 1 \langle (\text{Throw } a) \cdot F\{D\} := e2, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
| *FAss1Throw2*:  $uf, P, t \vdash 1 \langle \text{FAss } (\text{Val } v) \ F \ D \ (\text{Throw } a), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
| *CAS1Throw*:  $uf, P, t \vdash 1 \langle \text{Throw } a \cdot \text{compareAndSwap}(D \cdot F, e2, e3), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
| *CAS1Throw2*:  $uf, P, t \vdash 1 \langle \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Throw } a, e3), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
| *CAS1Throw3*:  $uf, P, t \vdash 1 \langle \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', \text{Throw } a), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
| *Call1ThrowObj*:  $uf, P, t \vdash 1 \langle (\text{Throw } a) \cdot M(es), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
| *Call1ThrowParams*:  $\llbracket es = \text{map Val } vs \ @ \ \text{Throw } a \ \# \ es' \rrbracket \implies uf, P, t \vdash 1 \langle (\text{Val } v) \cdot M(es), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
| *Block1Throw*:  $uf, P, t \vdash 1 \langle \{ V:T=\text{None}; \text{Throw } a \}, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
| *Synchronized1Throw1*:  $uf, P, t \vdash 1 \langle \text{sync}_V \ (\text{Throw } a) \ e, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
| *Synchronized1Throw2*:  
 $\llbracket xs ! V = \text{Addr } a'; V < \text{length } xs \rrbracket$   
 $\implies uf, P, t \vdash 1 \langle \text{insync}_V(a) \ \text{Throw } ad, (h, xs) \rangle -\llbracket \text{Unlock} \rightarrow a', \text{SyncUnlock } a' \rrbracket \rightarrow \langle \text{Throw } ad, (h, xs) \rangle$   
| *Synchronized1Throw2Fail*:  
 $\llbracket uf; xs ! V = \text{Addr } a'; V < \text{length } xs \rrbracket$   
 $\implies uf, P, t \vdash 1 \langle \text{insync}_V(a) \ \text{Throw } ad, (h, xs) \rangle -\llbracket \text{UnlockFail} \rightarrow a' \rrbracket \rightarrow \langle \text{THROW IllegalMonitorState}, (h, xs) \rangle$   
| *Synchronized1Throw2Null*:  
 $\llbracket xs ! V = \text{Null}; V < \text{length } xs \rrbracket$   
 $\implies uf, P, t \vdash 1 \langle \text{insync}_V(a) \ \text{Throw } ad, (h, xs) \rangle -\varepsilon \rightarrow \langle \text{THROW NullPointerException}, (h, xs) \rangle$   
| *Seq1Throw*:  $uf, P, t \vdash 1 \langle (\text{Throw } a); e2, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
| *Cond1Throw*:  $uf, P, t \vdash 1 \langle \text{if } (\text{Throw } a) \ e_1 \text{ else } e_2, s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$   
| *Throw1Throw*:  $uf, P, t \vdash 1 \langle \text{throw}(\text{Throw } a), s \rangle -\varepsilon \rightarrow \langle \text{Throw } a, s \rangle$

**inductive-cases** *red1-cases*:

$uf, P, t \vdash 1 \langle \text{new } C, s \rangle -ta \rightarrow \langle e', s' \rangle$   
 $uf, P, t \vdash 1 \langle \text{new } T[e], s \rangle -ta \rightarrow \langle e', s' \rangle$   
 $uf, P, t \vdash 1 \langle e \text{ «bop» } e', s \rangle -ta \rightarrow \langle e'', s' \rangle$   
 $uf, P, t \vdash 1 \langle \text{Var } V, s \rangle -ta \rightarrow \langle e', s' \rangle$   
 $uf, P, t \vdash 1 \langle V := e, s \rangle -ta \rightarrow \langle e', s' \rangle$   
 $uf, P, t \vdash 1 \langle a[i], s \rangle -ta \rightarrow \langle e', s' \rangle$   
 $uf, P, t \vdash 1 \langle a[i] := e, s \rangle -ta \rightarrow \langle e', s' \rangle$   
 $uf, P, t \vdash 1 \langle a \cdot \text{length}, s \rangle -ta \rightarrow \langle e', s' \rangle$   
 $uf, P, t \vdash 1 \langle e \cdot F\{D\}, s \rangle -ta \rightarrow \langle e', s' \rangle$   
 $uf, P, t \vdash 1 \langle e \cdot F\{D\} := e2, s \rangle -ta \rightarrow \langle e', s' \rangle$   
 $uf, P, t \vdash 1 \langle e \cdot \text{compareAndSwap}(D \cdot F, e', e''), s \rangle -ta \rightarrow \langle e''', s' \rangle$   
 $uf, P, t \vdash 1 \langle e \cdot M(es), s \rangle -ta \rightarrow \langle e', s' \rangle$   
 $uf, P, t \vdash 1 \langle \{V:T=vo; e\}, s \rangle -ta \rightarrow \langle e', s' \rangle$   
 $uf, P, t \vdash 1 \langle \text{sync}_V(o') e, s \rangle -ta \rightarrow \langle e', s' \rangle$   
 $uf, P, t \vdash 1 \langle \text{insync}_V(a) e, s \rangle -ta \rightarrow \langle e', s' \rangle$   
 $uf, P, t \vdash 1 \langle e;; e', s \rangle -ta \rightarrow \langle e'', s' \rangle$   
 $uf, P, t \vdash 1 \langle \text{throw } e, s \rangle -ta \rightarrow \langle e', s' \rangle$   
 $uf, P, t \vdash 1 \langle \text{try } e \text{ catch}(C V) e'', s \rangle -ta \rightarrow \langle e', s' \rangle$

**inductive** *Red1* ::

$\text{bool} \Rightarrow 'addr \text{ J1-prog} \Rightarrow 'thread\text{-}id \Rightarrow ('addr \text{ expr1} \times 'addr \text{ locals1}) \Rightarrow ('addr \text{ expr1} \times 'addr \text{ locals1})$   
 $\text{list} \Rightarrow 'heap$   
 $\Rightarrow ('addr, 'thread\text{-}id, 'heap) \text{ J1-thread-action}$   
 $\Rightarrow ('addr \text{ expr1} \times 'addr \text{ locals1}) \Rightarrow ('addr \text{ expr1} \times 'addr \text{ locals1}) \text{ list} \Rightarrow 'heap \Rightarrow \text{bool}$   
 $(-, -, \vdash 1 ((1 \langle -/-, - \rangle) \dashrightarrow / (1 \langle -/-, - \rangle))) [51, 51, 0, 0, 0, 0, 0, 0, 0] 81)$

**for** *uf* :: *bool* **and** *P* :: *'addr J1-prog* **and** *t* :: *'thread-id*

**where**

*red1Red*:

$uf, P, t \vdash 1 \langle e, (h, x) \rangle -ta \rightarrow \langle e', (h', x') \rangle$   
 $\implies uf, P, t \vdash 1 \langle (e, x)/exs, h \rangle -extTA2J1 P ta \rightarrow \langle (e', x')/exs, h' \rangle$

| *red1Call*:

$\llbracket \text{call1 } e = \lfloor (a, M, vs) \rfloor; \text{typeof-addr } h \text{ } a = \lfloor U \rfloor;$   
 $P \vdash \text{class-type-of } U \text{ sees } M:Ts \rightarrow T = \lfloor \text{body} \rfloor \text{ in } D;$   
 $\text{size } vs = \text{size } Ts \rrbracket$   
 $\implies uf, P, t \vdash 1 \langle (e, x)/exs, h \rangle -\varepsilon \rightarrow \langle (\text{blocks1 } 0 (\text{Class } D \# Ts) \text{ body}, \text{Addr } a \# vs @ \text{replicate}(\text{max-vars body}) \text{ undefined-value})/(e, x) \# exs, h \rangle$

| *red1Return*:

$\text{final } e' \implies uf, P, t \vdash 1 \langle (e', x')/(e, x) \# exs, h \rangle -\varepsilon \rightarrow \langle (\text{inline-call } e' e, x)/exs, h \rangle$

**abbreviation** *mred1g* :: *bool*  $\Rightarrow 'addr \text{ J1-prog} \Rightarrow ('addr, 'thread\text{-}id, ('addr \text{ expr1} \times 'addr \text{ locals1}) \times ('addr \text{ expr1} \times 'addr \text{ locals1}) \text{ list}, 'heap, 'addr, ('addr, 'thread\text{-}id) \text{ obs-event}) \text{ semantics}$

**where** *mred1g* *uf* *P*  $\equiv \lambda t ((ex, exs), h) ta ((ex', exs'), h'). uf, P, t \vdash 1 \langle ex/exs, h \rangle -ta \rightarrow \langle ex'/exs', h' \rangle$

**abbreviation** *mred1'* ::

$'addr \text{ J1-prog} \Rightarrow ('addr, 'thread\text{-}id, ('addr \text{ expr1} \times 'addr \text{ locals1}) \times ('addr \text{ expr1} \times 'addr \text{ locals1})$   
 $\text{list}, 'heap, 'addr, ('addr, 'thread\text{-}id) \text{ obs-event}) \text{ semantics}$

**where** *mred1'*  $\equiv \text{mred1g False}$

**abbreviation** *mred1* ::

$'addr \text{ J1-prog} \Rightarrow ('addr, 'thread\text{-}id, ('addr \text{ expr1} \times 'addr \text{ locals1}) \times ('addr \text{ expr1} \times 'addr \text{ locals1})$   
 $\text{list}, 'heap, 'addr, ('addr, 'thread\text{-}id) \text{ obs-event}) \text{ semantics}$

where  $mred1 \equiv mred1g \text{ True}$

**lemma** *red1-preserves-len*:  $uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies \text{length } (lcl \ s') = \text{length } (lcl \ s)$   
**and** *reds1-preserves-len*:  $uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies \text{length } (lcl \ s') = \text{length } (lcl \ s)$   
**by**(*induct rule*: *red1-reds1.inducts*)(*auto*)

**lemma** *reds1-preserves-elen*:  $uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies \text{length } es' = \text{length } es$   
**by**(*induct es arbitrary*: *es'*)(*auto elim*: *reds1.cases*)

**lemma** *red1-Val-iff* [*iff*]:  
 $\neg uf, P, t \vdash 1 \langle \text{Val } v, s \rangle -ta \rightarrow \langle e', s' \rangle$   
**by**(*auto elim*: *red1.cases*)

**lemma** *red1-Throw-iff* [*iff*]:  
 $\neg uf, P, t \vdash 1 \langle \text{Throw } a, xs \rangle -ta \rightarrow \langle e', s' \rangle$   
**by**(*auto elim*: *red1.cases*)

**lemma** *reds1-Nil-iff* [*iff*]:  
 $\neg uf, P, t \vdash 1 \langle [], s \rangle [-ta \rightarrow] \langle es', s' \rangle$   
**by**(*auto elim*: *reds1.cases*)

**lemma** *reds1-Val-iff* [*iff*]:  
 $\neg uf, P, t \vdash 1 \langle \text{map Val } vs, s \rangle [-ta \rightarrow] \langle es', s' \rangle$   
**by**(*induct vs arbitrary*: *es'*)(*auto elim*: *reds1.cases*)

**lemma** *reds1-map-Val-Throw-iff* [*iff*]:  
 $\neg uf, P, t \vdash 1 \langle \text{map Val } vs @ \text{Throw } a \# es, s \rangle [-ta \rightarrow] \langle es', s' \rangle$   
**by**(*induct vs arbitrary*: *es'*)(*auto elim*: *reds1.cases elim!*: *red1.cases*)

**lemma** *red1-max-vars-decr*:  $uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies \text{max-vars } e' \leq \text{max-vars } e$   
**and** *reds1-max-varss-decr*:  $uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies \text{max-varss } es' \leq \text{max-varss } es$   
**by**(*induct rule*: *red1-reds1.inducts*)(*auto*)

**lemma** *red1-new-thread-heap*:  $\llbracket uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{NewThread } t' \text{ ex } h \in \text{set } \{\!|ta|\!\}_t \rrbracket \implies h = \text{hp } s'$   
**and** *reds1-new-thread-heap*:  $\llbracket uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{NewThread } t' \text{ ex } h \in \text{set } \{\!|ta|\!\}_t \rrbracket \implies h = \text{hp } s'$   
**apply**(*induct rule*: *red1-reds1.inducts*)  
**apply**(*fastforce dest*: *red-ext-new-thread-heap simp add*: *ta-upd-simps*)  
**done**

**lemma** *red1-new-threadD*:  
 $\llbracket uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{NewThread } t' \text{ x } H \in \text{set } \{\!|ta|\!\}_t \rrbracket$   
 $\implies \exists a \ M \text{ vs } va \ T \ Ts \ Tr \ D. P, t \vdash \langle a \cdot M(\text{vs}), \text{hp } s \rangle -ta \rightarrow \text{ext } \langle va, \text{hp } s' \rangle \wedge \text{typeof-addr } (\text{hp } s) \ a =$   
 $\lfloor T \rfloor \wedge P \vdash \text{class-type-of } T \text{ sees } M:Ts \rightarrow Tr = \text{Native in } D$   
**and** *reds1-new-threadD*:  
 $\llbracket uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{NewThread } t' \text{ x } H \in \text{set } \{\!|ta|\!\}_t \rrbracket$   
 $\implies \exists a \ M \text{ vs } va \ T \ Ts \ Tr \ D. P, t \vdash \langle a \cdot M(\text{vs}), \text{hp } s \rangle -ta \rightarrow \text{ext } \langle va, \text{hp } s' \rangle \wedge \text{typeof-addr } (\text{hp } s) \ a =$   
 $\lfloor T \rfloor \wedge P \vdash \text{class-type-of } T \text{ sees } M:Ts \rightarrow Tr = \text{Native in } D$   
**by**(*induct rule*: *red1-reds1.inducts*)(*fastforce simp add*: *ta-upd-simps*)  
**done**

**lemma** *red1-call-synthesized*:  $\llbracket uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{call1 } e = \lfloor aMvs \rfloor \rrbracket \implies \text{synthesized-call } P \ (\text{hp } s) \ aMvs$   
**and** *reds1-calls-synthesized*:  $\llbracket uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{calls1 } es = \lfloor aMvs \rfloor \rrbracket \implies \text{synthe-}$

*sized-call*  $P$  (*hp s*) *aMvs*  
**apply**(*induct rule*: *red1-reds1.inducts*)  
**apply**(*auto split*: *if-split-asm simp add*: *is-vals-conv append-eq-map-conv synthesized-call-conv*)  
**apply** *blast*  
**done**

**lemma** *red1-preserves-B*:  $\llbracket uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \mathcal{B} \ e \ n \rrbracket \Longrightarrow \mathcal{B} \ e' \ n$   
**and** *reds1-preserves-Bs*:  $\llbracket uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \mathcal{B} s \ es \ n \rrbracket \Longrightarrow \mathcal{B} s \ es' \ n$   
**by**(*induct arbitrary*: *n* **and** *n rule*: *red1-reds1.inducts*)(*auto*)

**end**

**context** *J1-heap* **begin**

**lemma** *red1-heat-incr*:  $uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \Longrightarrow heat \ (hp \ s) \ (hp \ s')$   
**and** *reds1-heat-incr*:  $uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \Longrightarrow heat \ (hp \ s) \ (hp \ s')$   
**by**(*induct rule*: *red1-reds1.inducts*)(*auto intro*: *heat-heap-ops red-external-heat*)

**lemma** *Red1-heat-incr*:  $uf, P, t \vdash 1 \langle ex/exs, h \rangle -ta \rightarrow \langle ex'/exs', h' \rangle \Longrightarrow h \leq h'$   
**by**(*auto elim!*: *Red1.cases* *dest*: *red1-heat-incr*)

**end**

### 7.6.1 Silent moves

**context** *J1-heap-base* **begin**

**primrec**  $\tau move1 :: 'm \ prog \Rightarrow 'heap \Rightarrow ('a, 'b, 'addr) \ exp \Rightarrow bool$   
**and**  $\tau moves1 :: 'm \ prog \Rightarrow 'heap \Rightarrow ('a, 'b, 'addr) \ exp \ list \Rightarrow bool$   
**where**  
 $\tau move1 \ P \ h \ (new \ C) \longleftrightarrow False$   
 $\tau move1 \ P \ h \ (newA \ T \ [e]) \longleftrightarrow \tau move1 \ P \ h \ e \vee (\exists a. \ e = Throw \ a)$   
 $\tau move1 \ P \ h \ (Cast \ U \ e) \longleftrightarrow \tau move1 \ P \ h \ e \vee final \ e$   
 $\tau move1 \ P \ h \ (e \ instanceof \ T) \longleftrightarrow \tau move1 \ P \ h \ e \vee final \ e$   
 $\tau move1 \ P \ h \ (e \ll bop \gg e') \longleftrightarrow \tau move1 \ P \ h \ e \vee (\exists a. \ e = Throw \ a) \vee (\exists v. \ e = Val \ v \wedge (\tau move1 \ P \ h \ e' \vee final \ e'))$   
 $\tau move1 \ P \ h \ (Val \ v) \longleftrightarrow False$   
 $\tau move1 \ P \ h \ (Var \ V) \longleftrightarrow True$   
 $\tau move1 \ P \ h \ (V := e) \longleftrightarrow \tau move1 \ P \ h \ e \vee final \ e$   
 $\tau move1 \ P \ h \ (a[i]) \longleftrightarrow \tau move1 \ P \ h \ a \vee (\exists ad. \ a = Throw \ ad) \vee (\exists v. \ a = Val \ v \wedge (\tau move1 \ P \ h \ i \vee (\exists a. \ i = Throw \ a)))$   
 $\tau move1 \ P \ h \ (AAss \ a \ i \ e) \longleftrightarrow \tau move1 \ P \ h \ a \vee (\exists ad. \ a = Throw \ ad) \vee (\exists v. \ a = Val \ v \wedge (\tau move1 \ P \ h \ i \vee (\exists a. \ i = Throw \ a) \vee (\exists v. \ i = Val \ v \wedge (\tau move1 \ P \ h \ e \vee (\exists a. \ e = Throw \ a))))))$   
 $\tau move1 \ P \ h \ (a.length) \longleftrightarrow \tau move1 \ P \ h \ a \vee (\exists ad. \ a = Throw \ ad)$   
 $\tau move1 \ P \ h \ (e.F\{D\}) \longleftrightarrow \tau move1 \ P \ h \ e \vee (\exists a. \ e = Throw \ a)$   
 $\tau move1 \ P \ h \ (FAss \ e \ F \ D \ e') \longleftrightarrow \tau move1 \ P \ h \ e \vee (\exists a. \ e = Throw \ a) \vee (\exists v. \ e = Val \ v \wedge (\tau move1 \ P \ h \ e' \vee (\exists a. \ e' = Throw \ a)))$   
 $\tau move1 \ P \ h \ (e.compareAndSwap(D.F, e', e'')) \longleftrightarrow \tau move1 \ P \ h \ e \vee (\exists a. \ e = Throw \ a) \vee (\exists v. \ e = Val \ v \wedge (\tau move1 \ P \ h \ e' \vee (\exists a. \ e' = Throw \ a) \vee (\exists v. \ e' = Val \ v \wedge (\tau move1 \ P \ h \ e'' \vee (\exists a. \ e'' = Throw \ a))))))$   
 $\tau move1 \ P \ h \ (e.M(es)) \longleftrightarrow \tau move1 \ P \ h \ e \vee (\exists a. \ e = Throw \ a) \vee (\exists v. \ e = Val \ v \wedge (\tau moves1 \ P \ h \ es \vee (\exists vs \ a \ es'. \ es = map \ Val \ vs \ @ \ Throw \ a \ \# \ es') \vee (\exists vs. \ es = map \ Val \ vs \wedge (v = Null \vee (\forall T \ C \ Ts \ Tr \ D. \ typeof_h \ v = [T] \longrightarrow class-type-of' \ T =$

$[C] \longrightarrow P \vdash C \text{ sees } M; Ts \rightarrow Tr = \text{Native in } D \longrightarrow \tau \text{external-defs } D M))))$   
 $\mid \tau \text{move1 } P h (\{V:T=vo; e\}) \longleftrightarrow vo \neq \text{None} \vee \tau \text{move1 } P h e \vee \text{final } e$   
 $\mid \tau \text{move1 } P h (\text{sync}_{V'}(e) e') \longleftrightarrow \tau \text{move1 } P h e \vee (\exists a. e = \text{Throw } a)$   
 $\mid \tau \text{move1 } P h (\text{insync}_{V'}(\text{ad}) e) \longleftrightarrow \tau \text{move1 } P h e$   
 $\mid \tau \text{move1 } P h (e; e') \longleftrightarrow \tau \text{move1 } P h e \vee \text{final } e$   
 $\mid \tau \text{move1 } P h (\text{if } (e) e' \text{ else } e'') \longleftrightarrow \tau \text{move1 } P h e \vee \text{final } e$   
 $\mid \tau \text{move1 } P h (\text{while } (e) e') = \text{True}$   
 $\mid \tau \text{move1 } P h (\text{throw } e) \longleftrightarrow \tau \text{move1 } P h e \vee (\exists a. e = \text{Throw } a) \vee e = \text{null}$   
 $\mid \tau \text{move1 } P h (\text{try } e \text{ catch } (C V) e') \longleftrightarrow \tau \text{move1 } P h e \vee \text{final } e$

$\mid \tau \text{moves1 } P h [] \longleftrightarrow \text{False}$   
 $\mid \tau \text{moves1 } P h (e \# es) \longleftrightarrow \tau \text{move1 } P h e \vee (\exists v. e = \text{Val } v \wedge \tau \text{moves1 } P h es)$

**fun**  $\tau \text{Move1} :: 'm \text{ prog} \Rightarrow 'heap \Rightarrow (('a, 'b, 'addr) \text{ exp} \times 'c) \times (('a, 'b, 'addr) \text{ exp} \times 'd) \text{ list} \Rightarrow \text{bool}$   
**where**  
 $\tau \text{Move1 } P h ((e, x), es) = (\tau \text{move1 } P h e \vee \text{final } e)$

**definition**  $\tau \text{red1g} :: \text{bool} \Rightarrow 'addr \text{ J1-prog} \Rightarrow 'thread\text{-id} \Rightarrow 'heap \Rightarrow ('addr \text{ expr1} \times 'addr \text{ locals1}) \Rightarrow ('addr \text{ expr1} \times 'addr \text{ locals1}) \Rightarrow \text{bool}$   
**where**  $\tau \text{red1g } uf P t h es es'xs' = (uf, P, t \vdash 1 \langle \text{fst } es, (h, \text{snd } es) \rangle \dashv \varepsilon \rightarrow \langle \text{fst } es'xs', (h, \text{snd } es'xs') \rangle \wedge \tau \text{move1 } P h (\text{fst } es))$

**definition**  $\tau \text{reds1g} :: \text{bool} \Rightarrow 'addr \text{ J1-prog} \Rightarrow 'thread\text{-id} \Rightarrow 'heap \Rightarrow ('addr \text{ expr1 list} \times 'addr \text{ locals1}) \Rightarrow ('addr \text{ expr1 list} \times 'addr \text{ locals1}) \Rightarrow \text{bool}$   
**where**  
 $\tau \text{reds1g } uf P t h esxs es'xs' = (uf, P, t \vdash 1 \langle \text{fst } esxs, (h, \text{snd } esxs) \rangle \dashv \varepsilon \rightarrow \langle \text{fst } es'xs', (h, \text{snd } es'xs') \rangle \wedge \tau \text{moves1 } P h (\text{fst } esxs))$

**abbreviation**  $\tau \text{red1gt} :: \text{bool} \Rightarrow 'addr \text{ J1-prog} \Rightarrow 'thread\text{-id} \Rightarrow 'heap \Rightarrow ('addr \text{ expr1} \times 'addr \text{ locals1}) \Rightarrow ('addr \text{ expr1} \times 'addr \text{ locals1}) \Rightarrow \text{bool}$   
**where**  $\tau \text{red1gt } uf P t h \equiv (\tau \text{red1g } uf P t h)^{++}$

**abbreviation**  $\tau \text{reds1gt} :: \text{bool} \Rightarrow 'addr \text{ J1-prog} \Rightarrow 'thread\text{-id} \Rightarrow 'heap \Rightarrow ('addr \text{ expr1 list} \times 'addr \text{ locals1}) \Rightarrow ('addr \text{ expr1 list} \times 'addr \text{ locals1}) \Rightarrow \text{bool}$   
**where**  $\tau \text{reds1gt } uf P t h \equiv (\tau \text{reds1g } uf P t h)^{++}$

**abbreviation**  $\tau \text{red1gr} :: \text{bool} \Rightarrow 'addr \text{ J1-prog} \Rightarrow 'thread\text{-id} \Rightarrow 'heap \Rightarrow ('addr \text{ expr1} \times 'addr \text{ locals1}) \Rightarrow ('addr \text{ expr1} \times 'addr \text{ locals1}) \Rightarrow \text{bool}$   
**where**  $\tau \text{red1gr } uf P t h \equiv (\tau \text{red1g } uf P t h)^{**}$

**abbreviation**  $\tau \text{reds1gr} :: \text{bool} \Rightarrow 'addr \text{ J1-prog} \Rightarrow 'thread\text{-id} \Rightarrow 'heap \Rightarrow ('addr \text{ expr1 list} \times 'addr \text{ locals1}) \Rightarrow ('addr \text{ expr1 list} \times 'addr \text{ locals1}) \Rightarrow \text{bool}$   
**where**  $\tau \text{reds1gr } uf P t h \equiv (\tau \text{reds1g } uf P t h)^{**}$

**definition**  $\tau \text{Red1g} :: \text{bool} \Rightarrow 'addr \text{ J1-prog} \Rightarrow 'thread\text{-id} \Rightarrow 'heap \Rightarrow ('addr \text{ expr1} \times 'addr \text{ locals1}) \times (('addr \text{ expr1} \times 'addr \text{ locals1}) \text{ list}) \Rightarrow ('addr \text{ expr1} \times 'addr \text{ locals1}) \times (('addr \text{ expr1} \times 'addr \text{ locals1}) \text{ list}) \Rightarrow \text{bool}$   
**where**  $\tau \text{Red1g } uf P t h exxs ex'xs' = (uf, P, t \vdash 1 \langle \text{fst } exxs/\text{snd } exxs, h \rangle \dashv \varepsilon \rightarrow \langle \text{fst } ex'xs'/\text{snd } ex'xs', h \rangle)$

$ex'xs', h\rangle \wedge \tau Move1\ P\ h\ exxs)$

**abbreviation**  $\tau Red1gt ::$

$bool \Rightarrow 'addr\ J1\text{-}prog \Rightarrow 'thread\text{-}id \Rightarrow 'heap \Rightarrow ('addr\ expr1 \times 'addr\ locals1) \times (('addr\ expr1 \times 'addr\ locals1)\ list)$

$\Rightarrow ('addr\ expr1 \times 'addr\ locals1) \times (('addr\ expr1 \times 'addr\ locals1)\ list) \Rightarrow bool$

**where**  $\tau Red1gt\ uf\ P\ t\ h \equiv (\tau Red1g\ uf\ P\ t\ h)^{++}$

**abbreviation**  $\tau Red1gr ::$

$bool \Rightarrow 'addr\ J1\text{-}prog \Rightarrow 'thread\text{-}id \Rightarrow 'heap \Rightarrow ('addr\ expr1 \times 'addr\ locals1) \times (('addr\ expr1 \times 'addr\ locals1)\ list)$

$\Rightarrow ('addr\ expr1 \times 'addr\ locals1) \times (('addr\ expr1 \times 'addr\ locals1)\ list) \Rightarrow bool$

**where**  $\tau Red1gr\ uf\ P\ t\ h \equiv (\tau Red1g\ uf\ P\ t\ h)^{**}$

**abbreviation**  $\tau red1 ::$

$'addr\ J1\text{-}prog \Rightarrow 'thread\text{-}id \Rightarrow 'heap \Rightarrow ('addr\ expr1 \times 'addr\ locals1) \Rightarrow ('addr\ expr1 \times 'addr\ locals1) \Rightarrow bool$

**where**  $\tau red1 \equiv \tau red1g\ True$

**abbreviation**  $\tau reds1 ::$

$'addr\ J1\text{-}prog \Rightarrow 'thread\text{-}id \Rightarrow 'heap \Rightarrow ('addr\ expr1\ list \times 'addr\ locals1) \Rightarrow ('addr\ expr1\ list \times 'addr\ locals1) \Rightarrow bool$

**where**  $\tau reds1 \equiv \tau reds1g\ True$

**abbreviation**  $\tau red1t ::$

$'addr\ J1\text{-}prog \Rightarrow 'thread\text{-}id \Rightarrow 'heap \Rightarrow ('addr\ expr1 \times 'addr\ locals1) \Rightarrow ('addr\ expr1 \times 'addr\ locals1) \Rightarrow bool$

**where**  $\tau red1t \equiv \tau red1gt\ True$

**abbreviation**  $\tau reds1t ::$

$'addr\ J1\text{-}prog \Rightarrow 'thread\text{-}id \Rightarrow 'heap \Rightarrow ('addr\ expr1\ list \times 'addr\ locals1) \Rightarrow ('addr\ expr1\ list \times 'addr\ locals1) \Rightarrow bool$

**where**  $\tau reds1t \equiv \tau reds1gt\ True$

**abbreviation**  $\tau red1r ::$

$'addr\ J1\text{-}prog \Rightarrow 'thread\text{-}id \Rightarrow 'heap \Rightarrow ('addr\ expr1 \times 'addr\ locals1) \Rightarrow ('addr\ expr1 \times 'addr\ locals1) \Rightarrow bool$

**where**  $\tau red1r \equiv \tau red1gr\ True$

**abbreviation**  $\tau reds1r ::$

$'addr\ J1\text{-}prog \Rightarrow 'thread\text{-}id \Rightarrow 'heap \Rightarrow ('addr\ expr1\ list \times 'addr\ locals1) \Rightarrow ('addr\ expr1\ list \times 'addr\ locals1) \Rightarrow bool$

**where**  $\tau reds1r \equiv \tau reds1gr\ True$

**abbreviation**  $\tau Red1 ::$

$'addr\ J1\text{-}prog \Rightarrow 'thread\text{-}id \Rightarrow 'heap \Rightarrow ('addr\ expr1 \times 'addr\ locals1) \times (('addr\ expr1 \times 'addr\ locals1)\ list)$

$\Rightarrow ('addr\ expr1 \times 'addr\ locals1) \times (('addr\ expr1 \times 'addr\ locals1)\ list) \Rightarrow bool$

**where**  $\tau Red1 \equiv \tau Red1g\ True$

**abbreviation**  $\tau Red1t ::$

$'addr\ J1\text{-}prog \Rightarrow 'thread\text{-}id \Rightarrow 'heap \Rightarrow ('addr\ expr1 \times 'addr\ locals1) \times (('addr\ expr1 \times 'addr\ locals1)\ list)$

$\Rightarrow ('addr\ expr1 \times 'addr\ locals1) \times (('addr\ expr1 \times 'addr\ locals1)\ list) \Rightarrow bool$



**where**  $\tau Red1t \equiv \tau Red1gt \text{ True}$

**abbreviation**  $\tau Red1r ::$

$'addr \ J1\text{-prog} \Rightarrow 'thread\text{-id} \Rightarrow 'heap \Rightarrow ('addr \ expr1 \times 'addr \ locals1) \times (('addr \ expr1 \times 'addr \ locals1) \ list)$

$\Rightarrow ('addr \ expr1 \times 'addr \ locals1) \times (('addr \ expr1 \times 'addr \ locals1) \ list) \Rightarrow bool$

**where**  $\tau Red1r \equiv \tau Red1gr \text{ True}$

**abbreviation**  $\tau red1' ::$

$'addr \ J1\text{-prog} \Rightarrow 'thread\text{-id} \Rightarrow 'heap \Rightarrow ('addr \ expr1 \times 'addr \ locals1) \Rightarrow ('addr \ expr1 \times 'addr \ locals1) \Rightarrow bool$

**where**  $\tau red1' \equiv \tau red1g \text{ False}$

**abbreviation**  $\tau reds1' ::$

$'addr \ J1\text{-prog} \Rightarrow 'thread\text{-id} \Rightarrow 'heap \Rightarrow ('addr \ expr1 \ list \times 'addr \ locals1) \Rightarrow ('addr \ expr1 \ list \times 'addr \ locals1) \Rightarrow bool$

**where**  $\tau reds1' \equiv \tau reds1g \text{ False}$

**abbreviation**  $\tau red1't ::$

$'addr \ J1\text{-prog} \Rightarrow 'thread\text{-id} \Rightarrow 'heap \Rightarrow ('addr \ expr1 \times 'addr \ locals1) \Rightarrow ('addr \ expr1 \times 'addr \ locals1) \Rightarrow bool$

**where**  $\tau red1't \equiv \tau red1gt \text{ False}$

**abbreviation**  $\tau reds1't ::$

$'addr \ J1\text{-prog} \Rightarrow 'thread\text{-id} \Rightarrow 'heap \Rightarrow ('addr \ expr1 \ list \times 'addr \ locals1) \Rightarrow ('addr \ expr1 \ list \times 'addr \ locals1) \Rightarrow bool$

**where**  $\tau reds1't \equiv \tau reds1gt \text{ False}$

**abbreviation**  $\tau red1'r ::$

$'addr \ J1\text{-prog} \Rightarrow 'thread\text{-id} \Rightarrow 'heap \Rightarrow ('addr \ expr1 \times 'addr \ locals1) \Rightarrow ('addr \ expr1 \times 'addr \ locals1) \Rightarrow bool$

**where**  $\tau red1'r \equiv \tau red1gr \text{ False}$

**abbreviation**  $\tau reds1'r ::$

$'addr \ J1\text{-prog} \Rightarrow 'thread\text{-id} \Rightarrow 'heap \Rightarrow ('addr \ expr1 \ list \times 'addr \ locals1) \Rightarrow ('addr \ expr1 \ list \times 'addr \ locals1) \Rightarrow bool$

**where**  $\tau reds1'r \equiv \tau reds1gr \text{ False}$

**abbreviation**  $\tau Red1' ::$

$'addr \ J1\text{-prog} \Rightarrow 'thread\text{-id} \Rightarrow 'heap \Rightarrow ('addr \ expr1 \times 'addr \ locals1) \times (('addr \ expr1 \times 'addr \ locals1) \ list)$

$\Rightarrow ('addr \ expr1 \times 'addr \ locals1) \times (('addr \ expr1 \times 'addr \ locals1) \ list) \Rightarrow bool$

**where**  $\tau Red1' \equiv \tau Red1g \text{ False}$

**abbreviation**  $\tau Red1't ::$

$'addr \ J1\text{-prog} \Rightarrow 'thread\text{-id} \Rightarrow 'heap \Rightarrow ('addr \ expr1 \times 'addr \ locals1) \times (('addr \ expr1 \times 'addr \ locals1) \ list)$

$\Rightarrow ('addr \ expr1 \times 'addr \ locals1) \times (('addr \ expr1 \times 'addr \ locals1) \ list) \Rightarrow bool$

**where**  $\tau Red1't \equiv \tau Red1gt \text{ False}$

**abbreviation**  $\tau Red1'r ::$

$'addr \ J1\text{-prog} \Rightarrow 'thread\text{-id} \Rightarrow 'heap \Rightarrow ('addr \ expr1 \times 'addr \ locals1) \times (('addr \ expr1 \times 'addr \ locals1) \ list)$

$\Rightarrow ('addr \ expr1 \times 'addr \ locals1) \times (('addr \ expr1 \times 'addr \ locals1) \ list) \Rightarrow bool$

where  $\tau Red1' r \equiv \tau Red1gr \text{ False}$

**abbreviation**  $\tau MOVE1 ::$

$'m \text{ prog} \Rightarrow (((('addr \text{ expr1} \times 'addr \text{ locals1}) \times ('addr \text{ expr1} \times 'addr \text{ locals1}) \text{ list}) \times 'heap, ('addr, 'thread-id, 'heap) \text{ J1-thread-action}) \text{ trsys}$

where  $\tau MOVE1 P \equiv \lambda(exers, h) \text{ ta } s. \tau Move1 P h \text{ exers} \wedge \text{ta} = \varepsilon$

**lemma**  $\tau move1\text{-}\tau moves1\text{-intros}$ :

**fixes**  $e :: ('a, 'b, 'addr) \text{ exp}$  **and**  $es :: ('a, 'b, 'addr) \text{ exp list}$   
**shows**  $\tau move1NewArray: \tau move1 P h e \Rightarrow \tau move1 P h (\text{newA } T[e])$   
**and**  $\tau move1Cast: \tau move1 P h e \Rightarrow \tau move1 P h (\text{Cast } U e)$   
**and**  $\tau move1CastRed: \tau move1 P h (\text{Cast } U (\text{Val } v))$   
**and**  $\tau move1InstanceOf: \tau move1 P h e \Rightarrow \tau move1 P h (e \text{ instanceof } U)$   
**and**  $\tau move1InstanceOfRed: \tau move1 P h ((\text{Val } v) \text{ instanceof } U)$   
**and**  $\tau move1BinOp1: \tau move1 P h e \Rightarrow \tau move1 P h (e \ll bop \gg e')$   
**and**  $\tau move1BinOp2: \tau move1 P h e \Rightarrow \tau move1 P h (\text{Val } v \ll bop \gg e)$   
**and**  $\tau move1BinOp: \tau move1 P h (\text{Val } v \ll bop \gg \text{Val } v')$   
**and**  $\tau move1Var: \tau move1 P h (\text{Var } V)$   
**and**  $\tau move1LAss: \tau move1 P h e \Rightarrow \tau move1 P h (V := e)$   
**and**  $\tau move1LAssRed: \tau move1 P h (V := \text{Val } v)$   
**and**  $\tau move1AAcc1: \tau move1 P h e \Rightarrow \tau move1 P h (e[e'])$   
**and**  $\tau move1AAcc2: \tau move1 P h e \Rightarrow \tau move1 P h (\text{Val } v[e'])$   
**and**  $\tau move1AAss1: \tau move1 P h e \Rightarrow \tau move1 P h (AAss e e' e'')$   
**and**  $\tau move1AAss2: \tau move1 P h e \Rightarrow \tau move1 P h (AAss (\text{Val } v) e e')$   
**and**  $\tau move1AAss3: \tau move1 P h e \Rightarrow \tau move1 P h (AAss (\text{Val } v) (\text{Val } v') e)$   
**and**  $\tau move1ALength: \tau move1 P h e \Rightarrow \tau move1 P h (e.length)$   
**and**  $\tau move1FAcc: \tau move1 P h e \Rightarrow \tau move1 P h (e.F\{D\})$   
**and**  $\tau move1FAss1: \tau move1 P h e \Rightarrow \tau move1 P h (FAss e F D e')$   
**and**  $\tau move1FAss2: \tau move1 P h e \Rightarrow \tau move1 P h (FAss (\text{Val } v) F D e)$   
**and**  $\tau move1CAS1: \tau move1 P h e \Rightarrow \tau move1 P h (e.compareAndSwap(D.F, e', e''))$   
**and**  $\tau move1CAS2: \tau move1 P h e \Rightarrow \tau move1 P h (\text{Val } v.compareAndSwap(D.F, e, e''))$   
**and**  $\tau move1CAS3: \tau move1 P h e \Rightarrow \tau move1 P h (\text{Val } v.compareAndSwap(D.F, \text{Val } v', e))$   
**and**  $\tau move1CallObj: \tau move1 P h \text{ obj} \Rightarrow \tau move1 P h (\text{obj}.M(ps))$   
**and**  $\tau move1CallParams: \tau moves1 P h ps \Rightarrow \tau move1 P h (\text{Val } v.M(ps))$   
**and**  $\tau move1Call: (\bigwedge T C Ts \text{ Tr } D. \llbracket \text{typeof}_h v = T \rrbracket; \text{class-type-of}' T = C; P \vdash C \text{ sees } M: Ts \rightarrow Tr = \text{Native in } D \rrbracket \Rightarrow \tau external\text{-defs } D M) \Rightarrow \tau move1 P h (\text{Val } v.M(\text{map } \text{Val } vs))$   
**and**  $\tau move1BlockSome: \tau move1 P h \{V:T=[v]; e\}$   
**and**  $\tau move1Block: \tau move1 P h e \Rightarrow \tau move1 P h \{V:T=None; e\}$   
**and**  $\tau move1BlockRed: \tau move1 P h \{V:T=None; \text{Val } v\}$   
**and**  $\tau move1Sync: \tau move1 P h e \Rightarrow \tau move1 P h (\text{sync}_{V'}(e) e')$   
**and**  $\tau move1InSync: \tau move1 P h e \Rightarrow \tau move1 P h (\text{insync}_{V'}(a) e)$   
**and**  $\tau move1Seq: \tau move1 P h e \Rightarrow \tau move1 P h (e;;e')$   
**and**  $\tau move1SeqRed: \tau move1 P h (\text{Val } v;; e)$   
**and**  $\tau move1Cond: \tau move1 P h e \Rightarrow \tau move1 P h (\text{if } (e) e1 \text{ else } e2)$   
**and**  $\tau move1CondRed: \tau move1 P h (\text{if } (\text{Val } v) e1 \text{ else } e2)$   
**and**  $\tau move1WhileRed: \tau move1 P h (\text{while } (c) e)$   
**and**  $\tau move1Throw: \tau move1 P h e \Rightarrow \tau move1 P h (\text{throw } e)$   
**and**  $\tau move1ThrowNull: \tau move1 P h (\text{throw null})$   
**and**  $\tau move1Try: \tau move1 P h e \Rightarrow \tau move1 P h (\text{try } e \text{ catch } (C V) e'')$   
**and**  $\tau move1TryRed: \tau move1 P h (\text{try } \text{Val } v \text{ catch } (C V) e)$   
**and**  $\tau move1TryThrow: \tau move1 P h (\text{try } \text{Throw } a \text{ catch } (C V) e)$   
**and**  $\tau move1NewArrayThrow: \tau move1 P h (\text{newA } T[\text{Throw } a])$   
**and**  $\tau move1CastThrow: \tau move1 P h (\text{Cast } T (\text{Throw } a))$   
**and**  $\tau move1InstanceOfThrow: \tau move1 P h ((\text{Throw } a) \text{ instanceof } T)$

**and**  $\tau\text{move1BinOpThrow1}$ :  $\tau\text{move1 } P \ h \ (\text{Throw } a \ \langle\text{bop}\rangle \ e2)$   
**and**  $\tau\text{move1BinOpThrow2}$ :  $\tau\text{move1 } P \ h \ (\text{Val } v \ \langle\text{bop}\rangle \ \text{Throw } a)$   
**and**  $\tau\text{move1LAssThrow}$ :  $\tau\text{move1 } P \ h \ (V := (\text{Throw } a))$   
**and**  $\tau\text{move1AAccThrow1}$ :  $\tau\text{move1 } P \ h \ (\text{Throw } a[e])$   
**and**  $\tau\text{move1AAccThrow2}$ :  $\tau\text{move1 } P \ h \ (\text{Val } v[\text{Throw } a])$   
**and**  $\tau\text{move1AAssThrow1}$ :  $\tau\text{move1 } P \ h \ (A\text{Ass } (\text{Throw } a) \ e \ e')$   
**and**  $\tau\text{move1AAssThrow2}$ :  $\tau\text{move1 } P \ h \ (A\text{Ass } (\text{Val } v) \ (\text{Throw } a) \ e')$   
**and**  $\tau\text{move1AAssThrow3}$ :  $\tau\text{move1 } P \ h \ (A\text{Ass } (\text{Val } v) \ (\text{Val } v') \ (\text{Throw } a))$   
**and**  $\tau\text{move1ALengthThrow}$ :  $\tau\text{move1 } P \ h \ (\text{Throw } a.\text{length})$   
**and**  $\tau\text{move1FAccThrow}$ :  $\tau\text{move1 } P \ h \ (\text{Throw } a.F\{D\})$   
**and**  $\tau\text{move1FAssThrow1}$ :  $\tau\text{move1 } P \ h \ (\text{Throw } a.F\{D\} := e)$   
**and**  $\tau\text{move1FAssThrow2}$ :  $\tau\text{move1 } P \ h \ (F\text{Ass } (\text{Val } v) \ F \ D \ (\text{Throw } a))$   
**and**  $\tau\text{move1CASThrow1}$ :  $\tau\text{move1 } P \ h \ (\text{CompareAndSwap } (\text{Throw } a) \ D \ F \ e \ e')$   
**and**  $\tau\text{move1CASThrow2}$ :  $\tau\text{move1 } P \ h \ (\text{CompareAndSwap } (\text{Val } v) \ D \ F \ (\text{Throw } a) \ e')$   
**and**  $\tau\text{move1CASThrow3}$ :  $\tau\text{move1 } P \ h \ (\text{CompareAndSwap } (\text{Val } v) \ D \ F \ (\text{Val } v') \ (\text{Throw } a))$   
**and**  $\tau\text{move1CallThrowObj}$ :  $\tau\text{move1 } P \ h \ (\text{Throw } a.M(es))$   
**and**  $\tau\text{move1CallThrowParams}$ :  $\tau\text{move1 } P \ h \ (\text{Val } v.M(\text{map } \text{Val } vs \ @ \ \text{Throw } a \ \# \ es))$   
**and**  $\tau\text{move1BlockThrow}$ :  $\tau\text{move1 } P \ h \ \{V:T=None; \text{Throw } a\}$   
**and**  $\tau\text{move1SyncThrow}$ :  $\tau\text{move1 } P \ h \ (\text{sync}_{V'} \ (\text{Throw } a) \ e)$   
**and**  $\tau\text{move1SeqThrow}$ :  $\tau\text{move1 } P \ h \ (\text{Throw } a;;e)$   
**and**  $\tau\text{move1CondThrow}$ :  $\tau\text{move1 } P \ h \ (\text{if } (\text{Throw } a) \ e1 \ \text{else } e2)$   
**and**  $\tau\text{move1ThrowThrow}$ :  $\tau\text{move1 } P \ h \ (\text{throw } (\text{Throw } a))$

**and**  $\tau\text{moves1Hd}$ :  $\tau\text{move1 } P \ h \ e \implies \tau\text{moves1 } P \ h \ (e \ \# \ es)$   
**and**  $\tau\text{moves1Tl}$ :  $\tau\text{moves1 } P \ h \ es \implies \tau\text{moves1 } P \ h \ (\text{Val } v \ \# \ es)$   
**by** *fastforce*+

**lemma**  $\tau\text{moves1-map-Val } [dest!]$ :  
 $\tau\text{moves1 } P \ h \ (\text{map } \text{Val } es) \implies \text{False}$   
**by**(*induct es*)(*auto*)

**lemma**  $\tau\text{moves1-map-Val-ThrowD } [simp]$ :  $\tau\text{moves1 } P \ h \ (\text{map } \text{Val } vs \ @ \ \text{Throw } a \ \# \ es) = \text{False}$   
**by**(*induct vs*)(*fastforce*)+

**lemma** **fixes**  $e :: ('a, 'b, 'addr) \text{exp}$  **and**  $es :: ('a, 'b, 'addr) \text{exp list}$   
**shows**  $\tau\text{move1-not-call1}$ :  
 $\text{call1 } e = [(a, M, vs)] \implies \tau\text{move1 } P \ h \ e \longleftrightarrow (\text{synthesized-call } P \ h \ (a, M, vs) \longrightarrow \tau\text{external}' P \ h \ a \ M)$   
**and**  $\tau\text{moves1-not-calls1}$ :  
 $\text{calls1 } es = [(a, M, vs)] \implies \tau\text{moves1 } P \ h \ es \longleftrightarrow (\text{synthesized-call } P \ h \ (a, M, vs) \longrightarrow \tau\text{external}' P \ h \ a \ M)$   
**apply**(*induct e and es rule: call1.induct calls1.induct*)  
**apply**(*auto split: if-split-asm simp add: is-vals-conv*)  
**apply**(*fastforce simp add: synthesized-call-def map-eq-append-conv  $\tau\text{external}'$ -def  $\tau\text{external}$ -def dest: sees-method-fun*)  
**done**

**lemma**  $\text{red1-}\tau\text{-taD}$ :  $\llbracket \text{uf}, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \tau\text{move1 } P \ (hp \ s) \ e \rrbracket \implies ta = \varepsilon$   
**and**  $\text{reds1-}\tau\text{-taD}$ :  $\llbracket \text{uf}, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \tau\text{moves1 } P \ (hp \ s) \ es \rrbracket \implies ta = \varepsilon$   
**apply**(*induct rule: red1-reds1.inducts*)  
**apply**(*fastforce simp add: map-eq-append-conv  $\tau\text{external}'$ -def  $\tau\text{external}$ -def dest:  $\tau\text{external}'$ -red-external-TA-empty*)  
**done**

**lemma**  $\tau\text{move1-heap-unchanged}$ :  $\llbracket \text{uf}, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \tau\text{move1 } P \ (hp \ s) \ e \rrbracket \implies hp \ s' =$

$hp\ s$   
**and**  $\tau moves1\text{-heap-unchanged}$ :  $\llbracket uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \tau moves1\ P\ (hp\ s)\ es \rrbracket \implies hp\ s'$   
 $= hp\ s$   
**apply**(*induct rule: red1-reds1.inducts*)  
**apply**(*auto*)  
**apply**(*fastforce simp add: map-eq-append-conv  $\tau external'$ -def  $\tau external$ -def dest:  $\tau external'$ -red-external-heap-unch*)  
**done**

**lemma**  $\tau Move1\text{-iff}$ :

$\tau Move1\ P\ h\ exes \longleftrightarrow (let\ ((e, -), -) = exes\ in\ \tau move1\ P\ h\ e \vee final\ e)$   
**by**(*cases exes*)(*auto*)

**lemma**  $\tau red1\text{-iff}$  [*iff*]:

$\tau red1g\ uf\ P\ t\ h\ (e, xs)\ (e', xs') = (uf, P, t \vdash 1 \langle e, (h, xs) \rangle -\varepsilon \rightarrow \langle e', (h, xs') \rangle \wedge \tau move1\ P\ h\ e)$   
**by**(*simp add:  $\tau red1g$ -def*)

**lemma**  $\tau reds1\text{-iff}$  [*iff*]:

$\tau reds1g\ uf\ P\ t\ h\ (es, xs)\ (es', xs') = (uf, P, t \vdash 1 \langle es, (h, xs) \rangle [-\varepsilon \rightarrow] \langle es', (h, xs') \rangle \wedge \tau moves1\ P\ h\ es)$   
**by**(*simp add:  $\tau reds1g$ -def*)

**lemma**  $\tau red1t\text{-1step}$ :

$\llbracket uf, P, t \vdash 1 \langle e, (h, xs) \rangle -\varepsilon \rightarrow \langle e', (h, xs') \rangle; \tau move1\ P\ h\ e \rrbracket$   
 $\implies \tau red1gt\ uf\ P\ t\ h\ (e, xs)\ (e', xs')$   
**by**(*blast intro: tranclp.r-into-trancl*)

**lemma**  $\tau red1t\text{-2step}$ :

$\llbracket uf, P, t \vdash 1 \langle e, (h, xs) \rangle -\varepsilon \rightarrow \langle e', (h, xs') \rangle; \tau move1\ P\ h\ e;$   
 $uf, P, t \vdash 1 \langle e', (h, xs') \rangle -\varepsilon \rightarrow \langle e'', (h, xs'') \rangle; \tau move1\ P\ h\ e' \rrbracket$   
 $\implies \tau red1gt\ uf\ P\ t\ h\ (e, xs)\ (e'', xs'')$   
**by**(*blast intro: tranclp.trancl-into-trancl[OF  $\tau red1t\text{-1step}$ ]*)

**lemma**  $\tau red1t\text{-3step}$ :

$\llbracket uf, P, t \vdash 1 \langle e, (h, xs) \rangle -\varepsilon \rightarrow \langle e', (h, xs') \rangle; \tau move1\ P\ h\ e;$   
 $uf, P, t \vdash 1 \langle e', (h, xs') \rangle -\varepsilon \rightarrow \langle e'', (h, xs'') \rangle; \tau move1\ P\ h\ e';$   
 $uf, P, t \vdash 1 \langle e'', (h, xs'') \rangle -\varepsilon \rightarrow \langle e''', (h, xs''') \rangle; \tau move1\ P\ h\ e'' \rrbracket$   
 $\implies \tau red1gt\ uf\ P\ t\ h\ (e, xs)\ (e''', xs''')$   
**by**(*blast intro: tranclp.trancl-into-trancl[OF  $\tau red1t\text{-2step}$ ]*)

**lemma**  $\tau reds1t\text{-1step}$ :

$\llbracket uf, P, t \vdash 1 \langle es, (h, xs) \rangle [-\varepsilon \rightarrow] \langle es', (h, xs') \rangle; \tau moves1\ P\ h\ es \rrbracket$   
 $\implies \tau reds1gt\ uf\ P\ t\ h\ (es, xs)\ (es', xs')$   
**by**(*blast intro: tranclp.r-into-trancl*)

**lemma**  $\tau reds1t\text{-2step}$ :

$\llbracket uf, P, t \vdash 1 \langle es, (h, xs) \rangle [-\varepsilon \rightarrow] \langle es', (h, xs') \rangle; \tau moves1\ P\ h\ es;$   
 $uf, P, t \vdash 1 \langle es', (h, xs') \rangle [-\varepsilon \rightarrow] \langle es'', (h, xs'') \rangle; \tau moves1\ P\ h\ es' \rrbracket$   
 $\implies \tau reds1gt\ uf\ P\ t\ h\ (es, xs)\ (es'', xs'')$   
**by**(*blast intro: tranclp.trancl-into-trancl[OF  $\tau reds1t\text{-1step}$ ]*)

**lemma**  $\tau reds1t\text{-3step}$ :

$\llbracket uf, P, t \vdash 1 \langle es, (h, xs) \rangle [-\varepsilon \rightarrow] \langle es', (h, xs') \rangle; \tau moves1\ P\ h\ es;$   
 $uf, P, t \vdash 1 \langle es', (h, xs') \rangle [-\varepsilon \rightarrow] \langle es'', (h, xs'') \rangle; \tau moves1\ P\ h\ es';$   
 $uf, P, t \vdash 1 \langle es'', (h, xs'') \rangle [-\varepsilon \rightarrow] \langle es''', (h, xs''') \rangle; \tau moves1\ P\ h\ es'' \rrbracket$

$\Rightarrow \tau\text{reds1gt } uf P t h (es, xs) (es''', xs''')$   
**by**(blast intro: tranclp.trancl-into-trancl[OF  $\tau\text{reds1t-2step}$ ])

**lemma**  $\tau\text{red1r-1step}$ :  
 $\llbracket uf, P, t \vdash 1 \langle e, (h, xs) \rangle -\varepsilon \rightarrow \langle e', (h, xs') \rangle; \tau\text{move1 } P h e \rrbracket$   
 $\Rightarrow \tau\text{red1gr } uf P t h (e, xs) (e', xs')$   
**by**(blast intro: r-into-rtranclp)

**lemma**  $\tau\text{red1r-2step}$ :  
 $\llbracket uf, P, t \vdash 1 \langle e, (h, xs) \rangle -\varepsilon \rightarrow \langle e', (h, xs') \rangle; \tau\text{move1 } P h e;$   
 $uf, P, t \vdash 1 \langle e', (h, xs') \rangle -\varepsilon \rightarrow \langle e'', (h, xs'') \rangle; \tau\text{move1 } P h e' \rrbracket$   
 $\Rightarrow \tau\text{red1gr } uf P t h (e, xs) (e'', xs'')$   
**by**(blast intro: rtranclp.rtrancl-into-rtrancl[OF  $\tau\text{red1r-1step}$ ])

**lemma**  $\tau\text{red1r-3step}$ :  
 $\llbracket uf, P, t \vdash 1 \langle e, (h, xs) \rangle -\varepsilon \rightarrow \langle e', (h, xs') \rangle; \tau\text{move1 } P h e;$   
 $uf, P, t \vdash 1 \langle e', (h, xs') \rangle -\varepsilon \rightarrow \langle e'', (h, xs'') \rangle; \tau\text{move1 } P h e';$   
 $uf, P, t \vdash 1 \langle e'', (h, xs'') \rangle -\varepsilon \rightarrow \langle e''', (h, xs''') \rangle; \tau\text{move1 } P h e'' \rrbracket$   
 $\Rightarrow \tau\text{red1gr } uf P t h (e, xs) (e''', xs''')$   
**by**(blast intro: rtranclp.rtrancl-into-rtrancl[OF  $\tau\text{red1r-2step}$ ])

**lemma**  $\tau\text{reds1r-1step}$ :  
 $\llbracket uf, P, t \vdash 1 \langle es, (h, xs) \rangle [-\varepsilon \rightarrow] \langle es', (h, xs') \rangle; \tau\text{moves1 } P h es \rrbracket$   
 $\Rightarrow \tau\text{reds1gr } uf P t h (es, xs) (es', xs')$   
**by**(blast intro: r-into-rtranclp)

**lemma**  $\tau\text{reds1r-2step}$ :  
 $\llbracket uf, P, t \vdash 1 \langle es, (h, xs) \rangle [-\varepsilon \rightarrow] \langle es', (h, xs') \rangle; \tau\text{moves1 } P h es;$   
 $uf, P, t \vdash 1 \langle es', (h, xs') \rangle [-\varepsilon \rightarrow] \langle es'', (h, xs'') \rangle; \tau\text{moves1 } P h es' \rrbracket$   
 $\Rightarrow \tau\text{reds1gr } uf P t h (es, xs) (es'', xs'')$   
**by**(blast intro: rtranclp.rtrancl-into-rtrancl[OF  $\tau\text{reds1r-1step}$ ])

**lemma**  $\tau\text{reds1r-3step}$ :  
 $\llbracket uf, P, t \vdash 1 \langle es, (h, xs) \rangle [-\varepsilon \rightarrow] \langle es', (h, xs') \rangle; \tau\text{moves1 } P h es;$   
 $uf, P, t \vdash 1 \langle es', (h, xs') \rangle [-\varepsilon \rightarrow] \langle es'', (h, xs'') \rangle; \tau\text{moves1 } P h es';$   
 $uf, P, t \vdash 1 \langle es'', (h, xs'') \rangle [-\varepsilon \rightarrow] \langle es''', (h, xs''') \rangle; \tau\text{moves1 } P h es'' \rrbracket$   
 $\Rightarrow \tau\text{reds1gr } uf P t h (es, xs) (es''', xs''')$   
**by**(blast intro: rtranclp.rtrancl-into-rtrancl[OF  $\tau\text{reds1r-2step}$ ])

**lemma**  $\tau\text{red1t-preserves-len}$ :  $\tau\text{red1gt } uf P t h (e, xs) (e', xs') \Rightarrow \text{length } xs' = \text{length } xs$   
**by**(induct rule: tranclp-induct2)(auto dest: red1-preserves-len)

**lemma**  $\tau\text{red1r-preserves-len}$ :  $\tau\text{red1gr } uf P t h (e, xs) (e', xs') \Rightarrow \text{length } xs' = \text{length } xs$   
**by**(induct rule: rtranclp-induct2)(auto dest: red1-preserves-len)

**lemma**  $\tau\text{red1t-inj-}\tau\text{reds1t}$ :  $\tau\text{red1gt } uf P t h (e, xs) (e', xs') \Rightarrow \tau\text{reds1gt } uf P t h (e \# es, xs) (e' \# es, xs')$   
**by**(induct rule: tranclp-induct2)(auto intro: tranclp.trancl-into-trancl List1Red1  $\tau\text{moves1Hd}$ )

**lemma**  $\tau\text{reds1t-cons-}\tau\text{reds1t}$ :  $\tau\text{reds1gt } uf P t h (es, xs) (es', xs') \Rightarrow \tau\text{reds1gt } uf P t h (Val v \# es, xs) (Val v \# es', xs')$   
**by**(induct rule: tranclp-induct2)(auto intro: tranclp.trancl-into-trancl List1Red2  $\tau\text{moves1Tl}$ )

**lemma**  $\tau\text{red1r-inj-}\tau\text{reds1r}$ :  $\tau\text{red1gr } uf P t h (e, xs) (e', xs') \Rightarrow \tau\text{reds1gr } uf P t h (e \# es, xs) (e' \# es, xs')$

$es, xs')$

**by**(*induct rule*: *rtrancpl-induct2*)(*auto intro*: *rtrancpl.rtrancpl-into-rtrancpl List1Red1  $\tau$ moves1Hd*)

**lemma**  $\tau reds1r-cons-\tau reds1r$ :  $\tau reds1gr\ uf\ P\ t\ h\ (es, xs)\ (es', xs') \implies \tau reds1gr\ uf\ P\ t\ h\ (Val\ v\ \# \ es, xs)\ (Val\ v\ \# \ es', xs')$

**by**(*induct rule*: *rtrancpl-induct2*)(*auto intro*: *rtrancpl.rtrancpl-into-rtrancpl List1Red2  $\tau$ moves1Tl*)

**lemma** *NewArray- $\tau red1t-xt$* :

$\tau red1gt\ uf\ P\ t\ h\ (e, xs)\ (e', xs') \implies \tau red1gt\ uf\ P\ t\ h\ (newA\ T[e], xs)\ (newA\ T[e'], xs')$

**by**(*induct rule*: *trancpl-induct2*)(*auto intro*: *trancpl.trancpl-into-trancpl New1ArrayRed  $\tau$ move1NewArray*)

**lemma** *Cast- $\tau red1t-xt$* :

$\tau red1gt\ uf\ P\ t\ h\ (e, xs)\ (e', xs') \implies \tau red1gt\ uf\ P\ t\ h\ (Cast\ T\ e, xs)\ (Cast\ T\ e', xs')$

**by**(*induct rule*: *trancpl-induct2*)(*auto intro*: *trancpl.trancpl-into-trancpl Cast1Red  $\tau$ move1Cast*)

**lemma** *InstanceOf- $\tau red1t-xt$* :

$\tau red1gt\ uf\ P\ t\ h\ (e, xs)\ (e', xs') \implies \tau red1gt\ uf\ P\ t\ h\ (e\ instanceof\ T, xs)\ (e'\ instanceof\ T, xs')$

**by**(*induct rule*: *trancpl-induct2*)(*auto intro*: *trancpl.trancpl-into-trancpl InstanceOf1Red  $\tau$ move1InstanceOf*)

**lemma** *BinOp- $\tau red1t-xt1$* :

$\tau red1gt\ uf\ P\ t\ h\ (e1, xs)\ (e1', xs') \implies \tau red1gt\ uf\ P\ t\ h\ (e1\ \langle\!\langle\ bop\ \rangle\!\rangle\ e2, xs)\ (e1'\ \langle\!\langle\ bop\ \rangle\!\rangle\ e2, xs')$

**by**(*induct rule*: *trancpl-induct2*)(*auto intro*: *trancpl.trancpl-into-trancpl Bin1OpRed1  $\tau$ move1BinOp1*)

**lemma** *BinOp- $\tau red1t-xt2$* :

$\tau red1gt\ uf\ P\ t\ h\ (e2, xs)\ (e2', xs') \implies \tau red1gt\ uf\ P\ t\ h\ (Val\ v\ \langle\!\langle\ bop\ \rangle\!\rangle\ e2, xs)\ (Val\ v\ \langle\!\langle\ bop\ \rangle\!\rangle\ e2', xs')$

**by**(*induct rule*: *trancpl-induct2*)(*auto intro*: *trancpl.trancpl-into-trancpl Bin1OpRed2  $\tau$ move1BinOp2*)

**lemma** *LAss- $\tau red1t$* :

$\tau red1gt\ uf\ P\ t\ h\ (e, xs)\ (e', xs') \implies \tau red1gt\ uf\ P\ t\ h\ (V\ :=\ e, xs)\ (V\ :=\ e', xs')$

**by**(*induct rule*: *trancpl-induct2*)(*auto intro*: *trancpl.trancpl-into-trancpl LAss1Red  $\tau$ move1LAss*)

**lemma** *AAcc- $\tau red1t-xt1$* :

$\tau red1gt\ uf\ P\ t\ h\ (a, xs)\ (a', xs') \implies \tau red1gt\ uf\ P\ t\ h\ (a[i], xs)\ (a'[i], xs')$

**by**(*induct rule*: *trancpl-induct2*)(*auto intro*: *trancpl.trancpl-into-trancpl AAcc1Red1  $\tau$ move1AAcc1*)

**lemma** *AAcc- $\tau red1t-xt2$* :

$\tau red1gt\ uf\ P\ t\ h\ (i, xs)\ (i', xs') \implies \tau red1gt\ uf\ P\ t\ h\ (Val\ a[i], xs)\ (Val\ a[i'], xs')$

**by**(*induct rule*: *trancpl-induct2*)(*auto intro*: *trancpl.trancpl-into-trancpl AAcc1Red2  $\tau$ move1AAcc2*)

**lemma** *AAss- $\tau red1t-xt1$* :

$\tau red1gt\ uf\ P\ t\ h\ (a, xs)\ (a', xs') \implies \tau red1gt\ uf\ P\ t\ h\ (a[i] := e, xs)\ (a'[i] := e, xs')$

**by**(*induct rule*: *trancpl-induct2*)(*auto intro*: *trancpl.trancpl-into-trancpl AAss1Red1  $\tau$ move1AAss1*)

**lemma** *AAss- $\tau red1t-xt2$* :

$\tau red1gt\ uf\ P\ t\ h\ (i, xs)\ (i', xs') \implies \tau red1gt\ uf\ P\ t\ h\ (Val\ a[i] := e, xs)\ (Val\ a[i'] := e, xs')$

**by**(*induct rule*: *trancpl-induct2*)(*auto intro*: *trancpl.trancpl-into-trancpl AAss1Red2  $\tau$ move1AAss2*)

**lemma** *AAss- $\tau red1t-xt3$* :

$\tau red1gt\ uf\ P\ t\ h\ (e, xs)\ (e', xs') \implies \tau red1gt\ uf\ P\ t\ h\ (Val\ a[Val\ i] := e, xs)\ (Val\ a[Val\ i] := e', xs')$

**by**(*induct rule*: *trancpl-induct2*)(*auto intro*: *trancpl.trancpl-into-trancpl AAss1Red3  $\tau$ move1AAss3*)

**lemma** *ALength- $\tau red1t-xt$* :

$\tau red1gt\ uf\ P\ t\ h\ (a, xs)\ (a', xs') \implies \tau red1gt\ uf\ P\ t\ h\ (a.length, xs)\ (a'.length, xs')$

**by**(*induct rule*: *trancpl-induct2*)(*auto intro*: *trancpl.trancpl-into-trancpl ALength1Red  $\tau$ move1ALength*)

**lemma** *FAcc- $\tau$ red1t-xt*:

$\tau\text{red1gt uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gt uf } P \text{ t h } (e \cdot F\{D\}, xs) (e' \cdot F\{D\}, xs')$

**by**(*induct rule*: *trancpl-induct2*)(*auto intro*: *trancpl.trancpl-into-trancpl FAcc1Red  $\tau$ move1FAcc*)

**lemma** *FAss- $\tau$ red1t-xt1*:

$\tau\text{red1gt uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gt uf } P \text{ t h } (e \cdot F\{D\} := e2, xs) (e' \cdot F\{D\} := e2, xs')$

**by**(*induct rule*: *trancpl-induct2*)(*auto intro*: *trancpl.trancpl-into-trancpl FAss1Red1  $\tau$ move1FAss1*)

**lemma** *FAss- $\tau$ red1t-xt2*:

$\tau\text{red1gt uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gt uf } P \text{ t h } (Val \ v \cdot F\{D\} := e, xs) (Val \ v \cdot F\{D\} := e', xs')$

**by**(*induct rule*: *trancpl-induct2*)(*auto intro*: *trancpl.trancpl-into-trancpl FAss1Red2  $\tau$ move1FAss2*)

**lemma** *CAS- $\tau$ red1t-xt1*:

$\tau\text{red1gt uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gt uf } P \text{ t h } (e \cdot \text{compareAndSwap}(D \cdot F, e2, e3), xs) (e' \cdot \text{compareAndSwap}(D \cdot F, e2, e3), xs')$

**by**(*induct rule*: *trancpl-induct2*)(*auto intro*: *trancpl.trancpl-into-trancpl CAS1Red1*)

**lemma** *CAS- $\tau$ red1t-xt2*:

$\tau\text{red1gt uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gt uf } P \text{ t h } (Val \ v \cdot \text{compareAndSwap}(D \cdot F, e, e3), xs) (Val \ v \cdot \text{compareAndSwap}(D \cdot F, e', e3), xs')$

**by**(*induct rule*: *trancpl-induct2*)(*auto intro*: *trancpl.trancpl-into-trancpl CAS1Red2*)

**lemma** *CAS- $\tau$ red1t-xt3*:

$\tau\text{red1gt uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gt uf } P \text{ t h } (Val \ v \cdot \text{compareAndSwap}(D \cdot F, Val \ v', e), xs) (Val \ v \cdot \text{compareAndSwap}(D \cdot F, Val \ v', e'), xs')$

**by**(*induct rule*: *trancpl-induct2*)(*auto intro*: *trancpl.trancpl-into-trancpl CAS1Red3*)

**lemma** *Call- $\tau$ red1t-obj*:

$\tau\text{red1gt uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gt uf } P \text{ t h } (e \cdot M(ps), xs) (e' \cdot M(ps), xs')$

**by**(*induct rule*: *trancpl-induct2*)(*auto intro*: *trancpl.trancpl-into-trancpl Call1Obj  $\tau$ move1CallObj*)

**lemma** *Call- $\tau$ red1t-param*:

$\tau\text{red1gt uf } P \text{ t h } (es, xs) (es', xs') \implies \tau\text{red1gt uf } P \text{ t h } (Val \ v \cdot M(es), xs) (Val \ v \cdot M(es'), xs')$

**by**(*induct rule*: *trancpl-induct2*)(*auto intro*: *trancpl.trancpl-into-trancpl Call1Params  $\tau$ move1CallParams*)

**lemma** *Block-None- $\tau$ red1t-xt*:

$\tau\text{red1gt uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gt uf } P \text{ t h } (\{V:T=None; e\}, xs) (\{V:T=None; e'\}, xs')$

**by**(*induct rule*: *trancpl-induct2*)(*auto intro*: *trancpl.trancpl-into-trancpl  $\tau$ move1Block elim!: Block1Red*)

**lemma** *Block- $\tau$ red1t-Some*:

$\llbracket \tau\text{red1gt uf } P \text{ t h } (e, xs[V := v]) (e', xs'); V < \text{length } xs \rrbracket$   
 $\implies \tau\text{red1gt uf } P \text{ t h } (\{V:Ty=[v]; e\}, xs) (\{V:Ty=None; e'\}, xs')$

**by**(*blast intro*: *trancpl-into-trancpl2 Block1Some  $\tau$ move1BlockSome Block-None- $\tau$ red1t-xt*)

**lemma** *Sync- $\tau$ red1t-xt*:

$\tau\text{red1gt uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gt uf } P \text{ t h } (\text{sync}_V(e) \ e2, xs) (\text{sync}_V(e') \ e2, xs')$

**by**(*induct rule*: *trancpl-induct2*)(*auto intro*: *trancpl.trancpl-into-trancpl Synchronized1Red1  $\tau$ move1Sync*)

**lemma** *InSync- $\tau$ red1t-xt*:

$\tau\text{red1gt uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gt uf } P \text{ t h } (\text{insync}_V(a) \ e, xs) (\text{insync}_V(a) \ e', xs')$

**by**(*induct rule*: *trancpl-induct2*)(*auto intro*: *trancpl.trancpl-into-trancpl Synchronized1Red2  $\tau$ move1In-Sync*)

**lemma** *Seq- $\tau$ red1t-xt:*

$\tau\text{red1gt uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gt uf } P \text{ t h } (e;;e2, xs) (e';;e2, xs')$   
**by**(*induct rule: tranclp-induct2*)(*auto intro: tranclp.trancl-into-trancl Seq1Red  $\tau$ move1Seq*)

**lemma** *Cond- $\tau$ red1t-xt:*

$\tau\text{red1gt uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gt uf } P \text{ t h } (\text{if } (e) \text{ e1 else } e2, xs) (\text{if } (e') \text{ e1 else } e2, xs')$   
**by**(*induct rule: tranclp-induct2*)(*auto intro: tranclp.trancl-into-trancl Cond1Red  $\tau$ move1Cond*)

**lemma** *Throw- $\tau$ red1t-xt:*

$\tau\text{red1gt uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gt uf } P \text{ t h } (\text{throw } e, xs) (\text{throw } e', xs')$   
**by**(*induct rule: tranclp-induct2*)(*auto intro: tranclp.trancl-into-trancl Throw1Red  $\tau$ move1Throw*)

**lemma** *Try- $\tau$ red1t-xt:*

$\tau\text{red1gt uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gt uf } P \text{ t h } (\text{try } e \text{ catch } (C \ V) \ e2, xs) (\text{try } e' \text{ catch } (C \ V) \ e2, xs')$   
**by**(*induct rule: tranclp-induct2*)(*auto intro: tranclp.trancl-into-trancl Try1Red  $\tau$ move1Try*)

**lemma** *NewArray- $\tau$ red1r-xt:*

$\tau\text{red1gr uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf } P \text{ t h } (\text{newA } T[e], xs) (\text{newA } T[e'], xs')$   
**by**(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl New1ArrayRed  $\tau$ move1NewArray*)

**lemma** *Cast- $\tau$ red1r-xt:*

$\tau\text{red1gr uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf } P \text{ t h } (\text{Cast } T \ e, xs) (\text{Cast } T \ e', xs')$   
**by**(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl Cast1Red  $\tau$ move1Cast*)

**lemma** *InstanceOf- $\tau$ red1r-xt:*

$\tau\text{red1gr uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf } P \text{ t h } (e \text{ instanceof } T, xs) (e' \text{ instanceof } T, xs')$   
**by**(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl InstanceOf1Red  $\tau$ move1InstanceOf*)

**lemma** *BinOp- $\tau$ red1r-xt1:*

$\tau\text{red1gr uf } P \text{ t h } (e1, xs) (e1', xs') \implies \tau\text{red1gr uf } P \text{ t h } (e1 \llbracket \text{bop} \rrbracket e2, xs) (e1' \llbracket \text{bop} \rrbracket e2, xs')$   
**by**(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl Bin1OpRed1  $\tau$ move1BinOp1*)

**lemma** *BinOp- $\tau$ red1r-xt2:*

$\tau\text{red1gr uf } P \text{ t h } (e2, xs) (e2', xs') \implies \tau\text{red1gr uf } P \text{ t h } (\text{Val } v \llbracket \text{bop} \rrbracket e2, xs) (\text{Val } v \llbracket \text{bop} \rrbracket e2', xs')$   
**by**(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl Bin1OpRed2  $\tau$ move1BinOp2*)

**lemma** *LAss- $\tau$ red1r:*

$\tau\text{red1gr uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf } P \text{ t h } (V := e, xs) (V := e', xs')$   
**by**(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl LAss1Red  $\tau$ move1LAss*)

**lemma** *AAcc- $\tau$ red1r-xt1:*

$\tau\text{red1gr uf } P \text{ t h } (a, xs) (a', xs') \implies \tau\text{red1gr uf } P \text{ t h } (a[i], xs) (a'[i], xs')$   
**by**(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl AAcc1Red1  $\tau$ move1AAcc1*)

**lemma** *AAcc- $\tau$ red1r-xt2:*

$\tau\text{red1gr uf } P \text{ t h } (i, xs) (i', xs') \implies \tau\text{red1gr uf } P \text{ t h } (\text{Val } a[i], xs) (\text{Val } a[i'], xs')$   
**by**(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl AAcc1Red2  $\tau$ move1AAcc2*)

**lemma** *AAss- $\tau$ red1r-xt1:*



$\tau\text{red1gr uf } P \text{ t h } (a, xs) (a', xs') \implies \tau\text{red1gr uf } P \text{ t h } (a[i] := e, xs) (a'[i] := e, xs')$   
**by**(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl AAss1Red1  $\tau\text{move1AAss1}$* )

**lemma** *AAss- $\tau\text{red1r-xt2}$ :*

$\tau\text{red1gr uf } P \text{ t h } (i, xs) (i', xs') \implies \tau\text{red1gr uf } P \text{ t h } (Val\ a[i] := e, xs) (Val\ a[i'] := e, xs')$   
**by**(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl AAss1Red2  $\tau\text{move1AAss2}$* )

**lemma** *AAss- $\tau\text{red1r-xt3}$ :*

$\tau\text{red1gr uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf } P \text{ t h } (Val\ a[Val\ i] := e, xs) (Val\ a[Val\ i'] := e', xs')$   
**by**(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl AAss1Red3  $\tau\text{move1AAss3}$* )

**lemma** *ALength- $\tau\text{red1r-xt}$ :*

$\tau\text{red1gr uf } P \text{ t h } (a, xs) (a', xs') \implies \tau\text{red1gr uf } P \text{ t h } (a \cdot \text{length}, xs) (a' \cdot \text{length}, xs')$   
**by**(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl ALength1Red  $\tau\text{move1ALength}$* )

**lemma** *FAcc- $\tau\text{red1r-xt}$ :*

$\tau\text{red1gr uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf } P \text{ t h } (e \cdot F\{D\}, xs) (e' \cdot F\{D\}, xs')$   
**by**(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl FAcc1Red  $\tau\text{move1FAcc}$* )

**lemma** *FAss- $\tau\text{red1r-xt1}$ :*

$\tau\text{red1gr uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf } P \text{ t h } (e \cdot F\{D\} := e2, xs) (e' \cdot F\{D\} := e2, xs')$   
**by**(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl FAss1Red1  $\tau\text{move1FAss1}$* )

**lemma** *FAss- $\tau\text{red1r-xt2}$ :*

$\tau\text{red1gr uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf } P \text{ t h } (Val\ v \cdot F\{D\} := e, xs) (Val\ v \cdot F\{D\} := e', xs')$   
**by**(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl FAss1Red2  $\tau\text{move1FAss2}$* )

**lemma** *CAS- $\tau\text{red1r-xt1}$ :*

$\tau\text{red1gr uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf } P \text{ t h } (e \cdot \text{compareAndSwap}(D \cdot F, e2, e3), xs) (e' \cdot \text{compareAndSwap}(D \cdot F, e2, e3), xs')$   
**by**(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl CAS1Red1*)

**lemma** *CAS- $\tau\text{red1r-xt2}$ :*

$\tau\text{red1gr uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf } P \text{ t h } (Val\ v \cdot \text{compareAndSwap}(D \cdot F, e, e3), xs) (Val\ v \cdot \text{compareAndSwap}(D \cdot F, e', e3), xs')$   
**by**(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl CAS1Red2*)

**lemma** *CAS- $\tau\text{red1r-xt3}$ :*

$\tau\text{red1gr uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf } P \text{ t h } (Val\ v \cdot \text{compareAndSwap}(D \cdot F, Val\ v', e), xs) (Val\ v \cdot \text{compareAndSwap}(D \cdot F, Val\ v', e'), xs')$   
**by**(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl CAS1Red3*)

**lemma** *Call- $\tau\text{red1r-obj}$ :*

$\tau\text{red1gr uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf } P \text{ t h } (e \cdot M(ps), xs) (e' \cdot M(ps), xs')$   
**by**(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl Call1Obj  $\tau\text{move1CallObj}$* )

**lemma** *Call- $\tau\text{red1r-param}$ :*

$\tau\text{red1gr uf } P \text{ t h } (es, xs) (es', xs') \implies \tau\text{red1gr uf } P \text{ t h } (Val\ v \cdot M(es), xs) (Val\ v \cdot M(es'), xs')$   
**by**(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl Call1Params  $\tau\text{move1CallParams}$* )

**lemma** *Block-None- $\tau\text{red1r-xt}$ :*

$\tau\text{red1gr uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf } P \text{ t h } (\{V:T=None; e\}, xs) (\{V:T=None; e'\}, xs')$   
**by**(*induct rule: rtranclp-induct2*)(*auto intro: rtranclp.rtrancl-into-rtrancl  $\tau\text{move1Block elim!}$ : Block1Red*)

**lemma** *Block- $\tau$ red1r-Some*:

$\llbracket \tau\text{red1gr uf } P \text{ t h } (e, xs[V := v]) (e', xs') ; V < \text{length } xs \rrbracket$   
 $\implies \tau\text{red1gr uf } P \text{ t h } (\{V:Ty=[v]; e\}, xs) (\{V:Ty=None; e'\}, xs')$

**by**(blast intro: converse-rtranclp-into-rtranclp Block1Some  $\tau\text{move1BlockSome}$  Block-None- $\tau\text{red1r-xt}$ )

**lemma** *Sync- $\tau$ red1r-xt*:

$\tau\text{red1gr uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf } P \text{ t h } (\text{sync}_V (e) \ e2, xs) (\text{sync}_V (e') \ e2, xs')$

**by**(induct rule: rtranclp-induct2)(auto intro: rtranclp.rtrancl-into-rtrancl Synchronized1Red1  $\tau\text{move1Sync}$ )

**lemma** *InSync- $\tau$ red1r-xt*:

$\tau\text{red1gr uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf } P \text{ t h } (\text{insync}_V (a) \ e, xs) (\text{insync}_V (a) \ e', xs')$

**by**(induct rule: rtranclp-induct2)(auto intro: rtranclp.rtrancl-into-rtrancl Synchronized1Red2  $\tau\text{move1In-Sync}$ )

**lemma** *Seq- $\tau$ red1r-xt*:

$\tau\text{red1gr uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf } P \text{ t h } (e;;e2, xs) (e';;e2, xs')$

**by**(induct rule: rtranclp-induct2)(auto intro: rtranclp.rtrancl-into-rtrancl Seq1Red  $\tau\text{move1Seq}$ )

**lemma** *Cond- $\tau$ red1r-xt*:

$\tau\text{red1gr uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf } P \text{ t h } (\text{if } (e) \ e1 \text{ else } e2, xs) (\text{if } (e') \ e1 \text{ else } e2, xs')$

**by**(induct rule: rtranclp-induct2)(auto intro: rtranclp.rtrancl-into-rtrancl Cond1Red  $\tau\text{move1Cond}$ )

**lemma** *Throw- $\tau$ red1r-xt*:

$\tau\text{red1gr uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf } P \text{ t h } (\text{throw } e, xs) (\text{throw } e', xs')$

**by**(induct rule: rtranclp-induct2)(auto intro: rtranclp.rtrancl-into-rtrancl Throw1Red  $\tau\text{move1Throw}$ )

**lemma** *Try- $\tau$ red1r-xt*:

$\tau\text{red1gr uf } P \text{ t h } (e, xs) (e', xs') \implies \tau\text{red1gr uf } P \text{ t h } (\text{try } e \text{ catch } (C \ V) \ e2, xs) (\text{try } e' \text{ catch } (C \ V) \ e2, xs')$

**by**(induct rule: rtranclp-induct2)(auto intro: rtranclp.rtrancl-into-rtrancl Try1Red  $\tau\text{move1Try}$ )

**lemma**  $\tau\text{red1t-ThrowD}$  [dest]:  $\tau\text{red1gt uf } P \text{ t h } (\text{Throw } a, xs) (e'', xs'') \implies e'' = \text{Throw } a \wedge xs'' = xs$

**by**(induct rule: tranclp-induct2)(auto)

**lemma**  $\tau\text{red1r-ThrowD}$  [dest]:  $\tau\text{red1gr uf } P \text{ t h } (\text{Throw } a, xs) (e'', xs'') \implies e'' = \text{Throw } a \wedge xs'' = xs$

**by**(induct rule: rtranclp-induct2)(auto)

**lemma**  $\tau\text{Red1-conv}$  [iff]:

$\tau\text{Red1g uf } P \text{ t h } (ex, exs) (ex', exs') = (\text{uf}, P, t \vdash 1 \langle ex/exs, h \rangle -\varepsilon \rightarrow \langle ex'/exs', h \rangle \wedge \tau\text{Move1 } P \text{ h } (ex, exs))$

**by**(simp add:  $\tau\text{Red1g-def}$ )

**lemma**  $\tau\text{red1t-into-}\tau\text{Red1t}$ :

$\tau\text{red1gt uf } P \text{ t h } (e, xs) (e'', xs'') \implies \tau\text{Red1gt uf } P \text{ t h } ((e, xs), exs) ((e'', xs''), exs)$

**by**(induct rule: tranclp-induct2)(fastforce dest: red1Red intro:  $\tau\text{move1Block tranclp.intros}$ )+

**lemma**  $\tau\text{red1r-into-}\tau\text{Red1r}$ :

$\tau\text{red1gr uf } P \text{ t h } (e, xs) (e'', xs'') \implies \tau\text{Red1gr uf } P \text{ t h } ((e, xs), exs) ((e'', xs''), exs)$

**by**(induct rule: rtranclp-induct2)(fastforce dest: red1Red intro:  $\tau\text{move1Block rtranclp.intros}$ )+

**lemma**  $\text{red1-max-vars}$ :  $\text{uf}, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies \text{max-vars } e' \leq \text{max-vars } e$

**and**  $\text{reds1-max-varss}$ :  $\text{uf}, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies \text{max-varss } es' \leq \text{max-varss } es$

**by**(*induct rule: red1-reds1.inducts*) *auto*

**lemma**  $\tau red1t\text{-}max\text{-}vars: \tau red1gt \text{ uf } P \text{ t h } (e, xs) (e', xs') \implies max\text{-}vars \ e' \leq max\text{-}vars \ e$   
**by**(*induct rule: tranclp-induct2*)(*auto dest: red1-max-vars*)

**lemma**  $\tau red1r\text{-}max\text{-}vars: \tau red1gr \text{ uf } P \text{ t h } (e, xs) (e', xs') \implies max\text{-}vars \ e' \leq max\text{-}vars \ e$   
**by**(*induct rule: rtranclp-induct2*)(*auto dest: red1-max-vars*)

**lemma**  $\tau red1r\text{-}Val$ :

$\tau red1gr \text{ uf } P \text{ t h } (Val \ v, xs) \ s' \longleftrightarrow s' = (Val \ v, xs)$

**proof**

**assume**  $\tau red1gr \text{ uf } P \text{ t h } (Val \ v, xs) \ s'$

**thus**  $s' = (Val \ v, xs)$  **by** *induct*(*auto*)

**qed** *auto*

**lemma**  $\tau red1t\text{-}Val$ :

$\tau red1gt \text{ uf } P \text{ t h } (Val \ v, xs) \ s' \longleftrightarrow False$

**proof**

**assume**  $\tau red1gt \text{ uf } P \text{ t h } (Val \ v, xs) \ s'$

**thus** *False* **by** *induct auto*

**qed** *auto*

**lemma**  $\tau reds1r\text{-}map\text{-}Val$ :

$\tau reds1gr \text{ uf } P \text{ t h } (map \ Val \ vs, xs) \ s' \longleftrightarrow s' = (map \ Val \ vs, xs)$

**proof**

**assume**  $\tau reds1gr \text{ uf } P \text{ t h } (map \ Val \ vs, xs) \ s'$

**thus**  $s' = (map \ Val \ vs, xs)$  **by** *induct auto*

**qed** *auto*

**lemma**  $\tau reds1t\text{-}map\text{-}Val$ :

$\tau reds1gt \text{ uf } P \text{ t h } (map \ Val \ vs, xs) \ s' \longleftrightarrow False$

**proof**

**assume**  $\tau reds1gt \text{ uf } P \text{ t h } (map \ Val \ vs, xs) \ s'$

**thus** *False* **by** *induct auto*

**qed** *auto*

**lemma**  $\tau reds1r\text{-}map\text{-}Val\text{-}Throw$ :

$\tau reds1gr \text{ uf } P \text{ t h } (map \ Val \ vs \ @ \ Throw \ a \ \# \ es, xs) \ s' \longleftrightarrow s' = (map \ Val \ vs \ @ \ Throw \ a \ \# \ es, xs)$   
*(is ?lhs  $\longleftrightarrow$  ?rhs)*

**proof**

**assume** *?lhs* **thus** *?rhs* **by** *induct auto*

**qed** *auto*

**lemma**  $\tau reds1t\text{-}map\text{-}Val\text{-}Throw$ :

$\tau reds1gt \text{ uf } P \text{ t h } (map \ Val \ vs \ @ \ Throw \ a \ \# \ es, xs) \ s' \longleftrightarrow False$   
*(is ?lhs  $\longleftrightarrow$  ?rhs)*

**proof**

**assume** *?lhs* **thus** *?rhs* **by** *induct auto*

**qed** *auto*

**lemma**  $\tau red1r\text{-}Throw$ :

$\tau red1gr \text{ uf } P \text{ t h } (Throw \ a, xs) \ s' \longleftrightarrow s' = (Throw \ a, xs)$  *(is ?lhs  $\longleftrightarrow$  ?rhs)*

**proof**

**assume** *?lhs* **thus** *?rhs* **by** *induct auto*

**qed** *simp*

**lemma**  $\tau\text{red1t-Throw}$ :

$\tau\text{red1gt } uf \ P \ t \ h \ (\text{Throw } a, \ xs) \ s' \longleftrightarrow \text{False} \ (\text{is } ?lhs \longleftrightarrow ?rhs)$

**proof**

**assume**  $?lhs$  **thus**  $?rhs$  **by** *induct auto*

**qed** *simp*

**lemma** *red1-False-into-red1-True*:

$\text{False}, P, t \vdash 1 \ \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies \text{True}, P, t \vdash 1 \ \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle$

**and** *reds1-False-into-reds1-True*:

$\text{False}, P, t \vdash 1 \ \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies \text{True}, P, t \vdash 1 \ \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle$

**by** (*induct rule: red1-reds1.inducts*) (*auto intro: red1-reds1.intros*)

**lemma** *Red1-False-into-Red1-True*:

**assumes**  $\text{False}, P, t \vdash 1 \ \langle ex/exs, shr \ s \rangle -ta \rightarrow \langle ex'/exs', m' \rangle$

**shows**  $\text{True}, P, t \vdash 1 \ \langle ex/exs, shr \ s \rangle -ta \rightarrow \langle ex'/exs', m' \rangle$

**using** *assms*

**by**(*cases*)(*auto dest: Red1.intros red1-False-into-red1-True*)

**lemma** *red1-Suspend-is-call*:

$\llbracket uf, P, t \vdash 1 \ \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{Suspend } w \in \text{set } \llbracket ta \rrbracket_w \rrbracket \implies \text{call1 } e' \neq \text{None}$

**and** *reds-Suspend-is-calls*:

$\llbracket uf, P, t \vdash 1 \ \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{Suspend } w \in \text{set } \llbracket ta \rrbracket_w \rrbracket \implies \text{calls1 } es' \neq \text{None}$

**by**(*induct rule: red1-reds1.inducts*)(*auto dest: red-external-Suspend-StaySame*)

**lemma** *Red1-Suspend-is-call*:

$\llbracket uf, P, t \vdash 1 \ \langle (e, xs)/exs, h \rangle -ta \rightarrow \langle (e', xs')/exs', h' \rangle; \text{Suspend } w \in \text{set } \llbracket ta \rrbracket_w \rrbracket \implies \text{call1 } e' \neq \text{None}$

**by**(*auto elim!: Red1.cases dest: red1-Suspend-is-call*)

**lemma** *Red1-mthr: multithreaded final-expr1 (mred1g uf P)*

**by**(*unfold-locales*)(*fastforce elim!: Red1.cases dest: red1-new-thread-heap*)**+**

**lemma** *red1- $\tau\text{move1}$ -heap-unchanged*:  $\llbracket uf, P, t \vdash 1 \ \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \tau\text{move1 } P \ (hp \ s) \ e \rrbracket \implies hp \ s' = hp \ s$

**and** *red1- $\tau\text{moves1}$ -heap-unchanged*:  $\llbracket uf, P, t \vdash 1 \ \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \tau\text{moves1 } P \ (hp \ s) \ es \rrbracket \implies hp \ s' = hp \ s$

**apply**(*induct rule: red1-reds1.inducts*)

**apply**(*fastforce simp add: map-eq-append-conv  $\tau\text{external}'$ -def  $\tau\text{external}$ -def dest:  $\tau\text{external}'$ -red-external-heap-unch*)  
**done**

**lemma** *Red1- $\tau\text{mthr}$ -wf:  $\tau\text{multithreaded-wf final-expr1 (mred1g uf P) } (\tau\text{MOVE1 } P)$*

**proof** **–**

**interpret** *multithreaded final-expr1 mred1g uf P convert-RA*

**by**(*rule Red1-mthr*)

**show** *?thesis*

**proof**

**fix**  $x1 \ m1 \ t \ ta1 \ x1' \ m1'$

**assume**  $mred1g \ uf \ P \ t \ (x1, \ m1) \ ta1 \ (x1', \ m1') \ \tau\text{MOVE1 } P \ (x1, \ m1) \ ta1 \ (x1', \ m1')$

**thus**  $m1 = m1'$  **by**(*cases x1*)(*fastforce elim!: Red1.cases dest: red1- $\tau\text{move1}$ -heap-unchanged*)

**next**

**fix**  $s \ ta \ s'$

**assume**  $\tau\text{MOVE1 } P \ s \ ta \ s'$

**thus**  $ta = \varepsilon$  **by**(*simp add: split-beta*)

qed  
qed

end

**sublocale** *J1-heap-base* < *Red1-mthr*:

$\tau$ multithreaded-wf  
  final-expr1  
  mred1g uf *P*  
  convert-RA  
   $\tau$ MOVE1 *P*  
  for uf *P*  
by(rule *Red1- $\tau$ mthr-wf*)

**context** *J1-heap-base* **begin**

**lemma**  $\tau$ Red1't-into-Red1'- $\tau$ mthr-silent-movet:

$\tau$ Red1gt uf *P* t h (*ex2*, *exs2*) (*ex2''*, *exs2''*)  
   $\implies$  *Red1-mthr.silent-movet* uf *P* t ((*ex2*, *exs2*), *h*) ((*ex2''*, *exs2''*), *h*)  
apply(induct rule: *trancpl-induct2*)  
apply clarsimp  
apply(rule *trancpl.r-into-trancl*)  
apply(simp add: *Red1-mthr.silent-move-iff*)  
apply(erule *trancpl.trancl-into-trancl*)  
apply(simp add: *Red1-mthr.silent-move-iff*)  
done

**lemma**  $\tau$ Red1t-into-Red1'- $\tau$ mthr-silent-moves:

$\tau$ Red1gt uf *P* t h (*ex2*, *exs2*) (*ex2''*, *exs2''*)  
   $\implies$  *Red1-mthr.silent-moves* uf *P* t ((*ex2*, *exs2*), *h*) ((*ex2''*, *exs2''*), *h*)  
by(rule *trancpl-into-rtrancl*)(rule  $\tau$ Red1't-into-Red1'- $\tau$ mthr-silent-movet)

**lemma**  $\tau$ Red1'r-into-Red1'- $\tau$ mthr-silent-moves:

$\tau$ Red1gr uf *P* t h (*ex*, *exs*) (*ex'*, *exs'*)  $\implies$  *Red1-mthr.silent-moves* uf *P* t ((*ex*, *exs*), *h*) ((*ex'*, *exs'*), *h*)  
apply(induct rule: *rtranclp-induct2*)  
apply blast  
apply(erule *rtranclp.rtrancl-into-rtrancl*)  
apply(simp add: *Red1-mthr.silent-move-iff*)  
done

**lemma**  $\tau$ Red1r-rtranclpD:

$\tau$ Red1gr uf *P* t h s s'  $\implies$   $\tau$ trsys.silent-moves (mred1g uf *P* t) ( $\tau$ MOVE1 *P*) (*s*, *h*) (*s'*, *h*)  
apply(induct rule: *rtranclp-induct*)  
apply(auto elim!: *rtranclp.rtrancl-into-rtrancl* intro:  $\tau$ trsys.silent-move.intros)  
done

**lemma**  $\tau$ Red1t-trancplD:

$\tau$ Red1gt uf *P* t h s s'  $\implies$   $\tau$ trsys.silent-movet (mred1g uf *P* t) ( $\tau$ MOVE1 *P*) (*s*, *h*) (*s'*, *h*)  
apply(induct rule: *trancpl-induct*)  
apply(rule *trancpl.r-into-trancl*)  
apply(auto elim!: *trancpl.trancl-into-trancl* intro!:  $\tau$ trsys.silent-move.intros simp:  $\tau$ Red1g-def split-def)  
done

**lemma**  $\tau mreds1\text{-}Val\text{-}Nil$ :  $\tau trsys.silent\text{-}moves (mred1g\ uf\ P\ t) (\tau MOVE1\ P) (((Val\ v,\ xs), []), h) s \longleftrightarrow s = (((Val\ v,\ xs), []), h)$

**proof**

**assume**  $\tau trsys.silent\text{-}moves (mred1g\ uf\ P\ t) (\tau MOVE1\ P) (((Val\ v,\ xs), []), h) s$

**thus**  $s = (((Val\ v,\ xs), []), h)$

**by**  $induct(auto\ elim!:\ Red1\text{-}mthr.silent\text{-}move.cases\ Red1.cases)$

**qed** *auto*

**lemma**  $\tau mreds1\text{-}Throw\text{-}Nil$ :

$\tau trsys.silent\text{-}moves (mred1g\ uf\ P\ t) (\tau MOVE1\ P) (((Throw\ a,\ xs), []), h) s \longleftrightarrow s = (((Throw\ a,\ xs), []), h)$

**proof**

**assume**  $\tau trsys.silent\text{-}moves (mred1g\ uf\ P\ t) (\tau MOVE1\ P) (((Throw\ a,\ xs), []), h) s$

**thus**  $s = (((Throw\ a,\ xs), []), h)$

**by**  $induct(auto\ elim!:\ Red1\text{-}mthr.silent\text{-}move.cases\ Red1.cases)$

**qed** *auto*

**end**

**end**

## 7.7 Deadlock perservation for the intermediate language

**theory** *J1Deadlock* **imports**

*J1*

*../Framework/FWDeadlock*

*../Common/ExternalCallWF*

**begin**

**context** *J1-heap-base* **begin**

**lemma** *IUF-red-taD*:

$True, P, t \vdash 1 \langle e, s \rangle \text{-}ta \rightarrow \langle e', s' \rangle$

$\implies \exists e' ta' s'. False, P, t \vdash 1 \langle e, s \rangle \text{-}ta' \rightarrow \langle e', s' \rangle \wedge$

$collect\text{-}locks \llbracket ta' \rrbracket_l \subseteq collect\text{-}locks \llbracket ta \rrbracket_l \wedge set \llbracket ta' \rrbracket_c \subseteq set \llbracket ta \rrbracket_c \wedge collect\text{-}interrupts \llbracket ta' \rrbracket_i \subseteq collect\text{-}interrupts \llbracket ta \rrbracket_i \wedge$

$(\exists s. Red1\text{-}mthr.actions\text{-}ok\ s\ t\ ta')$

**and** *IUFs-reds-taD*:

$True, P, t \vdash 1 \langle es, s \rangle \text{-}ta \rightarrow \langle es', s' \rangle$

$\implies \exists es' ta' s'. False, P, t \vdash 1 \langle es, s \rangle \text{-}ta' \rightarrow \langle es', s' \rangle \wedge$

$collect\text{-}locks \llbracket ta' \rrbracket_l \subseteq collect\text{-}locks \llbracket ta \rrbracket_l \wedge set \llbracket ta' \rrbracket_c \subseteq set \llbracket ta \rrbracket_c \wedge collect\text{-}interrupts \llbracket ta' \rrbracket_i \subseteq collect\text{-}interrupts \llbracket ta \rrbracket_i \wedge$

$(\exists s. Red1\text{-}mthr.actions\text{-}ok\ s\ t\ ta')$

**proof** (*induct rule: red1-reds1.inducts*)

**case** *Red1InstanceOf* **thus** ?*case*

**using**  $[[hyps\text{-}subst\text{-}thin = true]]$

**by** (*auto intro!*: *exI red1-reds1.Red1InstanceOf simp del: split-paired-Ex*) (*(subst fst-conv snd-conv wset-def)+, simp*)

**next**

**case** *Red1CallExternal* **thus** ?*case*

**by** (*fastforce simp del: split-paired-Ex dest: red-external-ta-satisfiable* [**where** *final=final-expr1 :: ('addr expr1  $\times$  'addr val list)  $\times$  ('addr expr1  $\times$  'addr val list) list  $\Rightarrow$  bool*] *intro: red1-reds1.Red1CallExternal*)

```

next
  case Lock1Synchronized thus ?case
    by(auto intro!: exI exI[where x=(K$ None, (Map.empty, undefined), Map.empty, {})] red1-reds1.Lock1Synchronized
simp del: split-paired-Ex simp add: lock-ok-las-def finfun-upd-apply may-lock.intros(1))
next
  case (Synchronized1Red2 e s ta e' s' V a)
  then obtain e' ta' s'
    where False,P,t ⊢ 1 ⟨e,s⟩ -ta'→ ⟨e',s'⟩
    and L: collect-locks ⟦ta'⟧l ⊆ collect-locks ⟦ta⟧l ∧ set ⟦ta'⟧c ⊆ set ⟦ta⟧c ∧ collect-interrupts ⟦ta'⟧i
    ⊆ collect-interrupts ⟦ta⟧i
    and aok: ∃ s. Red1-mthr.actions-ok s t ta'
    by blast
  from ⟨False,P,t ⊢ 1 ⟨e,s⟩ -ta'→ ⟨e',s'⟩⟩ have False,P,t ⊢ 1 ⟨insyncV (a) e, s⟩ -ta'→ ⟨insyncV (a)
e', s'⟩
    by(rule red1-reds1.Synchronized1Red2)
  thus ?case using L aok by blast
next
  case Unlock1Synchronized thus ?case
    by(auto simp del: split-paired-Ex intro!: exI exI[where x=(K$ [(t, 0)], (Map.empty, undefined),
Map.empty, {})] red1-reds1.Unlock1Synchronized simp add: lock-ok-las-def finfun-upd-apply)
next
  case Unlock1SynchronizedFail thus ?case
    by(auto simp del: split-paired-Ex intro!: exI exI[where x=(K$ [(t, 0)], (Map.empty, unde-
fined), Map.empty, {})] red1-reds1.Unlock1Synchronized simp add: lock-ok-las-def finfun-upd-apply col-
lect-locks-def split: if-split-asm)
next
  case Synchronized1Throw2 thus ?case
    by(auto simp del: split-paired-Ex intro!: exI exI[where x=(K$ [(t, 0)], (Map.empty, undefined),
Map.empty, {})] red1-reds1.Synchronized1Throw2 simp add: lock-ok-las-def finfun-upd-apply)
next
  case Synchronized1Throw2Fail thus ?case
    by(auto simp del: split-paired-Ex intro!: exI exI[where x=(K$ [(t, 0)], (Map.empty, unde-
fined), Map.empty, {})] red1-reds1.Synchronized1Throw2 simp add: lock-ok-las-def finfun-upd-apply
collect-locks-def split: if-split-asm)
qed(fastforce intro: red1-reds1.intros)+

lemma IUF-Red1-taD:
  assumes True,P,t ⊢ 1 ⟨ex/exs, h⟩ -ta→ ⟨ex'/exs', h'⟩
  shows ∃ ex' exs' h' ta'. False,P,t ⊢ 1 ⟨ex/exs, h⟩ -ta'→ ⟨ex'/exs', h'⟩ ∧
    collect-locks ⟦ta'⟧l ⊆ collect-locks ⟦ta⟧l ∧ set ⟦ta'⟧c ⊆ set ⟦ta⟧c ∧ collect-interrupts ⟦ta'⟧i ⊆
    collect-interrupts ⟦ta⟧i ∧
    (∃ s. Red1-mthr.actions-ok s t ta')
using assms
apply(cases)
apply(safe dest!: IUF-red-taD)
apply(simp del: split-paired-Ex)
apply(rule exI conjI)+
apply(erule red1Red)
apply simp
apply blast
apply(rule exI conjI red1Call)+
apply(auto simp add: lock-ok-las-def)
apply(rule exI conjI red1Return)+
apply auto

```

done

**lemma** *mred1'-mred1-must-sync-eq*:

*Red1-mthr.must-sync False P t x (shr s) = Red1-mthr.must-sync True P t x (shr s)*

**proof**

**assume** *Red1-mthr.must-sync False P t x (shr s)*

**thus** *Red1-mthr.must-sync True P t x (shr s)*

**by**(rule *Red1-mthr.must-syncE*)(rule *Red1-mthr.must-syncI*, *auto simp add: split-def simp del: split-paired-Ex intro: Red1-False-into-Red1-True*)

**next**

**assume** *Red1-mthr.must-sync True P t x (shr s)*

**thus** *Red1-mthr.must-sync False P t x (shr s)*

**apply**(rule *Red1-mthr.must-syncE*)

**apply**(rule *Red1-mthr.must-syncI*)

**apply**(*cases x*)

**apply**(*auto simp add: split-beta split-paired-Ex*)

**apply**(*drule IUF-Red1-taD*)

**apply** *simp*

**apply** *blast*

**done**

**qed**

**lemma** *Red1-Red1'-deadlock-inv*:

*Red1-mthr.deadlock True P s = Red1-mthr.deadlock False P s*

**proof**(rule *iffI*)

**assume** *dead: Red1-mthr.deadlock True P s*

**show** *Red1-mthr.deadlock False P s*

**proof**(rule *multithreaded-base.deadlockI*)

**fix** *t x*

**assume** *tst: thr s t = [(x, no-wait-locks)]*

**and** *nfin: ¬ final-expr1 x*

**and** *wst: wset s t = None*

**with** *dead* **obtain** *ms: Red1-mthr.must-sync True P t x (shr s)*

**and** *cs* [rule-format]:  $\forall LT. Red1-mthr.can-sync True P t x (shr s) LT \longrightarrow (\exists lt \in LT. final-thread.must-wait final-expr1 s t lt (dom (thr s)))$

**by**(rule *Red1-mthr.deadlockD1*)

**from** *ms*[folded *mred1'-mred1-must-sync-eq*]

**show** *Red1-mthr.must-sync False P t x (shr s) ∧*

$(\forall LT. Red1-mthr.can-sync False P t x (shr s) LT \longrightarrow$

$(\exists lt \in LT. final-thread.must-wait final-expr1 s t lt (dom (thr s))))$

**proof**

**show**  $\forall LT. Red1-mthr.can-sync False P t x (shr s) LT \longrightarrow$

$(\exists lt \in LT. final-thread.must-wait final-expr1 s t lt (dom (thr s)))$

**proof**(*intro strip*)

**fix** *LT*

**assume** *Red1-mthr.can-sync False P t x (shr s) LT*

**then obtain** *ta x' m'* **where** *mred1' P t (x, shr s) ta (x', m')*

**and** [*simp*]: *LT = collect-locks*  $\llbracket ta \rrbracket_l <+> collect-cond-actions \llbracket ta \rrbracket_c <+> collect-interrupts$

$\llbracket ta \rrbracket_i$

**by**(rule *Red1-mthr.can-syncE*)

**hence** *mred1 P t (x, shr s) ta (x', m')* **by**(*auto simp add: split-beta intro: Red1-False-into-Red1-True*)

**hence** *Red1-mthr.can-sync True P t x (shr s) LT* **by**(rule *Red1-mthr.can-syncI*) *simp*

**thus**  $\exists lt \in LT. final-thread.must-wait final-expr1 s t lt (dom (thr s))$  **by**(rule *cs*)

**qed**



```

qed
next
  fix  $t\ x\ ln\ l$ 
  assume  $thr\ s\ t = \lfloor (x, ln) \rfloor\ 0 < ln\ \$\ l \neg waiting\ (wset\ s\ t)$ 
  thus  $\exists l\ t'.\ 0 < ln\ \$\ l \wedge t \neq t' \wedge thr\ s\ t' \neq None \wedge has-lock\ ((locks\ s)\ \$\ l)\ t'$ 
  by(rule Red1-mthr.deadlockD2[OF dead]) blast
next
  fix  $t\ x\ w$ 
  assume  $thr\ s\ t = \lfloor (x, no-wait-locks) \rfloor$ 
  thus  $wset\ s\ t \neq \lfloor PostWS\ w \rfloor$ 
  by(rule Red1-mthr.deadlockD3[OF dead, rule-format])
qed
next
assume dead: Red1-mthr.deadlock False  $P\ s$ 
show Red1-mthr.deadlock True  $P\ s$ 
proof(rule Red1-mthr.deadlockI)
  fix  $t\ x$ 
  assume  $tst: thr\ s\ t = \lfloor (x, no-wait-locks) \rfloor$ 
  and  $nfin: \neg final-expr1\ x$ 
  and  $wst: wset\ s\ t = None$ 
  with dead obtain  $ms: Red1-mthr.must-sync\ False\ P\ t\ x\ (shr\ s)$ 
  and  $cs\ [rule-format]: \forall LT. Red1-mthr.can-sync\ False\ P\ t\ x\ (shr\ s)\ LT \longrightarrow$ 
     $(\exists lt \in LT. final-thread.must-wait\ final-expr1\ s\ t\ lt\ (dom\ (thr\ s)))$ 
  by(rule Red1-mthr.deadlockD1)
  from  $ms[unfolded\ mred1'\text{-}mred1\text{-}must-sync\text{-}eq]$ 
  show Red1-mthr.must-sync True  $P\ t\ x\ (shr\ s) \wedge$ 
     $(\forall LT. Red1-mthr.can-sync\ True\ P\ t\ x\ (shr\ s)\ LT \longrightarrow$ 
     $(\exists lt \in LT. final-thread.must-wait\ final-expr1\ s\ t\ lt\ (dom\ (thr\ s))))$ 
  proof
  show  $\forall LT. Red1-mthr.can-sync\ True\ P\ t\ x\ (shr\ s)\ LT \longrightarrow$ 
     $(\exists lt \in LT. final-thread.must-wait\ final-expr1\ s\ t\ lt\ (dom\ (thr\ s)))$ 
  proof(intro strip)
  fix  $LT$ 
  assume Red1-mthr.can-sync True  $P\ t\ x\ (shr\ s)\ LT$ 
  then obtain  $ta\ x'\ m'$  where  $mred1\ P\ t\ (x, shr\ s)\ ta\ (x', m')$ 
  and  $[simp]: LT = collect-locks\ \llbracket ta \rrbracket_l <+> collect-cond-actions\ \llbracket ta \rrbracket_c <+> collect-interrupts\ \llbracket ta \rrbracket_i$ 
  by(rule Red1-mthr.can-syncE)
  then obtain  $e\ xs\ exs\ e'\ xs'\ exs'$  where  $x\ [simp]: x = ((e, xs), exs)\ x' = ((e', xs'), exs')$ 
  and  $red: True, P, t \vdash 1\ \langle (e, xs)/exs, shr\ s \rangle -ta \rightarrow \langle (e', xs')/exs', m' \rangle$  by(cases  $x$ , cases  $x'$ )
  fastforce
  from IUF-Red1-taD[OF red] obtain  $ex''\ exs''\ h''\ ta'$ 
  where  $red': False, P, t \vdash 1\ \langle (e, xs)/exs, shr\ s \rangle -ta' \rightarrow \langle ex''/exs'', h'' \rangle$ 
  and  $collect-locks\ \llbracket ta' \rrbracket_l <+> collect-cond-actions\ \llbracket ta' \rrbracket_c <+> collect-interrupts\ \llbracket ta' \rrbracket_i \subseteq$ 
 $collect-locks\ \llbracket ta \rrbracket_l <+> collect-cond-actions\ \llbracket ta \rrbracket_c <+> collect-interrupts\ \llbracket ta \rrbracket_i$ 
  by auto blast
  then obtain  $LT'$  where  $cs': Red1-mthr.can-sync\ False\ P\ t\ x\ (shr\ s)\ LT'$ 
  and  $LT': LT' \subseteq LT$  by(cases  $ex''$ )(fastforce intro!: Red1-mthr.can-syncI)
  with  $cs[of\ LT']$  show  $\exists lt \in LT. final-thread.must-wait\ final-expr1\ s\ t\ lt\ (dom\ (thr\ s))$  by auto
  qed
qed
next
  fix  $t\ x\ ln\ l$ 
  assume  $thr\ s\ t = \lfloor (x, ln) \rfloor\ 0 < ln\ \$\ l \neg waiting\ (wset\ s\ t)$ 

```

```

    thus  $\exists l\ t'.\ 0 < \text{ln } \$\ l \wedge t \neq t' \wedge \text{thr } s\ t' \neq \text{None} \wedge \text{has-lock } ((\text{locks } s)\ \$\ l)\ t'$ 
    by(rule Red1-mthr.deadlockD2[OF dead]) blast
next
  fix  $t\ x\ w$ 
  assume  $\text{thr } s\ t = \lfloor (x, \text{no-wait-locks}) \rfloor$ 
  thus  $\text{wset } s\ t \neq \lfloor \text{PostWS } w \rfloor$ 
  by(rule Red1-mthr.deadlockD3[OF dead, rule-format])
qed
qed
end
end

```

## 7.8 Program Compilation

**theory** *PCompiler*

**imports**

../Common/WellForm

../Common/BinOp

../Common/Conform

**begin**

**definition** *compM* ::  $(\text{mname} \Rightarrow \text{ty list} \Rightarrow \text{ty} \Rightarrow 'a \Rightarrow 'b) \Rightarrow 'a\ \text{mdecl} \Rightarrow 'b\ \text{mdecl}'$

**where**  $\text{compM } f \equiv \lambda(M, Ts, T, m). (M, Ts, T, \text{map-option } (f\ M\ Ts\ T)\ m)$

**definition** *compC* ::  $(\text{cname} \Rightarrow \text{mname} \Rightarrow \text{ty list} \Rightarrow \text{ty} \Rightarrow 'a \Rightarrow 'b) \Rightarrow 'a\ \text{cdecl} \Rightarrow 'b\ \text{cdecl}$

**where**  $\text{compC } f \equiv \lambda(C, D, Fdecls, Mdecls). (C, D, Fdecls, \text{map } (\text{compM } (f\ C))\ Mdecls)$

**primrec** *compP* ::  $(\text{cname} \Rightarrow \text{mname} \Rightarrow \text{ty list} \Rightarrow \text{ty} \Rightarrow 'a \Rightarrow 'b) \Rightarrow 'a\ \text{prog} \Rightarrow 'b\ \text{prog}$

**where**  $\text{compP } f\ (\text{Program } P) = \text{Program } (\text{map } (\text{compC } f)\ P)$

Compilation preserves the program structure. Therefore lookup functions either commute with compilation (like method lookup) or are preserved by it (like the subclass relation).

**lemma** *map-of-map4*:

$\text{map-of } (\text{map } (\lambda(x,a,b,c).(x,a,b,f\ x\ a\ b\ c))\ ts) =$   
 $(\lambda x. \text{map-option } (\lambda(a,b,c).(a,b,f\ x\ a\ b\ c))\ (\text{map-of } ts\ x))$

**apply**(*induct ts*)

**apply** *simp*

**apply**(*rule ext*)

**apply** *fastforce*

**done**

**lemma** *class-compP*:

$\text{class } P\ C = \text{Some } (D, fs, ms)$   
 $\implies \text{class } (\text{compP } f\ P)\ C = \text{Some } (D, fs, \text{map } (\text{compM } (f\ C))\ ms)$

**by**(*cases P*)(*simp add: class-def compP-def compC-def map-of-map4*)

**lemma** *class-compPD*:

$\text{class } (\text{compP } f\ P)\ C = \text{Some } (D, fs, cms)$   
 $\implies \exists ms. \text{class } P\ C = \text{Some}(D, fs, ms) \wedge cms = \text{map } (\text{compM } (f\ C))\ ms$

**by**(*cases P*)(*clarsimp simp add: class-def compP-def compC-def map-of-map4*)

**lemma** [simp]:  $\text{is-class } (\text{compP } f \text{ } P) \text{ } C = \text{is-class } P \text{ } C$

**lemma** [simp]:  $\text{class } (\text{compP } f \text{ } P) \text{ } C = \text{map-option } (\lambda c. \text{snd}(\text{compC } f \text{ } (C, c))) (\text{class } P \text{ } C)$

**lemma** *sees-methods-compP*:

$P \vdash C \text{ sees-methods } Mm \implies$   
 $\text{compP } f \text{ } P \vdash C \text{ sees-methods } (\lambda M. \text{map-option } (\lambda((Ts, T, m), D). ((Ts, T, \text{map-option } (f \text{ } D \text{ } M \text{ } Ts \text{ } T) \text{ } m), D)) (Mm \text{ } M))$

**lemma** *sees-method-compP*:

$P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D \implies$   
 $\text{compP } f \text{ } P \vdash C \text{ sees } M: Ts \rightarrow T = \text{map-option } (f \text{ } D \text{ } M \text{ } Ts \text{ } T) \text{ } m \text{ in } D$

**lemma** [simp]:

$P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D \implies$   
 $\text{method } (\text{compP } f \text{ } P) \text{ } C \text{ } M = (D, Ts, T, \text{map-option } (f \text{ } D \text{ } M \text{ } Ts \text{ } T) \text{ } m)$

**lemma** *sees-methods-compPD*:

$\llbracket cP \vdash C \text{ sees-methods } Mm'; cP = \text{compP } f \text{ } P \rrbracket \implies$   
 $\exists Mm. P \vdash C \text{ sees-methods } Mm \wedge$   
 $Mm' = (\lambda M. \text{map-option } (\lambda((Ts, T, m), D). ((Ts, T, \text{map-option } (f \text{ } D \text{ } M \text{ } Ts \text{ } T) \text{ } m), D)) (Mm \text{ } M))$

**lemma** *sees-method-compPD*:

$\text{compP } f \text{ } P \vdash C \text{ sees } M: Ts \rightarrow T = fm \text{ in } D \implies$   
 $\exists m. P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D \wedge \text{map-option } (f \text{ } D \text{ } M \text{ } Ts \text{ } T) \text{ } m = fm$

**lemma** *sees-method-native-compP* [simp]:

$\text{compP } f \text{ } P \vdash C \text{ sees } M: Ts \rightarrow T = \text{Native in } D \iff P \vdash C \text{ sees } M: Ts \rightarrow T = \text{Native in } D$   
**by**(*auto dest: sees-method-compPD sees-method-compP*)

**lemma** [simp]:  $\text{subcls1 } (\text{compP } f \text{ } P) = \text{subcls1 } P$

**by**(*fastforce simp add: is-class-def compC-def intro:subcls1I order-antisym dest:subcls1D*)

**lemma** [simp]:  $\text{is-type } (\text{compP } f \text{ } P) \text{ } T = \text{is-type } P \text{ } T$

**by**(*induct T*)(*auto cong: ty.case-cong*)

**lemma** *is-type-compP* [simp]:  $\text{is-type } (\text{compP } f \text{ } P) = \text{is-type } P$

**by** *auto*

**lemma** *compP-widen*[simp]:

$(\text{compP } f \text{ } P \vdash T \leq T') = (P \vdash T \leq T')$

**by**(*induct T' arbitrary: T*)(*simp-all add: widen-Class widen-Array*)

**lemma** [simp]:  $(\text{compP } f \text{ } P \vdash Ts [\leq] Ts') = (P \vdash Ts [\leq] Ts')$

**lemma** *is-lub-compP* [simp]:

$\text{is-lub } (\text{compP } f \text{ } P) = \text{is-lub } P$

**by**(*auto intro!: ext elim!: is-lub.cases intro: is-lub.intros*)

**lemma** [simp]:

**fixes**  $f :: \text{cname} \Rightarrow \text{mname} \Rightarrow \text{ty list} \Rightarrow \text{ty} \Rightarrow 'a \Rightarrow 'b$

**shows**  $(\text{compP } f \text{ } P \vdash C \text{ has-fields } FDTs) = (P \vdash C \text{ has-fields } FDTs)$

**lemma** [simp]:  $\text{fields } (\text{compP } f \text{ } P) \text{ } C = \text{fields } P \text{ } C$

**lemma** [simp]:  $(\text{compP } f \text{ } P \vdash C \text{ sees } F: T (fm) \text{ in } D) = (P \vdash C \text{ sees } F: T (fm) \text{ in } D)$

**lemma** [simp]:  $\text{field } (\text{compP } f \ P) \ F \ D = \text{field } P \ F \ D$

### 7.8.1 Invariance of *wf-prog* under compilation

**lemma** [iff]:  $\text{distinct-fst } (\text{classes } (\text{compP } f \ P)) = \text{distinct-fst } (\text{classes } P)$

**lemma** [iff]:  $\text{distinct-fst } (\text{map } (\text{compM } f) \ ms) = \text{distinct-fst } ms$

**lemma** [iff]:  $\text{wf-syscls } (\text{compP } f \ P) = \text{wf-syscls } P$   
**unfolding** *wf-syscls-def* **by** *auto*

**lemma** [iff]:  $\text{wf-fdecl } (\text{compP } f \ P) = \text{wf-fdecl } P$

**lemma** *set-compP*:  
 $(\text{class } (\text{compP } f \ P) \ C = \lfloor (D, fs, ms') \rfloor) \longleftrightarrow$   
 $(\exists ms. \text{class } P \ C = \lfloor (D, fs, ms) \rfloor \wedge ms' = \text{map } (\text{compM } (f \ C)) \ ms)$   
**by**(*cases P*)(*auto simp add: compC-def image-iff map-of-map4*)

**lemma** *compP-has-method*:  $\text{compP } f \ P \vdash C \text{ has } M \longleftrightarrow P \vdash C \text{ has } M$   
**unfolding** *has-method-def*  
**by**(*fastforce dest: sees-method-compPD intro: sees-method-compP*)

**lemma** *is-native-compP* [simp]:  $\text{is-native } (\text{compP } f \ P) = \text{is-native } P$   
**by**(*auto simp add: fun-eq-iff is-native.simps*)

**lemma**  $\tau\text{external-compP}$  [simp]:  
 $\tau\text{external } (\text{compP } f \ P) = \tau\text{external } P$   
**by**(*auto intro!: ext simp add:  $\tau\text{external-def}$* )

**context** *heap-base* **begin**

**lemma** *heap-clone-compP* [simp]:  
 $\text{heap-clone } (\text{compP } f \ P) = \text{heap-clone } P$   
**by**(*intro ext*)(*auto elim!: heap-clone.cases intro: heap-clone.intros*)

**lemma** *red-external-compP* [simp]:  
 $\text{compP } f \ P, t \vdash \langle a \cdot M(vs), h \rangle \text{ --ta--> ext } \langle va, h' \rangle \longleftrightarrow P, t \vdash \langle a \cdot M(vs), h \rangle \text{ --ta--> ext } \langle va, h' \rangle$   
**by**(*auto elim!: red-external.cases intro: red-external.intros*)

**lemma**  $\tau\text{external}'\text{-compP}$  [simp]:  
 $\tau\text{external}' (\text{compP } f \ P) = \tau\text{external}' P$   
**by**(*simp add:  $\tau\text{external}'\text{-def}$  [abs-def]*)

**end**

**lemma** *wf-overriding-compP* [simp]:  $\text{wf-overriding } (\text{compP } f \ P) \ D \ (\text{compM } (f \ C) \ m) = \text{wf-overriding } P \ D \ m$   
**by**(*cases m*)(*fastforce intro: sees-method-compP[where f=f] dest: sees-method-compPD[where f=f] simp add: compM-def*)

**lemma** *wf-cdecl-compPI*:  
**assumes** *wf1-imp-wf2*:  
 $\bigwedge C \ M \ Ts \ T \ m. \llbracket \text{wf-mdecl } wf_1 \ P \ C \ (M, Ts, T, \lfloor m \rfloor); P \vdash C \text{ sees } M:Ts \rightarrow T = \lfloor m \rfloor \text{ in } C \rrbracket$

```

     $\Rightarrow$  wf-mdecl wf2 (compP f P) C (M, Ts, T, [f C M Ts T m])
  and wfcP1:  $\forall C$  rest. class P C = [rest]  $\longrightarrow$  wf-cdecl wf1 P (C, rest)
  and xcomp: class (compP f P) C = [rest']
  and wf: wf-prog p P
  shows wf-cdecl wf2 (compP f P) (C, rest')
proof -
  obtain D fs ms' where x: rest' = (D, fs, ms') by(cases rest')
  with xcomp obtain ms where xsrc: class P C = [(D,fs,ms)]
    and ms': ms' = map (compM (f C)) ms
    by(auto simp add: set-compP compC-def)
  from xsrc wfcP1 have wf1: wf-cdecl wf1 P (C,D,fs,ms) by blast
  { fix field
    assume field  $\in$  set fs
    with wf1 have wf-fdecl (compP f P) field by(simp add: wf-cdecl-def)
  }
  moreover from wf1 have distinct-fst fs by(simp add: wf-cdecl-def)
  moreover
  { fix m
    assume mset': m  $\in$  set ms'
    obtain M Ts' T' body' where m: m = (M, Ts', T', body') by(cases m)
    with ms' obtain body where mf: body' = map-option (f C M Ts' T') body
      and mset: (M, Ts', T', body)  $\in$  set ms using mset'
      by(clarsimp simp add: image-iff compM-def)
    moreover from mset xsrc wfcP1 have wf-mdecl wf1 P C (M,Ts',T',body)
      by(fastforce simp add: wf-cdecl-def)
    moreover from wf xsrc mset x have P  $\vdash$  C sees M:Ts' $\rightarrow$ T' = body in C
      by(auto intro: mdecl-visible)
    ultimately have wf-mdecl wf2 (compP f P) C m using m
      by(cases body)(simp add: wf-mdecl-def, auto intro: wf1-imp-wf2) }
  moreover from wf1 have distinct-fst ms by(simp add: wf-cdecl-def)
  with ms' have distinct-fst ms' by(auto)
  moreover
  { assume CObj: C  $\neq$  Object
    with xsrc wfcP1
    have part1: is-class (compP f P) D  $\neg$  compP f P  $\vdash$  D  $\preceq^*$  C
      by(auto simp add: wf-cdecl-def)
    { fix m
      assume mset': m  $\in$  set ms'
      obtain M Ts T body' where m: m = (M, Ts, T, body') by(cases m)
      with mset' ms' obtain body where mf: body' = map-option (f C M Ts T) body
        and mset: (M, Ts, T, body)  $\in$  set ms
        by(clarsimp simp add: image-iff compM-def)
      from wf1 CObj mset
      have wf-overriding P D (M, Ts, T, body) by(auto simp add: wf-cdecl-def simp del: wf-overriding.simps)
      hence wf-overriding (compP f P) D m unfolding m mf
        by(subst (asm) wf-overriding-compP[symmetric, where f=f and C=C])(simp del: wf-overriding.simps
wf-overriding-compP add: compM-def) }
      note this part1 }
    moreover
    { assume C = Thread
      with wf1 ms' have  $\exists m. (run, [], Void, m) \in$  set ms'
        by(fastforce simp add: wf-cdecl-def image-iff compM-def)+ }
    ultimately show ?thesis unfolding x wf-cdecl-def by blast
  }
qed

```

**lemma** *wf-prog-compPI*:

**assumes** *lift*:

$\bigwedge C M Ts T m.$

$\llbracket P \vdash C \text{ sees } M:Ts \rightarrow T = \lfloor m \rfloor \text{ in } C; \text{ wf-mdecl } wf_1 P C (M, Ts, T, \lfloor m \rfloor) \rrbracket$

$\implies \text{ wf-mdecl } wf_2 (\text{compP } f P) C (M, Ts, T, \lfloor f C M Ts T m \rfloor)$

**and** *wf*: *wf-prog* *wf*<sub>1</sub> *P*

**shows** *wf-prog* *wf*<sub>2</sub> (*compP* *f* *P*)

**using** *wf*

**apply** (*clarsimp simp add:wf-prog-def2*)

**apply**(*rule wf-cdecl-compPI[OF lift], assumption+*)

**apply**(*auto intro: wf*)

**done**

**lemma** *wf-cdecl-compPD*:

**assumes** *wf1-imp-wf2*:

$\bigwedge C M Ts T m. \llbracket \text{ wf-mdecl } wf_1 (\text{compP } f P) C (M, Ts, T, \lfloor f C M Ts T m \rfloor); \text{ compP } f P \vdash C \text{ sees } M:Ts \rightarrow T = \lfloor f C M Ts T m \rfloor \text{ in } C \rrbracket$

$\implies \text{ wf-mdecl } wf_2 P C (M, Ts, T, \lfloor m \rfloor)$

**and** *wfCP1*:  $\forall C \text{ rest. class } (\text{compP } f P) C = \lfloor \text{rest} \rfloor \longrightarrow \text{ wf-cdecl } wf_1 (\text{compP } f P) (C, \text{rest})$

**and** *xcomp*:  $\text{class } P C = \lfloor \text{rest} \rfloor$

**and** *wf*: *wf-prog* *wf-md* (*compP* *f* *P*)

**shows** *wf-cdecl* *wf*<sub>2</sub> *P* (*C*, *rest*)

**proof** –

**obtain** *D fs ms'* **where** *x*: *rest* = (*D*, *fs*, *ms'*) **by**(*cases rest*)

**with** *xcomp* **have** *xsrc*:  $\text{class } (\text{compP } f P) C = \lfloor (D, fs, \text{map } (\text{compM } (f C)) ms') \rfloor$

**by**(*auto simp add: set-compP compC-def*)

**from** *xsrc wfCP1* **have** *wf1*: *wf-cdecl* *wf*<sub>1</sub> (*compP* *f* *P*) (*C*, *D*, *fs*, *map* (*compM* (*f* *C*)) *ms'*) **by** *blast*

**{ fix** *field*

**assume** *field*  $\in \text{set } fs$

**with** *wf1* **have** *wf-fdecl* *P* *field* **by**(*simp add: wf-cdecl-def*)

**}**

**moreover from** *wf1* **have** *distinct-fst fs* **by**(*simp add: wf-cdecl-def*)

**moreover**

**{ fix** *m*

**assume** *mset'*: *m*  $\in \text{set } ms'$

**obtain** *M Ts' T' body'* **where** *m*: *m* = (*M*, *Ts'*, *T'*, *body'*) **by**(*cases m*)

**hence** *mset*: (*M*, *Ts'*, *T'*, *map-option* (*f* *C* *M Ts' T'*) *body'*)  $\in \text{set } (\text{map } (\text{compM } (f C)) ms')$  **using**

*mset'*

**by**(*auto simp add: image-iff compM-def intro: rev-bexI*)

**moreover from** *wf xsrc mset x* **have** *compP* *f* *P*  $\vdash C \text{ sees } M:Ts' \rightarrow T' = \text{map-option } (f C M Ts' T') \text{ body' in } C$

**by**(*auto intro: mdecl-visible*)

**moreover from** *mset wfCP1* [*rule-format, OF xsrc*]

**have** *wf-mdecl* *wf*<sub>1</sub> (*compP* *f* *P*) *C* (*M*, *Ts'*, *T'*, *map-option* (*f* *C* *M Ts' T'*) *body'*)

**by**(*auto simp add: wf-cdecl-def*)

**ultimately have** *wf-mdecl* *wf*<sub>2</sub> *P* *C* *m* **using** *m*

**by**(*cases body'*)(*simp add: wf-mdecl-def, auto intro: wf1-imp-wf2*) **}**

**moreover from** *wf1* **have** *distinct-fst ms'* **by**(*simp add: wf-cdecl-def*)

**moreover**

**{ assume** *CObj*: *C*  $\neq \text{Object}$

**with** *xsrc wfCP1*

**have** *part1*: *is-class* *P* *D*  $\neg P \vdash D \preceq^* C$

**by**(*auto simp add: wf-cdecl-def*)

```

{ fix m
  assume mset': m ∈ set ms'
  with wf1 CObj have wf-overriding (compP f P) D (compM (f C) m)
    by(simp add: wf-cdecl-def del: wf-overriding-compP)
  hence wf-overriding P D m by simp }
note this part1 }
moreover
{ assume C = Thread
  with wf1 have ∃ m. (run, [], Void, m) ∈ set ms'
  by(fastforce simp add: wf-cdecl-def image-iff compM-def)+ }
ultimately show ?thesis unfolding x wf-cdecl-def by blast
qed

```

```

lemma wf-prog-compPD:
assumes wf: wf-prog wf1 (compP f P)
and lift:
  ∧ C M Ts T m.
  [| compP f P ⊢ C sees M:Ts→T = [f C M Ts T m] in C; wf-mdecl wf1 (compP f P) C (M,Ts,T,
  [f C M Ts T m]) |]
  ⇒ wf-mdecl wf2 P C (M,Ts,T,[m])
shows wf-prog wf2 P
using wf
apply(clarsimp simp add:wf-prog-def2)
apply(rule wf-cdecl-compPD[OF lift], assumption+)
apply(auto intro: wf)
done

```

```

lemma WT-binop-compP [simp]: compP f P ⊢ T1 «bop» T2 :: T ⟷ P ⊢ T1 «bop» T2 :: T
by(cases bop)(fastforce)+

```

```

lemma WTrt-binop-compP [simp]: compP f P ⊢ T1 «bop» T2 : T ⟷ P ⊢ T1 «bop» T2 : T
by(cases bop)(fastforce)+

```

```

lemma binop-relevant-class-compP [simp]: binop-relevant-class bop (compP f P) = binop-relevant-class
bop P
by(cases bop) simp-all

```

```

lemma is-class-compP [simp]:
  is-class (compP f P) = is-class P
by(simp add: is-class-def fun-eq-iff)

```

```

lemma has-field-compP [simp]:
  compP f P ⊢ C has F:T (fm) in D ⟷ P ⊢ C has F:T (fm) in D
by(auto simp add: has-field-def)

```

```

context heap-base begin

```

```

lemma compP-addr-loc-type [simp]:
  addr-loc-type (compP f P) = addr-loc-type P
by(auto elim!: addr-loc-type.cases intro: addr-loc-type.intros intro!: ext)

```

```

lemma conf-compP [simp]:
  compP f P, h ⊢ v :≤ T ⟷ P, h ⊢ v :≤ T
by(simp add: conf-def)

```

**lemma** *compP-conf*:  $\text{conf } (\text{compP } f \ P) = \text{conf } P$   
**by**(*auto simp add: conf-def intro!: ext*)

**lemma** *compP-confs*:  $\text{compP } f \ P, h \vdash vs \ [:\leq] \ Ts \longleftrightarrow P, h \vdash vs \ [:\leq] \ Ts$   
**by**(*simp add: compP-conf*)

**lemma** *tconf-compP* [*simp*]:  $\text{compP } f \ P, h \vdash t \ \sqrt{t} \longleftrightarrow P, h \vdash t \ \sqrt{t}$   
**by**(*auto simp add: tconf-def*)

**lemma** *wf-start-state-compP* [*simp*]:  
 $\text{wf-start-state } (\text{compP } f \ P) = \text{wf-start-state } P$   
**by**(*auto 4 6 simp add: fun-eq-iff wf-start-state.simps compP-conf dest: sees-method-compP[where f=f] sees-method-compPD[where f=f]*)

**end**

**lemma** *compP-addr-conv*:  
 $\text{addr-conv addr2thead-id thread-id2addr typeof-addr } (\text{compP } f \ P) = \text{addr-conv addr2thead-id thread-id2addr typeof-addr } P$   
**unfolding** *addr-conv-def*  
**by** *simp*

**lemma** *compP-heap*:  
 $\text{heap addr2thead-id thread-id2addr allocate typeof-addr heap-write } (\text{compP } f \ P) =$   
 $\text{heap addr2thead-id thread-id2addr allocate typeof-addr heap-write } P$   
**unfolding** *heap-def compP-addr-conv heap-axioms-def*  
**by** *auto*

**lemma** *compP-heap-conf*:  
 $\text{heap-conf addr2thead-id thread-id2addr empty-heap allocate typeof-addr heap-write hconf } (\text{compP } f \ P) =$   
 $\text{heap-conf addr2thead-id thread-id2addr empty-heap allocate typeof-addr heap-write hconf } P$   
**unfolding** *heap-conf-def heap-conf-axioms-def compP-heap*  
**unfolding** *heap-base.compP-conf heap-base.compP-addr-loc-type is-type-compP is-class-compP*  
**by**(*rule refl*)

**lemma** *compP-heap-conf-read*:  
 $\text{heap-conf-read addr2thead-id thread-id2addr empty-heap allocate typeof-addr heap-read heap-write hconf } (\text{compP } f \ P) =$   
 $\text{heap-conf-read addr2thead-id thread-id2addr empty-heap allocate typeof-addr heap-read heap-write hconf } P$   
**unfolding** *heap-conf-read-def heap-conf-read-axioms-def*  
**unfolding** *compP-heap-conf heap-base.compP-conf heap-base.compP-addr-loc-type*  
**by**(*rule refl*)

compiler composition

**lemma** *compM-compM*:  
 $\text{compM } f \ (\text{compM } g \ md) = \text{compM } (\lambda M \ Ts \ T. f \ M \ Ts \ T \circ g \ M \ Ts \ T) \ md$   
**by**(*cases md*)(*simp add: compM-def option.map-comp o-def*)

**lemma** *compC-compC*:  
 $\text{compC } f \ (\text{compC } g \ cd) = \text{compC } (\lambda C \ M \ Ts \ T. f \ C \ M \ Ts \ T \circ g \ C \ M \ Ts \ T) \ cd$   
**by**(*simp add: compC-def split-beta compM-compM*)



**lemma** *compP-compP*:

$$\text{by}(\text{cases } P)(\text{simp add: compC-compC})$$

end

## 7.9 Compilation Stage 2

theory Compiler2

```
imports PCompiler J1State ../JVM/JVMInstructions
```

begin

$$\text{primrec } \text{compE2} :: 'addr \text{ expr1} \Rightarrow 'addr \text{ instr list}$$
$$\text{and } compEs2 :: 'addr \ expr1 \ list \Rightarrow 'addr \ instr \ list$$

where

$$compE2 \text{ (new } C) = [New \ C]$$
$$compE2 \ (newA \ T \ e) = compE2 \ e \ @ \ [NewArray \ T]$$
$$compE2 \ (Cast \ T \ e) = compE2 \ e \ @ \ [Checkcast \ T]$$
$$compE2 (e \text{ instanceof } T) = compE2 \ e \ @ \ [Instanceof \ T]$$
$$| \text{compE2 } (Val\ v) = [Push\ v]$$
$$compE2\ (e1\ \ll bop\ \gg\ e2) = compE2\ e1\ @\ compE2\ e2\ @\ [BinOpInstr\ bop]$$
$$compE2 \ (Var \ i) = [Load \ i]$$
$$compE2 \ (i:=e) = compE2 \ e \ @ \ [Store \ i, \ Push \ Unit]$$
$$compE2(a[i]) = compE2\ a @ compE2\ i @ [ALoad]$$
$$[compE2 \ (a[i] := e) = compE2 \ a \ @ \ compE2 \ i \ @ \ compE2 \ e \ @ \ [AStore, Push \ Unit]$$
$$compE2 \ (a \cdot length) = compE2 \ a \ @ \ [ALength]$$
$$compE2 (e \cdot F \{ D \}) = compE2 e @ [Getfield F D]$$
$$compE2 (e1 \cdot F\{D\} := e2) = compE2 e1 @ compE2 e2 @ [Putfield F D, Push Unit]$$
$$compE2\ (e \cdot compareAndSwap(D \cdot F, e', e'')) = compE2\ e\ @\ compE2\ e'\ @\ compE2\ e''\ @\ [CAS\ F\ D]$$
$$compE2\ (e \cdot M(es)) = compE2\ e\ @\ compEs2\ es\ @\ [Invoke\ M\ (size\ es)]$$
$$| \text{compE2 } (\{i:T=vo; e\}) = (\text{case } vo \text{ of } None \Rightarrow [] \mid [v] \Rightarrow [Push\ v, Store\ i]) \text{ @ compE2 } e$$
$$compE2 \ (sync_V \ (o') \ e) = compE2 \ o' \ @ \ [Dup, Store \ V, MEnter] \ @$$

```
compE2 e @ [Load V, MExit, Goto 4, Load V, MExit, ThrowExc]
```

| *compE2* (*insync<sub>V</sub>* (*a*) *e*) = [*Goto 1*] — Define *insync* sensibly

$$|compE2\ (e1;;e2) = compE2\ e1\ @\ [Pop]\ @\ compE2\ e2$$
$$| \text{compE2 } (\text{if } (e) \ e_1 \ \text{else } e_2) =$$
$$(let\ cnd = compE2\ e;$$
$$thn = compE2\ e_1;$$
$$els = compE2\ e_2;$$

```
test = IfFalse (int(size thn + 2));
```

```
thnex = Goto (int(size els + 1))
```

$$in\ cnd\ @\ [test]\ @\ thn\ @\ [thnex]\ @\ els)$$
$$| \text{compE2 } (\text{while } (e) \ c) =$$

```
(let cnd = compE2 e;
```

$$bdy = compE2\ c;$$

```
test = IfFalse (int(size bdy + 3));
```

$$loop = Goto (-int(size\ bdy + size\ cnd + 2))$$
$$in\ cnd\ @\ [test]\ @\ bdy\ @\ [Pop]\ @\ [loop]\ @\ [Push\ Unit])$$
$$compE2 \ (throw \ e) = compE2 \ e \ @ \ [ThrowExc]$$
$$| \text{compE2 } (\text{try } e1 \text{ catch}(C \ i) \ e2) =$$

```
(let catch = compE2 e2
```

$$in\ compE2\ e1\ @\ [Goto\ (int(size\ catch)+2),\ Store\ i]\ @\ catch)$$

```

| compEs2 [] = []
| compEs2 (e#es) = compE2 e @ compEs2 es

```

Compilation of exception table. Is given start address of code to compute absolute addresses necessary in exception table.

```

fun compxE2 :: 'addr expr1 ⇒ pc ⇒ nat ⇒ ex-table
and compEs2 :: 'addr expr1 list ⇒ pc ⇒ nat ⇒ ex-table
where
  compxE2 (new C) pc d = []
  compxE2 (newA T[e]) pc d = compxE2 e pc d
  compxE2 (Cast T e) pc d = compxE2 e pc d
  compxE2 (e instanceof T) pc d = compxE2 e pc d
  compxE2 (Val v) pc d = []
  compxE2 (e1 «bop» e2) pc d =
    compxE2 e1 pc d @ compxE2 e2 (pc + size(compE2 e1)) (d+1)
  compxE2 (Var i) pc d = []
  compxE2 (i:=e) pc d = compxE2 e pc d
  compxE2 (a[i]) pc d = compxE2 a pc d @ compxE2 i (pc + size (compE2 a)) (d + 1)
  compxE2 (a[i] := e) pc d =
    (let pc1 = pc + size (compE2 a);
     pc2 = pc1 + size (compE2 e)
     in compxE2 a pc d @ compxE2 i pc1 (d + 1) @ compxE2 e pc2 (d + 2))
  compxE2 (a.length) pc d = compxE2 a pc d
  compxE2 (e.F{D}) pc d = compxE2 e pc d
  compxE2 (e1.F{D} := e2) pc d = compxE2 e1 pc d @ compxE2 e2 (pc + size (compE2 e1)) (d + 1)
  compxE2 (e.compareAndSwap(D.F, e', e'')) pc d =
    (let pc1 = pc + size (compE2 e);
     pc2 = pc1 + size (compE2 e')
     in compxE2 e pc d @ compxE2 e' pc1 (d + 1) @ compxE2 e'' pc2 (d + 2))
  compxE2 (e.M(es)) pc d = compxE2 e pc d @ compEs2 es (pc + size(compE2 e)) (d+1)
  compxE2 ({i:T=vo; e}) pc d = compxE2 e (case vo of None ⇒ pc | [v] ⇒ Suc (Suc pc)) d
  compxE2 (syncV (o') e) pc d =
    (let pc1 = pc + size (compE2 o') + 3;
     pc2 = pc1 + size(compE2 e)
     in compxE2 o' pc d @ compxE2 e pc1 d @ [(pc1, pc2, None, Suc (Suc (Suc pc2)), d)])
  compxE2 (insyncV (a) e) pc d = []
  compxE2 (e1;;e2) pc d =
    compxE2 e1 pc d @ compxE2 e2 (pc+size(compE2 e1)+1) d
  compxE2 (if (e) e1 else e2) pc d =
    (let pc1 = pc + size(compE2 e) + 1;
     pc2 = pc1 + size(compE2 e1) + 1
     in compxE2 e pc d @ compxE2 e1 pc1 d @ compxE2 e2 pc2 d)
  compxE2 (while (b) e) pc d =
    compxE2 b pc d @ compxE2 e (pc+size(compE2 b)+1) d
  compxE2 (throw e) pc d = compxE2 e pc d
  compxE2 (try e1 catch (C i) e2) pc d =
    (let pc1 = pc + size(compE2 e1)
     in compxE2 e1 pc d @ compxE2 e2 (pc1+2) d @ [(pc,pc1,Some C,pc1+1,d)])

  compEs2 [] pc d = []
  compEs2 (e#es) pc d = compxE2 e pc d @ compEs2 es (pc+size(compE2 e)) (d+1)

```

**lemmas** *compxE2-compEs2-induct* =  
*compxE2-compEs2.induct*[  
*unfolded meta-all5-eq-conv meta-all4-eq-conv meta-all3-eq-conv meta-all2-eq-conv meta-all-eq-conv*,  
*case-names*  
*new NewArray Cast InstanceOf Val BinOp Var LAss AAcc AAss ALen FAcc FAss Call Block*  
*Synchronized InSynchronized Seq Cond While throw TryCatch*  
*Nil Cons*]

**lemma** *compE2-neq-Nil* [*simp*]: *compE2 e* ≠ []  
**by**(*induct e*) *auto*

**declare** *compE2-neq-Nil*[*symmetric, simp*]

**lemma** *compEs2-append* [*simp*]: *compEs2 (es @ es')* = *compEs2 es @ compEs2 es'*  
**by**(*induct es*) *auto*

**lemma** *compEs2-eq-Nil-conv* [*simp*]: *compEs2 es* = []  $\longleftrightarrow$  *es* = []  
**by**(*cases es*) *auto*

**lemma** *compEs2-map-Val*: *compEs2 (map Val vs)* = *map Push vs*  
**by**(*induct vs*) *auto*

**lemma** *compE2-0th-neq-Invoke* [*simp*]:  
*compE2 e ! 0* ≠ *Invoke M n*  
**by**(*induct e*)(*auto simp add: nth-append*)

**declare** *compE2-0th-neq-Invoke*[*symmetric, simp*]

**lemma** *compEs2-append* [*simp*]:  
*compEs2 (es @ es') pc d* = *compEs2 es pc d @ compEs2 es' (length (compEs2 es) + pc) (length es + d)*  
**by**(*induct es arbitrary: pc d*)(*auto simp add: ac-simps*)

**lemma** *compEs2-map-Val* [*simp*]: *compEs2 (map Val vs) pc d* = []  
**by**(*induct vs arbitrary: d pc*) *auto*

**lemma** *compE2-blocks1* [*simp*]:  
*compE2 (blocks1 n Ts body)* = *compE2 body*  
**by**(*induct n Ts body rule: blocks1.induct*)(*auto*)

**lemma** *compxE2-blocks1* [*simp*]:  
*compxE2 (blocks1 n Ts body)* = *compxE2 body*  
**by**(*induct n Ts body rule: blocks1.induct*)(*auto intro!: ext*)

**lemma** *fixes e :: 'addr expr1 and es :: 'addr expr1 list*  
**shows** *compE2-not-Return: Return* ∉ *set (compE2 e)*  
**and** *compEs2-not-Return: Return* ∉ *set (compEs2 es)*  
**by**(*induct e and es rule: compE2.induct compEs2.induct*)(*auto*)

**primrec** *max-stack* :: 'addr expr1 ⇒ nat  
**and** *max-stacks* :: 'addr expr1 list ⇒ nat  
**where**  
*max-stack (new C)* = 1  
| *max-stack (newA T[e])* = *max-stack e*

```

| max-stack (Cast C e) = max-stack e
| max-stack (e instanceof T) = max-stack e
| max-stack (Val v) = 1
| max-stack (e1 «bop» e2) = max (max-stack e1) (max-stack e2) + 1
| max-stack (Var i) = 1
| max-stack (i:=e) = max-stack e
| max-stack (a[i]) = max (max-stack a) (max-stack i + 1)
| max-stack (a[i] := e) = max (max (max-stack a) (max-stack i + 1)) (max-stack e + 2)
| max-stack (a.length) = max-stack a
| max-stack (e.F{D}) = max-stack e
| max-stack (e1.F{D} := e2) = max (max-stack e1) (max-stack e2) + 1
| max-stack (e.compareAndSwap(D.F, e', e')) = max (max (max-stack e) (max-stack e' + 1)) (max-stack
e'' + 2)
| max-stack (e.M(es)) = max (max-stack e) (max-stacks es) + 1
| max-stack ({i:T=vo; e}) = max-stack e
| max-stack (syncV (o') e) = max (max-stack o') (max (max-stack e) 2)
| max-stack (insyncV (a) e) = 1
| max-stack (e1;;e2) = max (max-stack e1) (max-stack e2)
| max-stack (if (e) e1 else e2) =
  max (max-stack e) (max (max-stack e1) (max-stack e2))
| max-stack (while (e) c) = max (max-stack e) (max-stack c)
| max-stack (throw e) = max-stack e
| max-stack (try e1 catch(C i) e2) = max (max-stack e1) (max-stack e2)

| max-stacks [] = 0
| max-stacks (e#es) = max (max-stack e) (1 + max-stacks es)

```

**lemma** *max-stack1*:  $1 \leq \text{max-stack } e$

**lemma** *max-stacks-ge-length*:  $\text{max-stacks } es \geq \text{length } es$

**by**(*induct es, auto*)

**lemma** *max-stack-blocks1* [*simp*]:

*max-stack* (*blocks1 n Ts body*) = *max-stack body*

**by**(*induct n Ts body rule: blocks1.induct*) *auto*

**definition** *compMb2* :: '*addr expr1*  $\Rightarrow$  '*addr jvm-method*

**where**

*compMb2*  $\equiv$   $\lambda \text{body.}$

*let ins* = *compE2 body* @ [*Return*];

*xt* = *compxE2 body 0 0*

*in* (*max-stack body, max-vars body, ins, xt*)

**definition** *compP2* :: '*addr J1-prog*  $\Rightarrow$  '*addr jvm-prog*

**where** *compP2*  $\equiv$  *compP* ( $\lambda C M Ts T. \text{compMb2}$ )

**lemma** *compMb2*:

*compMb2 e* = (*max-stack e, max-vars e, (compE2 e* @ [*Return*]), *compxE2 e 0 0*)

**by** (*simp add: compMb2-def*)

**end**

## 7.10 Various Operations for Exception Tables

**theory** *Exception-Tables* **imports**

*Compiler2*

*../Common/ExternalCallWF*

*../JVM/JVMExceptions*

**begin**

**definition**  $pcs :: ex\text{-}table \Rightarrow nat\ set$

**where**  $pcs\ xt \equiv \bigcup (f,t,C,h,d) \in set\ xt. \{f \dots t\}$

**lemma** *pcs-subset*:

**fixes**  $e :: 'addr\ expr1$  **and**  $es :: 'addr\ expr1\ list$

**shows**  $pcs(compxE2\ e\ pc\ d) \subseteq \{pc \dots pc + size(compE2\ e)\}$

**and**  $pcs(compxEs2\ es\ pc\ d) \subseteq \{pc \dots pc + size(compEs2\ es)\}$

**apply**(*induct*  $e\ pc\ d$  **and**  $es\ pc\ d$  *rule: compxE2-compxEs2-induct*)

**apply** (*simp-all* *add:pcs-def*)

**apply** (*fastforce*)**+**

**done**

**lemma** *pcs-Nil* [*simp*]:  $pcs\ [] = \{\}$

**by**(*simp* *add:pcs-def*)

**lemma** *pcs-Cons* [*simp*]:  $pcs\ (x\#\!xt) = \{fst\ x \dots fst(snd\ x)\} \cup pcs\ xt$

**by**(*auto* *simp* *add: pcs-def*)

**lemma** *pcs-append* [*simp*]:  $pcs(xt_1\ @\ xt_2) = pcs\ xt_1 \cup pcs\ xt_2$

**by**(*simp* *add:pcs-def*)

**lemma** [*simp*]:  $pc < pc_0 \vee pc_0 + size(compE2\ e) \leq pc \implies pc \notin pcs(compxE2\ e\ pc_0\ d)$

**using** *pcs-subset* **by** *fastforce*

**lemma** [*simp*]:  $pc < pc_0 \vee pc_0 + size(compEs2\ es) \leq pc \implies pc \notin pcs(compxEs2\ es\ pc_0\ d)$

**using** *pcs-subset* **by** *fastforce*

**lemma** [*simp*]:  $pc_1 + size(compE2\ e_1) \leq pc_2 \implies pcs(compxE2\ e_1\ pc_1\ d_1) \cap pcs(compxE2\ e_2\ pc_2\ d_2) = \{\}$

**using** *pcs-subset* **by** *fastforce*

**lemma** [*simp*]:  $pc_1 + size(compE2\ e) \leq pc_2 \implies pcs(compxE2\ e\ pc_1\ d_1) \cap pcs(compxEs2\ es\ pc_2\ d_2) = \{\}$

**using** *pcs-subset* **by** *fastforce*

**lemma** *match-ex-table-append-not-pcs* [*simp*]:

$pc \notin pcs\ xt_0 \implies match\text{-}ex\text{-}table\ P\ C\ pc\ (xt_0\ @\ xt_1) = match\text{-}ex\text{-}table\ P\ C\ pc\ xt_1$

**by** (*induct*  $xt_0$ ) (*auto* *simp: matches-ex-entry-def*)

**lemma** *outside-pcs-not-matches-entry* [*simp*]:

$\llbracket x \in set\ xt; pc \notin pcs\ xt \rrbracket \implies \neg matches\text{-}ex\text{-}entry\ P\ D\ pc\ x$

**by**(*auto* *simp:matches-ex-entry-def pcs-def*)

**lemma** *outside-pcs-compxE2-not-matches-entry* [*simp*]:

**assumes**  $xe \in set(compxE2\ e\ pc\ d)$

**and**  $outside: pc' < pc \vee pc + size(compE2\ e) \leq pc'$

**shows**  $\neg \text{matches-ex-entry } P \ C \ pc' \ xe$   
**proof**  
**assume**  $\text{matches-ex-entry } P \ C \ pc' \ xe$   
**with**  $xe$  **have**  $pc' \in \text{pcs}(\text{compxE2 } e \ pc \ d)$   
**by**( $\text{force simp add:matches-ex-entry-def pcs-def}$ )  
**with**  $\text{outside}$  **show**  $\text{False}$  **by**  $\text{simp}$   
**qed**

**lemma**  $\text{outside-pcs-compxEs2-not-matches-entry [simp]:}$   
**assumes**  $xe: xe \in \text{set}(\text{compxEs2 } es \ pc \ d)$   
**and**  $\text{outside: } pc' < pc \vee pc + \text{size}(\text{compEs2 } es) \leq pc'$   
**shows**  $\neg \text{matches-ex-entry } P \ C \ pc' \ xe$   
**proof**  
**assume**  $\text{matches-ex-entry } P \ C \ pc' \ xe$   
**with**  $xe$  **have**  $pc' \in \text{pcs}(\text{compxEs2 } es \ pc \ d)$   
**by**( $\text{force simp add:matches-ex-entry-def pcs-def}$ )  
**with**  $\text{outside}$  **show**  $\text{False}$  **by**  $\text{simp}$   
**qed**

**lemma**  $\text{match-ex-table-app[simp]:}$   
 $\forall xte \in \text{set } xt_1. \neg \text{matches-ex-entry } P \ D \ pc \ xte \implies$   
 $\text{match-ex-table } P \ D \ pc \ (xt_1 @ xt) = \text{match-ex-table } P \ D \ pc \ xt$   
**by**( $\text{induct } xt_1$ )  $\text{simp-all}$

**lemma**  $\text{match-ex-table-eq-NoneI [simp]:}$   
 $\forall x \in \text{set } xtab. \neg \text{matches-ex-entry } P \ C \ pc \ x \implies$   
 $\text{match-ex-table } P \ C \ pc \ xtab = \text{None}$   
**using**  $\text{match-ex-table-app[where ?xt = []]}$  **by**  $\text{fastforce}$

**lemma**  $\text{match-ex-table-not-pcs-None:}$   
 $pc \notin \text{pcs } xt \implies \text{match-ex-table } P \ C \ pc \ xt = \text{None}$   
**by**( $\text{auto intro: match-ex-table-eq-NoneI}$ )

**lemma**  $\text{match-ex-entry:}$   
**fixes**  $\text{start}$  **shows**  
 $\text{matches-ex-entry } P \ C \ pc \ (\text{start}, \text{end}, \text{catch-type}, \text{handler}) =$   
 $(\text{start} \leq pc \wedge pc < \text{end} \wedge (\text{case } \text{catch-type} \text{ of } \text{None} \Rightarrow \text{True} \mid \lfloor C' \rfloor \Rightarrow P \vdash C \preceq^* C'))$   
**by**( $\text{simp add:matches-ex-entry-def}$ )

**lemma**  $\text{pcs-compxE2D [dest]:}$   
 $pc \in \text{pcs}(\text{compxE2 } e \ pc' \ d) \implies pc' \leq pc \wedge pc < pc' + \text{length}(\text{compE2 } e)$   
**using**  $\text{pcs-subset}$  **by**( $\text{fastforce}$ )

**lemma**  $\text{pcs-compxEs2D [dest]:}$   
 $pc \in \text{pcs}(\text{compxEs2 } es \ pc' \ d) \implies pc' \leq pc \wedge pc < pc' + \text{length}(\text{compEs2 } es)$   
**using**  $\text{pcs-subset}$  **by**( $\text{fastforce}$ )

**definition**  $\text{shift} :: \text{nat} \Rightarrow \text{ex-table} \Rightarrow \text{ex-table}$   
**where**  
 $\text{shift } n \ xt \equiv \text{map } (\lambda(\text{from}, \text{to}, C, \text{handler}, \text{depth}). (n + \text{from}, n + \text{to}, C, n + \text{handler}, \text{depth})) \ xt$

**lemma**  $\text{shift-0 [simp]:}$   $\text{shift } 0 \ xt = xt$   
**by**( $\text{induct } xt$ )( $\text{auto simp:shift-def}$ )

**lemma** *shift-Nil* [simp]:  $\text{shift } n \ [] = []$   
**by**(simp add:shift-def)

**lemma** *shift-Cons-tuple* [simp]:  
 $\text{shift } n ((\text{from}, \text{to}, C, \text{handler}, \text{depth}) \# xt) = (\text{from} + n, \text{to} + n, C, \text{handler} + n, \text{depth}) \# \text{shift } n \ xt$   
**by**(simp add: shift-def)

**lemma** *shift-append* [simp]:  $\text{shift } n (xt_1 @ xt_2) = \text{shift } n \ xt_1 @ \text{shift } n \ xt_2$   
**by**(simp add:shift-def)

**lemma** *shift-shift* [simp]:  $\text{shift } m (\text{shift } n \ xt) = \text{shift } (m+n) \ xt$   
**by**(simp add: shift-def split-def)

**lemma** *fixes*  $e :: 'addr \text{expr1}$  **and**  $es :: 'addr \text{expr1 list}$   
**shows** *shift-compxE2*:  $\text{shift } pc (\text{compxE2 } e \ pc' \ d) = \text{compxE2 } e \ (pc' + pc) \ d$   
**and** *shift-compxEs2*:  $\text{shift } pc (\text{compxEs2 } es \ pc' \ d) = \text{compxEs2 } es \ (pc' + pc) \ d$   
**by**(induct  $e$  **and**  $es$  arbitrary:  $pc \ pc' \ d$  **and**  $pc \ pc' \ d$  rule: *compE2.induct compEs2.induct*)  
(auto simp:shift-def ac-simps)

**lemma** *compxE2-size-convs* [simp]:  $n \neq 0 \implies \text{compxE2 } e \ n \ d = \text{shift } n (\text{compxE2 } e \ 0 \ d)$   
**and** *compxEs2-size-convs*:  $n \neq 0 \implies \text{compxEs2 } es \ n \ d = \text{shift } n (\text{compxEs2 } es \ 0 \ d)$   
**by**(simp-all add:shift-compxE2 shift-compxEs2)

**lemma** *pcs-shift-conv* [simp]:  $pcs (\text{shift } n \ xt) = (+) \ n \ ' \ pcs \ xt$   
**apply**(auto simp add: shift-def pcs-def)  
**apply**(rule-tac  $x=x-n$  **in** *image-eqI*)  
**apply**(auto)  
**apply**(rule *beXI*)  
**prefer** 2  
**apply**(assumption)  
**apply**(auto)  
**done**

**lemma** *image-plus-const-conv* [simp]:  
**fixes**  $m :: nat$   
**shows**  $m \in (+) \ n \ ' \ A \longleftrightarrow m \geq n \wedge m - n \in A$   
**by**(force)

**lemma** *match-ex-table-shift-eq-None-conv* [simp]:  
 $\text{match-ex-table } P \ C \ pc (\text{shift } n \ xt) = \text{None} \longleftrightarrow pc < n \vee \text{match-ex-table } P \ C \ (pc - n) \ xt = \text{None}$   
**by**(induct  $xt$ )(auto simp add: match-ex-entry split: if-split-asm)

**lemma** *match-ex-table-shift-pc-None*:  
 $pc \geq n \implies \text{match-ex-table } P \ C \ pc (\text{shift } n \ xt) = \text{None} \longleftrightarrow \text{match-ex-table } P \ C \ (pc - n) \ xt = \text{None}$   
**by**(simp add: match-ex-table-shift-eq-None-conv)

**lemma** *match-ex-table-shift-eq-Some-conv* [simp]:  
 $\text{match-ex-table } P \ C \ pc (\text{shift } n \ xt) = \lfloor (pc', d) \rfloor \longleftrightarrow$   
 $pc \geq n \wedge pc' \geq n \wedge \text{match-ex-table } P \ C \ (pc - n) \ xt = \lfloor (pc' - n, d) \rfloor$   
**by**(induct  $xt$ )(auto simp add: match-ex-entry split: if-split-asm)

**lemma** *match-ex-table-shift*:  
 $\text{match-ex-table } P \ C \ pc \ xt = \lfloor (pc', d) \rfloor \implies \text{match-ex-table } P \ C \ (n + pc) (\text{shift } n \ xt) = \lfloor (n + pc', d) \rfloor$

**by**(*simp add: match-ex-table-shift-eq-Some-conv*)

**lemma** *match-ex-table-shift-pcD*:

*match-ex-table P C pc (shift n xt) = [(pc', d)]  $\implies$  pc  $\geq$  n  $\wedge$  pc'  $\geq$  n  $\wedge$  match-ex-table P C (pc - n) xt = [(pc' - n, d)]*

**by**(*simp add: match-ex-table-shift-eq-Some-conv*)

**lemma** *match-ex-table-pcsD*: *match-ex-table P C pc xt = [(pc', D)]  $\implies$  pc  $\in$  pcs xt*

**by**(*induct xt*)(*auto split: if-split-asm simp add: match-ex-entry*)

**definition** *stack-xlift* :: *nat  $\Rightarrow$  ex-table  $\Rightarrow$  ex-table*

**where** *stack-xlift n xt  $\equiv$  map ( $\lambda$ (from,to,C,handler,depth). (from, to, C, handler, n + depth)) xt*

**lemma** *stack-xlift-0* [*simp*]: *stack-xlift 0 xt = xt*

**by**(*induct xt, auto simp add: stack-xlift-def*)

**lemma** *stack-xlift-Nil* [*simp*]: *stack-xlift n [] = []*

**by**(*simp add: stack-xlift-def*)

**lemma** *stack-xlift-Cons-tuple* [*simp*]:

*stack-xlift n ((from, to, C, handler, depth) # xt) = (from, to, C, handler, depth + n) # stack-xlift n xt*

**by**(*simp add: stack-xlift-def*)

**lemma** *stack-xlift-append* [*simp*]: *stack-xlift n (xt @ xt') = stack-xlift n xt @ stack-xlift n xt'*

**by**(*simp add: stack-xlift-def*)

**lemma** *stack-xlift-stack-xlift* [*simp*]: *stack-xlift n (stack-xlift m xt) = stack-xlift (n + m) xt*

**by**(*simp add: stack-xlift-def split-def*)

**lemma** *fixes e :: 'addr expr1 and es :: 'addr expr1 list*

**shows** *stack-xlift-compxE2*: *stack-xlift n (compxE2 e pc d) = compxE2 e pc (n + d)*

**and** *stack-xlift-compEs2*: *stack-xlift n (compEs2 es pc d) = compEs2 es pc (n + d)*

**by**(*induct e and es arbitrary: d pc and d pc rule: compE2.induct compEs2.induct*)

(*auto simp add: shift-compxE2 simp del: compxE2-size-convs*)

**lemma** *compxE2-stack-xlift-convs* [*simp*]: *d > 0  $\implies$  compxE2 e pc d = stack-xlift d (compxE2 e pc 0)*

**and** *compEs2-stack-xlift-convs* [*simp*]: *d > 0  $\implies$  compEs2 es pc d = stack-xlift d (compEs2 es pc 0)*

**by**(*simp-all add: stack-xlift-compxE2 stack-xlift-compEs2*)

**lemma** *stack-xlift-shift* [*simp*]: *stack-xlift d (shift n xt) = shift n (stack-xlift d xt)*

**by**(*induct xt*)(*auto*)

**lemma** *pcs-stack-xlift-conv* [*simp*]: *pcs (stack-xlift n xt) = pcs xt*

**by**(*auto simp add: pcs-def stack-xlift-def*)

**lemma** *match-ex-table-stack-xlift-eq-None-conv* [*simp*]:

*match-ex-table P C pc (stack-xlift d xt) = None  $\longleftrightarrow$  match-ex-table P C pc xt = None*

**by**(*induct xt*)(*auto simp add: match-ex-entry*)

**lemma** *match-ex-table-stack-xlift-eq-Some-conv* [*simp*]:



$\text{match-ex-table } P \ C \ pc \ (\text{stack-xlift } n \ xt) = \lfloor (pc', d) \rfloor \longleftrightarrow d \geq n \wedge \text{match-ex-table } P \ C \ pc \ xt = \lfloor (pc', d - n) \rfloor$

**by**(*induct xt*)(*auto simp add: match-ex-entry*)

**lemma** *match-ex-table-stack-xliftD*:

$\text{match-ex-table } P \ C \ pc \ (\text{stack-xlift } n \ xt) = \lfloor (pc', d) \rfloor \implies d \geq n \wedge \text{match-ex-table } P \ C \ pc \ xt = \lfloor (pc', d - n) \rfloor$

**by**(*simp*)

**lemma** *match-ex-table-stack-xlift*:

$\text{match-ex-table } P \ C \ pc \ xt = \lfloor (pc', d) \rfloor \implies \text{match-ex-table } P \ C \ pc \ (\text{stack-xlift } n \ xt) = \lfloor (pc', n + d) \rfloor$

**by** *simp*

**lemma** *pcs-stack-xlift*:  $\text{pcs } (\text{stack-xlift } n \ xt) = \text{pcs } xt$

**by**(*auto simp add: stack-xlift-def pcs-def*)

**lemma** *match-ex-table-None-append* [*simp*]:

$\text{match-ex-table } P \ C \ pc \ xt = \text{None}$

$\implies \text{match-ex-table } P \ C \ pc \ (xt @ xt') = \text{match-ex-table } P \ C \ pc \ xt'$

**by**(*induct xt, auto*)

**lemma** *match-ex-table-Some-append* [*simp*]:

$\text{match-ex-table } P \ C \ pc \ xt = \lfloor (pc', d) \rfloor \implies \text{match-ex-table } P \ C \ pc \ (xt @ xt') = \lfloor (pc', d) \rfloor$

**by**(*induct xt*)(*auto*)

**lemma** *match-ex-table-append*:

$\text{match-ex-table } P \ C \ pc \ (xt @ xt') = (\text{case match-ex-table } P \ C \ pc \ xt \text{ of } \text{None} \Rightarrow \text{match-ex-table } P \ C \ pc \ xt' \mid \text{Some } pcd \Rightarrow \text{Some } pcd)$

**by**(*auto*)

**lemma** *match-ex-table-pc-length-compE2*:

$\text{match-ex-table } P \ a \ pc \ (\text{compxE2 } e \ pc' \ d) = \lfloor pcd \rfloor \implies pc' \leq pc \wedge pc < \text{length } (\text{compE2 } e) + pc'$

**and** *match-ex-table-pc-length-compEs2*:

$\text{match-ex-table } P \ a \ pc \ (\text{compEs2 } es \ pc' \ d) = \lfloor pcd \rfloor \implies pc' \leq pc \wedge pc < \text{length } (\text{compEs2 } es) + pc'$

**using** *pcs-subset* **by**(*cases pcd, fastforce dest!: match-ex-table-pcsD*)**+**

**lemma** *match-ex-table-compxE2-shift-conv*:

$f > 0 \implies \text{match-ex-table } P \ C \ pc \ (\text{compxE2 } e \ f \ d) = \lfloor (pc', d') \rfloor \longleftrightarrow pc \geq f \wedge pc' \geq f \wedge \text{match-ex-table } P \ C \ (pc - f) \ (\text{compxE2 } e \ 0 \ d) = \lfloor (pc' - f, d') \rfloor$

**by** *simp*

**lemma** *match-ex-table-compEs2-shift-conv*:

$f > 0 \implies \text{match-ex-table } P \ C \ pc \ (\text{compEs2 } es \ f \ d) = \lfloor (pc', d') \rfloor \longleftrightarrow pc \geq f \wedge pc' \geq f \wedge \text{match-ex-table } P \ C \ (pc - f) \ (\text{compEs2 } es \ 0 \ d) = \lfloor (pc' - f, d') \rfloor$

**by**(*simp add: compEs2-size-convs*)

**lemma** *match-ex-table-compxE2-stack-conv*:

$d > 0 \implies \text{match-ex-table } P \ C \ pc \ (\text{compxE2 } e \ 0 \ d) = \lfloor (pc', d') \rfloor \longleftrightarrow d' \geq d \wedge \text{match-ex-table } P \ C \ pc \ (\text{compxE2 } e \ 0 \ 0) = \lfloor (pc', d' - d) \rfloor$

**by** *simp*

**lemma** *match-ex-table-compEs2-stack-conv*:

$d > 0 \implies \text{match-ex-table } P \ C \ pc \ (\text{compxEs2 } es \ 0 \ d) = \lfloor (pc', d') \rfloor \longleftrightarrow d' \geq d \wedge \text{match-ex-table } P \ C \ pc \ (\text{compxEs2 } es \ 0 \ 0) = \lfloor (pc', d' - d) \rfloor$   
**by**(*simp add: compxEs2-stack-xlift-convs*)

**lemma fixes**  $e :: 'addr \ expr1$  **and**  $es :: 'addr \ expr1 \ list$

**shows**  $\text{match-ex-table-compxE2-not-same: match-ex-table } P \ C \ pc \ (\text{compxE2 } e \ n \ d) = \lfloor (pc', d') \rfloor \implies pc \neq pc'$

**and**  $\text{match-ex-table-compxEs2-not-same: match-ex-table } P \ C \ pc \ (\text{compxEs2 } es \ n \ d) = \lfloor (pc', d') \rfloor \implies pc \neq pc'$

**apply**(*induct e n d and es n d rule: compxE2-compxEs2-induct*)

**apply**(*auto simp add: match-ex-table-append match-ex-entry simp del: compxE2-size-convs compxEs2-size-convs compxE2-stack-xlift-convs compxEs2-stack-xlift-convs split: if-split-asm*)

**done**

**end**

## 7.11 Type rules for the intermediate language

**theory** *J1WellType* **imports**

*J1State*

*../Common/ExternalCallWF*

*../Common/SemiType*

**begin**

**declare** *Listn.lesub-list-impl-same-size*[*simp del*] *listE-length* [*simp del*]

### 7.11.1 Well-Typedness

**type-synonym**

*env1* = *ty list* — type environment indexed by variable number

**inductive** *WT1* ::  $'addr \ J1\text{-prog} \Rightarrow env1 \Rightarrow 'addr \ expr1 \Rightarrow ty \Rightarrow bool$  ( $-, \vdash 1 - :: -$  [51,0,0,51] 50)

**and** *WTs1* ::  $'addr \ J1\text{-prog} \Rightarrow env1 \Rightarrow 'addr \ expr1 \ list \Rightarrow ty \ list \Rightarrow bool$  ( $-, \vdash 1 - [::] -$  [51,0,0,51] 50)

**for**  $P :: 'addr \ J1\text{-prog}$

**where**

*WT1New*:

$is\text{-class } P \ C \implies$

$P, E \vdash 1 \text{ new } C :: Class \ C$

| *WT1NewArray*:

$\llbracket P, E \vdash 1 \ e :: Integer; is\text{-type } P \ (T[]) \rrbracket \implies$

$P, E \vdash 1 \ \text{newA } T[e] :: T[]$

| *WT1Cast*:

$\llbracket P, E \vdash 1 \ e :: T; P \vdash U \leq T \vee P \vdash T \leq U; is\text{-type } P \ U \rrbracket$

$\implies P, E \vdash 1 \ \text{Cast } U \ e :: U$

| *WT1InstanceOf*:

$\llbracket P, E \vdash 1 \ e :: T; P \vdash U \leq T \vee P \vdash T \leq U; is\text{-type } P \ U; is\text{-refT } U \rrbracket$

$\implies P, E \vdash 1 \ e \ \text{instanceof } U :: Boolean$

| *WT1Val*:

$\text{typeof } v = Some \ T \implies$

$P, E \vdash 1 \text{ Val } v :: T$

| *WT1Var*:  
 $\llbracket E!V = T; V < \text{size } E \rrbracket \implies$   
 $P, E \vdash 1 \text{ Var } V :: T$

| *WT1BinOp*:  
 $\llbracket P, E \vdash 1 e1 :: T1; P, E \vdash 1 e2 :: T2; P \vdash T1 \llbracket \text{bop} \rrbracket T2 :: T \rrbracket$   
 $\implies P, E \vdash 1 e1 \llbracket \text{bop} \rrbracket e2 :: T$

| *WT1LAss*:  
 $\llbracket E!i = T; i < \text{size } E; P, E \vdash 1 e :: T'; P \vdash T' \leq T \rrbracket$   
 $\implies P, E \vdash 1 i := e :: \text{Void}$

| *WT1AAcc*:  
 $\llbracket P, E \vdash 1 a :: T[]; P, E \vdash 1 i :: \text{Integer} \rrbracket$   
 $\implies P, E \vdash 1 a[i] :: T$

| *WT1AAss*:  
 $\llbracket P, E \vdash 1 a :: T[]; P, E \vdash 1 i :: \text{Integer}; P, E \vdash 1 e :: T'; P \vdash T' \leq T \rrbracket$   
 $\implies P, E \vdash 1 a[i] := e :: \text{Void}$

| *WT1ALength*:  
 $P, E \vdash 1 a :: T[] \implies P, E \vdash 1 a.\text{length} :: \text{Integer}$

| *WTFAcc1*:  
 $\llbracket P, E \vdash 1 e :: U; \text{class-type-of}' U = [C]; P \vdash C \text{ sees } F:T (fm) \text{ in } D \rrbracket$   
 $\implies P, E \vdash 1 e.F\{D\} :: T$

| *WTFAss1*:  
 $\llbracket P, E \vdash 1 e1 :: U; \text{class-type-of}' U = [C]; P \vdash C \text{ sees } F:T (fm) \text{ in } D; P, E \vdash 1 e2 :: T'; P \vdash T' \leq T \rrbracket$   
 $\implies P, E \vdash 1 e1.F\{D\} := e2 :: \text{Void}$

| *WTCAS1*:  
 $\llbracket P, E \vdash 1 e1 :: U; \text{class-type-of}' U = [C]; P \vdash C \text{ sees } F:T (fm) \text{ in } D; \text{volatile } fm;$   
 $P, E \vdash 1 e2 :: T'; P \vdash T' \leq T; P, E \vdash 1 e3 :: T''; P \vdash T'' \leq T \rrbracket$   
 $\implies P, E \vdash 1 e1.\text{compareAndSwap}(D.F, e2, e3) :: \text{Boolean}$

| *WT1Call*:  
 $\llbracket P, E \vdash 1 e :: U; \text{class-type-of}' U = [C]; P \vdash C \text{ sees } M:Ts \rightarrow T = m \text{ in } D;$   
 $P, E \vdash 1 es [::] Ts'; P \vdash Ts' \leq Ts \rrbracket$   
 $\implies P, E \vdash 1 e.M(es) :: T$

| *WT1Block*:  
 $\llbracket \text{is-type } P T; P, E@[T] \vdash 1 e :: T'; \text{case } vo \text{ of } None \Rightarrow \text{True} \mid [v] \Rightarrow \exists T'. \text{typeof } v = [T'] \wedge P \vdash T' \leq T \rrbracket$   
 $\implies P, E \vdash 1 \{ V:T=vo; e \} :: T'$

| *WT1Synchronized*:  
 $\llbracket P, E \vdash 1 o' :: T; \text{is-refT } T; T \neq NT; P, E@[Class \text{ Object}] \vdash 1 e :: T' \rrbracket$   
 $\implies P, E \vdash 1 \text{sync}_V(o') e :: T'$

| *WT1Seq*:

$$\llbracket P, E \vdash 1 e_1 :: T_1; P, E \vdash 1 e_2 :: T_2 \rrbracket \\ \implies P, E \vdash 1 e_1;;e_2 :: T_2$$

| *WT1Cond*:  
 $\llbracket P, E \vdash 1 e :: \text{Boolean}; P, E \vdash 1 e_1 :: T_1; P, E \vdash 1 e_2 :: T_2; P \vdash \text{lub}(T_1, T_2) = T \rrbracket \\ \implies P, E \vdash 1 \text{if } (e) e_1 \text{ else } e_2 :: T$

| *WT1While*:  
 $\llbracket P, E \vdash 1 e :: \text{Boolean}; P, E \vdash 1 c :: T \rrbracket \\ \implies P, E \vdash 1 \text{while } (e) c :: \text{Void}$

| *WT1Throw*:  
 $\llbracket P, E \vdash 1 e :: \text{Class } C; P \vdash C \preceq^* \text{Throwable} \rrbracket \implies \\ P, E \vdash 1 \text{throw } e :: \text{Void}$

| *WT1Try*:  
 $\llbracket P, E \vdash 1 e_1 :: T; P, E@[ \text{Class } C ] \vdash 1 e_2 :: T; \text{is-class } P C \rrbracket \\ \implies P, E \vdash 1 \text{try } e_1 \text{ catch}(C V) e_2 :: T$

| *WT1Nil*:  $P, E \vdash 1 [] [::] []$

| *WT1Cons*:  $\llbracket P, E \vdash 1 e :: T; P, E \vdash 1 es [::] Ts \rrbracket \implies P, E \vdash 1 e\#es [::] T\#Ts$

**declare** *WT1-WTs1.intros*[*intro!*]

**declare** *WT1Nil*[*iff*]

**inductive-cases** *WT1-WTs1-cases*[*elim!*]:

$P, E \vdash 1 \text{Val } v :: T$   
 $P, E \vdash 1 \text{Var } i :: T$   
 $P, E \vdash 1 \text{Cast } D e :: T$   
 $P, E \vdash 1 e \text{ instanceof } U :: T$   
 $P, E \vdash 1 i := e :: T$   
 $P, E \vdash 1 \{i:U=vo; e\} :: T$   
 $P, E \vdash 1 e1;;e2 :: T$   
 $P, E \vdash 1 \text{if } (e) e1 \text{ else } e2 :: T$   
 $P, E \vdash 1 \text{while } (e) c :: T$   
 $P, E \vdash 1 \text{throw } e :: T$   
 $P, E \vdash 1 \text{try } e1 \text{ catch}(C i) e2 :: T$   
 $P, E \vdash 1 e \cdot F\{D\} :: T$   
 $P, E \vdash 1 e1 \cdot F\{D\} := e2 :: T$   
 $P, E \vdash 1 e \cdot \text{compareAndSwap}(D \cdot F, e', e'') :: T$   
 $P, E \vdash 1 e1 \ll \text{bop} \gg e2 :: T$   
 $P, E \vdash 1 \text{new } C :: T$   
 $P, E \vdash 1 \text{newA } T'[e] :: T$   
 $P, E \vdash 1 a[i] := e :: T$   
 $P, E \vdash 1 a[i] :: T$   
 $P, E \vdash 1 a \cdot \text{length} :: T$   
 $P, E \vdash 1 e \cdot M(es) :: T$   
 $P, E \vdash 1 \text{sync}_V(o') e :: T$   
 $P, E \vdash 1 \text{insync}_V(a) e :: T$   
 $P, E \vdash 1 [] [::] Ts$   
 $P, E \vdash 1 e\#es [::] Ts$

**lemma** *WTs1-same-size*:  $P, E \vdash 1 es [::] Ts \implies \text{size } es = \text{size } Ts$

**by** (*induct es arbitrary: Ts*) *auto*

**lemma** *WTs1-snoc-cases*:

**assumes** *wt*:  $P, E \vdash 1 \text{ es } @ [e] [::] Ts$

**obtains**  $T \ Ts' \text{ where } P, E \vdash 1 \text{ es } [::] Ts' \ P, E \vdash 1 \ e :: T$

**proof** –

**from** *wt* **have**  $\exists T \ Ts'. \ P, E \vdash 1 \text{ es } [::] Ts' \wedge P, E \vdash 1 \ e :: T$

**by**(*induct es arbitrary: Ts*) *auto*

**thus** *thesis* **by**(*auto intro: that*)

**qed**

**lemma** *WTs1-append*:

**assumes** *wt*:  $P, Env \vdash 1 \text{ es } @ \text{ es}' [::] Ts$

**obtains**  $Ts' \ Ts'' \text{ where } P, Env \vdash 1 \text{ es } [::] Ts' \ P, Env \vdash 1 \text{ es}' [::] Ts''$

**proof** –

**from** *wt* **have**  $\exists Ts' \ Ts''. \ P, Env \vdash 1 \text{ es } [::] Ts' \wedge P, Env \vdash 1 \text{ es}' [::] Ts''$

**by**(*induct es arbitrary: Ts*) *auto*

**thus** *?thesis* **by**(*auto intro: that*)

**qed**

**lemma** *WT1-not-contains-insync*:  $P, E \vdash 1 \ e :: T \implies \neg \text{contains-insync } e$

**and** *WTs1-not-contains-insyncs*:  $P, E \vdash 1 \text{ es } [::] Ts \implies \neg \text{contains-insyncs } es$

**by**(*induct rule: WT1-WTs1.inducts*) *auto*

**lemma** *WT1-expr-locks*:  $P, E \vdash 1 \ e :: T \implies \text{expr-locks } e = (\lambda a. \ 0)$

**and** *WTs1-expr-lockss*:  $P, E \vdash 1 \text{ es } [::] Ts \implies \text{expr-lockss } es = (\lambda a. \ 0)$

**by**(*induct rule: WT1-WTs1.inducts*)(*auto*)

**lemma** **assumes** *wf*: *wf-prog wfmd P*

**shows** *WT1-unique*:  $P, E \vdash 1 \ e :: T1 \implies P, E \vdash 1 \ e :: T2 \implies T1 = T2$

**and** *WTs1-unique*:  $P, E \vdash 1 \text{ es } [::] Ts1 \implies P, E \vdash 1 \text{ es } [::] Ts2 \implies Ts1 = Ts2$

**apply**(*induct arbitrary: T2 and Ts2 rule: WT1-WTs1.inducts*)

**apply** *blast*

**apply** *blast*

**apply** *blast*

**apply** *blast*

**apply** *clarsimp*

**apply** *blast*

**apply**(*blast dest: WT-binop-fun*)

**apply** *blast*

**apply** *blast*

**apply** *blast*

**apply** *blast*

**apply** (*blast dest: sees-field-idemp sees-field-fun*)

**apply** (*blast dest: sees-field-fun*)

**apply** *blast*

**apply**(*erule WT1-WTs1-cases*)

**apply**(*simp*)

**apply** (*blast dest: sees-method-idemp sees-method-fun*)

**apply** *blast*

**apply** *blast*

**apply** *blast*

```

apply(blast dest: is-lub-unique[OF wf])
apply blast
apply blast
apply blast
apply blast
apply blast
done

```

```

lemma assumes wf: wf-prog p P
  shows WT1-is-type:  $P, E \vdash 1 e :: T \implies \text{set } E \subseteq \text{types } P \implies \text{is-type } P T$ 
  and WTs1-is-type:  $P, E \vdash 1 es [::] Ts \implies \text{set } E \subseteq \text{types } P \implies \text{set } Ts \subseteq \text{types } P$ 
apply(induct rule: WT1-WTs1.inducts)
apply simp
apply simp
apply simp
apply simp
apply (simp add: typeof-lit-is-type)
apply (fastforce intro: nth-mem)
apply(simp add: WT-binop-is-type)
apply(simp)
apply(simp del: is-type-array add: is-type-ArrayD)
apply(simp)
apply(simp)
apply (simp add: sees-field-is-type[OF - wf])
apply simp
apply simp
apply(fastforce dest!: sees-wf-mdecl[OF wf] simp: wf-mdecl-def)
apply(simp)
apply(simp add: is-class-Object[OF wf])
apply simp
apply(blast dest: is-lub-is-type[OF wf])
apply simp
apply simp
apply simp
apply simp
apply(simp)
done

```

**lemma** blocks1-WT:

```

   $\llbracket P, Env @ Ts \vdash 1 \text{ body} :: T; \text{set } Ts \subseteq \text{types } P \rrbracket \implies P, Env \vdash 1 \text{ blocks1 } (\text{length } Env) Ts \text{ body} :: T$ 
proof(induct n $\equiv$ length Env Ts body arbitrary: Env rule: blocks1.induct)
  case 1 thus ?case by simp
next
  case (2 T' Ts e)
  note IH =  $\langle \bigwedge Env'. \llbracket \text{Suc } (\text{length } Env) = \text{length } Env'; P, Env' @ Ts \vdash 1 e :: T; \text{set } Ts \subseteq \text{types } P \rrbracket \implies P, Env' \vdash 1 \text{ blocks1 } (\text{length } Env') Ts e :: T \rangle$ 
  from  $\langle \text{set } (T' \# Ts) \subseteq \text{types } P \rangle$  have  $\text{set } Ts \subseteq \text{types } P$  is-type P T' by(auto)
  moreover from  $\langle P, Env @ T' \# Ts \vdash 1 e :: T \rangle$  have  $P, (Env @ [T']) @ Ts \vdash 1 e :: T$  by simp
  note IH[OF - this]
  ultimately show ?case by auto
qed

```

```

lemma WT1-fv:  $\llbracket P, E \vdash 1 e :: T; \mathcal{B} e (\text{length } E); \text{syncvars } e \rrbracket \implies \text{fv } e \subseteq \{0..<\text{length } E\}$ 
and WTs1-fvs:  $\llbracket P, E \vdash 1 es [::] Ts; \mathcal{B} s es (\text{length } E); \text{syncvarss } es \rrbracket \implies \text{fvs } es \subseteq \{0..<\text{length } E\}$ 

```

```

proof(induct rule: WT1-WTs1.inducts)
  case (WT1Synchronized E e1 T e2 T' V)
    note IH1 =  $\langle \llbracket \mathcal{B} \ e1 \ (length \ E); \ syncvars \ e1 \rrbracket \implies fv \ e1 \subseteq \{0..<length \ E\} \rangle$ 
    note IH2 =  $\langle \llbracket \mathcal{B} \ e2 \ (length \ (E \ @ \ [Class \ Object])); \ syncvars \ e2 \rrbracket \implies fv \ e2 \subseteq \{0..<length \ (E \ @ \ [Class \ Object])\} \rangle$ 
    from  $\langle \mathcal{B} \ (sync_V \ (e1) \ e2) \ (length \ E) \rangle$  have [simp]:  $V = length \ E$ 
    and B1:  $\mathcal{B} \ e1 \ (length \ E)$  and B2:  $\mathcal{B} \ e2 \ (Suc \ (length \ E))$  by auto
    from  $\langle syncvars \ (sync_V \ (e1) \ e2) \rangle$  have sync1: syncvars e1 and sync2: syncvars e2 and V:  $V \notin fv \ e2$  by auto
    have  $fv \ e2 \subseteq \{0..<length \ E\}$ 
    proof
      fix x
      assume x:  $x \in fv \ e2$ 
      with V have  $x \neq length \ E$  by auto
      moreover from IH2 B2 sync2 have  $fv \ e2 \subseteq \{0..<Suc \ (length \ E)\}$  by auto
      with x have  $x < Suc \ (length \ E)$  by auto
      ultimately show  $x \in \{0..<length \ E\}$  by auto
    qed
    with IH1[OF B1 sync1] show ?case by(auto)
  next
    case (WT1Cond E e e1 T1 e2 T2 T)
    thus ?case by(auto del: subsetI)
qed fastforce+

end

```

## 7.12 Well-Formedness of Intermediate Language

**theory** *J1WellForm* **imports**

*../J/DefAss*

*J1WellType*

**begin**

### 7.12.1 Well-formedness

**definition** *wf-J1-mdecl* ::  $'addr \ J1-prog \Rightarrow cname \Rightarrow 'addr \ expr1 \ mdecl \Rightarrow bool$

**where**

$wf-J1-mdecl \ P \ C \equiv \lambda(M, Ts, T, body). \ (\exists T'. \ P, Class \ C \# Ts \vdash_1 body :: T' \wedge P \vdash T' \leq T) \wedge$   
 $\mathcal{D} \ body \ [\{..size \ Ts\}] \wedge \mathcal{B} \ body \ (size \ Ts + 1) \wedge syncvars \ body$

**lemma** *wf-J1-mdecl*[*simp*]:

$wf-J1-mdecl \ P \ C \ (M, Ts, T, body) \equiv$   
 $((\exists T'. \ P, Class \ C \# Ts \vdash_1 body :: T' \wedge P \vdash T' \leq T) \wedge$   
 $\mathcal{D} \ body \ [\{..size \ Ts\}] \wedge \mathcal{B} \ body \ (size \ Ts + 1)) \wedge syncvars \ body$

**by** (*simp add: wf-J1-mdecl-def*)

**abbreviation** *wf-J1-prog* ::  $'addr \ J1-prog \Rightarrow bool$

**where** *wf-J1-prog* == *wf-prog wf-J1-mdecl*

**end**

## 7.13 Preservation of Well-Typedness in Stage 2

```

theory TypeComp
imports
  Exception-Tables
  J1WellForm
  ../BV/BVSpec
  HOL-Library.Prefix-Order
  HOL-Library.Sublist
begin

locale TC0 =
  fixes P :: 'addr J1-prog and mxl :: nat
begin

definition ty :: ty list  $\Rightarrow$  'addr expr1  $\Rightarrow$  ty
where ty E e  $\equiv$  THE T. P, E  $\vdash$  1 e :: T

definition tyl :: ty list  $\Rightarrow$  nat set  $\Rightarrow$  tyl
where tyl E A'  $\equiv$  map ( $\lambda i$ . if  $i \in A' \wedge i < \text{size } E$  then OK(E!i) else Err) [0.. $\text{mxl}$ ]

definition tyi' :: ty list  $\Rightarrow$  ty list  $\Rightarrow$  nat set option  $\Rightarrow$  tyi'
where tyi' ST E A  $\equiv$  case A of None  $\Rightarrow$  None | [A']  $\Rightarrow$  Some(ST, tyl E A')

definition after :: ty list  $\Rightarrow$  nat set option  $\Rightarrow$  ty list  $\Rightarrow$  'addr expr1  $\Rightarrow$  tyi'
  where after E A ST e  $\equiv$  tyi' (ty E e # ST) E (A  $\sqcup$  A e)

end

locale TC1 = TC0 +
  fixes wfmd
  assumes wf-prog: wf-prog wfmd P
begin

lemma ty-def2 [simp]: P, E  $\vdash$  1 e :: T  $\Longrightarrow$  ty E e = T
apply(unfold ty-def ty-def)
apply(blast intro: the-equality WT1-unique[OF wf-prog])
done

end

context TC0 begin

lemma tyi'-None [simp]: tyi' ST E None = None
by(simp add:tyi'-def)

lemma tyl-app-diff[simp]:
  tyl (E@[T]) (A - {size E}) = tyl E A
by(auto simp add:tyl-def hyperset-defs)

lemma tyi'-app-diff[simp]:
  tyi' ST (E @ [T]) (A  $\ominus$  size E) = tyi' ST E A
by(auto simp add:tyi'-def hyperset-defs)

```



**lemma** *ty<sub>l</sub>-antimono*:

$A \subseteq A' \implies P \vdash ty_l E A' [\leq_\top] ty_l E A$   
**by**(*auto simp:ty<sub>l</sub>-def list-all2-conv-all-nth*)

**lemma** *ty<sub>i</sub>'-antimono*:

$A \subseteq A' \implies P \vdash ty_i' ST E [A'] \leq' ty_i' ST E [A]$   
**by**(*auto simp:ty<sub>i</sub>'-def ty<sub>l</sub>-def list-all2-conv-all-nth*)

**lemma** *ty<sub>l</sub>-env-antimono*:

$P \vdash ty_l (E @ [T]) A [\leq_\top] ty_l E A$   
**by**(*auto simp:ty<sub>l</sub>-def list-all2-conv-all-nth*)

**lemma** *ty<sub>i</sub>'-env-antimono*:

$P \vdash ty_i' ST (E @ [T]) A \leq' ty_i' ST E A$   
**by**(*auto simp:ty<sub>i</sub>'-def ty<sub>l</sub>-def list-all2-conv-all-nth*)

**lemma** *ty<sub>i</sub>'-incr*:

$P \vdash ty_i' ST (E @ [T]) [insert (size E) A] \leq' ty_i' ST E [A]$   
**by**(*auto simp:ty<sub>i</sub>'-def ty<sub>l</sub>-def list-all2-conv-all-nth*)

**lemma** *ty<sub>l</sub>-incr*:

$P \vdash ty_l (E @ [T]) (insert (size E) A) [\leq_\top] ty_l E A$   
**by**(*auto simp: hyperset-defs ty<sub>l</sub>-def list-all2-conv-all-nth*)

**lemma** *ty<sub>l</sub>-in-types*:

$set E \subseteq types P \implies ty_l E A \in list mxl (err (types P))$   
**by**(*auto simp add:ty<sub>l</sub>-def intro!:listI dest!: nth-mem*)

**function** *compT* :: *ty list*  $\Rightarrow$  *nat hyperset*  $\Rightarrow$  *ty list*  $\Rightarrow$  '*addr expr1*  $\Rightarrow$  *ty<sub>i</sub>' list*

**and** *compTs* :: *ty list*  $\Rightarrow$  *nat hyperset*  $\Rightarrow$  *ty list*  $\Rightarrow$  '*addr expr1 list*  $\Rightarrow$  *ty<sub>i</sub>' list*

**where**

*compT* *E A ST* (*new C*) = []  
| *compT* *E A ST* (*newA T* [e]) = *compT* *E A ST* e @ [after *E A ST* e]  
| *compT* *E A ST* (*Cast C* e) = *compT* *E A ST* e @ [after *E A ST* e]  
| *compT* *E A ST* (*e instanceof T*) = *compT* *E A ST* e @ [after *E A ST* e]  
| *compT* *E A ST* (*Val v*) = []  
| *compT* *E A ST* (*e1* «bop» *e2*) =  
  (*let* *ST1* = *ty E e1* # *ST*; *A1* = *A*  $\sqcup$   $\mathcal{A}$  *e1* *in*  
   *compT* *E A ST* *e1* @ [after *E A ST* *e1*] @  
   *compT* *E A1 ST1* *e2* @ [after *E A1 ST1* *e2*])  
| *compT* *E A ST* (*Var i*) = []  
| *compT* *E A ST* (*i* := *e*) = *compT* *E A ST* e @ [after *E A ST* e, *ty<sub>i</sub>' ST E* (*A*  $\sqcup$   $\mathcal{A}$  e  $\sqcup$  [{i})]  
| *compT* *E A ST* (*a*[*i*]) =  
  (*let* *ST1* = *ty E a* # *ST*; *A1* = *A*  $\sqcup$   $\mathcal{A}$  *a*  
   *in compT* *E A ST* *a* @ [after *E A ST* *a*] @ *compT* *E A1 ST1* *i* @ [after *E A1 ST1* *i*])  
| *compT* *E A ST* (*a*[*i*] := *e*) =  
  (*let* *ST1* = *ty E a* # *ST*; *A1* = *A*  $\sqcup$   $\mathcal{A}$  *a*;  
   *ST2* = *ty E i* # *ST1*; *A2* = *A1*  $\sqcup$   $\mathcal{A}$  *i*; *A3* = *A2*  $\sqcup$   $\mathcal{A}$  *e*

$$\begin{aligned}
& \text{in } \text{compT } E \ A \ ST \ a \ @ \ [\text{after } E \ A \ ST \ a] \ @ \ \text{compT } E \ A1 \ ST1 \ i \ @ \ [\text{after } E \ A1 \ ST1 \ i] \ @ \ \text{compT } E \\
& A2 \ ST2 \ e \ @ \ [\text{after } E \ A2 \ ST2 \ e, \ ty_i' \ ST \ E \ A3]) \\
& | \text{compT } E \ A \ ST \ (a \cdot \text{length}) = \text{compT } E \ A \ ST \ a \ @ \ [\text{after } E \ A \ ST \ a] \\
& | \text{compT } E \ A \ ST \ (e \cdot F\{D\}) = \text{compT } E \ A \ ST \ e \ @ \ [\text{after } E \ A \ ST \ e] \\
& | \text{compT } E \ A \ ST \ (e1 \cdot F\{D\} := e2) = \\
& \quad (\text{let } ST1 = ty \ E \ e1 \ # ST; A1 = A \sqcup \mathcal{A} \ e1; A2 = A1 \sqcup \mathcal{A} \ e2 \\
& \quad \text{in } \text{compT } E \ A \ ST \ e1 \ @ \ [\text{after } E \ A \ ST \ e1] \ @ \ \text{compT } E \ A1 \ ST1 \ e2 \ @ \ [\text{after } E \ A1 \ ST1 \ e2] \ @ \ [ty_i' \\
& ST \ E \ A2]) \\
& | \text{compT } E \ A \ ST \ (e1 \cdot \text{compareAndSwap}(D \cdot F, \ e2, \ e3)) = \\
& \quad (\text{let } ST1 = ty \ E \ e1 \ # ST; A1 = A \sqcup \mathcal{A} \ e1; ST2 = ty \ E \ e2 \ # ST1; A2 = A1 \sqcup \mathcal{A} \ e2; A3 = A2 \\
& \sqcup \mathcal{A} \ e3 \\
& \text{in } \text{compT } E \ A \ ST \ e1 \ @ \ [\text{after } E \ A \ ST \ e1] \ @ \ \text{compT } E \ A1 \ ST1 \ e2 \ @ \ [\text{after } E \ A1 \ ST1 \ e2] \ @ \ \text{compT } \\
& E \ A2 \ ST2 \ e3 \ @ \ [\text{after } E \ A2 \ ST2 \ e3]) \\
& | \text{compT } E \ A \ ST \ (e \cdot M(es)) = \\
& \quad \text{compT } E \ A \ ST \ e \ @ \ [\text{after } E \ A \ ST \ e] \ @ \\
& \quad \text{compTs } E \ (A \sqcup \mathcal{A} \ e) \ (ty \ E \ e \ # ST) \ es \\
& | \text{compT } E \ A \ ST \ \{i:T=None; e\} = \text{compT } (E@[T]) \ (A \ominus i) \ ST \ e \\
& | \text{compT } E \ A \ ST \ \{i:T=[v]; e\} = \\
& \quad [\text{after } E \ A \ ST \ (Val \ v), \ ty_i' \ ST \ (E@[T]) \ (A \sqcup [\{i\}])] \ @ \ \text{compT } (E@[T]) \ (A \sqcup [\{i\}]) \ ST \ e \\
& | \text{compT } E \ A \ ST \ (\text{sync}_i \ (e1) \ e2) = \\
& \quad (\text{let } A1 = A \sqcup \mathcal{A} \ e1 \sqcup [\{i\}]; E1 = E \ @ \ [Class \ Object]; ST2 = ty \ E1 \ e2 \ # ST; A2 = A1 \sqcup \mathcal{A} \ e2 \\
& \text{in } \text{compT } E \ A \ ST \ e1 \ @ \\
& \quad [\text{after } E \ A \ ST \ e1, \\
& \quad \quad ty_i' \ (Class \ Object \ # \ Class \ Object \ # ST) \ E \ (A \sqcup \mathcal{A} \ e1), \\
& \quad \quad ty_i' \ (Class \ Object \ # ST) \ E1 \ A1, \\
& \quad \quad ty_i' \ ST \ E1 \ A1] \ @ \\
& \quad \text{compT } E1 \ A1 \ ST \ e2 \ @ \\
& \quad [ty_i' \ ST2 \ E1 \ A2, \ ty_i' \ (Class \ Object \ # ST2) \ E1 \ A2, \ ty_i' \ ST2 \ E1 \ A2, \\
& \quad \quad ty_i' \ (Class \ Throwable \ # ST) \ E1 \ A1, \\
& \quad \quad ty_i' \ (Class \ Object \ # \ Class \ Throwable \ # ST) \ E1 \ A1, \\
& \quad \quad ty_i' \ (Class \ Throwable \ # ST) \ E1 \ A1]) \\
& | \text{compT } E \ A \ ST \ (\text{insync}_i \ (a) \ e) = [] \\
& | \text{compT } E \ A \ ST \ (e1;;e2) = \\
& \quad (\text{let } A1 = A \sqcup \mathcal{A} \ e1 \text{ in} \\
& \quad \text{compT } E \ A \ ST \ e1 \ @ \ [\text{after } E \ A \ ST \ e1, \ ty_i' \ ST \ E \ A1] \ @ \\
& \quad \text{compT } E \ A1 \ ST \ e2) \\
& | \text{compT } E \ A \ ST \ (\text{if } (e) \ e1 \ \text{else } e2) = \\
& \quad (\text{let } A0 = A \sqcup \mathcal{A} \ e; \tau = ty_i' \ ST \ E \ A0 \text{ in} \\
& \quad \text{compT } E \ A \ ST \ e \ @ \ [\text{after } E \ A \ ST \ e, \tau] \ @ \\
& \quad \text{compT } E \ A0 \ ST \ e1 \ @ \ [\text{after } E \ A0 \ ST \ e1, \tau] \ @ \\
& \quad \text{compT } E \ A0 \ ST \ e2) \\
& | \text{compT } E \ A \ ST \ (\text{while } (e) \ c) = \\
& \quad (\text{let } A0 = A \sqcup \mathcal{A} \ e; A1 = A0 \sqcup \mathcal{A} \ c; \tau = ty_i' \ ST \ E \ A0 \text{ in} \\
& \quad \text{compT } E \ A \ ST \ e \ @ \ [\text{after } E \ A \ ST \ e, \tau] \ @ \\
& \quad \text{compT } E \ A0 \ ST \ c \ @ \ [\text{after } E \ A0 \ ST \ c, \ ty_i' \ ST \ E \ A1, \ ty_i' \ ST \ E \ A0]) \\
& | \text{compT } E \ A \ ST \ (\text{throw } e) = \text{compT } E \ A \ ST \ e \ @ \ [\text{after } E \ A \ ST \ e] \\
& | \text{compT } E \ A \ ST \ (\text{try } e1 \ \text{catch}(C \ i) \ e2) = \\
& \quad \text{compT } E \ A \ ST \ e1 \ @ \ [\text{after } E \ A \ ST \ e1] \ @ \\
& \quad [ty_i' \ (Class \ C \ # ST) \ E \ A, \ ty_i' \ ST \ (E@[Class \ C]) \ (A \sqcup [\{i\}])] \ @ \\
& \quad \text{compT } (E@[Class \ C]) \ (A \sqcup [\{i\}]) \ ST \ e2 \\
& | \text{compTs } E \ A \ ST \ [] = []
\end{aligned}$$

```

| compTs E A ST (e#es) = compT E A ST e @ [after E A ST e] @
    compTs E (A  $\sqcup$  (A e)) (ty E e # ST) es
by pat-completeness simp-all
termination
apply(relation case-sum ( $\lambda p$ . size (snd (snd (snd p)))) ( $\lambda p$ . size-list size (snd (snd (snd p)))) <*mlex*>
  {})
apply(rule wf-mlex[OF wf-empty])
apply(rule mlex-less, simp)+
done

lemmas compT-compTs-induct =
  compT-compTs.induct[
    unfolded meta-all5-eq-conv meta-all4-eq-conv meta-all3-eq-conv meta-all2-eq-conv meta-all-eq-conv,
    case-names
    new NewArray Cast InstanceOf Val BinOp Var LAss AAcc AAss ALen FAcc FAss CompareAndSwap
    Call BlockNone BlockSome
    Synchronized InSynchronized Seq Cond While throw TryCatch
    Nil Cons]

definition compTa :: ty list  $\Rightarrow$  nat hyperset  $\Rightarrow$  ty list  $\Rightarrow$  'addr expr1  $\Rightarrow$  tyi' list
where compTa E A ST e  $\equiv$  compT E A ST e @ [after E A ST e]

lemmas compE2-not-Nil = compE2-neq-Nil
declare compE2-not-Nil[simp]

lemma compT-sizes[simp]:
  shows size(compT E A ST e) = size(compE2 e) - 1
  and size(compTs E A ST es) = size(compEs2 es)
apply(induct E A ST e and E A ST es rule: compT-compTs-induct)
apply(auto split:nat-diff-split)
done

lemma compT-None-not-Some [simp]:  $\lfloor \tau \rfloor \notin \text{set} (\text{compT } E \text{ None } ST \text{ e})$ 
  and compTs-None-not-Some [simp]:  $\lfloor \tau \rfloor \notin \text{set} (\text{compTs } E \text{ None } ST \text{ es})$ 
by(induct E A  $\equiv$  None :: nat hyperset ST e and E A  $\equiv$  None :: nat hyperset ST es rule: compT-compTs-induct)
  (simp-all add:after-def)

lemma pair-eq-tyi'-conv:
  ( $\lfloor (ST, LT) \rfloor = \text{ty}_i' \text{ ST}_0 \text{ E A} = (\text{case } A \text{ of None} \Rightarrow \text{False} \mid \text{Some } A \Rightarrow (ST = ST_0 \wedge LT = \text{ty}_l \text{ E A}))$ )
by(simp add:tyi'-def)

lemma pair-conv-tyi':  $\lfloor (ST, \text{ty}_l \text{ E A}) \rfloor = \text{ty}_i' \text{ ST E } \lfloor A \rfloor$ 
by(simp add:tyi'-def)

lemma tyi'-antimono2:
   $\llbracket E \leq E'; A \subseteq A' \rrbracket \Longrightarrow P \vdash \text{ty}_i' \text{ ST E}' \lfloor A' \rfloor \leq' \text{ty}_i' \text{ ST E } \lfloor A \rfloor$ 
by(auto simp:tyi'-def tyl-def list-all2-conv-all-nth less-eq-list-def prefix-def)

declare tyi'-antimono [intro!] after-def[simp] pair-conv-tyi'[simp] pair-eq-tyi'-conv[simp]

lemma compT-LT-prefix:
   $\llbracket \lfloor (ST, LT) \rfloor \in \text{set} (\text{compT } E \text{ A ST}_0 \text{ e}); B \text{ e } (size \text{ E}) \rrbracket \Longrightarrow P \vdash \lfloor (ST, LT) \rfloor \leq' \text{ty}_i' \text{ ST E A}$ 
  and compTs-LT-prefix:

```

```

[[ [(ST,LT)] ∈ set(compTs E A ST0 es); Bs es (size E) ]] ⇒ P ⊢ [(ST,LT)] ≤' tyi' ST E A
proof(induct E A ST0 e and E A ST0 es rule: compT-compTs-induct)
  case FAss thus ?case by(fastforce simp:hyperset-defs elim!:sup-state-opt-trans)
next
  case BinOp thus ?case
    by(fastforce simp:hyperset-defs elim!:sup-state-opt-trans split:bop.splits)
next
  case Seq thus ?case by(fastforce simp:hyperset-defs elim!:sup-state-opt-trans)
next
  case While thus ?case by(fastforce simp:hyperset-defs elim!:sup-state-opt-trans)
next
  case Cond thus ?case by(fastforce simp:hyperset-defs elim!:sup-state-opt-trans)
next
  case BlockNone thus ?case by(auto)
next
  case BlockSome thus ?case
    by(clarsimp simp only: tyi'-def)(fastforce intro: tyi'-incr simp add: hyperset-defs elim: sup-state-opt-trans)
next
  case Call thus ?case by(fastforce simp:hyperset-defs elim!:sup-state-opt-trans)
next
  case Cons thus ?case
    by(fastforce simp:hyperset-defs elim!:sup-state-opt-trans)
next
  case TryCatch thus ?case
    by(fastforce simp:hyperset-defs intro!: tyi'-incr elim!:sup-state-opt-trans)
next
  case NewArray thus ?case by(auto simp add: hyperset-defs)
next
  case AAcc thus ?case by(fastforce simp:hyperset-defs elim!:sup-state-opt-trans)
next
  case AAss thus ?case by(auto simp:hyperset-defs Un-ac elim!:sup-state-opt-trans)
next
  case ALen thus ?case by(auto simp add: hyperset-defs)
next
  case CompareAndSwap thus ?case by(auto simp: hyperset-defs Un-ac elim!:sup-state-opt-trans)
next
  case Synchronized thus ?case
    by(fastforce simp add: hyperset-defs elim: sup-state-opt-trans intro: sup-state-opt-trans[OF tyi'-incr]
    tyi'-antimono2)
qed (auto simp:hyperset-defs)

declare tyi'-antimono [rule del] after-def[simp del] pair-conv-tyi'[simp del] pair-eq-tyi'-conv[simp del]

lemma OK-None-states [iff]: OK None ∈ states P mxs mxl
by(simp add: JVM-states-unfold)

end

context TC1 begin

lemma after-in-states:
[[ P, E ⊢ 1 e :: T; set E ⊆ types P; set ST ⊆ types P; size ST + max-stack e ≤ mxs ]]
⇒ OK (after E A ST e) ∈ states P mxs mxl
apply(subgoal-tac size ST + 1 ≤ mxs)

```

```

apply(simp add:after-def tyi'-def JVM-states-unfold tyi-in-types)
apply(clarify intro!: exI)
apply(rule conjI)
  apply(rule exI[where x=length ST + 1], fastforce)
apply(clarsimp)
apply(rule conjI[OF WT1-is-type[OF wf-prog]], auto intro: listI)
using max-stack1[of e] by simp

```

**end**

**context** TC0 **begin**

```

lemma OK-tyi'-in-statesI [simp]:
   $\llbracket \text{set } E \subseteq \text{types } P; \text{set } ST \subseteq \text{types } P; \text{size } ST \leq \text{mxs} \rrbracket$ 
   $\implies OK (ty_i' ST E A) \in \text{states } P \text{ mxs mxl}$ 
apply(simp add:tyi'-def JVM-states-unfold tyi-in-types)
apply(blast intro!:listI)
done

```

**end**

```

lemma is-class-type-aux: is-class P C  $\implies$  is-type P (Class C)
by(simp)

```

**context** TC1 **begin**

```

declare is-type.simps[simp del] subsetI[rule del]

```

**theorem**

```

shows compT-states:
   $\llbracket P, E \vdash 1 e :: T; \text{set } E \subseteq \text{types } P; \text{set } ST \subseteq \text{types } P;$ 
     $\text{size } ST + \text{max-stack } e \leq \text{mxs}; \text{size } E + \text{max-vars } e \leq \text{mxl} \rrbracket$ 
   $\implies OK \text{ ' set } (compT E A ST e) \subseteq \text{states } P \text{ mxs mxl}$ 
  (is PROP ?P e E T A ST)

```

```

and compTs-states:
   $\llbracket P, E \vdash 1 es[::]Ts; \text{set } E \subseteq \text{types } P; \text{set } ST \subseteq \text{types } P;$ 
     $\text{size } ST + \text{max-stacks } es \leq \text{mxs}; \text{size } E + \text{max-varss } es \leq \text{mxl} \rrbracket$ 
   $\implies OK \text{ ' set } (compTs E A ST es) \subseteq \text{states } P \text{ mxs mxl}$ 
  (is PROP ?Ps es E Ts A ST)

```

```

proof(induct E A ST e and E A ST es arbitrary: T and Ts rule: compT-compTs-induct)
  case new thus ?case by(simp)
next
  case (Cast C e) thus ?case by (auto simp:after-in-states)
next
  case InstanceOf thus ?case by (auto simp:after-in-states)
next
  case Val thus ?case by(simp)
next
  case Var thus ?case by(simp)
next
  case LAss thus ?case by(auto simp:after-in-states)
next
  case FAcc thus ?case by(auto simp:after-in-states)

```

```

next
  case FAss thus ?case
    by(auto simp:image-Un WT1-is-type[OF wf-prog] after-in-states)
next
  case CompareAndSwap thus ?case by(auto simp:image-Un WT1-is-type[OF wf-prog] after-in-states)
next
  case Seq thus ?case
    by(auto simp:image-Un after-in-states)
next
  case BinOp thus ?case
    by(auto simp:image-Un WT1-is-type[OF wf-prog] after-in-states)
next
  case Cond thus ?case
    by(force simp:image-Un WT1-is-type[OF wf-prog] after-in-states)
next
  case While thus ?case
    by(auto simp:image-Un WT1-is-type[OF wf-prog] after-in-states)
next
  case BlockNone thus ?case by auto
next
  case (BlockSome E A ST i ty v exp)
    with max-stack1[of exp] show ?case by(auto intro: after-in-states)
next
  case (TryCatch E A ST e1 C i e2)
    moreover have size ST + 1 ≤ mxs using TryCatch.prems max-stack1[of e1] by auto
    ultimately show ?case
      by(auto simp:image-Un WT1-is-type[OF wf-prog] after-in-states
        is-class-type-aux)
next
  case Nil thus ?case by simp
next
  case Cons thus ?case
    by(auto simp:image-Un WT1-is-type[OF wf-prog] after-in-states)
next
  case throw thus ?case
    by(auto simp: WT1-is-type[OF wf-prog] after-in-states)
next
  case Call thus ?case
    by(auto simp:image-Un WT1-is-type[OF wf-prog] after-in-states)
next
  case NewArray thus ?case
    by(auto simp:image-Un WT1-is-type[OF wf-prog] after-in-states)
next
  case AAcc thus ?case by(auto simp:image-Un WT1-is-type[OF wf-prog] after-in-states)
next
  case AAss thus ?case by(auto simp:image-Un WT1-is-type[OF wf-prog] after-in-states)
next
  case ALen thus ?case by(auto simp:image-Un WT1-is-type[OF wf-prog] after-in-states)
next
  case InSynchronized thus ?case by auto
next
  case (Synchronized E A ST i exp1 exp2)
    from  $\langle P, E \vdash 1 \text{ sync}_i (exp1) \ exp2 :: T \rangle$  obtain T1
      where wt1:  $P, E \vdash 1 \text{ exp1} :: T1$  and T1: is-refT T1 T1 ≠ NT

```

```

  and wt2: P, E@[Class Object] ⊢ 1 exp2 :: T by auto
  moreover note E = ⟨set E ⊆ types P⟩ with wf-prog
  have E': set (E@[Class Object]) ⊆ types P by(auto simp add: is-type.simps)
  moreover from wf-prog wt2 E' have T: is-type P T by(rule WT1-is-type)
  note ST = ⟨set ST ⊆ types P⟩ with wf-prog
  have ST': set (Class Object # ST) ⊆ types P by(auto simp add: is-type.simps)
  moreover from wf-prog have throwable: is-type P (Class Throwable)
    unfolding is-type.simps by(rule is-class-Throwable)
  ultimately show ?case using Synchronized max-stack1[of exp2] T
    by(auto simp add: image-Un after-in-states)
qed

```

```

declare is-type.simps[simp] subsetI[intro!]

```

```

end

```

```

locale TC2 = TC0 +
  fixes Tr :: ty and mxs :: pc
begin

```

```

definition

```

```

  wt-instrs :: 'addr instr list ⇒ ex-table ⇒ tyi' list ⇒ bool ((⊢ -, - /[:]/ -) [0,0,51] 50)
where
  ⊢ is,xt [::] τs ≡ size is < size τs ∧ pcs xt ⊆ {0..size is} ∧ (∀ pc < size is. P, Tr, mxs, size τs, xt ⊢
  is!pc, pc :: τs)

```

```

lemmas wt-defs = wt-instrs-def wt-instr-def app-def eff-def norm-eff-def

```

```

lemma wt-instrs-Nil [simp]: τs ≠ [] ⇒ ⊢ [], [] [::] τs
by(simp add: wt-defs)

```

```

end

```

```

locale TC3 = TC1 + TC2

```

```

lemma eff-None [simp]: eff i P pc et None = []
by (simp add: Effect.eff-def)

```

```

declare split-comp-eq[simp del]

```

```

lemma wt-instr-appR:

```

```

  ⟦ P, T, m, mpc, xt ⊢ is!pc, pc :: τs;
    pc < size is; size is < size τs; mpc ≤ size τs; mpc ≤ mpc' ⟧
  ⇒ P, T, m, mpc', xt ⊢ is!pc, pc :: τs@τs'
by (fastforce simp: wt-instr-def app-def)

```

```

lemma relevant-entries-shift [simp]:

```

```

  relevant-entries P i (pc+n) (shift n xt) = shift n (relevant-entries P i pc xt)
  apply (induct xt)
  apply (unfold relevant-entries-def shift-def)
  apply simp
  apply (auto simp add: is-relevant-entry-def)

```

done

**lemma** *xcpt-eff-shift* [*simp*]:  
 $xcpt\text{-}eff\ i\ P\ (pc+n)\ \tau\ (shift\ n\ xt) =$   
 $map\ (\lambda(pc,\tau). (pc + n, \tau))\ (xcpt\text{-}eff\ i\ P\ pc\ \tau\ xt)$   
**apply**(*simp add: xcpt-eff-def*)  
**apply**(*cases*  $\tau$ )  
**apply**(*auto simp add: shift-def*)  
**done**

**lemma** *eff-shift* [*simp*]:  
 $app_i\ (i, P, pc, m, T, \tau) \implies$   
 $eff\ i\ P\ (pc+n)\ (shift\ n\ xt)\ (Some\ \tau) =$   
 $map\ (\lambda(pc,\tau). (pc+n,\tau))\ (eff\ i\ P\ pc\ xt\ (Some\ \tau))$   
**apply**(*simp add: eff-def norm-eff-def*)  
**apply**(*cases*  $i, auto$ )  
**done**

**lemma** *xcpt-app-shift* [*simp*]:  
 $xcpt\text{-}app\ i\ P\ (pc+n)\ m\ (shift\ n\ xt)\ \tau = xcpt\text{-}app\ i\ P\ pc\ m\ xt\ \tau$   
**by** (*simp add: xcpt-app-def*) (*auto simp add: shift-def*)

**lemma** *wt-instr-appL*:  
 $\llbracket P, T, m, mpc, xt \vdash i, pc :: \tau s; pc < size\ \tau s; mpc \leq size\ \tau s \rrbracket$   
 $\implies P, T, m, mpc + size\ \tau s', shift\ (size\ \tau s')\ xt \vdash i, pc + size\ \tau s' :: \tau s' @ \tau s$   
**apply**(*clarsimp simp add: wt-instr-def app-def*)  
**apply**(*auto*)  
**apply**(*cases*  $i, auto$ )  
**done**

**lemma** *wt-instr-Cons*:  
 $\llbracket P, T, m, mpc - 1, \rrbracket \vdash i, pc - 1 :: \tau s;$   
 $0 < pc; 0 < mpc; pc < size\ \tau s + 1; mpc \leq size\ \tau s + 1 \rrbracket$   
 $\implies P, T, m, mpc, \rrbracket \vdash i, pc :: \tau \# \tau s$   
**apply**(*drule wt-instr-appL[where  $\tau s' = [\tau]$ ]*)  
**apply** *arith*  
**apply** *arith*  
**apply** (*simp split: nat-diff-split-asm*)  
**done**

**lemma** *wt-instr-append*:  
 $\llbracket P, T, m, mpc - size\ \tau s', \rrbracket \vdash i, pc - size\ \tau s' :: \tau s;$   
 $size\ \tau s' \leq pc; size\ \tau s' \leq mpc; pc < size\ \tau s + size\ \tau s'; mpc \leq size\ \tau s + size\ \tau s' \rrbracket$   
 $\implies P, T, m, mpc, \rrbracket \vdash i, pc :: \tau s' @ \tau s$   
**apply**(*drule wt-instr-appL[where  $\tau s' = \tau s$ ]*)  
**apply** *arith*  
**apply** *arith*



**apply** (*simp split:nat-diff-split-asm*)  
**done**

**lemma** *xcpt-app-pcs*:

$pc \notin pcs \ x t \implies xcpt\text{-}app \ i \ P \ pc \ m\ x s \ x t \ \tau$   
**by** (*auto simp add: xcpt-app-def relevant-entries-def is-relevant-entry-def pcs-def*)

**lemma** *xcpt-eff-pcs*:

$pc \notin pcs \ x t \implies xcpt\text{-}eff \ i \ P \ pc \ \tau \ x t = []$   
**by** (*cases*  $\tau$ )  
*(auto simp add: is-relevant-entry-def xcpt-eff-def relevant-entries-def pcs-def*  
*intro!: filter-False)*

**lemma** *pcs-shift*:

$pc < n \implies pc \notin pcs \ (shift \ n \ x t)$   
**by** (*auto simp add: shift-def pcs-def*)

**lemma** *xcpt-eff-shift-pc-ge-n*: **assumes**  $x \in set \ (xcpt\text{-}eff \ i \ P \ pc \ \tau \ (shift \ n \ x t))$

**shows**  $n \leq pc$

**proof** –

{ **assume**  $pc < n$   
**hence**  $pc \notin pcs \ (shift \ n \ x t)$  **by**(*rule pcs-shift*)  
**with** *assms* **have** *False*  
**by**(*auto simp add: pcs-def xcpt-eff-def is-relevant-entry-def relevant-entries-def split-beta cong:*  
*filter-cong*) }  
**thus** *?thesis* **by**(*cases*  $n \leq pc$ )(*auto*)  
**qed**

**lemma** *wt-instr-appRx*:

$\llbracket P, T, m, mpc, x t \vdash is!pc, pc :: \tau s; pc < size \ is; size \ is < size \ \tau s; mpc \leq size \ \tau s \rrbracket$   
 $\implies P, T, m, mpc, x t @ shift \ (size \ is) \ x t' \vdash is!pc, pc :: \tau s$   
**apply**(*clarsimp simp:wt-instr-def eff-def app-def*)  
**apply**(*fastforce dest: xcpt-eff-shift-pc-ge-n intro!: xcpt-app-pcs[OF pcs-shift]*)  
**done**

**lemma** *wt-instr-appLx*:

$\llbracket P, T, m, mpc, x t \vdash i, pc :: \tau s; pc \notin pcs \ x t' \rrbracket$   
 $\implies P, T, m, mpc, x t' @ x t \vdash i, pc :: \tau s$   
**by** (*auto simp:wt-instr-def app-def eff-def xcpt-app-pcs xcpt-eff-pcs*)

**context** *TC2* **begin**

**lemma** *wt-instrs-extR*:

$\vdash is, x t [::] \tau s \implies \vdash is, x t [::] \tau s @ \tau s'$   
**by**(*auto simp add:wt-instrs-def wt-instr-appR*)

**lemma** *wt-instrs-ext*:

$\llbracket \vdash is_1, x t_1 [::] \tau s_1 @ \tau s_2; \vdash is_2, x t_2 [::] \tau s_2; size \ \tau s_1 = size \ is_1 \rrbracket$   
 $\implies \vdash is_1 @ is_2, x t_1 @ shift \ (size \ is_1) \ x t_2 [::] \tau s_1 @ \tau s_2$

```

apply(clarsimp simp:wt-instrs-def)
apply(rule conjI, fastforce)
apply(rule conjI, fastforce simp add: pcs-shift-conv)
apply clarsimp
apply(rule conjI, fastforce simp:wt-instr-appRx)
apply clarsimp
apply(erule-tac x = pc - size is1 in allE)+
apply(thin-tac P  $\longrightarrow$  Q for P Q)
apply(erule impE, arith)
apply(drule-tac  $\tau s' = \tau s_1$  in wt-instr-appL)
  apply arith
  apply simp
apply(fastforce simp add:add commute intro!: wt-instr-appLx)
done

```

```

corollary wt-instrs-ext2:
   $\llbracket \vdash is_2, xt_2 [::] \tau s_2; \vdash is_1, xt_1 [::] \tau s_1 @ \tau s_2; size \tau s_1 = size is_1 \rrbracket$ 
   $\implies \vdash is_1 @ is_2, xt_1 @ shift (size is_1) xt_2 [::] \tau s_1 @ \tau s_2$ 
by(rule wt-instrs-ext)

```

```

corollary wt-instrs-ext-prefix [trans]:
   $\llbracket \vdash is_1, xt_1 [::] \tau s_1 @ \tau s_2; \vdash is_2, xt_2 [::] \tau s_3; size \tau s_1 = size is_1; \tau s_3 \leq \tau s_2 \rrbracket$ 
   $\implies \vdash is_1 @ is_2, xt_1 @ shift (size is_1) xt_2 [::] \tau s_1 @ \tau s_2$ 
by(bestsimp simp:less-eq-list-def prefix-def elim: wt-instrs-ext dest:wt-instrs-extR)

```

```

corollary wt-instrs-app:
  assumes is1:  $\vdash is_1, xt_1 [::] \tau s_1 @ [\tau]$ 
  assumes is2:  $\vdash is_2, xt_2 [::] \tau \# \tau s_2$ 
  assumes s: size  $\tau s_1 = size is_1$ 
  shows  $\vdash is_1 @ is_2, xt_1 @ shift (size is_1) xt_2 [::] \tau s_1 @ \tau \# \tau s_2$ 
proof –
  from is1 have  $\vdash is_1, xt_1 [::] (\tau s_1 @ [\tau]) @ \tau s_2$ 
  by (rule wt-instrs-extR)
  hence  $\vdash is_1, xt_1 [::] \tau s_1 @ \tau \# \tau s_2$  by simp
  from this is2 s show ?thesis by (rule wt-instrs-ext)
qed

```

```

corollary wt-instrs-app-last [trans]:
   $\llbracket \vdash is_2, xt_2 [::] \tau \# \tau s_2; \vdash is_1, xt_1 [::] \tau s_1; last \tau s_1 = \tau; size \tau s_1 = size is_1 + 1 \rrbracket$ 
   $\implies \vdash is_1 @ is_2, xt_1 @ shift (size is_1) xt_2 [::] \tau s_1 @ \tau s_2$ 
apply(cases  $\tau s_1$  rule:rev-cases)
apply simp
apply(simp add:wt-instrs-app)
done

```

```

corollary wt-instrs-append-last [trans]:
   $\llbracket \vdash is, xt [::] \tau s; P, T_r, m\bar{x}s, m\bar{p}c, [] \vdash i, pc :: \tau s;$ 

```

```

    pc = size is; mpc = size  $\tau s$ ; size is + 1 < size  $\tau s$  ]
  =>  $\vdash is@[i], xt [::] \tau s$ 
apply(clarsimp simp add:wt-instrs-def)
apply(rule conjI, fastforce)
apply(fastforce intro!:wt-instr-appLx[where  $xt = []$ ,simplified]
      dest!:less-antisym)
done

```

```

corollary wt-instrs-app2:
  [  $\vdash (is_2 :: 'b \text{ instr list}), xt_2 [::] \tau' \# \tau s_2; \vdash is_1, xt_1 [::] \tau \# \tau s_1 @[\tau']$ ;
     $xt' = xt_1 @ \text{shift (size } is_1) \text{ } xt_2$ ; size  $\tau s_1 + 1 = \text{size } is_1$  ]
  =>  $\vdash is_1 @ is_2, xt' [::] \tau \# \tau s_1 @ \tau' \# \tau s_2$ 
using wt-instrs-app[where  $? \tau s_1.0 = \tau \# \tau s_1$  and  $?b = 'b$ ] by simp

```

```

corollary wt-instrs-app2-simp[trans,simp]:
  [  $\vdash (is_2 :: 'b \text{ instr list}), xt_2 [::] \tau' \# \tau s_2; \vdash is_1, xt_1 [::] \tau \# \tau s_1 @[\tau']$ ; size  $\tau s_1 + 1 = \text{size } is_1$  ]
  =>  $\vdash is_1 @ is_2, xt_1 @ \text{shift (size } is_1) \text{ } xt_2 [::] \tau \# \tau s_1 @ \tau' \# \tau s_2$ 
using wt-instrs-app[where  $? \tau s_1.0 = \tau \# \tau s_1$  and  $?b = 'b$ ] by simp

```

```

corollary wt-instrs-Cons[simp]:
  [  $\tau s \neq []; \vdash [i], [] [::] [\tau, \tau']; \vdash is, xt [::] \tau \# \tau s$  ]
  =>  $\vdash i \# is, \text{shift } 1 \text{ } xt [::] \tau \# \tau' \# \tau s$ 
using wt-instrs-app2[where  $?is_1.0 = [i]$  and  $? \tau s_1.0 = []$  and  $?is_2.0 = is$ 
      and  $?xt_1.0 = []$ ]
by simp

```

```

corollary wt-instrs-Cons2[trans]:
  assumes  $\tau s: \vdash is, xt [::] \tau s$ 
  assumes  $i: P, T_r, mxs, mpc, [] \vdash i, 0 :: \tau \# \tau s$ 
  assumes  $mpc: mpc = \text{size } \tau s + 1$ 
  shows  $\vdash i \# is, \text{shift } 1 \text{ } xt [::] \tau \# \tau s$ 
proof -
  from  $\tau s$  have  $\tau s \neq []$  by (auto simp: wt-instrs-def)
  with  $mpc \ i$  have  $\vdash [i], [] [::] [\tau] @ \tau s$  by (simp add: wt-instrs-def)
  with  $\tau s$  show  $?thesis$  by (fastforce dest: wt-instrs-ext)
qed

```

```

lemma wt-instrs-last-incr[trans]:
  [  $\vdash is, xt [::] \tau s @[\tau]; P \vdash \tau \leq' \tau'$  ] =>  $\vdash is, xt [::] \tau s @[\tau']$ 
apply(clarsimp simp add:wt-instrs-def wt-instr-def)
apply(rule conjI)
apply(fastforce)
apply(clarsimp)
apply(rename-tac pc' tau')
apply(erule allE, erule (1) impE)
apply(clarsimp)
apply(drule (1) bspec)
apply(clarsimp)
apply(subgoal-tac pc' = size  $\tau s$ )

```

```

prefer 2
apply(clarsimp simp:app-def)
apply(drule (1) bspec)
apply(clarsimp)
apply(auto elim!:sup-state-opt-trans)
done

```

```

end

```

```

lemma [iff]: xcpt-app i P pc mxs []  $\tau$ 
by (simp add: xcpt-app-def relevant-entries-def)

```

```

lemma [simp]: xcpt-eff i P pc  $\tau$  [] = []
by (simp add: xcpt-eff-def relevant-entries-def)

```

```

context TC2 begin

```

```

lemma wt-New:
   $\llbracket \text{is-class } P \ C; \text{size } ST < mxs \rrbracket \implies$ 
   $\vdash [\text{New } C], [] \text{ } [::] [ty_i' \ ST \ E \ A, ty_i' \ (\text{Class } C \# ST) \ E \ A]$ 
by(simp add:wt-defs ty_i'-def)

```

```

lemma wt-Cast:
  is-type P T  $\implies$ 
   $\vdash [\text{Checkcast } T], [] \text{ } [::] [ty_i' \ (U \# ST) \ E \ A, ty_i' \ (T \# ST) \ E \ A]$ 
by(simp add: ty_i'-def wt-defs)

```

```

lemma wt-Instanceof:
   $\llbracket \text{is-type } P \ T; \text{is-ref } T \ U \rrbracket \implies$ 
   $\vdash [\text{Instanceof } T], [] \text{ } [::] [ty_i' \ (U \# ST) \ E \ A, ty_i' \ (\text{Boolean } \# ST) \ E \ A]$ 
by(simp add: ty_i'-def wt-defs)

```

```

lemma wt-Push:
   $\llbracket \text{size } ST < mxs; \text{typeof } v = \text{Some } T \rrbracket$ 
   $\implies \vdash [\text{Push } v], [] \text{ } [::] [ty_i' \ ST \ E \ A, ty_i' \ (T \# ST) \ E \ A]$ 
by(simp add: ty_i'-def wt-defs)

```

```

lemma wt-Pop:
   $\vdash [\text{Pop}], [] \text{ } [::] (ty_i' \ (T \# ST) \ E \ A \ \# \ ty_i' \ ST \ E \ A \ \# \ \tau s)$ 
by(simp add: ty_i'-def wt-defs)

```

```

lemma wt-BinOpInstr:
   $P \vdash T1 \llbracket \text{bop} \rrbracket T2 :: T \implies \vdash [\text{BinOpInstr } \text{bop}], [] \text{ } [::] [ty_i' \ (T2 \# T1 \# ST) \ E \ A, ty_i' \ (T \# ST) \ E \ A]$ 
by(auto simp:ty_i'-def wt-defs dest: WT-binop-WTrt-binop intro: list-all2-refl)

```

```

lemma wt-Load:
   $\llbracket \text{size } ST < mxs; \text{size } E \leq mxl; i \in A; i < \text{size } E \rrbracket$ 
   $\implies \vdash [\text{Load } i], [] \text{ } [::] [ty_i' \ ST \ E \ A, ty_i' \ (E!i \ \# \ ST) \ E \ A]$ 
by(auto simp add:ty_i'-def wt-defs ty_l-def hyperset-defs intro: widens-refl)

```

**lemma** *wt-Store*:

$\llbracket P \vdash T \leq E!i; i < \text{size } E; \text{size } E \leq \text{mxl} \rrbracket \implies$   
 $\vdash [\text{Store } i], [] \text{ } [::] [ty_i' (T \# ST) E A, ty_i' ST E (\lfloor \{i\} \rfloor \sqcup A)]$   
**by**(*auto simp: hyperset-defs nth-list-update ty\_i'-def wt-defs ty\_i-def*  
*intro: list-all2-all-nthI*)

**lemma** *wt-Get*:

$\llbracket P \vdash C \text{ sees } F:T (fm) \text{ in } D; \text{class-type-of}' U = \lfloor C \rfloor \rrbracket \implies$   
 $\vdash [\text{Getfield } F D], [] \text{ } [::] [ty_i' (U \# ST) E A, ty_i' (T \# ST) E A]$   
**by**(*cases U*)(*auto simp: ty\_i'-def wt-defs dest: sees-field-idemp sees-field-decl-above intro: widens-refl*  
*widen-trans widen-array-object*)

**lemma** *wt-Put*:

$\llbracket P \vdash C \text{ sees } F:T (fm) \text{ in } D; \text{class-type-of}' U = \lfloor C \rfloor; P \vdash T' \leq T \rrbracket \implies$   
 $\vdash [\text{Putfield } F D], [] \text{ } [::] [ty_i' (T' \# U \# ST) E A, ty_i' ST E A]$   
**by**(*cases U*)(*auto 4 3 intro: sees-field-idemp widen-trans widen-array-object dest: sees-field-decl-above*  
*simp: ty\_i'-def wt-defs*)

**lemma** *wt-CAS*:

$\llbracket P \vdash C \text{ sees } F:T (fm) \text{ in } D; \text{class-type-of}' U' = \lfloor C \rfloor; \text{volatile } fm; P \vdash T2 \leq T; P \vdash T3 \leq T \rrbracket \implies$   
 $\vdash [\text{CAS } F D], [] \text{ } [::] [ty_i' (T3 \# T2 \# U' \# ST) E A, ty_i' (\text{Boolean} \# ST) E A]$   
**by**(*cases U'*)(*auto 4 4 simp add: ty\_i'-def wt-defs intro: sees-field-idemp widen-trans widen-array-object*  
*dest: sees-field-decl-above*)

**lemma** *wt-Throw*:

$P \vdash C \preceq^* \text{Throwable} \implies \vdash [\text{ThrowExc}], [] \text{ } [::] [ty_i' (\text{Class } C \# ST) E A, \tau']$   
**by**(*simp add: ty\_i'-def wt-defs*)

**lemma** *wt-IfFalse*:

$\llbracket 2 \leq i; \text{nat } i < \text{size } \tau s + 2; P \vdash ty_i' ST E A \leq' \tau s ! \text{nat}(i - 2) \rrbracket$   
 $\implies \vdash [\text{IfFalse } i], [] \text{ } [::] [ty_i' (\text{Boolean} \# ST) E A \# ty_i' ST E A \# \tau s]$   
**by**(*auto simp add: ty\_i'-def wt-defs eval-nat-numeral nat-diff-distrib*)

**lemma** *wt-Goto*:

$\llbracket 0 \leq \text{int } pc + i; \text{nat } (\text{int } pc + i) < \text{size } \tau s; \text{size } \tau s \leq \text{mpc};$   
 $P \vdash \tau s!pc \leq' \tau s ! \text{nat } (\text{int } pc + i) \rrbracket$   
 $\implies P, T, \text{mxs}, \text{mpc}, [] \vdash \text{Goto } i, pc :: \tau s$   
**by**(*clarsimp simp add: wt-defs*)

**end**

**context** *TC3* **begin**

**lemma** *wt-Invoke*:

$\llbracket \text{size } es = \text{size } Ts'; \text{class-type-of}' U = \lfloor C \rfloor; P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D; P \vdash Ts' [\leq] Ts \rrbracket$   
 $\implies \vdash [\text{Invoke } M (\text{size } es)], [] \text{ } [::] [ty_i' (\text{rev } Ts' @ U \# ST) E A, ty_i' (T \# ST) E A]$   
**apply**(*clarsimp simp add: ty\_i'-def wt-defs*)  
**apply** *safe*  
**apply**(*simp-all (no-asm-use)*)  
**apply**(*auto simp add: intro: widens-refl*)  
**done**

**end**

**declare** *nth-append*[*simp del*]

**declare** [*simproc del: list-to-set-comprehension*]

**context** *TC2* **begin**

**corollary** *wt-instrs-app3*[*simp*]:

$\llbracket \vdash (is_2 :: 'b \text{ instr list}), [] \rrbracket [::] (\tau' \# \tau s_2); \vdash is_1, xt_1 [::] \tau \# \tau s_1 @ [\tau']; \text{size } \tau s_1 + 1 = \text{size } is_1 \rrbracket$   
 $\implies \vdash (is_1 @ is_2), xt_1 [::] \tau \# \tau s_1 @ \tau' \# \tau s_2$

**using** *wt-instrs-app2*[**where** *?xt2.0* = [] **and** *?b* = 'b] **by** (*simp add:shift-def*)

**corollary** *wt-instrs-Cons3*[*simp*]:

$\llbracket \tau s \neq []; \vdash [i], [] [::] [\tau, \tau']; \vdash is, [] [::] \tau' \# \tau s \rrbracket$   
 $\implies \vdash (i \# is), [] [::] \tau \# \tau' \# \tau s$

**using** *wt-instrs-Cons*[**where** *?xt* = []]

**by** (*simp add:shift-def*)

**lemma** *wt-instrs-xapp*:

$\llbracket \vdash is_1 @ is_2, xt [::] \tau s_1 @ ty_i' (Class D \# ST) E A \# \tau s_2;$   
 $\forall \tau \in \text{set } \tau s_1. \forall ST' LT'. \tau = \text{Some}(ST', LT') \longrightarrow$   
 $\text{size } ST \leq \text{size } ST' \wedge P \vdash \text{Some}(\text{drop}(\text{size } ST' - \text{size } ST) ST', LT') \leq' ty_i' ST E A;$   
 $\text{size } is_1 = \text{size } \tau s_1; \text{size } ST < \text{m}xs; \text{case } Co \text{ of } None \Rightarrow D = \text{Throwable} \mid \text{Some } C \Rightarrow D = C \wedge$   
 $is\text{-class } P C \rrbracket \implies$   
 $\vdash is_1 @ is_2, xt @ [(0, \text{size } is_1 - \text{Suc } n, Co, \text{size } is_1, \text{size } ST)] [::] \tau s_1 @ ty_i' (Class D \# ST) E A \#$   
 $\tau s_2$

**apply**(*simp add:wt-instrs-def split del: option.split-asm*)

**apply**(*rule conjI*)

**apply**(*clarsimp split del: option.split-asm*)

**apply** *arith*

**apply**(*clarsimp split del: option.split-asm*)

**apply**(*erule allE, erule (1) impE*)

**apply**(*clarsimp simp add: wt-instr-def app-def eff-def split del: option.split-asm*)

**apply**(*rule conjI*)

**apply** (*thin-tac*  $\forall x \in A \cup B. P x$  **for**  $A B P$ )

**apply** (*thin-tac*  $\forall x \in A \cup B. P x$  **for**  $A B P$ )

**apply** (*clarsimp simp add: xcpt-app-def relevant-entries-def split del: option.split-asm*)

**apply** (*simp add: nth-append is-relevant-entry-def split: if-split-asm split del: option.split-asm*)

**apply** (*drule-tac*  $x = \tau s_1!pc$  **in** *bspec*)

**apply** (*blast intro: nth-mem*)

**apply** *fastforce*

**apply** *fastforce*

**apply** (*rule conjI*)

**apply**(*clarsimp split del: option.split-asm*)

**apply** (*erule disjE, blast*)

**apply** (*erule disjE, blast*)

**apply** (*clarsimp simp add: xcpt-eff-def relevant-entries-def split: if-split-asm*)

**apply**(*clarsimp split del: option.split-asm*)

**apply** (*erule disjE, blast*)

**apply** (*erule disjE, blast*)

**apply** (*clarsimp simp add: xcpt-eff-def relevant-entries-def split: if-split-asm split del: option.split-asm*)

**apply** (*simp add: nth-append is-relevant-entry-def split: if-split-asm split del: option.split-asm*)

```

apply (drule-tac  $x = \tau s_1!pc$  in bspec)
apply (blast intro: nth-mem)
apply (fastforce simp add: tyi'-def)
done

```

**lemma** *wt-instrs-xapp-Some[trans]*:

```

 $\llbracket \vdash is_1 @ is_2, xt \llbracket :: \tau s_1 @ ty_i' (Class\ C \# ST) \ E\ A \# \tau s_2;$ 
 $\forall \tau \in set\ \tau s_1. \forall ST' LT'. \tau = Some(ST', LT') \longrightarrow$ 
 $size\ ST \leq size\ ST' \wedge P \vdash Some\ (drop\ (size\ ST' - size\ ST)\ ST', LT') \leq' ty_i' ST\ E\ A;$ 
 $size\ is_1 = size\ \tau s_1; is-class\ P\ C; size\ ST < mxs \rrbracket \Longrightarrow$ 
 $\vdash is_1 @ is_2, xt @ [(0, size\ is_1 - Suc\ n, Some\ C, size\ is_1, size\ ST)] \llbracket :: \tau s_1 @ ty_i' (Class\ C \# ST) \ E\ A$ 
 $\# \tau s_2$ 
by(erule ( $\mathcal{J}$ ) wt-instrs-xapp) simp

```

**lemma** *wt-instrs-xapp-Any*:

```

 $\llbracket \vdash is_1 @ is_2, xt \llbracket :: \tau s_1 @ ty_i' (Class\ Throwable \# ST) \ E\ A \# \tau s_2;$ 
 $\forall \tau \in set\ \tau s_1. \forall ST' LT'. \tau = Some(ST', LT') \longrightarrow$ 
 $size\ ST \leq size\ ST' \wedge P \vdash Some\ (drop\ (size\ ST' - size\ ST)\ ST', LT') \leq' ty_i' ST\ E\ A;$ 
 $size\ is_1 = size\ \tau s_1; size\ ST < mxs \rrbracket \Longrightarrow$ 
 $\vdash is_1 @ is_2, xt @ [(0, size\ is_1 - Suc\ n, None, size\ is_1, size\ ST)] \llbracket :: \tau s_1 @ ty_i' (Class\ Throwable \# ST)$ 
 $\ E\ A \# \tau s_2$ 
by(erule ( $\mathcal{J}$ ) wt-instrs-xapp) simp

```

**end**

```

declare [simproc add: list-to-set-comprehension]
declare nth-append[simp]

```

**lemma** *drop-Cons-Suc*:

```

 $\bigwedge xs. drop\ n\ xs = y \# ys \Longrightarrow drop\ (Suc\ n)\ xs = ys$ 
apply (induct  $n$ )
apply simp
apply (simp add: drop-Suc)
done

```

**lemma** *drop-mess*:

```

 $\llbracket Suc\ (length\ xs_0) \leq length\ xs; drop\ (length\ xs - Suc\ (length\ xs_0))\ xs = x \# xs_0 \rrbracket$ 
 $\Longrightarrow drop\ (length\ xs - length\ xs_0)\ xs = xs_0$ 
apply (cases  $xs$ )
apply simp
apply (simp add: Suc-diff-le)
apply (case-tac length list - length xs0)
apply simp
apply (simp add: drop-Cons-Suc)
done

```

**lemma** *drop-mess2*:

```

assumes len: Suc (Suc (length xs0)) ≤ length xs
and drop: drop (length xs - Suc (Suc (length xs0))) xs = x1 # x2 # xs0
shows drop (length xs - length xs0) xs = xs0
proof(cases  $xs$ )
case Nil with assms show ?thesis by simp
next
case (Cons x xs')

```

```

note Cons[simp]
show ?thesis
proof(cases xs')
  case Nil with assms show ?thesis by(simp)
next
  case (Cons x' xs'')
  note Cons[simp]
  show ?thesis
  proof(rule drop-mess)
    from len show Suc (length xs0) ≤ length xs by simp
  next
    have drop (length xs - length (x2 # xs0)) xs = x2 # xs0
    proof(rule drop-mess)
      from len show Suc (length (x2 # xs0)) ≤ length xs by(simp)
    next
      from drop show drop (length xs - Suc (length (x2 # xs0))) xs = x1 # x2 # xs0 by simp
    qed
    thus drop (length xs - Suc (length xs0)) xs = x2 # xs0 by(simp)
  qed
qed
qed

```

**abbreviation** postfix :: 'a list ⇒ 'a list ⇒ bool ((-/ >>= -) [51, 50] 50) **where**  
 postfix xs ys ≡ suffix ys xs

**lemma** postfix-conv-eq-length-drop:

$ST' \gg= ST \longleftrightarrow \text{length } ST \leq \text{length } ST' \wedge \text{drop } (\text{length } ST' - \text{length } ST) ST' = ST$

**apply**(auto)

**apply** (metis append-eq-conv-conj append-take-drop-id diff-is-0-eq drop-0 linorder-not-less nat-le-linear suffix-take)

**apply** (metis append-take-drop-id length-drop suffix-take same-append-eq size-list-def)

**by** (metis suffix-drop)

**declare** suffix-ConsI[simp]

**context** TC0 **begin**

**declare** after-def[simp] pair-eq-ty<sub>i</sub>'-conv[simp]

**lemma**

**assumes** ST0 >>= ST'

**shows** compT-ST-prefix:

$[(ST, LT)] \in \text{set}(\text{compT } E \ A \ ST0 \ e) \implies ST \gg= ST'$

**and** compTs-ST-prefix:

$[(ST, LT)] \in \text{set}(\text{compTs } E \ A \ ST0 \ es) \implies ST \gg= ST'$

**using** assms

**by**(induct E A ST0 e **and** E A ST0 es rule: compT-compTs-induct) auto

**declare** after-def[simp del] pair-eq-ty<sub>i</sub>'-conv[simp del]

**end**

**declare** suffix-ConsI[simp del]



**lemma** *fun-of-simp* [*simp*]: *fun-of* *S* *x y* = ((*x,y*) ∈ *S*)  
**by** (*simp add: fun-of-def*)

**declare** *widens-refl* [*iff*]

**context** *TC3* **begin**

**theorem** *compT-wt-instrs*:

$\llbracket P, E \vdash 1 \ e :: T; \mathcal{D} \ e \ A; \mathcal{B} \ e \ (\text{size } E); \text{size } ST + \text{max-stack } e \leq \text{mxs}; \text{size } E + \text{max-vars } e \leq \text{mxl}; \text{set } E \subseteq \text{types } P \rrbracket$

$\implies \vdash \text{compE2 } e, \text{compxE2 } e \ 0 \ (\text{size } ST) \ [\cdot] \ ty_i' \ ST \ E \ A \ \# \ \text{compT } E \ A \ ST \ e \ @ \ [\text{after } E \ A \ ST \ e]$   
**(is** *PROP* *?P* *e* *E* *T* *A* *ST*)

**and** *compTs-wt-instrs*:

$\llbracket P, E \vdash 1 \ es [\cdot] \ Ts; \mathcal{D} \ s \ es \ A; \mathcal{B} \ s \ es \ (\text{size } E); \text{size } ST + \text{max-stacks } es \leq \text{mxs}; \text{size } E + \text{max-varss } es \leq \text{mxl}; \text{set } E \subseteq \text{types } P \rrbracket$

$\implies \text{let } \tau s = ty_i' \ ST \ E \ A \ \# \ \text{compTs } E \ A \ ST \ es$

$\text{in } \vdash \text{compEs2 } es, \text{compxEs2 } es \ 0 \ (\text{size } ST) \ [\cdot] \ \tau s \wedge \text{last } \tau s = ty_i' \ (\text{rev } Ts \ @ \ ST) \ E \ (A \sqcup \mathcal{A} \ s \ es)$

**(is** *PROP* *?Ps* *es* *E* *Ts* *A* *ST*)

**proof**(*induct* *E* *A* *ST* *e* **and** *E* *A* *ST* *es* *arbitrary*: *T* **and** *Ts* *rule*: *compT-compTs-induct*)

**case** (*TryCatch* *E* *A* *ST* *e*<sub>1</sub> *C* *i* *e*<sub>2</sub>)

**hence** [*simp*]: *i* = *size* *E* **by** *simp*

**have** *wt*<sub>1</sub>: *P, E* ⊢ 1 *e*<sub>1</sub> :: *T* **and** *wt*<sub>2</sub>: *P, E* ⊢ [*Class* *C*] ⊢ 1 *e*<sub>2</sub> :: *T*

**and** *class*: *is-class* *P* *C* **using** *TryCatch* **by** *auto*

**let** *?A*<sub>1</sub> = *A* ⊔ *A* *e*<sub>1</sub> **let** *?A*<sub>i</sub> = *A* ⊔ [*i*] **let** *?E*<sub>i</sub> = *E* @ [*Class* *C*]

**let** *?τ* = *ty*<sub>i</sub>' *ST* *E* *A* **let** *?τ*<sub>s1</sub> = *compT* *E* *A* *ST* *e*<sub>1</sub>

**let** *?τ*<sub>1</sub> = *ty*<sub>i</sub>' (*T* # *ST*) *E* *?A*<sub>1</sub> **let** *?τ*<sub>2</sub> = *ty*<sub>i</sub>' (*Class* *C* # *ST*) *E* *A*

**let** *?τ*<sub>3</sub> = *ty*<sub>i</sub>' *ST* *?E*<sub>i</sub> *?A*<sub>i</sub> **let** *?τ*<sub>s2</sub> = *compT* *?E*<sub>i</sub> *?A*<sub>i</sub> *ST* *e*<sub>2</sub>

**let** *?τ*<sub>2</sub>' = *ty*<sub>i</sub>' (*T* # *ST*) *?E*<sub>i</sub> (*?A*<sub>i</sub> ⊔ *A* *e*<sub>2</sub>)

**let** *?τ*' = *ty*<sub>i</sub>' (*T* # *ST*) *E* (*A* ⊔ *A* *e*<sub>1</sub> ⊔ (*A* *e*<sub>2</sub> ⊖ *i*))

**let** *?go* = *Goto* (*int*(*size*(*compE2* *e*<sub>2</sub>)) + 2)

**have** *PROP* *?P* *e*<sub>2</sub> *?E*<sub>i</sub> *T* *?A*<sub>i</sub> *ST* **by** *fact*

**hence** ⊢ *compE2* *e*<sub>2</sub>, *compxE2* *e*<sub>2</sub> 0 (*size* *ST*) [*·*] (*?τ*<sub>3</sub> # *?τ*<sub>s2</sub>) @ [*?τ*<sub>2</sub>']

**using** *TryCatch.prem*s *class* **by** (*auto simp:after-def*)

**also have** *?A*<sub>i</sub> ⊔ *A* *e*<sub>2</sub> = (*A* ⊔ *A* *e*<sub>2</sub>) ⊔ [*size* *E*]

**by** (*fastforce simp:hyperset-defs*)

**also have** *P* ⊢ *ty*<sub>i</sub>' (*T* # *ST*) *?E*<sub>i</sub> ... ≤' *ty*<sub>i</sub>' (*T* # *ST*) *E* (*A* ⊔ *A* *e*<sub>2</sub>)

**by** (*simp add:hyperset-defs ty<sub>i</sub>-incr ty<sub>i</sub>'-def*)

**also have** *P* ⊢ ... ≤' *ty*<sub>i</sub>' (*T* # *ST*) *E* (*A* ⊔ *A* *e*<sub>1</sub> ⊔ (*A* *e*<sub>2</sub> ⊖ *i*))

**by** (*auto intro!: ty<sub>i</sub>-antimono simp:hyperset-defs ty<sub>i</sub>'-def*)

**also have** (*?τ*<sub>3</sub> # *?τ*<sub>s2</sub>) @ [*?τ*'] = *?τ*<sub>3</sub> # *?τ*<sub>s2</sub> @ [*?τ*'] **by** *simp*

**also have** ⊢ [*Store* *i*], [] [*·*] *?τ*<sub>2</sub> # [] @ [*?τ*<sub>3</sub>]

**using** *TryCatch.prem*s

**by** (*auto simp:nth-list-update wt-defs ty<sub>i</sub>'-def ty<sub>i</sub>-def*

*list-all2-conv-all-nth hyperset-defs*)

**also have** [] @ (*?τ*<sub>3</sub> # *?τ*<sub>s2</sub> @ [*?τ*']) = (*?τ*<sub>3</sub> # *?τ*<sub>s2</sub> @ [*?τ*']) **by** *simp*

**also have** *P, T<sub>r</sub>, mxs, size(compE2* *e*<sub>2</sub>) + 3, [] ⊢ *?go*, 0 :: *?τ*<sub>1</sub> # *?τ*<sub>2</sub> # *?τ*<sub>3</sub> # *?τ*<sub>s2</sub> @ [*?τ*']

**by** (*auto simp: hyperset-defs ty<sub>i</sub>'-def wt-defs nth-Cons nat-add-distrib*

*fun-of-def intro: ty<sub>i</sub>-antimono list-all2-refl split.nat.split*)

**also have** ⊢ *compE2* *e*<sub>1</sub>, *compxE2* *e*<sub>1</sub> 0 (*size* *ST*) [*·*] *?τ* # *?τ*<sub>s1</sub> @ [*?τ*<sub>1</sub>]

**using** *TryCatch* **by** (*auto simp:after-def*)

**also have** *?τ* # *?τ*<sub>s1</sub> @ [*?τ*<sub>1</sub>] # *?τ*<sub>2</sub> # *?τ*<sub>3</sub> # *?τ*<sub>s2</sub> @ [*?τ*'] =

(*?τ* # *?τ*<sub>s1</sub> @ [*?τ*<sub>1</sub>]) @ *?τ*<sub>2</sub> # *?τ*<sub>3</sub> # *?τ*<sub>s2</sub> @ [*?τ*'] **by** *simp*

```

also have compE2 e1 @ ?go # [Store i] @ compE2 e2 =
  (compE2 e1 @ [?go]) @ (Store i # compE2 e2) by simp
also
let ?Q  $\tau = \forall ST' LT'. \tau = [(ST', LT')] \longrightarrow$ 
  size ST  $\leq$  size ST'  $\wedge P \vdash \text{Some}(\text{drop}(\text{size ST}' - \text{size ST}) ST', LT') \leq' ty_i' ST E A$ 
{
  have ?Q (ty_i' ST E A) by(clarsimp simp add: ty_i'-def)
  moreover have ?Q (ty_i' (T # ST) E ?A1)
    by (fastforce simp add: ty_i'-def hyperset-defs intro!: ty_l-antimono)
  moreover { fix  $\tau$ 
    assume  $\tau: \tau \in \text{set}(\text{compT } E A ST e_1)$ 
    hence  $\forall ST' LT'. \tau = [(ST', LT')] \longrightarrow ST' \gg ST$  by(auto intro: compT-ST-prefix[OF
      suffix-order.order-refl])
    with  $\tau$  have ?Q  $\tau$  unfolding postfix-conv-eq-length-drop using  $\langle \mathcal{B}(\text{try } e_1 \text{ catch}(C \ i) \ e_2) (\text{length } E) \rangle$ 
    by(fastforce dest!: compT-LT-prefix simp add: ty_i'-def) }
  ultimately
  have  $\forall \tau \in \text{set}(\text{ty}_i' ST E A \# \text{compT } E A ST e_1 @ [\text{ty}_i' (T \# ST) E ?A_1]). ?Q \tau$  by auto
}
also from TryCatch.prem.s max-stack1[of e1] have size ST + 1  $\leq$  mxs by auto
ultimately show ?case using wt1 wt2 TryCatch.prem.s class
  by (simp add:after-def)(erule-tac x=0 in meta-allE, simp)
next
case (Synchronized E A ST i e1 e2)
note wt =  $\langle P, E \vdash 1 \text{ sync}_i(e1) \ e2 :: T \rangle$ 
then obtain U where wt1:  $P, E \vdash 1 \ e1 :: U$ 
  and U: is-refT U U  $\neq NT$ 
  and wt2:  $P, E @ [\text{Class Object}] \vdash 1 \ e2 :: T$  by auto
from  $\langle \mathcal{B}(\text{sync}_i(e1) \ e2) (\text{length } E) \rangle$  have [simp]:  $i = \text{length } E$ 
  and B1:  $\mathcal{B} \ e1 (\text{length } E)$  and B2:  $\mathcal{B} \ e2 (\text{length } (E @ [\text{Class Object}] ))$  by auto

note lenST =  $\langle \text{length } ST + \text{max-stack}(\text{sync}_i(e1) \ e2) \leq \text{mxs} \rangle$ 
note lenE =  $\langle \text{length } E + \text{max-vars}(\text{sync}_i(e1) \ e2) \leq \text{mxl} \rangle$ 

let ?A1 =  $A \sqcup \mathcal{A} \ e1$  let ?A2 =  $?A1 \sqcup [\{i\}]$ 
let ?A3 =  $?A2 \sqcup \mathcal{A} \ e2$  let ?A4 =  $?A1 \sqcup \mathcal{A} \ e2$ 
let ?E1 =  $E @ [\text{Class Object}]$ 
let ? $\tau$  =  $ty_i' ST E A$  let ? $\tau$ s1 =  $\text{compT } E A ST e1$ 
let ? $\tau$ 1 =  $ty_i' (U \# ST) E ?A1$ 
let ? $\tau$ 1' =  $ty_i' (\text{Class Object} \# \text{Class Object} \# ST) E ?A1$ 
let ? $\tau$ 1'' =  $ty_i' (\text{Class Object} \# ST) ?E1 ?A2$ 
let ? $\tau$ 1''' =  $ty_i' ST ?E1 ?A2$ 
let ? $\tau$ s2 =  $\text{compT } ?E1 ?A2 ST e2$ 
let ? $\tau$ 2 =  $ty_i' (T \# ST) ?E1 ?A3$  let ? $\tau$ 2' =  $ty_i' (\text{Class Object} \# T \# ST) ?E1 ?A3$ 
let ? $\tau$ 2'' = ? $\tau$ 2
let ? $\tau$ 3 =  $ty_i' (\text{Class Throwable} \# ST) ?E1 ?A2$ 
let ? $\tau$ 3' =  $ty_i' (\text{Class Object} \# \text{Class Throwable} \# ST) ?E1 ?A2$ 
let ? $\tau$ 3'' = ? $\tau$ 3
let ? $\tau$ ' =  $ty_i' (T \# ST) E ?A4$ 

from lenE lenST max-stack1[of e2] U
have  $\vdash [\text{Load } i, \text{MExit}, \text{ThrowExc}], [] [::] [?\tau 3, ?\tau 3', ?\tau 3'', ?\tau]$ 
  by(auto simp add: ty_i'-def ty_l-def wt-defs hyperset-defs nth-Cons split: nat.split)
also have  $P, T_r, \text{mxs}, 5, [] \vdash \text{Goto } 4, 0 :: [?\tau 2'', ?\tau 3, ?\tau 3', ?\tau 3'', ?\tau]$ 

```

```

  by(auto simp: hyperset-defs tyi'-def wt-defs intro: tyl-antimono tyl-incr)
also have P, Tr, mxs, 6, [] ⊢ MExit, 0 :: [?τ 2', ?τ 2'', ?τ 3, ?τ 3', ?τ 3'', ?τ]
  by(auto simp: hyperset-defs tyi'-def wt-defs intro: tyl-antimono tyl-incr)
also from lenE lenST max-stack1[of e2]
have P, Tr, mxs, 7, [] ⊢ Load i, 0 :: [?τ 2, ?τ 2', ?τ 2'', ?τ 3, ?τ 3', ?τ 3'', ?τ]
  by(auto simp: hyperset-defs tyi'-def wt-defs tyl-def intro: tyl-antimono)
also from ⟨D (synci (e1) e2) A⟩ have D e2 (A ⊔ A e1 ⊔ [{length E}])
  by(auto elim!: D-mono' simp add: hyperset-defs)
with ⟨PROP ?P e2 ?E1 T ?A2 ST⟩ Synchronized wt2 is-class-Object[OF wf-prog]
have ⊢ compE2 e2, compxE2 e2 0 (size ST) [::] ?τ 1'''#?τ s2@[?τ 2]
  by(auto simp add: after-def)
finally have ⊢ (compE2 e2 @ [Load i, MExit, Goto 4]) @ [Load i, MExit, ThrowExc], compxE2 e2
0 (size ST) [::]
  (?τ 1''' # ?τ s2 @ [?τ 2, ?τ 2', ?τ 2'']) @ [?τ 3, ?τ 3', ?τ 3'', ?τ]
  by(simp)
hence ⊢ (compE2 e2 @ [Load i, MExit, Goto 4]) @ [Load i, MExit, ThrowExc],
  compxE2 e2 0 (size ST) @ [(0, size (compE2 e2 @ [Load i, MExit, Goto 4]) - Suc 2, None,
size (compE2 e2 @ [Load i, MExit, Goto 4]), size ST)] [::]
  (?τ 1''' # ?τ s2 @ [?τ 2, ?τ 2', ?τ 2'']) @ [?τ 3, ?τ 3', ?τ 3'', ?τ]
proof(rule wt-instrs-xapp-Any)
  from lenST show length ST < mxs by simp
next
show ∀ τ ∈ set (?τ 1''' # ?τ s2 @ [?τ 2, ?τ 2', ?τ 2'']). ∀ ST' LT'.
  τ = [(ST', LT')] ⟶ length ST ≤ length ST' ∧
  P ⊢ [(drop (length ST' - length ST) ST', LT')] ≤' tyi' ST (E @ [Class Object]) ?A2
proof(intro strip)
  fix τ ST' LT'
  assume τ ∈ set (?τ 1''' # ?τ s2 @ [?τ 2, ?τ 2', ?τ 2'']) τ = [(ST', LT')]
  hence τ: [(ST', LT')] ∈ set (?τ 1''' # ?τ s2 @ [?τ 2, ?τ 2', ?τ 2'']) by simp
  show length ST ≤ length ST' ∧ P ⊢ [(drop (length ST' - length ST) ST', LT')] ≤' tyi' ST (E
@ [Class Object]) ?A2
  proof(cases [(ST', LT')] ∈ set ?τ s2)
    case True
    from compT-ST-prefix[OF suffix-order.order-refl this] compT-LT-prefix[OF this B2]
    show ?thesis unfolding postfix-conv-eq-length-drop by(simp add: tyi'-def)
  next
    case False
    with τ show ?thesis
    by(auto simp add: tyi'-def hyperset-defs intro: tyl-antimono)
  qed
qed
qed simp
hence ⊢ compE2 e2 @ [Load i, MExit, Goto 4, Load i, MExit, ThrowExc],
  compxE2 e2 0 (size ST) @ [(0, size (compE2 e2), None, Suc (Suc (Suc (size (compE2 e2))))),
size ST)] [::]
  ?τ 1''' # ?τ s2 @ [?τ 2, ?τ 2', ?τ 2'', ?τ 3, ?τ 3', ?τ 3'', ?τ] by simp
also from wt1 ⟨set E ⊆ types P⟩ have is-type P U by(rule WT1-is-type[OF wf-prog])
with U have P ⊢ U ≤ Class Object by(auto elim!: is-refT.cases intro: subcls-C-Object[OF - wf-prog]
widen-array-object)
with lenE lenST max-stack1[of e2]
have ⊢ [Dup, Store i, MEnter], [] [::] [?τ 1, ?τ 1', ?τ 1''] @ [?τ 1''']
  by(auto simp add: tyi'-def tyl-def wt-defs hyperset-defs nth-Cons nth-list-update list-all2-conv-all-nth
split: nat.split)
finally have ⊢ Dup # Store i # MEnter # compE2 e2 @ [Load i, MExit, Goto 4, Load i, MExit,

```

$\text{ThrowExc}]$ ,  
 $\text{compxE2 } e2 \ 3 \ (\text{size } ST) \ @ \ [(\ 3, \ 3 + \text{size } (\text{compE2 } e2), \ \text{None}, \ 6 + \text{size } (\text{compE2 } e2), \ \text{size } ST)]$   
 $[\ :: ] \ ?\tau 1 \ \# \ ?\tau 1' \ \# \ ?\tau 1'' \ \# \ ?\tau 1''' \ \# \ ?\tau s2 \ @ \ [?\tau 2, \ ?\tau 2', \ ?\tau 2'', \ ?\tau 3, \ ?\tau 3', \ ?\tau 3'', \ ?\tau]$   
**by**(*simp add: eval-nat-numeral shift-def*)  
**also from**  $\langle \text{PROP } ?P \ e1 \ E \ U \ A \ ST \rangle \ wt1 \ B1 \ \langle \mathcal{D} \ (\text{sync}_i \ (e1) \ e2) \ A \rangle \ \text{lenE} \ \text{lenST} \ \langle \text{set } E \subseteq \text{types } P \rangle$   
**have**  $\vdash \text{compE2 } e1, \text{compxE2 } e1 \ 0 \ (\text{size } ST) \ [\ :: ] \ ?\tau \# \ ?\tau s1 \ @ [?\tau 1]$   
**by**(*auto simp add: after-def*)  
**finally show**  $?case$  **using**  $wt1 \ wt2 \ wt$  **by**(*simp add: after-def ac-simps shift-Cons-tuple hyperUn-assoc*)  
**next**  
**case new** **thus**  $?case$  **by**(*auto simp add: after-def wt-New*)  
**next**  
**case**  $(\text{BinOp } E \ A \ ST \ e_1 \ \text{bop} \ e_2)$   
**have**  $T: P, E \vdash 1 \ e_1 \ \ll \text{bop} \gg \ e_2 \ :: \ T$  **by fact**  
**then obtain**  $T_1 \ T_2$  **where**  $T_1: P, E \vdash 1 \ e_1 \ :: \ T_1$  **and**  $T_2: P, E \vdash 1 \ e_2 \ :: \ T_2$  **and**  
 $\text{bopT}: P \vdash T_1 \ll \text{bop} \gg T_2 \ :: \ T$  **by auto**  
**let**  $?A_1 = A \sqcup \mathcal{A} \ e_1$  **let**  $?A_2 = ?A_1 \sqcup \mathcal{A} \ e_2$   
**let**  $? \tau = \text{ty}_i' \ ST \ E \ A$  **let**  $? \tau s_1 = \text{compT } E \ A \ ST \ e_1$   
**let**  $? \tau_1 = \text{ty}_i' \ (T_1 \# ST) \ E \ ?A_1$  **let**  $? \tau s_2 = \text{compT } E \ ?A_1 \ (T_1 \# ST) \ e_2$   
**let**  $? \tau_2 = \text{ty}_i' \ (T_2 \# T_1 \# ST) \ E \ ?A_2$  **let**  $? \tau' = \text{ty}_i' \ (T \# ST) \ E \ ?A_2$   
**from**  $\text{bopT}$  **have**  $\vdash [\text{BinOpInstr } \text{bop}], [] \ [\ :: ] \ [?\tau_2, ?\tau']$  **by**(*rule wt-BinOpInstr*)  
**also from**  $\text{BinOp.hyps}(2)[\text{of } T_2] \ \text{BinOp.premis } T_2 \ T_1$   
**have**  $\vdash \text{compE2 } e_2, \text{compxE2 } e_2 \ 0 \ (\text{size } (\text{ty } E \ e_1 \# ST)) \ [\ :: ] \ ?\tau_1 \# \ ?\tau s_2 \ @ [?\tau_2]$  **by** (*auto simp: after-def*)  
**also from**  $\text{BinOp } T_1$  **have**  $\vdash \text{compE2 } e_1, \text{compxE2 } e_1 \ 0 \ (\text{size } ST) \ [\ :: ] \ ?\tau \# \ ?\tau s_1 \ @ [?\tau_1]$   
**by** (*auto simp: after-def*)  
**finally show**  $?case$  **using**  $T \ T_1 \ T_2$  **by** (*simp add: after-def hyperUn-assoc*)  
**next**  
**case**  $(\text{Cons } E \ A \ ST \ e \ es)$   
**have**  $P, E \vdash 1 \ e \ \# \ es \ [\ :: ] \ Ts$  **by fact**  
**then obtain**  $T_e \ Ts'$  **where**  
 $T_e: P, E \vdash 1 \ e \ :: \ T_e$  **and**  $Ts': P, E \vdash 1 \ es \ [\ :: ] \ Ts'$  **and**  
 $Ts: Ts = T_e \# Ts'$  **by auto**  
**let**  $?A_e = A \sqcup \mathcal{A} \ e$   
**let**  $? \tau = \text{ty}_i' \ ST \ E \ A$  **let**  $? \tau s_e = \text{compT } E \ A \ ST \ e$   
**let**  $? \tau_e = \text{ty}_i' \ (T_e \# ST) \ E \ ?A_e$  **let**  $? \tau s' = \text{compTs } E \ ?A_e \ (T_e \# ST) \ es$   
**let**  $? \tau s = ? \tau \ \# \ ? \tau s_e \ @ \ (? \tau_e \ \# \ ? \tau s')$   
**from**  $\text{Cons.hyps}(2) \ \text{Cons.premis } T_e \ Ts'$   
**have**  $\vdash \text{compEs2 } es, \text{compxEs2 } es \ 0 \ (\text{size } (T_e \# ST)) \ [\ :: ] \ ?\tau_e \# \ ?\tau s' \ \text{by} \ (\text{simp add: after-def})$   
**also from**  $\text{Cons } T_e$  **have**  $\vdash \text{compE2 } e, \text{compxE2 } e \ 0 \ (\text{size } ST) \ [\ :: ] \ ?\tau \# \ ?\tau s_e \ @ [?\tau_e]$  **by** (*auto simp: after-def*)  
**moreover**  
**from**  $\text{Cons.hyps}(2)[\text{OF } Ts'] \ \text{Cons.premis } T_e \ Ts' \ Ts$   
**have last**  $? \tau s = \text{ty}_i' \ (\text{rev } Ts \ @ \ ST) \ E \ (?A_e \sqcup \mathcal{A} \ es)$  **by simp**  
**ultimately show**  $?case$  **using**  $T_e$   
**by**(*auto simp add: after-def hyperUn-assoc shift-compxEs2 stack-xlift-compxEs2 simp del: compxE2-size-convs compxEs2-size-convs compxEs2-stack-xlift-convs compxE2-stack-xlift-convs intro: wt-instrs-app2*)  
**next**  
**case**  $(\text{FAss } E \ A \ ST \ e_1 \ F \ D \ e_2)$   
**hence**  $\text{Void}: P, E \vdash 1 \ e_1 \cdot F \{D\} \ := \ e_2 \ :: \ \text{Void}$  **by auto**  
**then obtain**  $U \ C \ T \ T' \ \text{fm}$  **where**  
 $C: P, E \vdash 1 \ e_1 \ :: \ U$  **and**  $U: \text{class-type-of}' \ U = \lfloor C \rfloor$  **and**  $\text{sees}: P \vdash C \ \text{sees } F:T \ (\text{fm}) \ \text{in } D$  **and**  
 $T': P, E \vdash 1 \ e_2 \ :: \ T'$  **and**  $T'-T: P \vdash T' \leq T$  **by auto**  
**let**  $?A_1 = A \sqcup \mathcal{A} \ e_1$  **let**  $?A_2 = ?A_1 \sqcup \mathcal{A} \ e_2$   
**let**  $? \tau = \text{ty}_i' \ ST \ E \ A$  **let**  $? \tau s_1 = \text{compT } E \ A \ ST \ e_1$

```

let  $\tau_1 = ty_i' (U \# ST) E \tau_{A_1}$  let  $\tau_{s_2} = compT E \tau_{A_1} (U \# ST) e_2$ 
let  $\tau_2 = ty_i' (T \# U \# ST) E \tau_{A_2}$  let  $\tau_3 = ty_i' ST E \tau_{A_2}$ 
let  $\tau' = ty_i' (Void \# ST) E \tau_{A_2}$ 
from  $FAss.premis sees T'-T U$ 
have  $\vdash [Putfield F D, Push Unit], [] [::] [\tau_2, \tau_3, \tau']$ 
  by (fastforce simp add: wt-Push wt-Put)
also from  $FAss.hyps(2)[of T'] FAss.premis T' C$ 
have  $\vdash compE2 e_2, compxE2 e_2 0 (size ST + 1) [::] \tau_1 \# \tau_{s_2} @ [\tau_2]$ 
  by (auto simp add: after-def hyperUn-assoc)
also from  $FAss C$  have  $\vdash compE2 e_1, compxE2 e_1 0 (size ST) [::] \tau \# \tau_{s_1} @ [\tau_1]$ 
  by (auto simp add: after-def)
finally show  $\tau_{case}$  using  $Void C T'$  by (simp add: after-def hyperUn-assoc)
next
  case  $Val$  thus  $\tau_{case}$  by (auto simp: after-def wt-Push)
next
  case  $(Cast T exp)$  thus  $\tau_{case}$  by (auto simp: after-def wt-Cast)
next
  case  $(InstanceOf E A ST e)$  thus  $\tau_{case}$ 
    by (auto simp: after-def intro!: wt-Instanceof wt-instrs-app3 intro: widen-refT refT-widen)
next
  case  $(BlockNone E A ST i Ti e)$ 
    from  $\langle P, E \vdash 1 \{i: Ti = None; e\} :: T \rangle$  have  $wte: P, E @ [Ti] \vdash 1 e :: T$ 
    and  $Ti: is-type P Ti$  by auto
    let  $\tau_s = ty_i' ST E A \# compT (E @ [Ti]) (A \ominus i) ST e$ 
    from  $BlockNone wte Ti$ 
    have  $\vdash compE2 e, compxE2 e 0 (size ST) [::] \tau_s @ [ty_i' (T \# ST) (E @ [Ti]) (A \ominus (size E) \sqcup \mathcal{A} e)]$ 
    by (auto simp add: after-def)
    also have  $P \vdash ty_i' (T \# ST) (E @ [Ti]) (A \ominus size E \sqcup \mathcal{A} e) \leq' ty_i' (T \# ST) (E @ [Ti]) ((A \sqcup \mathcal{A} e) \ominus size E)$ 
    by (auto simp add: hyperset-defs intro: ty_i'-antimono)
    also have  $\dots = ty_i' (T \# ST) E (A \sqcup \mathcal{A} e)$  by simp
    also have  $P \vdash \dots \leq' ty_i' (T \# ST) E (A \sqcup (\mathcal{A} e \ominus i))$ 
    by (auto simp add: hyperset-defs intro: ty_i'-antimono)
    finally show  $\tau_{case}$  using  $BlockNone.premis$  by (simp add: after-def)
next
  case  $(BlockSome E A ST i Ti v e)$ 
    from  $\langle P, E \vdash 1 \{i: Ti = [v]; e\} :: T \rangle$  obtain  $Tv$ 
    where  $Tv: P, E \vdash 1 Val v :: Tv P \vdash Tv \leq Ti$ 
    and  $wte: P, E @ [Ti] \vdash 1 e :: T$ 
    and  $Ti: is-type P Ti$  by auto
    from  $\langle length ST + max-stack \{i: Ti = [v]; e\} \leq mxs \rangle$ 
    have  $lenST: length ST + max-stack e \leq mxs$  by simp
    from  $\langle length E + max-vars \{i: Ti = [v]; e\} \leq mxl \rangle$ 
    have  $lenE: length (E @ [Ti]) + max-vars e \leq mxl$  by simp
    from  $\langle \mathcal{B} \{i: Ti = [v]; e\} (length E) \rangle$  have  $[simp]: i = length E$ 
    and  $B: \mathcal{B} e (length (E @ [Ti]))$  by auto

    from  $BlockSome wte$ 
    have  $\vdash compE2 e, compxE2 e 0 (size ST) [::] (ty_i' ST (E @ [Ti]) (A \sqcup [\{length E\}]) \# compT (E @ [Ti]) (A \sqcup [\{i\}]) ST e) @ [ty_i' (T \# ST) (E @ [Ti]) (A \sqcup [\{size E\}] \sqcup \mathcal{A} e)]$ 
    by (auto simp add: after-def)
    also have  $P \vdash ty_i' (T \# ST) (E @ [Ti]) (A \sqcup [\{length E\}] \sqcup \mathcal{A} e) \leq' ty_i' (T \# ST) (E @ [Ti]) ((A \sqcup \mathcal{A} e) \ominus length E)$ 

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    by(auto simp add: hyperset-defs intro: tyi'-antimono)
  also have ... = tyi' (T # ST) E (A ⊔ A e) by simp
  also have P ⊢ ... ≤' tyi' (T # ST) E (A ⊔ (A e ⊖ i))
    by(auto simp add: hyperset-defs intro: tyi'-antimono)
  also note append-Cons
  also {
    from lenST max-stack1[of e] Tv
    have ⊢ [Push v], [] [::] [tyi' ST E A, tyi' (ty E (Val v) # ST) E A]
      by(auto intro: wt-Push)
    moreover from Tv lenE
    have ⊢ [Store (length E)], [] [::] [tyi' (Tv # ST) (E @ [Ti]) (A ⊖ length E), tyi' ST (E @ [Ti])
      ([{length E}] ⊔ (A ⊖ length E))]
      by -(rule wt-Store, auto)
    moreover have tyi' (Tv # ST) (E @ [Ti]) (A ⊖ length E) = tyi' (Tv # ST) E A by(simp add:
      tyi'-def)
    moreover have [{length E}] ⊔ (A ⊖ length E) = A ⊔ [{length E}] by(simp add: hyperset-defs)
    ultimately have ⊢ [Push v, Store (length E)], [] [::] [tyi' ST E A, tyi' (Tv # ST) E A, tyi' ST
      (E @ [Ti]) (A ⊔ [{length E}])]
      using Tv by(auto intro: wt-instrs-Cons3)
  }
  finally show ?case using Tv ⟨P, E ⊢ 1 {i: Ti = [v]; e} :: T⟩ wte by(simp add: after-def)
next
  case Var thus ?case by(auto simp: after-def wt-Load)
next
  case FAcc thus ?case by(auto simp: after-def wt-Get)
next
  case (LAss E A ST i e) thus ?case using max-stack1[of e]
    by(auto simp: hyper-insert-comm after-def wt-Store wt-Push simp del: hyperUn-comm hyperUn-leftComm)
next
  case Nil thus ?case by auto
next
  case throw thus ?case by(auto simp add: after-def wt-Throw)
next
  case (While E A ST e c)
  obtain Tc where wte: P, E ⊢ 1 e :: Boolean and wtc: P, E ⊢ 1 c :: Tc
    and [simp]: T = Void using While by auto
  have [simp]: ty E (while (e) c) = Void using While by simp
  let ?A0 = A ⊔ A e let ?A1 = ?A0 ⊔ A c
  let ?τ = tyi' ST E A let ?τse = compT E A ST e
  let ?τe = tyi' (Boolean # ST) E ?A0 let ?τ1 = tyi' ST E ?A0
  let ?τsc = compT E ?A0 ST c let ?τc = tyi' (Tc # ST) E ?A1
  let ?τ2 = tyi' ST E ?A1 let ?τ' = tyi' (Void # ST) E ?A0
  let ?τs = (?τ # ?τse @ [?τe]) @ ?τ1 # ?τsc @ [?τc, ?τ2, ?τ1, ?τ']
  have ⊢ [], [] [::] [] @ ?τs by(simp add: wt-instrs-def)
  also
  from While.hyps(1)[of Boolean] While.prem
  have ⊢ compE2 e, compxE2 e 0 (size ST) [::] ?τ # ?τse @ [?τe]
    by (auto simp: after-def)
  also
  have [] @ ?τs = (?τ # ?τse) @ ?τe # ?τ1 # ?τsc @ [?τc, ?τ2, ?τ1, ?τ'] by simp
  also
  let ?ne = size(compE2 e) let ?nc = size(compE2 c)
  let ?if = IfFalse (int ?nc + 3)
  have ⊢ [?if], [] [::] ?τe # ?τ1 # ?τsc @ [?τc, ?τ2, ?τ1, ?τ']

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by(simp add: wt-instr-Cons wt-instr-append wt-IfFalse
    nat-add-distrib split: nat-diff-split)
also
have (?τ # ?τse) @ (?τe # ?τ1 # ?τsc @ [?τc, ?τ2, ?τ1, ?τ]) = ?τs by simp
also from While.hyps(2)[of Tc] While.premis wtc
have ⊢ compE2 c, compxE2 c 0 (size ST) [::] ?τ1 # ?τsc @ [?τc]
  by (auto simp: after-def)
also have ?τs = (?τ # ?τse @ [?τe, ?τ1] @ ?τsc) @ [?τc, ?τ2, ?τ1, ?τ] by simp
also have ⊢ [Pop], [] [::] [?τc, ?τ2] by (simp add: wt-Pop)
also have (?τ # ?τse @ [?τe, ?τ1] @ ?τsc) @ [?τc, ?τ2, ?τ1, ?τ] = ?τs by simp
also let ?go = Goto (-int(?nc+?ne+2))
have P ⊢ ?τ2 ≤' ?τ by (fastforce intro: tyi'-antimono simp: hyperset-defs)
hence P, Tr, mxs, size ?τs, [] ⊢ ?go, ?ne+?nc+2 :: ?τs
  by (simp add: wt-Goto split: nat-diff-split)
also have ?τs = (?τ # ?τse @ [?τe, ?τ1] @ ?τsc @ [?τc, ?τ2]) @ [?τ1, ?τ]
  by simp
also have ⊢ [Push Unit], [] [::] [?τ1, ?τ]
  using While.premis max-stack1 [of c] by (auto simp add: wt-Push)
finally show ?case using wtc wte
  by (simp add: after-def)
next
case (Cond E A ST e1 e2)
obtain T1 T2 where wte: P, E ⊢ 1 e :: Boolean
  and wt1: P, E ⊢ 1 e1 :: T1 and wt2: P, E ⊢ 1 e2 :: T2
  and sub1: P ⊢ T1 ≤ T and sub2: P ⊢ T2 ≤ T
  using Cond by (auto dest: is-lub-upper)
have [simp]: ty E (if (e) e1 else e2) = T using Cond by simp
let ?A0 = A ⊔ A e let ?A2 = ?A0 ⊔ A e2 let ?A1 = ?A0 ⊔ A e1
let ?A' = ?A0 ⊔ A e1 ⊓ A e2
let ?τ2 = tyi' ST E ?A0 let ?τ' = tyi' (T#ST) E ?A'
let ?τs2 = compT E ?A0 ST e2
have PROP ?P e2 E T2 ?A0 ST by fact
hence ⊢ compE2 e2, compxE2 e2 0 (size ST) [::] (?τ2#?τs2) @ [tyi' (T2#ST) E ?A2]
  using Cond.premis wt2 by (auto simp add: after-def)
also have P ⊢ tyi' (T2#ST) E ?A2 ≤' ?τ' using sub2
  by (auto simp add: hyperset-defs tyi'-def intro!: tyi-antimono)
also
let ?τ3 = tyi' (T1 # ST) E ?A1
let ?g2 = Goto(int (size (compE2 e2) + 1))
from sub1 have P, Tr, mxs, size(compE2 e2)+2, [] ⊢ ?g2, 0 :: ?τ3#(?τ2#?τs2)@[?τ]
  by (cases length (compE2 e2))
  (auto simp: hyperset-defs wt-defs nth-Cons tyi'-def neq-Nil-conv
    split: nat.split intro!: tyi-antimono)
also let ?τs1 = compT E ?A0 ST e1
have PROP ?P e1 E T1 ?A0 ST by fact
hence ⊢ compE2 e1, compxE2 e1 0 (size ST) [::] ?τ2 # ?τs1 @ [?τ3]
  using Cond.premis wt1 by (auto simp add: after-def)
also
let ?τs12 = ?τ2 # ?τs1 @ ?τ3 # (?τ2 # ?τs2) @ [?τ]
let ?τ1 = tyi' (Boolean#ST) E ?A0
let ?g1 = IfFalse(int (size (compE2 e1) + 2))
let ?code = compE2 e1 @ ?g2 # compE2 e2
have ⊢ [?g1], [] [::] [?τ1] @ ?τs12
  by (simp add: wt-IfFalse nat-add-distrib split: nat-diff-split)

```

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also (wt-instrs-ext2) have [ $? \tau_1$ ] @  $? \tau_{s_{12}} = ? \tau_1 \# ? \tau_{s_{12}}$  by simp also
let  $? \tau = ty_i' ST E A$ 
have PROP  $? P \in E$  Boolean  $A ST$  by fact
hence  $\vdash compE2 e, compxE2 e 0 (size ST) [::] ? \tau \# compT E A ST e @ [? \tau_1]$ 
  using Cond.premis wte by(auto simp add:after-def)
finally show  $?case$  using wte  $wt_1 wt_2$  by(simp add:after-def hyperUn-assoc)
next
case (Call E A ST e M es)
from  $\langle P, E \vdash 1 e \cdot M(es) :: T \rangle$ 
obtain  $U C D Ts m Ts'$ 
  where  $C: P, E \vdash 1 e :: U$ 
  and icto: class-type-of'  $U = \lfloor C \rfloor$ 
  and method:  $P \vdash C$  sees  $M: Ts \rightarrow T = m$  in  $D$ 
  and wtes:  $P, E \vdash 1 es [::] Ts'$  and subs:  $P \vdash Ts' \leq Ts$ 
  by(cases) auto
from wtes have same-size:  $size es = size Ts'$  by(rule WTs1-same-size)
let  $?A_0 = A \sqcup \mathcal{A} e$  let  $?A_1 = ?A_0 \sqcup \mathcal{A} s es$ 
let  $? \tau = ty_i' ST E A$  let  $? \tau_{s_e} = compT E A ST e$ 
let  $? \tau_e = ty_i' (U \# ST) E ?A_0$ 
let  $? \tau_{s_{es}} = compTs E ?A_0 (U \# ST) es$ 
let  $? \tau_1 = ty_i' (rev Ts' @ U \# ST) E ?A_1$ 
let  $? \tau' = ty_i' (T \# ST) E ?A_1$ 
have  $\vdash [Invoke M (size es)], [] [::] [? \tau_1, ? \tau]$ 
  by(rule wt-Invoke[OF same-size icto method subs])
also
from Call.hyps(2)[of  $Ts'$ ] Call.premis wtes  $C$ 
have  $\vdash compEs2 es, compxEs2 es 0 (size ST+1) [::] ? \tau_e \# ? \tau_{s_{es}}$ 
   $last (? \tau_e \# ? \tau_{s_{es}}) = ? \tau_1$ 
  by(auto simp add:after-def)
also have  $(? \tau_e \# ? \tau_{s_{es}}) @ [? \tau] = ? \tau_e \# ? \tau_{s_{es}} @ [? \tau]$  by simp
also have  $\vdash compE2 e, compxE2 e 0 (size ST) [::] ? \tau \# ? \tau_{s_e} @ [? \tau_e]$ 
  using Call  $C$  by(auto simp add:after-def)
finally show  $?case$  using Call.premis  $C$ 
  by(simp add:after-def hyperUn-assoc shift-compxEs2 stack-xlift-compxEs2 del: compxEs2-stack-xlift-conv
compxEs2-size-conv)
next
case Seq thus  $?case$ 
  by(auto simp:after-def)
  (fastforce simp:wt-Push wt-Pop hyperUn-assoc
  intro:wt-instrs-app2 wt-instrs-Cons)
next
case (NewArray E A ST Ta e)
from  $\langle P, E \vdash 1 newA Ta[e] :: T \rangle$ 
have  $\vdash [NewArray Ta], [] [::] [ty_i' (Integer \# ST) E (A \sqcup \mathcal{A} e), ty_i' (Ta[] \# ST) E (A \sqcup \mathcal{A} e)]$ 
  by(auto simp:hyperset-defs  $ty_i'$ -def wt-defs  $ty_1$ -def)
with NewArray show  $?case$  by(auto simp: after-def intro: wt-instrs-app3)
next
case (ALen E A ST exp)
{ fix  $T$ 
  have  $\vdash [ALength], [] [::] [ty_i' (T[] \# ST) E (A \sqcup \mathcal{A} exp), ty_i' (Integer \# ST) E (A \sqcup \mathcal{A} exp)]$ 
  by(auto simp:hyperset-defs  $ty_i'$ -def wt-defs  $ty_1$ -def) }
with ALen show  $?case$  by(auto simp add: after-def)(rule wt-instrs-app2, auto)
next
case (AAcc E A ST a i)

```



```

from  $\langle P, E \vdash 1 \ a[i] :: T \rangle$  have  $wta: P, E \vdash 1 \ a :: T[]$  and  $wti: P, E \vdash 1 \ i :: Integer$  by auto
let  $?A1 = A \sqcup \mathcal{A} \ a$  let  $?A2 = ?A1 \sqcup \mathcal{A} \ i$ 
let  $? \tau = ty_i' \ ST \ E \ A$  let  $? \tau sa = compT \ E \ A \ ST \ a$ 
let  $? \tau 1 = ty_i' \ (T[] \# ST) \ E \ ?A1$  let  $? \tau si = compT \ E \ ?A1 \ (T[] \# ST) \ i$ 
let  $? \tau 2 = ty_i' \ (Integer \# T[] \# ST) \ E \ ?A2$  let  $? \tau' = ty_i' \ (T \# ST) \ E \ ?A2$ 
have  $\vdash [ALoad], [] \ [::] \ [? \tau 2, ? \tau']$  by (auto simp add: ty_i'-def wt-defs)
also from  $AAcc.hyps(2)[of \ Integer]$   $AAcc.premis \ wti \ wta$ 
have  $\vdash compE2 \ i, compxE2 \ i \ 0 \ (size \ ST + 1) \ [::] \ ? \tau 1 \# ? \tau si @ [? \tau 2]$ 
by (auto simp add: after-def)
also from  $wta \ AAcc$  have  $\vdash compE2 \ a, compxE2 \ a \ 0 \ (size \ ST) \ [::] \ ? \tau \# ? \tau sa @ [? \tau 1]$ 
by (auto simp add: after-def)
finally show  $?case$  using  $wta \ wti \ \langle P, E \vdash 1 \ a[i] :: T \rangle$  by (simp add: after-def hyperUn-assoc)
next
case ( $AAss \ E \ A \ ST \ a \ i \ e$ )
note  $wt = \langle P, E \vdash 1 \ a[i] := e :: T \rangle$ 
then obtain  $Ta \ U$  where  $wta: P, E \vdash 1 \ a :: Ta[]$  and  $wti: P, E \vdash 1 \ i :: Integer$ 
and  $wte: P, E \vdash 1 \ e :: U$  and  $U: P \vdash U \leq Ta$  and  $[simp]: T = Void$  by auto
let  $?A1 = A \sqcup \mathcal{A} \ a$  let  $?A2 = ?A1 \sqcup \mathcal{A} \ i$  let  $?A3 = ?A2 \sqcup \mathcal{A} \ e$ 
let  $? \tau = ty_i' \ ST \ E \ A$  let  $? \tau sa = compT \ E \ A \ ST \ a$ 
let  $? \tau 1 = ty_i' \ (Ta[] \# ST) \ E \ ?A1$  let  $? \tau si = compT \ E \ ?A1 \ (Ta[] \# ST) \ i$ 
let  $? \tau 2 = ty_i' \ (Integer \# Ta[] \# ST) \ E \ ?A2$  let  $? \tau se = compT \ E \ ?A2 \ (Integer \# Ta[] \# ST) \ e$ 
let  $? \tau 3 = ty_i' \ (U \# Integer \# Ta[] \# ST) \ E \ ?A3$  let  $? \tau 4 = ty_i' \ ST \ E \ ?A3$ 
let  $? \tau' = ty_i' \ (Void \# ST) \ E \ ?A3$ 
from  $\langle length \ ST + max-stack \ (a[i] := e) \leq mxs \rangle$ 
have  $\vdash [AStore, Push \ Unit], [] \ [::] \ [? \tau 3, ? \tau 4, ? \tau']$ 
by (auto simp add: ty_i'-def wt-defs nth-Cons split: nat.split)
also from  $AAss.hyps(3)[of \ U]$   $wte \ AAss.premis \ wta \ wti$ 
have  $\vdash compE2 \ e, compxE2 \ e \ 0 \ (size \ ST + 2) \ [::] \ ? \tau 2 \# ? \tau se @ [? \tau 3]$ 
by (auto simp add: after-def)
also from  $AAss.hyps(2)[of \ Integer]$   $wti \ wta \ AAss.premis$ 
have  $\vdash compE2 \ i, compxE2 \ i \ 0 \ (size \ ST + 1) \ [::] \ ? \tau 1 \# ? \tau si @ [? \tau 2]$ 
by (auto simp add: after-def)
also from  $wta \ AAss$  have  $\vdash compE2 \ a, compxE2 \ a \ 0 \ (size \ ST) \ [::] \ ? \tau \# ? \tau sa @ [? \tau 1]$ 
by (auto simp add: after-def)
finally show  $?case$  using  $wta \ wti \ wte \ \langle P, E \vdash 1 \ a[i] := e :: T \rangle$ 
by (simp add: after-def hyperUn-assoc)
next
case ( $CompareAndSwap \ E \ A \ ST \ e1 \ D \ F \ e2 \ e3$ )
note  $wt = \langle P, E \vdash 1 \ e1 \cdot compareAndSwap(D \cdot F, \ e2, \ e3) :: T \rangle$ 
then obtain  $T1 \ T2 \ T3 \ C \ fm \ T'$  where  $[simp]: T = Boolean$ 
and  $wt1: P, E \vdash 1 \ e1 :: T1$  class-type-of'  $T1 = \lfloor C \rfloor \ P \vdash C \ sees \ F:T' \ (fm) \ in \ D \ volatile \ fm$ 
and  $wt2: P, E \vdash 1 \ e2 :: T2$   $P \vdash T2 \leq T'$  and  $wt3: P, E \vdash 1 \ e3 :: T3$   $P \vdash T3 \leq T'$ 
by auto
let  $?A1 = A \sqcup \mathcal{A} \ e1$  let  $?A2 = ?A1 \sqcup \mathcal{A} \ e2$  let  $?A3 = ?A2 \sqcup \mathcal{A} \ e3$ 
let  $? \tau = ty_i' \ ST \ E \ A$  let  $? \tau s1 = compT \ E \ A \ ST \ e1$ 
let  $? \tau 1 = ty_i' \ (T1 \# ST) \ E \ ?A1$  let  $? \tau s2 = compT \ E \ ?A1 \ (T1 \# ST) \ e2$ 
let  $? \tau 2 = ty_i' \ (T2 \# T1 \# ST) \ E \ ?A2$  let  $? \tau s3 = compT \ E \ ?A2 \ (T2 \# T1 \# ST) \ e3$ 
let  $? \tau 3 = ty_i' \ (T3 \# T2 \# T1 \# ST) \ E \ ?A3$ 
let  $? \tau' = ty_i' \ (Boolean \# ST) \ E \ ?A3$ 
from  $\langle length \ ST + max-stack \ (e1 \cdot compareAndSwap(D \cdot F, \ e2, \ e3)) \leq mxs \rangle$ 
have  $\vdash [CAS \ F \ D], [] \ [::] \ [? \tau 3, ? \tau']$  using  $wt1 \ wt2 \ wt3$ 
by (cases \ T1) (auto simp add: ty_i'-def wt-defs nth-Cons split: nat.split intro: sees-field-idemp widen-trans[OF
widen-array-object] dest: sees-field-decl-above)
also from  $CompareAndSwap.hyps(3)[of \ T3]$   $wt3 \ CompareAndSwap.premis \ wt1 \ wt2$ 

```

```

have  $\vdash \text{compE2 } e3, \text{compxE2 } e3 \ 0 \ (\text{size } ST+2) \ [\because] \ ?\tau2\#?\tau s3@[\tau3]$ 
  by(auto simp add: after-def)
also from CompareAndSwap.hyps(2)[of T2] wt2 wt1 CompareAndSwap.prems
have  $\vdash \text{compE2 } e2, \text{compxE2 } e2 \ 0 \ (\text{size } ST+1) \ [\because] \ ?\tau1\#?\tau s2@[\tau2]$ 
  by(auto simp add: after-def)
also from wt1 CompareAndSwap have  $\vdash \text{compE2 } e1, \text{compxE2 } e1 \ 0 \ (\text{size } ST) \ [\because] \ ?\tau\#?\tau s1@[\tau1]$ 
  by(auto simp add: after-def)
also have ty E (e1 • compareAndSwap(D • F, e2, e3)) = T using wt by(rule ty-def2)
ultimately show ?case using wt1 wt2 wt3
  by(simp add: after-def hyperUn-assoc)
next
  case (InSynchronized i a exp) thus ?case by auto
qed

end

lemma states-compP [simp]: states (compP f P) mxs mxl = states P mxs mxl
by (simp add: JVM-states-unfold)

lemma [simp]: appi (i, compP f P, pc, mpc, T,  $\tau$ ) = appi (i, P, pc, mpc, T,  $\tau$ )
proof –
  { fix ST LT
    have appi (i, compP f P, pc, mpc, T, (ST, LT)) = appi (i, P, pc, mpc, T, (ST, LT))
      proof(cases i)
        case (Invoke M n)
          have  $\bigwedge C \ Ts \ D. (\exists T \ m. \text{compP } f \ P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D) \longleftrightarrow (\exists T \ m. P \vdash C \text{ sees } M: Ts \rightarrow T = m \text{ in } D)$ 
            by(auto dest!: sees-method-compPD dest: sees-method-compP)
          with Invoke show ?thesis by clarsimp
        qed(simp-all) }
    thus ?thesis by(cases  $\tau$ ) simp
  }
qed

lemma [simp]: is-relevant-entry (compP f P) i = is-relevant-entry P i
apply (rule ext) +
apply (unfold is-relevant-entry-def)
apply (cases i)
apply auto
done

lemma [simp]: relevant-entries (compP f P) i pc xt = relevant-entries P i pc xt
by (simp add: relevant-entries-def)

lemma [simp]: app i (compP f P) mpc T pc mxl xt  $\tau$  = app i P mpc T pc mxl xt  $\tau$ 
apply (simp add: app-def xcpt-app-def eff-def xcpt-eff-def norm-eff-def)
apply (fastforce simp add: image-def)
done

lemma [simp]: app i P mpc T pc mxl xt  $\tau \implies \text{eff } i \ (\text{compP } f \ P) \ pc \ xt \ \tau = \text{eff } i \ P \ pc \ xt \ \tau$ 
apply (clarsimp simp add: eff-def norm-eff-def xcpt-eff-def app-def)
apply (cases i)
apply (auto)
done

```

**lemma**  $[simp]: widen (compP f P) = widen P$   
**apply** (rule ext)+  
**apply** (simp)  
**done**

**lemma**  $[simp]: compP f P \vdash \tau \leq' \tau' = P \vdash \tau \leq' \tau'$   
**by** (simp add: sup-state-opt-def sup-state-def sup-ty-opt-def)

**lemma**  $[simp]: compP f P, T, mpc, mxl, xt \vdash i, pc :: \tau s = P, T, mpc, mxl, xt \vdash i, pc :: \tau s$   
**by** (simp add: wt-instr-def cong: conj-cong)

**declare**  $TC0.compT-sizes[simp]$   $TC1.ty-def2[OF TC1.intro, simp]$

**lemma** *compT-method*:

**fixes**  $e$  **and**  $A$  **and**  $C$  **and**  $Ts$  **and**  $mxl_0$   
**defines**  $[simp]: E \equiv Class C \# Ts$   
**and**  $[simp]: A \equiv [\{..size Ts\}]$   
**and**  $[simp]: A' \equiv A \sqcup \mathcal{A} e$   
**and**  $[simp]: mxs \equiv max-stack e$   
**and**  $[simp]: mxl_0 \equiv max-vars e$   
**and**  $[simp]: mxl \equiv 1 + size Ts + mxl_0$

**assumes** *wf-prog*:  $wf-prog p P$

**shows**  $\llbracket P, E \vdash 1 e :: T; \mathcal{D} e A; \mathcal{B} e (size E); set E \subseteq types P; P \vdash T \leq T' \rrbracket \implies$   
 $wt-method (compP2 P) C Ts T' mxs mxl_0 (compE2 e @ [Return]) (compxE2 e 0 0)$   
 $(TC0.ty_i' mxl \llbracket E A \# TC0.compTa P mxl E A \rrbracket e)$

**using** *wf-prog*

**apply** (simp add: wt-method-def TC0.compTa-def TC0.after-def compP2-def compMb2-def)

**apply** (rule conjI)

**apply** (simp add: check-types-def TC0.OK-ty<sub>i</sub>'-in-statesI)

**apply** (rule conjI)

**apply** (frule WT1-is-type[OF wf-prog])

**apply** simp

**apply** (insert max-stack1[of e])

**apply** (fastforce intro!: TC0.OK-ty<sub>i</sub>'-in-statesI)

**apply** (erule (1) TC1.compT-states[OF TC1.intro])

**apply** simp

**apply** simp

**apply** simp

**apply** simp

**apply** (rule conjI)

**apply** (fastforce simp add: wt-start-def TC0.ty<sub>i</sub>'-def TC0.ty<sub>1</sub>-def list-all2-conv-all-nth nth-Cons split: nat.split dest: less-antisym)

**apply** (frule (1) TC3.compT-wt-instrs[OF TC3.intro[OF TC1.intro], where  $ST = []$  and  $mxs = max-stack e$  and  $mxl = 1 + size Ts + max-vars e$ ])

**apply** simp

**apply** simp

**apply** simp

**apply** simp

**apply** simp

**apply** (clarsimp simp: TC2.wt-instrs-def TC0.after-def)

**apply** (rule conjI)

**apply** (fastforce)

**apply** (clarsimp)

```

apply(drule (1) less-antisym)
apply(thin-tac  $\forall x. P\ x$  for P)
apply(clarsimp simp: TC2.wt-defs xcpt-app-pcs xcpt-eff-pcs TC0.tyi'-def)
done

```

```

definition compTP :: 'addr J1-prog  $\Rightarrow$  tyP
where
  compTP P C M  $\equiv$ 
    let (D,Ts,T,meth) = method P C M;
      e = the meth;
      E = Class C # Ts;
      A = [..];
      mxl = 1 + size Ts + max-vars e
    in (TC0.tyi' mxl [] E A # TC0.compTa P mxl E A [] e)

```

```

theorem wt-compTP-compP2:
  wf-J1-prog P  $\Longrightarrow$  wf-jvm-prog compTP P (compP2 P)
apply (simp add: wf-jvm-prog-phi-def compP2-def compMb2-def)
apply (rule wf-prog-compPI)
prefer 2 apply assumption
apply (clarsimp simp add: wf-mdecl-def)
apply (simp add: compTP-def)
apply (rule compT-method [simplified compP2-def compMb2-def, simplified])
apply assumption+
apply (drule (1) sees-wf-mdecl)
apply (simp add: wf-mdecl-def)
apply (fastforce intro: sees-method-is-class)
apply assumption
done

```

```

theorem wt-compP2:
  wf-J1-prog P  $\Longrightarrow$  wf-jvm-prog (compP2 P)
by(auto simp add: wf-jvm-prog-def intro: wt-compTP-compP2)

end

```

## 7.14 Unobservable steps for the JVM

```

theory JVMTau imports
  TypeComp
  ../JVM/JVMThreaded
  ../Framework/FWLTS
begin

declare nth-append [simp del]
declare Listn.lesub-list-impl-same-size[simp del]
declare listE-length [simp del]

declare match-ex-table-append-not-pcs[simp del]
  outside-pcs-not-matches-entry [simp del]

```

*outside-pcs-comp $\bar{x}$ E2-not-matches-entry* [simp del]  
*outside-pcs-comp $\bar{x}$ Es2-not-matches-entry* [simp del]

**context** *JVM-heap-base* **begin**

**primrec**  $\tau instr :: 'm prog \Rightarrow 'heap \Rightarrow 'addr val list \Rightarrow 'addr instr \Rightarrow bool$

**where**

$\tau instr P h stk (Load n) = True$   
 $\tau instr P h stk (Store n) = True$   
 $\tau instr P h stk (Push v) = True$   
 $\tau instr P h stk (New C) = False$   
 $\tau instr P h stk (NewArray T) = False$   
 $\tau instr P h stk ALoad = False$   
 $\tau instr P h stk AStore = False$   
 $\tau instr P h stk ALength = False$   
 $\tau instr P h stk (Getfield F D) = False$   
 $\tau instr P h stk (Putfield F D) = False$   
 $\tau instr P h stk (CAS F D) = False$   
 $\tau instr P h stk (Checkcast T) = True$   
 $\tau instr P h stk (Instanceof T) = True$   
 $\tau instr P h stk (Invoke M n) =$   
 $(n < length\ stk \wedge$   
 $(stk ! n = Null \vee$   
 $(\forall T Ts Tr D. typeof\_addr h (the\_Addr (stk ! n)) = \lfloor T \rfloor \longrightarrow P \vdash class\_type\_of\ T\ sees\ M:Ts \rightarrow Tr =$   
 $Native\ in\ D \longrightarrow \tau external\_defs\ D\ M)))$   
 $\tau instr P h stk Return = True$   
 $\tau instr P h stk Pop = True$   
 $\tau instr P h stk Dup = True$   
 $\tau instr P h stk Swap = True$   
 $\tau instr P h stk (BinOpInstr bop) = True$   
 $\tau instr P h stk (Goto i) = True$   
 $\tau instr P h stk (IfFalse i) = True$   
 $\tau instr P h stk ThrowExc = True$   
 $\tau instr P h stk MEnter = False$   
 $\tau instr P h stk MExit = False$

**inductive**  $\tau move2 :: 'm prog \Rightarrow 'heap \Rightarrow 'addr val list \Rightarrow 'addr expr1 \Rightarrow nat \Rightarrow 'addr option \Rightarrow bool$

**and**  $\tau moves2 :: 'm prog \Rightarrow 'heap \Rightarrow 'addr val list \Rightarrow 'addr expr1 list \Rightarrow nat \Rightarrow 'addr option \Rightarrow bool$

**for**  $P :: 'm prog$  **and**  $h :: 'heap$  **and**  $stk :: 'addr val list$

**where**

$\tau move2 xcp: pc < length (compE2 e) \Longrightarrow \tau move2 P h stk e pc \lfloor xcp \rfloor$   
 $\tau move2 NewArray: \tau move2 P h stk e pc xcp \Longrightarrow \tau move2 P h stk (newA\ T \lfloor e \rfloor) pc xcp$   
 $\tau move2 Cast: \tau move2 P h stk e pc xcp \Longrightarrow \tau move2 P h stk (Cast\ T\ e) pc xcp$   
 $\tau move2 CastRed: \tau move2 P h stk (Cast\ T\ e) (length (compE2 e)) None$   
 $\tau move2 InstanceOf: \tau move2 P h stk e pc xcp \Longrightarrow \tau move2 P h stk (e\ instanceof\ T) pc xcp$   
 $\tau move2 InstanceOfRed: \tau move2 P h stk (e\ instanceof\ T) (length (compE2 e)) None$   
 $\tau move2 Val: \tau move2 P h stk (Val\ v)\ 0\ None$   
 $\tau move2 BinOp1:$   
 $\tau move2 P h stk e1 pc xcp \Longrightarrow \tau move2 P h stk (e1 \ll bop \gg e2) pc xcp$

$\tau\text{move2BinOp2:}$   
 $\tau\text{move2 } P \ h \ stk \ e2 \ pc \ xcp \Longrightarrow \tau\text{move2 } P \ h \ stk \ (e1 \llbracket \text{bop} \rrbracket e2) \ (length \ (compE2 \ e1) + pc) \ xcp$   
 $\tau\text{move2BinOp:}$   
 $\tau\text{move2 } P \ h \ stk \ (e1 \llbracket \text{bop} \rrbracket e2) \ (length \ (compE2 \ e1) + length \ (compE2 \ e2)) \ None$

$\tau\text{move2Var:}$   
 $\tau\text{move2 } P \ h \ stk \ (Var \ V) \ 0 \ None$

$\tau\text{move2LAss:}$   
 $\tau\text{move2 } P \ h \ stk \ e \ pc \ xcp \Longrightarrow \tau\text{move2 } P \ h \ stk \ (V := e) \ pc \ xcp$   
 $\tau\text{move2LAssRed1:}$   
 $\tau\text{move2 } P \ h \ stk \ (V := e) \ (length \ (compE2 \ e)) \ None$   
 $\tau\text{move2LAssRed2: } \tau\text{move2 } P \ h \ stk \ (V := e) \ (Suc \ (length \ (compE2 \ e))) \ None$

$\tau\text{move2AAcc1: } \tau\text{move2 } P \ h \ stk \ a \ pc \ xcp \Longrightarrow \tau\text{move2 } P \ h \ stk \ (a[i]) \ pc \ xcp$   
 $\tau\text{move2AAcc2: } \tau\text{move2 } P \ h \ stk \ i \ pc \ xcp \Longrightarrow \tau\text{move2 } P \ h \ stk \ (a[i]) \ (length \ (compE2 \ a) + pc) \ xcp$

$\tau\text{move2AAss1: } \tau\text{move2 } P \ h \ stk \ a \ pc \ xcp \Longrightarrow \tau\text{move2 } P \ h \ stk \ (a[i] := e) \ pc \ xcp$   
 $\tau\text{move2AAss2: } \tau\text{move2 } P \ h \ stk \ i \ pc \ xcp \Longrightarrow \tau\text{move2 } P \ h \ stk \ (a[i] := e) \ (length \ (compE2 \ a) + pc) \ xcp$   
 $\tau\text{move2AAss3: } \tau\text{move2 } P \ h \ stk \ e \ pc \ xcp \Longrightarrow \tau\text{move2 } P \ h \ stk \ (a[i] := e) \ (length \ (compE2 \ a) + length \ (compE2 \ i) + pc) \ xcp$   
 $\tau\text{move2AAssRed: } \tau\text{move2 } P \ h \ stk \ (a[i] := e) \ (Suc \ (length \ (compE2 \ a) + length \ (compE2 \ i) + length \ (compE2 \ e))) \ None$

$\tau\text{move2ALength: } \tau\text{move2 } P \ h \ stk \ a \ pc \ xcp \Longrightarrow \tau\text{move2 } P \ h \ stk \ (a \cdot length) \ pc \ xcp$

$\tau\text{move2FAcc: } \tau\text{move2 } P \ h \ stk \ e \ pc \ xcp \Longrightarrow \tau\text{move2 } P \ h \ stk \ (e \cdot F\{D\}) \ pc \ xcp$

$\tau\text{move2FAss1: } \tau\text{move2 } P \ h \ stk \ e \ pc \ xcp \Longrightarrow \tau\text{move2 } P \ h \ stk \ (e \cdot F\{D\} := e') \ pc \ xcp$   
 $\tau\text{move2FAss2: } \tau\text{move2 } P \ h \ stk \ e' \ pc \ xcp \Longrightarrow \tau\text{move2 } P \ h \ stk \ (e \cdot F\{D\} := e') \ (length \ (compE2 \ e) + pc) \ xcp$   
 $\tau\text{move2FAssRed: } \tau\text{move2 } P \ h \ stk \ (e \cdot F\{D\} := e') \ (Suc \ (length \ (compE2 \ e) + length \ (compE2 \ e'))) \ None$

$\tau\text{move2CAS1: } \tau\text{move2 } P \ h \ stk \ e \ pc \ xcp \Longrightarrow \tau\text{move2 } P \ h \ stk \ (e \cdot compareAndSwap(D \cdot F, \ e', \ e'')) \ pc \ xcp$   
 $\tau\text{move2CAS2: } \tau\text{move2 } P \ h \ stk \ e' \ pc \ xcp \Longrightarrow \tau\text{move2 } P \ h \ stk \ (e \cdot compareAndSwap(D \cdot F, \ e', \ e'')) \ (length \ (compE2 \ e) + pc) \ xcp$   
 $\tau\text{move2CAS3: } \tau\text{move2 } P \ h \ stk \ e'' \ pc \ xcp \Longrightarrow \tau\text{move2 } P \ h \ stk \ (e \cdot compareAndSwap(D \cdot F, \ e', \ e'')) \ (length \ (compE2 \ e) + length \ (compE2 \ e') + pc) \ xcp$

$\tau\text{move2CallObj:}$   
 $\tau\text{move2 } P \ h \ stk \ obj \ pc \ xcp \Longrightarrow \tau\text{move2 } P \ h \ stk \ (obj \cdot M(ps)) \ pc \ xcp$   
 $\tau\text{move2CallParams:}$   
 $\tau\text{moves2 } P \ h \ stk \ ps \ pc \ xcp \Longrightarrow \tau\text{move2 } P \ h \ stk \ (obj \cdot M(ps)) \ (length \ (compE2 \ obj) + pc) \ xcp$   
 $\tau\text{move2Call:}$   
 $\llbracket length \ ps < length \ stk;$   
 $stk \ ! \ length \ ps = Null \vee$   
 $(\forall T \ Ts \ Tr \ D. \text{typeof-addr } h \ (the\text{-Addr} \ (stk \ ! \ length \ ps)) = \lfloor T \rfloor \longrightarrow P \vdash \text{class-type-of } T \text{ sees}$   
 $M : Ts \rightarrow Tr = \text{Native in } D \longrightarrow \tau\text{external-defs } D \ M) \rrbracket$   
 $\Longrightarrow \tau\text{move2 } P \ h \ stk \ (obj \cdot M(ps)) \ (length \ (compE2 \ obj) + length \ (compEs2 \ ps)) \ None$

$\tau\text{move2BlockSome1:}$

$\tau\text{move2 } P \ h \ stk \ \{ V:T=\lfloor v \rfloor; \ e \} \ 0 \ None$   
 $\tau\text{move2BlockSome2:}$   
 $\tau\text{move2 } P \ h \ stk \ \{ V:T=\lfloor v \rfloor; \ e \} \ (Suc \ 0) \ None$   
 $\tau\text{move2BlockSome:}$   
 $\tau\text{move2 } P \ h \ stk \ e \ pc \ xcp \implies \tau\text{move2 } P \ h \ stk \ \{ V:T=\lfloor v \rfloor; \ e \} \ (Suc \ (Suc \ pc)) \ xcp$   
 $\tau\text{move2BlockNone:}$   
 $\tau\text{move2 } P \ h \ stk \ e \ pc \ xcp \implies \tau\text{move2 } P \ h \ stk \ \{ V:T=None; \ e \} \ pc \ xcp$

$\tau\text{move2Sync1:}$   
 $\tau\text{move2 } P \ h \ stk \ o' \ pc \ xcp \implies \tau\text{move2 } P \ h \ stk \ (sync_V \ (o') \ e) \ pc \ xcp$   
 $\tau\text{move2Sync2:}$   
 $\tau\text{move2 } P \ h \ stk \ (sync_V \ (o') \ e) \ (length \ (compE2 \ o')) \ None$   
 $\tau\text{move2Sync3:}$   
 $\tau\text{move2 } P \ h \ stk \ (sync_V \ (o') \ e) \ (Suc \ (length \ (compE2 \ o'))) \ None$   
 $\tau\text{move2Sync4:}$   
 $\tau\text{move2 } P \ h \ stk \ e \ pc \ xcp \implies \tau\text{move2 } P \ h \ stk \ (sync_V \ (o') \ e) \ (Suc \ (Suc \ (Suc \ (length \ (compE2 \ o') \ + \ pc)))) \ xcp$   
 $\tau\text{move2Sync5:}$   
 $\tau\text{move2 } P \ h \ stk \ (sync_V \ (o') \ e) \ (Suc \ (Suc \ (Suc \ (length \ (compE2 \ o') \ + \ length \ (compE2 \ e))))) \ None$   
 $\tau\text{move2Sync6:}$   
 $\tau\text{move2 } P \ h \ stk \ (sync_V \ (o') \ e) \ (5 \ + \ length \ (compE2 \ o') \ + \ length \ (compE2 \ e)) \ None$   
 $\tau\text{move2Sync7:}$   
 $\tau\text{move2 } P \ h \ stk \ (sync_V \ (o') \ e) \ (6 \ + \ length \ (compE2 \ o') \ + \ length \ (compE2 \ e)) \ None$   
 $\tau\text{move2Sync8:}$   
 $\tau\text{move2 } P \ h \ stk \ (sync_V \ (o') \ e) \ (8 \ + \ length \ (compE2 \ o') \ + \ length \ (compE2 \ e)) \ None$

$\tau\text{move2InSync: } \tau\text{move2 } P \ h \ stk \ (insync_V \ (a) \ e) \ 0 \ None$

$\tau\text{move2Seq1:}$   
 $\tau\text{move2 } P \ h \ stk \ e \ pc \ xcp \implies \tau\text{move2 } P \ h \ stk \ (e;;e') \ pc \ xcp$   
 $\tau\text{move2SeqRed:}$   
 $\tau\text{move2 } P \ h \ stk \ (e;;e') \ (length \ (compE2 \ e)) \ None$   
 $\tau\text{move2Seq2:}$   
 $\tau\text{move2 } P \ h \ stk \ e' \ pc \ xcp \implies \tau\text{move2 } P \ h \ stk \ (e;;e') \ (Suc \ (length \ (compE2 \ e) \ + \ pc)) \ xcp$

$\tau\text{move2Cond:}$   
 $\tau\text{move2 } P \ h \ stk \ e \ pc \ xcp \implies \tau\text{move2 } P \ h \ stk \ (if \ (e) \ e1 \ else \ e2) \ pc \ xcp$   
 $\tau\text{move2CondRed:}$   
 $\tau\text{move2 } P \ h \ stk \ (if \ (e) \ e1 \ else \ e2) \ (length \ (compE2 \ e)) \ None$   
 $\tau\text{move2CondThen:}$   
 $\tau\text{move2 } P \ h \ stk \ e1 \ pc \ xcp$   
 $\implies \tau\text{move2 } P \ h \ stk \ (if \ (e) \ e1 \ else \ e2) \ (Suc \ (length \ (compE2 \ e) \ + \ pc)) \ xcp$   
 $\tau\text{move2CondThenExit:}$   
 $\tau\text{move2 } P \ h \ stk \ (if \ (e) \ e1 \ else \ e2) \ (Suc \ (length \ (compE2 \ e) \ + \ length \ (compE2 \ e1))) \ None$   
 $\tau\text{move2CondElse:}$   
 $\tau\text{move2 } P \ h \ stk \ e2 \ pc \ xcp$   
 $\implies \tau\text{move2 } P \ h \ stk \ (if \ (e) \ e1 \ else \ e2) \ (Suc \ (Suc \ (length \ (compE2 \ e) \ + \ length \ (compE2 \ e1) \ + \ pc))) \ xcp$

$\tau\text{move2While1:}$   
 $\tau\text{move2 } P \ h \ stk \ c \ pc \ xcp \implies \tau\text{move2 } P \ h \ stk \ (while \ (c) \ e) \ pc \ xcp$   
 $\tau\text{move2While2:}$   
 $\tau\text{move2 } P \ h \ stk \ e \ pc \ xcp \implies \tau\text{move2 } P \ h \ stk \ (while \ (c) \ e) \ (Suc \ (length \ (compE2 \ c) \ + \ pc)) \ xcp$

$\tau\text{move2While3}$ : — Jump back to condition  
 $\tau\text{move2 } P \ h \ stk \ (\text{while } (c) \ e) \ (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } c) + \text{length } (\text{compE2 } e)))) \ None$   
 $\tau\text{move2While4}$ : — last instruction: Push Unit  
 $\tau\text{move2 } P \ h \ stk \ (\text{while } (c) \ e) \ (\text{Suc } (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } c) + \text{length } (\text{compE2 } e)))) \ None$   
 $\tau\text{move2While5}$ : — IfFalse instruction  
 $\tau\text{move2 } P \ h \ stk \ (\text{while } (c) \ e) \ (\text{length } (\text{compE2 } c)) \ None$   
 $\tau\text{move2While6}$ : — Pop instruction  
 $\tau\text{move2 } P \ h \ stk \ (\text{while } (c) \ e) \ (\text{Suc } (\text{length } (\text{compE2 } c) + \text{length } (\text{compE2 } e))) \ None$

$\tau\text{move2Throw1}$ :  
 $\tau\text{move2 } P \ h \ stk \ e \ pc \ xcp \Longrightarrow \tau\text{move2 } P \ h \ stk \ (\text{throw } e) \ pc \ xcp$   
 $\tau\text{move2Throw2}$ :  
 $\tau\text{move2 } P \ h \ stk \ (\text{throw } e) \ (\text{length } (\text{compE2 } e)) \ None$

$\tau\text{move2Try1}$ :  
 $\tau\text{move2 } P \ h \ stk \ e \ pc \ xcp \Longrightarrow \tau\text{move2 } P \ h \ stk \ (\text{try } e \ \text{catch}(C \ V) \ e') \ pc \ xcp$   
 $\tau\text{move2TryJump}$ :  
 $\tau\text{move2 } P \ h \ stk \ (\text{try } e \ \text{catch}(C \ V) \ e') \ (\text{length } (\text{compE2 } e)) \ None$   
 $\tau\text{move2TryCatch2}$ :  
 $\tau\text{move2 } P \ h \ stk \ (\text{try } e \ \text{catch}(C \ V) \ e') \ (\text{Suc } (\text{length } (\text{compE2 } e))) \ None$   
 $\tau\text{move2Try2}$ :  
 $\tau\text{move2 } P \ h \ stk \ \{V:T=None; e'\} \ pc \ xcp$   
 $\Longrightarrow \tau\text{move2 } P \ h \ stk \ (\text{try } e \ \text{catch}(C \ V) \ e') \ (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e) + pc))) \ xcp$

$\tau\text{moves2Hd}$ :  
 $\tau\text{move2 } P \ h \ stk \ e \ pc \ xcp \Longrightarrow \tau\text{moves2 } P \ h \ stk \ (e \ \# \ es) \ pc \ xcp$   
 $\tau\text{moves2Tl}$ :  
 $\tau\text{moves2 } P \ h \ stk \ es \ pc \ xcp \Longrightarrow \tau\text{moves2 } P \ h \ stk \ (e \ \# \ es) \ (\text{length } (\text{compE2 } e) + pc) \ xcp$

**inductive-cases**  $\tau\text{move2-cases}$ :

$\tau\text{move2 } P \ h \ stk \ (\text{new } C) \ pc \ xcp$   
 $\tau\text{move2 } P \ h \ stk \ (\text{newA } T \ [e]) \ pc \ xcp$   
 $\tau\text{move2 } P \ h \ stk \ (\text{Cast } T \ e) \ pc \ xcp$   
 $\tau\text{move2 } P \ h \ stk \ (e \ \text{instanceof } T) \ pc \ xcp$   
 $\tau\text{move2 } P \ h \ stk \ (\text{Val } v) \ pc \ xcp$   
 $\tau\text{move2 } P \ h \ stk \ (\text{Var } V) \ pc \ xcp$   
 $\tau\text{move2 } P \ h \ stk \ (e1 \ \llbracket \text{bop} \rrbracket \ e2) \ pc \ xcp$   
 $\tau\text{move2 } P \ h \ stk \ (V := e) \ pc \ xcp$   
 $\tau\text{move2 } P \ h \ stk \ (e1 \ [e2]) \ pc \ xcp$   
 $\tau\text{move2 } P \ h \ stk \ (e1 \ [e2] := e3) \ pc \ xcp$   
 $\tau\text{move2 } P \ h \ stk \ (e1 \cdot \text{length}) \ pc \ xcp$   
 $\tau\text{move2 } P \ h \ stk \ (e1 \cdot F\{D\}) \ pc \ xcp$   
 $\tau\text{move2 } P \ h \ stk \ (e1 \cdot F\{D\} := e3) \ pc \ xcp$   
 $\tau\text{move2 } P \ h \ stk \ (e1 \cdot \text{compareAndSwap}(D \cdot F, \ e2, \ e3)) \ pc \ xcp$   
 $\tau\text{move2 } P \ h \ stk \ (e \cdot M(ps)) \ pc \ xcp$   
 $\tau\text{move2 } P \ h \ stk \ \{V:T=vo; e\} \ pc \ xcp$   
 $\tau\text{move2 } P \ h \ stk \ (\text{sync}_V \ (e1) \ e2) \ pc \ xcp$   
 $\tau\text{move2 } P \ h \ stk \ (e1 ;; e2) \ pc \ xcp$   
 $\tau\text{move2 } P \ h \ stk \ (\text{if } (e1) \ e2 \ \text{else } e3) \ pc \ xcp$   
 $\tau\text{move2 } P \ h \ stk \ (\text{while } (e1) \ e2) \ pc \ xcp$   
 $\tau\text{move2 } P \ h \ stk \ (\text{try } e1 \ \text{catch}(C \ V) \ e2) \ pc \ xcp$   
 $\tau\text{move2 } P \ h \ stk \ (\text{throw } e) \ pc \ xcp$

**lemma**  $\tau\text{moves2xcp}$ :  $pc < \text{length } (\text{compEs2 } es) \Longrightarrow \tau\text{moves2 } P \ h \ stk \ es \ pc \ [xcp]$



```

proof(induct es arbitrary: pc)
  case Nil thus ?case by simp
next
  case (Cons e es)
  note IH =  $\langle \wedge pc. pc < \text{length}(\text{compEs2 } es) \implies \tau\text{moves2 } P \ h \ stk \ es \ pc \ [xcp] \rangle$ 
  note pc =  $\langle pc < \text{length}(\text{compEs2 } (e \# es)) \rangle$ 
  show ?case
  proof(cases pc < length(compE2 e))
    case True
    thus ?thesis by(auto intro:  $\tau\text{moves2Hd}$   $\tau\text{move2xcp}$ )
  next
  case False
  with pc IH[of pc - length(compE2 e)]
  have  $\tau\text{moves2 } P \ h \ stk \ es \ (pc - \text{length}(\text{compE2 } e)) \ [xcp]$  by(simp)
  hence  $\tau\text{moves2 } P \ h \ stk \ (e \# es) \ (\text{length}(\text{compE2 } e) + (pc - \text{length}(\text{compE2 } e))) \ [xcp]$ 
    by(rule  $\tau\text{moves2Tl}$ )
  with False show ?thesis by simp
qed
qed

lemma  $\tau\text{move2-intros'}$ :
  shows  $\tau\text{move2CastRed'}$ :  $pc = \text{length}(\text{compE2 } e) \implies \tau\text{move2 } P \ h \ stk \ (\text{Cast } T \ e) \ pc \ \text{None}$ 
  and  $\tau\text{move2InstanceOfRed'}$ :  $pc = \text{length}(\text{compE2 } e) \implies \tau\text{move2 } P \ h \ stk \ (e \ \text{instanceof } T) \ pc \ \text{None}$ 
  and  $\tau\text{move2BinOp2'}$ :  $\llbracket \tau\text{move2 } P \ h \ stk \ e2 \ pc \ xcp; pc' = \text{length}(\text{compE2 } e1) + pc \rrbracket \implies \tau\text{move2 } P \ h \ stk \ (e1 \llcorner \text{bop} \llcorner e2) \ pc' \ xcp$ 
  and  $\tau\text{move2BinOp'}$ :  $pc = \text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2) \implies \tau\text{move2 } P \ h \ stk \ (e1 \llcorner \text{bop} \llcorner e2) \ pc \ \text{None}$ 
  and  $\tau\text{move2LAssRed1'}$ :  $pc = \text{length}(\text{compE2 } e) \implies \tau\text{move2 } P \ h \ stk \ (V := e) \ pc \ \text{None}$ 
  and  $\tau\text{move2LAssRed2'}$ :  $pc = \text{Suc}(\text{length}(\text{compE2 } e)) \implies \tau\text{move2 } P \ h \ stk \ (V := e) \ pc \ \text{None}$ 
  and  $\tau\text{move2AAcc2'}$ :  $\llbracket \tau\text{move2 } P \ h \ stk \ i \ pc \ xcp; pc' = \text{length}(\text{compE2 } a) + pc \rrbracket \implies \tau\text{move2 } P \ h \ stk \ (a[i]) \ pc' \ xcp$ 
  and  $\tau\text{move2AAss2'}$ :  $\llbracket \tau\text{move2 } P \ h \ stk \ i \ pc \ xcp; pc' = \text{length}(\text{compE2 } a) + pc \rrbracket \implies \tau\text{move2 } P \ h \ stk \ (a[i] := e) \ pc' \ xcp$ 
  and  $\tau\text{move2AAss3'}$ :  $\llbracket \tau\text{move2 } P \ h \ stk \ e \ pc \ xcp; pc' = \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + pc \rrbracket \implies \tau\text{move2 } P \ h \ stk \ (a[i] := e) \ pc' \ xcp$ 
  and  $\tau\text{move2AAssRed'}$ :  $pc = \text{Suc}(\text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + \text{length}(\text{compE2 } e)) \implies \tau\text{move2 } P \ h \ stk \ (a[i] := e) \ pc \ \text{None}$ 
  and  $\tau\text{move2FAss2'}$ :  $\llbracket \tau\text{move2 } P \ h \ stk \ e' \ pc \ xcp; pc' = \text{length}(\text{compE2 } e) + pc \rrbracket \implies \tau\text{move2 } P \ h \ stk \ (e \cdot F\{D\} := e') \ pc' \ xcp$ 
  and  $\tau\text{move2FAssRed'}$ :  $pc = \text{Suc}(\text{length}(\text{compE2 } e) + \text{length}(\text{compE2 } e')) \implies \tau\text{move2 } P \ h \ stk \ (e \cdot F\{D\} := e') \ pc \ \text{None}$ 
  and  $\tau\text{move2CAS2'}$ :  $\llbracket \tau\text{move2 } P \ h \ stk \ e2 \ pc \ xcp; pc' = \text{length}(\text{compE2 } e1) + pc \rrbracket \implies \tau\text{move2 } P \ h \ stk \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ pc' \ xcp$ 
  and  $\tau\text{move2CAS3'}$ :  $\llbracket \tau\text{move2 } P \ h \ stk \ e3 \ pc \ xcp; pc' = \text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2) + pc \rrbracket \implies \tau\text{move2 } P \ h \ stk \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ pc' \ xcp$ 
  and  $\tau\text{move2CallParams'}$ :  $\llbracket \tau\text{moves2 } P \ h \ stk \ ps \ pc \ xcp; pc' = \text{length}(\text{compE2 } obj) + pc \rrbracket \implies \tau\text{move2 } P \ h \ stk \ (obj \cdot M(ps)) \ pc' \ xcp$ 
  and  $\tau\text{move2Call'}$ :  $\llbracket pc = \text{length}(\text{compE2 } obj) + \text{length}(\text{compEs2 } ps); \text{length } ps < \text{length } stk;$ 
     $stk \ ! \ \text{length } ps = \text{Null} \ \vee$ 
     $(\forall T \ Ts \ Tr \ D. \ \text{typeof-addr } h \ (\text{the-Addr } (stk \ ! \ \text{length } ps)) = \lfloor T \rfloor \longrightarrow P \vdash \text{class-type-of } T \ \text{sees } M:Ts \rightarrow Tr = \text{Native in } D \longrightarrow \tau\text{external-defs } D \ M) \rrbracket$ 
     $\implies \tau\text{move2 } P \ h \ stk \ (obj \cdot M(ps)) \ pc \ \text{None}$ 
  and  $\tau\text{move2BlockSome2}$ :  $pc = \text{Suc } 0 \implies \tau\text{move2 } P \ h \ stk \ \{V:T=\lfloor v \rfloor; e\} \ pc \ \text{None}$ 
  and  $\tau\text{move2BlockSome'}$ :  $\llbracket \tau\text{move2 } P \ h \ stk \ e \ pc \ xcp; pc' = \text{Suc}(\text{Suc } pc) \rrbracket \implies \tau\text{move2 } P \ h \ stk$ 

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$\{V:T=[v]; e\} pc' xcp$   
**and**  $\tau move2Sync2'$ :  $pc = length (compE2\ o') \implies \tau move2\ P\ h\ stk\ (sync_V\ (o')\ e)\ pc\ None$   
**and**  $\tau move2Sync3'$ :  $pc = Suc\ (length\ (compE2\ o')) \implies \tau move2\ P\ h\ stk\ (sync_V\ (o')\ e)\ pc\ None$   
**and**  $\tau move2Sync4'$ :  $\llbracket \tau move2\ P\ h\ stk\ e\ pc\ xcp; pc' = Suc\ (Suc\ (Suc\ (length\ (compE2\ o') + pc))) \rrbracket \implies \tau move2\ P\ h\ stk\ (sync_V\ (o')\ e)\ pc' xcp$   
**and**  $\tau move2Sync5'$ :  $pc = Suc\ (Suc\ (Suc\ (length\ (compE2\ o') + length\ (compE2\ e)))) \implies \tau move2\ P\ h\ stk\ (sync_V\ (o')\ e)\ pc\ None$   
**and**  $\tau move2Sync6'$ :  $pc = 5 + length\ (compE2\ o') + length\ (compE2\ e) \implies \tau move2\ P\ h\ stk\ (sync_V\ (o')\ e)\ pc\ None$   
**and**  $\tau move2Sync7'$ :  $pc = 6 + length\ (compE2\ o') + length\ (compE2\ e) \implies \tau move2\ P\ h\ stk\ (sync_V\ (o')\ e)\ pc\ None$   
**and**  $\tau move2Sync8'$ :  $pc = 8 + length\ (compE2\ o') + length\ (compE2\ e) \implies \tau move2\ P\ h\ stk\ (sync_V\ (o')\ e)\ pc\ None$   
**and**  $\tau move2SeqRed'$ :  $pc = length\ (compE2\ e) \implies \tau move2\ P\ h\ stk\ (e;;e')\ pc\ None$   
**and**  $\tau move2Seq2'$ :  $\llbracket \tau move2\ P\ h\ stk\ e'\ pc\ xcp; pc' = Suc\ (length\ (compE2\ e) + pc) \rrbracket \implies \tau move2\ P\ h\ stk\ (e;;e')\ pc' xcp$   
**and**  $\tau move2CondRed'$ :  $pc = length\ (compE2\ e) \implies \tau move2\ P\ h\ stk\ (if\ (e)\ e1\ else\ e2)\ pc\ None$   
**and**  $\tau move2CondThen'$ :  $\llbracket \tau move2\ P\ h\ stk\ e1\ pc\ xcp; pc' = Suc\ (length\ (compE2\ e) + pc) \rrbracket \implies \tau move2\ P\ h\ stk\ (if\ (e)\ e1\ else\ e2)\ pc' xcp$   
**and**  $\tau move2CondThenExit'$ :  $pc = Suc\ (length\ (compE2\ e) + length\ (compE2\ e1)) \implies \tau move2\ P\ h\ stk\ (if\ (e)\ e1\ else\ e2)\ pc\ None$   
**and**  $\tau move2CondElse'$ :  $\llbracket \tau move2\ P\ h\ stk\ e2\ pc\ xcp; pc' = Suc\ (Suc\ (length\ (compE2\ e) + length\ (compE2\ e1) + pc)) \rrbracket \implies \tau move2\ P\ h\ stk\ (if\ (e)\ e1\ else\ e2)\ pc' xcp$   
**and**  $\tau move2While2'$ :  $\llbracket \tau move2\ P\ h\ stk\ e\ pc\ xcp; pc' = Suc\ (length\ (compE2\ c) + pc) \rrbracket \implies \tau move2\ P\ h\ stk\ (while\ (c)\ e)\ pc' xcp$   
**and**  $\tau move2While3'$ :  $pc = Suc\ (Suc\ (length\ (compE2\ c) + length\ (compE2\ e))) \implies \tau move2\ P\ h\ stk\ (while\ (c)\ e)\ pc\ None$   
**and**  $\tau move2While4'$ :  $pc = Suc\ (Suc\ (Suc\ (length\ (compE2\ c) + length\ (compE2\ e)))) \implies \tau move2\ P\ h\ stk\ (while\ (c)\ e)\ pc\ None$   
**and**  $\tau move2While5'$ :  $pc = length\ (compE2\ c) \implies \tau move2\ P\ h\ stk\ (while\ (c)\ e)\ pc\ None$   
**and**  $\tau move2While6'$ :  $pc = Suc\ (length\ (compE2\ c) + length\ (compE2\ e)) \implies \tau move2\ P\ h\ stk\ (while\ (c)\ e)\ pc\ None$   
**and**  $\tau move2Throw2'$ :  $pc = length\ (compE2\ e) \implies \tau move2\ P\ h\ stk\ (throw\ e)\ pc\ None$   
**and**  $\tau move2TryJump'$ :  $pc = length\ (compE2\ e) \implies \tau move2\ P\ h\ stk\ (try\ e\ catch\ (C\ V)\ e')\ pc\ None$   
**and**  $\tau move2TryCatch2'$ :  $pc = Suc\ (length\ (compE2\ e)) \implies \tau move2\ P\ h\ stk\ (try\ e\ catch\ (C\ V)\ e')\ pc\ None$   
**and**  $\tau move2Try2'$ :  $\llbracket \tau move2\ P\ h\ stk\ \{V:T=None; e'\}\ pc\ xcp; pc' = Suc\ (Suc\ (length\ (compE2\ e) + pc)) \rrbracket \implies \tau move2\ P\ h\ stk\ (try\ e\ catch\ (C\ V)\ e')\ pc' xcp$   
**and**  $\tau moves2Tl'$ :  $\llbracket \tau moves2\ P\ h\ stk\ es\ pc\ xcp; pc' = length\ (compE2\ e) + pc \rrbracket \implies \tau moves2\ P\ h\ stk\ (e\ \# es)\ pc' xcp$   
**apply**(blast intro:  $\tau move2$ - $\tau moves2.intros$ )+  
**done**

**lemma**  $\tau move2$ -iff:  $\tau move2\ P\ h\ stk\ e\ pc\ xcp \longleftrightarrow pc < length\ (compE2\ e) \wedge (xcp = None \longrightarrow \tau instr\ P\ h\ stk\ (compE2\ e\ !\ pc))$  (**is** ?lhs1  $\longleftrightarrow$  ?rhs1)

**and**  $\tau moves2$ -iff:  $\tau moves2\ P\ h\ stk\ es\ pc\ xcp \longleftrightarrow pc < length\ (compEs2\ es) \wedge (xcp = None \longrightarrow \tau instr\ P\ h\ stk\ (compEs2\ es\ !\ pc))$  (**is** ?lhs2  $\longleftrightarrow$  ?rhs2)

**proof** –

**have** rhs1lhs1:  $\llbracket \tau instr\ P\ h\ stk\ (compE2\ e\ !\ pc); pc < length\ (compE2\ e) \rrbracket \implies \tau move2\ P\ h\ stk\ e\ pc\ None$

**and** rhs2lhs2:  $\llbracket \tau instr\ P\ h\ stk\ (compEs2\ es\ !\ pc); pc < length\ (compEs2\ es) \rrbracket \implies \tau moves2\ P\ h\ stk\ es\ pc\ None$

**apply**(*induct e and es arbitrary: pc and pc rule: compE2.induct compEs2.induct*)  
**apply**(*force intro:  $\tau$ move2- $\tau$ moves2.intros  $\tau$ move2-intros' simp add: nth-append nth-Cons' not-less-eq*  
*split: if-split-asm*) +  
**done**

{ **assume**  $pc < \text{length} (\text{compE2 } e) \text{ xcp} \neq \text{None}$   
**hence**  $\tau\text{move2 } P \ h \ stk \ e \ pc \ xcp$  **by**(*auto intro:  $\tau$ move2xcp*) }  
**with**  $rhs1lhs1$  **have**  $?rhs1 \implies ?lhs1$  **by**(*cases xcp*) *auto*  
**moreover** {  
**assume**  $pc < \text{length} (\text{compEs2 } es) \text{ xcp} \neq \text{None}$   
**hence**  $\tau\text{moves2 } P \ h \ stk \ es \ pc \ xcp$  **by**(*auto intro:  $\tau$ moves2xcp*) }  
**with**  $rhs2lhs2$  **have**  $?rhs2 \implies ?lhs2$  **by**(*cases xcp*) *auto*  
**moreover** **have**  $?lhs1 \implies ?rhs1$  **and**  $?lhs2 \implies ?rhs2$   
**by**(*induct rule:  $\tau$ move2- $\tau$ moves2.inducts*)(*fastforce simp add: nth-append eval-nat-numeral*) +  
**ultimately show**  $?lhs1 \longleftrightarrow ?rhs1 \ ?lhs2 \longleftrightarrow ?rhs2$  **by** *blast* +  
**qed**

**lemma**  $\tau\text{move2-pc-length-compE2}$ :  $\tau\text{move2 } P \ h \ stk \ e \ pc \ xcp \implies pc < \text{length} (\text{compE2 } e)$   
**and**  $\tau\text{moves2-pc-length-compEs2}$ :  $\tau\text{moves2 } P \ h \ stk \ es \ pc \ xcp \implies pc < \text{length} (\text{compEs2 } es)$   
**by**(*simp-all add:  $\tau$ move2-iff  $\tau$ moves2-iff*)

**lemma**  $\tau\text{move2-pc-length-compE2-conv}$ :  $pc \geq \text{length} (\text{compE2 } e) \implies \neg \tau\text{move2 } P \ h \ stk \ e \ pc \ xcp$   
**by**(*auto dest:  $\tau$ move2-pc-length-compE2*)

**lemma**  $\tau\text{moves2-pc-length-compEs2-conv}$ :  $pc \geq \text{length} (\text{compEs2 } es) \implies \neg \tau\text{moves2 } P \ h \ stk \ es \ pc \ xcp$   
**by**(*auto dest:  $\tau$ moves2-pc-length-compEs2*)

**lemma**  $\tau\text{moves2-append}$  [*elim*]:  
 $\tau\text{moves2 } P \ h \ stk \ es \ pc \ xcp \implies \tau\text{moves2 } P \ h \ stk \ (es @ es') \ pc \ xcp$   
**by**(*auto simp add:  $\tau$ moves2-iff nth-append*)

**lemma**  $\text{append-}\tau\text{moves2}$ :  
 $\tau\text{moves2 } P \ h \ stk \ es \ pc \ xcp \implies \tau\text{moves2 } P \ h \ stk \ (es' @ es) (\text{length} (\text{compEs2 } es') + pc) \ xcp$   
**by**(*simp add:  $\tau$ moves2-iff*)

**lemma** [*dest*]:  
**shows**  $\tau\text{move2-NewArrayD}$ :  $\llbracket \tau\text{move2 } P \ h \ stk \ (\text{newA } T [e]) \ pc \ xcp; pc < \text{length} (\text{compE2 } e) \rrbracket \implies$   
 $\tau\text{move2 } P \ h \ stk \ e \ pc \ xcp$   
**and**  $\tau\text{move2-CastD}$ :  $\llbracket \tau\text{move2 } P \ h \ stk \ (\text{Cast } T \ e) \ pc \ xcp; pc < \text{length} (\text{compE2 } e) \rrbracket \implies \tau\text{move2 } P$   
 $h \ stk \ e \ pc \ xcp$   
**and**  $\tau\text{move2-InstanceOfD}$ :  $\llbracket \tau\text{move2 } P \ h \ stk \ (e \ \text{instanceof } T) \ pc \ xcp; pc < \text{length} (\text{compE2 } e) \rrbracket \implies$   
 $\tau\text{move2 } P \ h \ stk \ e \ pc \ xcp$   
**and**  $\tau\text{move2-BinOp1D}$ :  $\llbracket \tau\text{move2 } P \ h \ stk \ (e1 \ \text{«bop» } e2) \ pc' \ xcp'; pc' < \text{length} (\text{compE2 } e1) \rrbracket \implies$   
 $\tau\text{move2 } P \ h \ stk \ e1 \ pc' \ xcp'$   
**and**  $\tau\text{move2-BinOp2D}$ :  
 $\llbracket \tau\text{move2 } P \ h \ stk \ (e1 \ \text{«bop» } e2) (\text{length} (\text{compE2 } e1) + pc') \ xcp'; pc' < \text{length} (\text{compE2 } e2) \rrbracket \implies$   
 $\tau\text{move2 } P \ h \ stk \ e2 \ pc' \ xcp'$   
**and**  $\tau\text{move2-LAssD}$ :  $\llbracket \tau\text{move2 } P \ h \ stk \ (V := e) \ pc \ xcp; pc < \text{length} (\text{compE2 } e) \rrbracket \implies \tau\text{move2 } P \ h$   
 $stk \ e \ pc \ xcp$   
**and**  $\tau\text{move2-AAccD1}$ :  $\llbracket \tau\text{move2 } P \ h \ stk \ (a[i]) \ pc \ xcp; pc < \text{length} (\text{compE2 } a) \rrbracket \implies \tau\text{move2 } P \ h$   
 $stk \ a \ pc \ xcp$   
**and**  $\tau\text{move2-AAccD2}$ :  $\llbracket \tau\text{move2 } P \ h \ stk \ (a[i]) (\text{length} (\text{compE2 } a) + pc) \ xcp; pc < \text{length} (\text{compE2}$   
 $i) \rrbracket \implies \tau\text{move2 } P \ h \ stk \ i \ pc \ xcp$   
**and**  $\tau\text{move2-AAssD1}$ :  $\llbracket \tau\text{move2 } P \ h \ stk \ (a[i] := e) \ pc \ xcp; pc < \text{length} (\text{compE2 } a) \rrbracket \implies \tau\text{move2}$

$P \ h \ stk \ a \ pc \ xcp$

**and**  $\tau move2-AAssD2$ :  $\llbracket \tau move2 \ P \ h \ stk \ (a[i] := e) \ (length \ (compE2 \ a) + pc) \ xcp; \ pc < length \ (compE2 \ i) \rrbracket \implies \tau move2 \ P \ h \ stk \ i \ pc \ xcp$

**and**  $\tau move2-AAssD3$ :

$\llbracket \tau move2 \ P \ h \ stk \ (a[i] := e) \ (length \ (compE2 \ a) + length \ (compE2 \ i) + pc) \ xcp; \ pc < length \ (compE2 \ e) \rrbracket \implies \tau move2 \ P \ h \ stk \ e \ pc \ xcp$

**and**  $\tau move2-ALengthD$ :  $\llbracket \tau move2 \ P \ h \ stk \ (a.length) \ pc \ xcp; \ pc < length \ (compE2 \ a) \rrbracket \implies \tau move2 \ P \ h \ stk \ a \ pc \ xcp$

**and**  $\tau move2-FAccD$ :  $\llbracket \tau move2 \ P \ h \ stk \ (e.F\{D\}) \ pc \ xcp; \ pc < length \ (compE2 \ e) \rrbracket \implies \tau move2 \ P \ h \ stk \ e \ pc \ xcp$

**and**  $\tau move2-FAssD1$ :  $\llbracket \tau move2 \ P \ h \ stk \ (e.F\{D\} := e') \ pc \ xcp; \ pc < length \ (compE2 \ e) \rrbracket \implies \tau move2 \ P \ h \ stk \ e \ pc \ xcp$

**and**  $\tau move2-FAssD2$ :  $\llbracket \tau move2 \ P \ h \ stk \ (e.F\{D\} := e') \ (length \ (compE2 \ e) + pc) \ xcp; \ pc < length \ (compE2 \ e') \rrbracket \implies \tau move2 \ P \ h \ stk \ e' \ pc \ xcp$

**and**  $\tau move2-CASD1$ :  $\llbracket \tau move2 \ P \ h \ stk \ (e1.compareAndSwap(D.F, e2, e3)) \ pc \ xcp; \ pc < length \ (compE2 \ e1) \rrbracket \implies \tau move2 \ P \ h \ stk \ e1 \ pc \ xcp$

**and**  $\tau move2-CASD2$ :  $\llbracket \tau move2 \ P \ h \ stk \ (e1.compareAndSwap(D.F, e2, e3)) \ (length \ (compE2 \ e1) + pc) \ xcp; \ pc < length \ (compE2 \ e2) \rrbracket \implies \tau move2 \ P \ h \ stk \ e2 \ pc \ xcp$

**and**  $\tau move2-CASD3$ :

$\llbracket \tau move2 \ P \ h \ stk \ (e1.compareAndSwap(D.F, e2, e3)) \ (length \ (compE2 \ e1) + length \ (compE2 \ e2) + pc) \ xcp; \ pc < length \ (compE2 \ e3) \rrbracket \implies \tau move2 \ P \ h \ stk \ e3 \ pc \ xcp$

**and**  $\tau move2-CallObjD$ :  $\llbracket \tau move2 \ P \ h \ stk \ (e.M(es)) \ pc \ xcp; \ pc < length \ (compE2 \ e) \rrbracket \implies \tau move2 \ P \ h \ stk \ e \ pc \ xcp$

**and**  $\tau move2-BlockNoneD$ :  $\tau move2 \ P \ h \ stk \ \{V:T=None; e\} \ pc \ xcp \implies \tau move2 \ P \ h \ stk \ e \ pc \ xcp$

**and**  $\tau move2-BlockSomeD$ :  $\tau move2 \ P \ h \ stk \ \{V:T=[v]; e\} \ (Suc \ (Suc \ pc)) \ xcp \implies \tau move2 \ P \ h \ stk \ e \ pc \ xcp$

**and**  $\tau move2-sync1D$ :  $\llbracket \tau move2 \ P \ h \ stk \ (sync_V \ (o') \ e) \ pc \ xcp; \ pc < length \ (compE2 \ o') \rrbracket \implies \tau move2 \ P \ h \ stk \ o' \ pc \ xcp$

**and**  $\tau move2-sync2D$ :

$\llbracket \tau move2 \ P \ h \ stk \ (sync_V \ (o') \ e) \ (Suc \ (Suc \ (Suc \ (length \ (compE2 \ o') + pc)))) \ xcp; \ pc < length \ (compE2 \ e) \rrbracket \implies \tau move2 \ P \ h \ stk \ e \ pc \ xcp$

**and**  $\tau move2-Seq1D$ :  $\llbracket \tau move2 \ P \ h \ stk \ (e1;; e2) \ pc \ xcp; \ pc < length \ (compE2 \ e1) \rrbracket \implies \tau move2 \ P \ h \ stk \ e1 \ pc \ xcp$

**and**  $\tau move2-Seq2D$ :  $\tau move2 \ P \ h \ stk \ (e1;; e2) \ (Suc \ (length \ (compE2 \ e1) + pc')) \ xcp' \implies \tau move2 \ P \ h \ stk \ e2 \ pc' \ xcp'$

**and**  $\tau move2-IfCondD$ :  $\llbracket \tau move2 \ P \ h \ stk \ (if \ (e) \ e1 \ else \ e2) \ pc \ xcp; \ pc < length \ (compE2 \ e) \rrbracket \implies \tau move2 \ P \ h \ stk \ e \ pc \ xcp$

**and**  $\tau move2-IfThenD$ :

$\llbracket \tau move2 \ P \ h \ stk \ (if \ (e) \ e1 \ else \ e2) \ (Suc \ (length \ (compE2 \ e) + pc')) \ xcp'; \ pc' < length \ (compE2 \ e1) \rrbracket \implies \tau move2 \ P \ h \ stk \ e1 \ pc' \ xcp'$

**and**  $\tau move2-IfElseD$ :

$\tau move2 \ P \ h \ stk \ (if \ (e) \ e1 \ else \ e2) \ (Suc \ (Suc \ (length \ (compE2 \ e) + length \ (compE2 \ e1) + pc')) \ xcp' \implies \tau move2 \ P \ h \ stk \ e2 \ pc' \ xcp'$

**and**  $\tau move2-WhileCondD$ :  $\llbracket \tau move2 \ P \ h \ stk \ (while \ (c) \ b) \ pc \ xcp; \ pc < length \ (compE2 \ c) \rrbracket \implies \tau move2 \ P \ h \ stk \ c \ pc \ xcp$

**and**  $\tau move2-ThrowD$ :  $\llbracket \tau move2 \ P \ h \ stk \ (throw \ e) \ pc \ xcp; \ pc < length \ (compE2 \ e) \rrbracket \implies \tau move2 \ P \ h \ stk \ e \ pc \ xcp$

**and**  $\tau move2-Try1D$ :  $\llbracket \tau move2 \ P \ h \ stk \ (try \ e1 \ catch \ (C' \ V) \ e2) \ pc \ xcp; \ pc < length \ (compE2 \ e1) \rrbracket \implies \tau move2 \ P \ h \ stk \ e1 \ pc \ xcp$

**apply**(*auto elim!*:  $\tau move2-cases$  *intro*:  $\tau move2xcp$  *dest*:  $\tau move2-pc-length-compE2$ )

**done**

**lemma**  $\tau move2-Invoke$ :

$\llbracket \tau move2 \ P \ h \ stk \ e \ pc \ None; \ compE2 \ e ! \ pc = Invoke \ M \ n \rrbracket$

$\implies n < \text{length } \text{stk} \wedge (\text{stk} ! n = \text{Null} \vee (\forall T \ Ts \ Tr \ D. \text{typeof-addr } h \ (\text{the-Addr } (\text{stk} ! n)) = \lfloor T \rfloor \implies P \vdash \text{class-type-of } T \text{ sees } M:Ts \rightarrow Tr = \text{Native in } D \longrightarrow \tau \text{external-defs } D \ M))$

**and**  $\tau \text{moves2-Invoke}$ :

$\llbracket \tau \text{moves2 } P \ h \ \text{stk} \ es \ pc \ \text{None}; \text{compEs2 } es ! \ pc = \text{Invoke } M \ n \rrbracket$

$\implies n < \text{length } \text{stk} \wedge (\text{stk} ! n = \text{Null} \vee (\forall T \ C \ Ts \ Tr \ D. \text{typeof-addr } h \ (\text{the-Addr } (\text{stk} ! n)) = \lfloor T \rfloor \implies P \vdash \text{class-type-of } T \text{ sees } M:Ts \rightarrow Tr = \text{Native in } D \longrightarrow \tau \text{external-defs } D \ M))$

**by**( $\text{simp-all add: } \tau \text{move2-iff } \tau \text{moves2-iff split-beta}$ )

**lemmas**  $\tau \text{move2-compE2-not-Invoke} = \tau \text{move2-Invoke}$

**lemmas**  $\tau \text{moves2-compEs2-not-Invoke} = \tau \text{moves2-Invoke}$

**lemma**  $\tau \text{move2-blocks1 } [\text{simp}]$ :

$\tau \text{move2 } P \ h \ \text{stk} \ (\text{blocks1 } n \ Ts \ \text{body}) \ pc' \ xcp' = \tau \text{move2 } P \ h \ \text{stk} \ \text{body} \ pc' \ xcp'$

**by**( $\text{simp add: } \tau \text{move2-iff}$ )

**lemma**  $\tau \text{instr-stk-append}$ :

$\tau \text{instr } P \ h \ \text{stk} \ i \implies \tau \text{instr } P \ h \ (\text{stk} @ \text{vs}) \ i$

**by**( $\text{cases } i$ )( $\text{auto simp add: nth-append}$ )

**lemma**  $\tau \text{move2-stk-append}$ :

$\tau \text{move2 } P \ h \ \text{stk} \ e \ pc \ xcp \implies \tau \text{move2 } P \ h \ (\text{stk} @ \text{vs}) \ e \ pc \ xcp$

**by**( $\text{simp add: } \tau \text{move2-iff } \tau \text{instr-stk-append}$ )

**lemma**  $\tau \text{moves2-stk-append}$ :

$\tau \text{moves2 } P \ h \ \text{stk} \ es \ pc \ xcp \implies \tau \text{moves2 } P \ h \ (\text{stk} @ \text{vs}) \ es \ pc \ xcp$

**by**( $\text{simp add: } \tau \text{moves2-iff } \tau \text{instr-stk-append}$ )

**fun**  $\tau \text{Move2} :: 'addr \ \text{jvm-prog} \Rightarrow ('addr, 'heap) \ \text{jvm-state} \Rightarrow \text{bool}$

**where**

$\tau \text{Move2 } P \ (xcp, h, []) = \text{False}$

|  $\tau \text{Move2 } P \ (xcp, h, (\text{stk}, \text{loc}, C, M, pc) \# \text{frs}) =$

$(\text{let } (-, -, \text{meth}) = \text{method } P \ C \ M; (-, -, \text{ins}, \text{xt}) = \text{the meth}$

$\text{in } (pc < \text{length ins} \wedge (xcp = \text{None} \longrightarrow \tau \text{instr } P \ h \ \text{stk} \ (\text{ins} ! pc))))$

**lemma**  $\tau \text{Move2-iff}$ :

$\tau \text{Move2 } P \ \sigma = (\text{let } (xcp, h, \text{frs}) = \sigma$

$\text{in case frs of } [] \Rightarrow \text{False}$

|  $(\text{stk}, \text{loc}, C, M, pc) \# \text{frs}' \Rightarrow$

$(\text{let } (-, -, \text{meth}) = \text{method } P \ C \ M; (-, -, \text{ins}, \text{xt}) = \text{the meth}$

$\text{in } (pc < \text{length ins} \wedge (xcp = \text{None} \longrightarrow \tau \text{instr } P \ h \ \text{stk} \ (\text{ins} ! pc))))$

**by**( $\text{cases } \sigma$ )( $\text{clarsimp split: list.splits simp add: fun-eq-iff split-beta}$ )

**lemma**  $\tau \text{instr-compP } [\text{simp}]$ :  $\tau \text{instr } (\text{compP } f \ P) \ h \ \text{stk} \ i \longleftrightarrow \tau \text{instr } P \ h \ \text{stk} \ i$

**by**( $\text{cases } i$ )  $\text{auto}$

**lemma**  $[\text{simp}]$ : **fixes**  $e :: 'addr \ \text{expr1}$  **and**  $es :: 'addr \ \text{expr1 list}$

**shows**  $\tau \text{move2-compP}: \tau \text{move2 } (\text{compP } f \ P) \ h \ \text{stk} \ e = \tau \text{move2 } P \ h \ \text{stk} \ e$

**and**  $\tau \text{moves2-compP}: \tau \text{moves2 } (\text{compP } f \ P) \ h \ \text{stk} \ es = \tau \text{moves2 } P \ h \ \text{stk} \ es$

**by**( $\text{auto simp add: } \tau \text{move2-iff } \tau \text{moves2-iff fun-eq-iff}$ )

**lemma**  $\tau \text{Move2-compP2}$ :

$P \vdash C \text{ sees } M:Ts \rightarrow T = \lfloor \text{body} \rfloor \text{ in } D \implies$

$\tau \text{Move2 } (\text{compP2 } P) \ (xcp, h, (\text{stk}, \text{loc}, C, M, pc) \# \text{frs}) =$

(case  $xcp$  of None  $\Rightarrow \tau\text{move2 } P \ h \ stk \ body \ pc \ xcp \vee pc = \text{length } (\text{compE2 } body) \mid \text{Some } a \Rightarrow pc < \text{Suc } (\text{length } (\text{compE2 } body)))$   
**by**(*clarsimp simp add:  $\tau\text{move2-iff compP2-def compMb2-def nth-append nth-Cons' split: option.splits if-split-asm}$* )

**abbreviation**  $\tau\text{MOVE2} ::$

' $\text{addr jvm-prog} \Rightarrow ((\text{'addr option} \times \text{'addr frame list}) \times \text{'heap}, (\text{'addr}, \text{'thread-id}, \text{'heap}) \text{ jvm-thread-action})$   
*trsys*

**where**  $\tau\text{MOVE2 } P \equiv \lambda((xcp, frs), h) \ ta \ s. \ \tau\text{Move2 } P \ (xcp, h, frs) \wedge ta = \varepsilon$

**lemma**  $\tau\text{jvmd-heap-unchanged}$ :

$\llbracket P, t \vdash \text{Normal } (xcp, h, frs) -\varepsilon\text{-jvmd} \rightarrow \text{Normal } (xcp', h', frs'); \tau\text{Move2 } P \ (xcp, h, frs) \rrbracket$   
 $\Rightarrow h = h'$

**apply**(*erule jvmd-NormalE*)

**apply**(*clarsimp*)

**apply**(*cases xcp*)

**apply**(*rename-tac stk loc C M pc FRS M' Ts T meth mxs mxl ins xt*)

**apply**(*case-tac ins ! pc*)

**prefer** 19 — *BinOpInstr*

**apply**(*rename-tac bop*)

**apply**(*case-tac the (binop bop (hd (tl stk)) (hd stk))*)

**apply**(*auto simp add: split-beta  $\tau\text{external-def split: if-split-asm}$* )

**apply**(*fastforce simp add: check-def has-method-def  $\tau\text{external-def dest:  $\tau\text{external-red-external-aggr-heap-unchanged}$$* )  
**done**

**lemma**  $\text{mexecd-}\tau\text{mthr-wf}$ :

$\tau\text{multithreaded-wf JVM-final } (\text{mexecd } P) \ (\tau\text{MOVE2 } P)$

**proof**

**fix**  $t \ x \ h \ ta \ x' \ h'$

**assume**  $\text{mexecd } P \ t \ (x, h) \ ta \ (x', h')$

**and**  $\tau\text{MOVE2 } P \ (x, h) \ ta \ (x', h')$

**thus**  $h = h'$

**by**(*cases x*)(*cases x', auto dest:  $\tau\text{jvmd-heap-unchanged}$* )

**next**

**fix**  $s \ ta \ s'$

**assume**  $\tau\text{MOVE2 } P \ s \ ta \ s' \ \text{thus } ta = \varepsilon \ \text{by}(\text{simp add: split-beta})$

**qed**

**end**

**sublocale**  $\text{JVM-heap-base} < \text{execd-mthr}$ :

$\tau\text{multithreaded-wf}$

$\text{JVM-final}$

$\text{mexecd } P$

$\text{convert-RA}$

$\tau\text{MOVE2 } P$

**for**  $P$

**by**(*rule mexecd- $\tau\text{mthr-wf}$* )

**context**  $\text{JVM-heap-base}$  **begin**

**lemma**  $\tau\text{exec-1-taD}$ :

**assumes**  $\text{exec: exec-1-d } P \ t \ (\text{Normal } (xcp, h, frs)) \ ta \ (\text{Normal } (xcp', h', frs'))$

**and**  $\tau: \tau\text{Move2 } P \ (xcp, h, frs)$

```

  shows  $ta = \varepsilon$ 
using assms
apply(auto elim!: jvmd-NormalE simp add: split-beta)
apply(cases xcp)
apply auto
apply(rename-tac stk loc C M pc FRS)
apply(case-tac instrs-of P C M ! pc)
apply(simp-all split: if-split-asm)
apply(auto simp add: check-def has-method-def  $\tau$ external-def dest!:  $\tau$ external-red-external-aggr-TA-empty)
done

end

end

```

## 7.15 JVM Semantics for the delay bisimulation proof from intermediate language to byte code

**theory** *Execs* **imports** *JVMTau* **begin**

```

declare match-ex-table-app [simp del]
  match-ex-table-eq-NoneI [simp del]
  compxE2-size-convs [simp del]
  compxE2-stack-xlift-convs [simp del]
  compxEs2-stack-xlift-convs [simp del]

```

**type-synonym**

```

('addr, 'heap) check-instr' =
  'addr instr  $\Rightarrow$  'addr jvm-prog  $\Rightarrow$  'heap  $\Rightarrow$  'addr val list  $\Rightarrow$  'addr val list  $\Rightarrow$  cname  $\Rightarrow$  mname  $\Rightarrow$  pc
 $\Rightarrow$  'addr frame list  $\Rightarrow$  bool

```

**primrec** *check-instr'* :: ('addr, 'heap) *check-instr'*

**where**

*check-instr'-Load*:

```

check-instr' (Load n) P h stk loc C M0 pc frs =
  True

```

| *check-instr'-Store*:

```

check-instr' (Store n) P h stk loc C0 M0 pc frs =
  (0 < length stk)

```

| *check-instr'-Push*:

```

check-instr' (Push v) P h stk loc C0 M0 pc frs =
  True

```

| *check-instr'-New*:

```

check-instr' (New C) P h stk loc C0 M0 pc frs =
  True

```

| *check-instr'-NewArray*:

```

check-instr' (NewArray T) P h stk loc C0 M0 pc frs =
  (0 < length stk)

```

- | *check-instr'-ALoad*:  
 $check\_instr' \ ALoad \ P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$   
 $(1 < length \ stk)$
- | *check-instr'-AStore*:  
 $check\_instr' \ AStore \ P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$   
 $(2 < length \ stk)$
- | *check-instr'-ALength*:  
 $check\_instr' \ ALength \ P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$   
 $(0 < length \ stk)$
- | *check-instr'-Getfield*:  
 $check\_instr' \ (Getfield \ F \ C) \ P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$   
 $(0 < length \ stk)$
- | *check-instr'-Putfield*:  
 $check\_instr' \ (Putfield \ F \ C) \ P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$   
 $(1 < length \ stk)$
- | *check-instr'-CAS*:  
 $check\_instr' \ (CAS \ F \ C) \ P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$   
 $(2 < length \ stk)$
- | *check-instr'-Checkcast*:  
 $check\_instr' \ (Checkcast \ T) \ P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$   
 $(0 < length \ stk)$
- | *check-instr'-InstanceOf*:  
 $check\_instr' \ (InstanceOf \ T) \ P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$   
 $(0 < length \ stk)$
- | *check-instr'-Invoke*:  
 $check\_instr' \ (Invoke \ M \ n) \ P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$   
 $(n < length \ stk)$
- | *check-instr'-Return*:  
 $check\_instr' \ Return \ P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$   
 $(0 < length \ stk)$
- | *check-instr'-Pop*:  
 $check\_instr' \ Pop \ P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$   
 $(0 < length \ stk)$
- | *check-instr'-Dup*:  
 $check\_instr' \ Dup \ P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$   
 $(0 < length \ stk)$
- | *check-instr'-Swap*:  
 $check\_instr' \ Swap \ P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$   
 $(1 < length \ stk)$
- | *check-instr'-BinOpInstr*:  
 $check\_instr' \ (BinOpInstr \ bop) \ P \ h \ stk \ loc \ C_0 \ M_0 \ pc \ frs =$



$(1 < \text{length } \text{stk})$

| *check-instr'-IfFalse*:  
 $\text{check-instr}' (\text{IfFalse } b) P h \text{ stk } \text{loc } C_0 M_0 pc \text{ frs} =$   
 $(0 < \text{length } \text{stk} \wedge 0 \leq \text{int } pc + b)$

| *check-instr'-Goto*:  
 $\text{check-instr}' (\text{Goto } b) P h \text{ stk } \text{loc } C_0 M_0 pc \text{ frs} =$   
 $(0 \leq \text{int } pc + b)$

| *check-instr'-Throw*:  
 $\text{check-instr}' \text{ThrowExc } P h \text{ stk } \text{loc } C_0 M_0 pc \text{ frs} =$   
 $(0 < \text{length } \text{stk})$

| *check-instr'-MEnter*:  
 $\text{check-instr}' \text{MEnter } P h \text{ stk } \text{loc } C_0 M_0 pc \text{ frs} =$   
 $(0 < \text{length } \text{stk})$

| *check-instr'-MExit*:  
 $\text{check-instr}' \text{MExit } P h \text{ stk } \text{loc } C_0 M_0 pc \text{ frs} =$   
 $(0 < \text{length } \text{stk})$

**definition** *ci-stk-offer* :: ('addr, 'heap) *check-instr'*  $\Rightarrow$  bool

**where**

*ci-stk-offer ci* =  
 $(\forall \text{ins } P h \text{ stk } \text{stk}' \text{loc } C M pc \text{ frs}. ci \text{ ins } P h \text{ stk } \text{loc } C M pc \text{ frs} \longrightarrow ci \text{ ins } P h (\text{stk} @ \text{stk}') \text{loc } C M pc \text{ frs})$

**lemma** *ci-stk-offerI*:

$(\bigwedge \text{ins } P h \text{ stk } \text{stk}' \text{loc } C M pc \text{ frs}. ci \text{ ins } P h \text{ stk } \text{loc } C M pc \text{ frs} \Longrightarrow ci \text{ ins } P h (\text{stk} @ \text{stk}') \text{loc } C M pc \text{ frs}) \Longrightarrow ci\text{-stk-offer } ci$

**unfolding** *ci-stk-offer-def* **by** blast

**lemma** *ci-stk-offerD*:

$\llbracket ci\text{-stk-offer } ci; ci \text{ ins } P h \text{ stk } \text{loc } C M pc \text{ frs} \rrbracket \Longrightarrow ci \text{ ins } P h (\text{stk} @ \text{stk}') \text{loc } C M pc \text{ frs}$

**unfolding** *ci-stk-offer-def* **by** blast

**lemma** *check-instr'-stk-offer*:

*ci-stk-offer check-instr'*

**proof**(rule *ci-stk-offerI*)

**fix** *ins P h stk stk' loc C M pc frs*

**assume** *check-instr' ins P h stk loc C M pc frs*

**thus** *check-instr' ins P h (stk @ stk') loc C M pc frs*

**by**(cases *ins*) auto

**qed**

**context** *JVM-heap-base* **begin**

**lemma** *check-instr-imp-check-instr'*:

$\text{check-instr } \text{ins } P h \text{ stk } \text{loc } C M pc \text{ frs} \Longrightarrow \text{check-instr}' \text{ ins } P h \text{ stk } \text{loc } C M pc \text{ frs}$

**by**(cases *ins*) auto

**lemma** *check-instr-stk-offer*:

```

    ci-stk-offer check-instr
  proof(rule ci-stk-offerI)
    fix ins P h stk stk' loc C M pc frs
    assume check-instr ins P h stk loc C M pc frs
    thus check-instr ins P h (stk @ stk') loc C M pc frs
      by(cases ins)(auto simp add: nth-append hd-append neq-Nil-conv tl-append split: list.split)
  qed

end

```

```

primrec jump-ok :: 'addr instr list  $\Rightarrow$  nat  $\Rightarrow$  nat  $\Rightarrow$  bool
where jump-ok [] n n' = True
| jump-ok (x # xs) n n' = (jump-ok xs (Suc n) n'  $\wedge$ 
  (case x of IfFalse m  $\Rightarrow$   $\neg$  int n  $\leq$  m  $\wedge$  m  $\leq$  int (n' + length xs)
    | Goto m  $\Rightarrow$   $\neg$  int n  $\leq$  m  $\wedge$  m  $\leq$  int (n' + length xs)
    | -  $\Rightarrow$  True))

```

```

lemma jump-ok-append [simp]:
  jump-ok (xs @ xs') n n'  $\longleftrightarrow$  jump-ok xs n (n' + length xs')  $\wedge$  jump-ok xs' (n + length xs) n'
apply(induct xs arbitrary: n)
apply(simp)
apply(auto split: instr.split)
done

```

```

lemma jump-ok-GotoD:
   $\llbracket$  jump-ok ins n n'; ins ! pc = Goto m; pc < length ins  $\rrbracket \Longrightarrow \neg$  int (pc + n)  $\leq$  m  $\wedge$  m < int (length ins - pc + n')
apply(induct ins arbitrary: n n' pc)
apply(simp)
apply(clarsimp)
apply(case-tac pc)
apply(fastforce)+
done

```

```

lemma jump-ok-IfFalseD:
   $\llbracket$  jump-ok ins n n'; ins ! pc = IfFalse m; pc < length ins  $\rrbracket \Longrightarrow \neg$  int (pc + n)  $\leq$  m  $\wedge$  m < int (length ins - pc + n')
apply(induct ins arbitrary: n n' pc)
apply(simp)
apply(clarsimp)
apply(case-tac pc)
apply(fastforce)+
done

```

```

lemma fixes e :: 'addr expr1 and es :: 'addr expr1 list
  shows compE2-jump-ok [intro!]: jump-ok (compE2 e) n (Suc n')
  and compEs2-jump-ok [intro!]: jump-ok (compEs2 es) n (Suc n')
apply(induct e and es arbitrary: n n' and n n' rule: compE2.induct compEs2.induct)
apply(auto split: bop.split)
done

```

```

lemma fixes e :: 'addr expr1 and es :: 'addr expr1 list
  shows compE1-Goto-not-same:  $\llbracket$  compE2 e ! pc = Goto i; pc < length (compE2 e)  $\rrbracket \Longrightarrow$  nat (int

```

```

pc + i) ≠ pc
  and compEs2-Goto-not-same:  $\llbracket \text{compEs2 } es ! pc = \text{Goto } i; pc < \text{length } (\text{compEs2 } es) \rrbracket \implies \text{nat } (\text{int } pc + i) \neq pc$ 
apply(induct e and es arbitrary: pc i and pc i rule: compE2.induct compEs2.induct)
apply(auto simp add: nth-Cons nth-append split: if-split-asm bop.split-asm nat.splits)
apply fastforce+
done

```

```

fun ins-jump-ok :: 'addr instr  $\Rightarrow$  nat  $\Rightarrow$  bool
where
  ins-jump-ok (Goto m) l = ( $-(\text{int } l) \leq m$ )
| ins-jump-ok (IfFalse m) l = ( $-(\text{int } l) \leq m$ )
| ins-jump-ok - = True

```

**definition** wf-ci :: ('addr, 'heap) check-instr'  $\Rightarrow$  bool

```

where
  wf-ci ci  $\longleftrightarrow$ 
    ci-stk-offer ci  $\wedge$  ci  $\leq$  check-instr'  $\wedge$ 
    ( $\forall ins P h stk loc C M pc pc' frs. ci ins P h stk loc C M pc frs \longrightarrow \text{ins-jump-ok } ins pc' \longrightarrow ci ins P h stk loc C M pc' frs$ )

```

**lemma** wf-ciI:

```

 $\llbracket ci\text{-stk-offer } ci;$ 
 $\bigwedge ins P h stk loc C M pc frs. ci ins P h stk loc C M pc frs \implies \text{check-instr}' ins P h stk loc C M pc frs;$ 
 $\bigwedge ins P h stk loc C M pc pc' frs. \llbracket ci ins P h stk loc C M pc frs; \text{ins-jump-ok } ins pc' \rrbracket \implies ci ins P h stk loc C M pc' frs \rrbracket$ 
 $\implies wf\text{-ci } ci$ 

```

**unfolding** wf-ci-def le-fun-def le-bool-def

**by** blast

**lemma** check-instr'-pc:

```

 $\llbracket \text{check-instr}' ins P h stk loc C M pc frs; \text{ins-jump-ok } ins pc' \rrbracket \implies \text{check-instr}' ins P h stk loc C M pc' frs$ 
by(cases ins) auto

```

**lemma** wf-ci-check-instr' [iff]:

```

  wf-ci check-instr'
apply(rule wf-ciI)
  apply(rule check-instr'-stk-offer)
  apply(assumption)
apply(erule (1) check-instr'-pc)
done

```

**lemma** jump-ok-ins-jump-ok:

```

 $\llbracket \text{jump-ok } ins n n'; pc < \text{length } ins \rrbracket \implies \text{ins-jump-ok } (ins ! pc) (pc + n)$ 
apply(induct ins arbitrary: n n' pc)
apply(fastforce simp add: nth-Cons' gr0-conv-Suc split: instr.split-asm)+
done

```

**context** JVM-heap-base **begin**

**lemma** check-instr-pc:

```

 $\llbracket \text{check-instr } ins P h stk loc C M pc frs; \text{ins-jump-ok } ins pc' \rrbracket \implies \text{check-instr } ins P h stk loc C M$ 

```

*pc' frs*  
**by**(*cases ins*) *auto*

**lemma** *wf-ci-check-instr* [*iff*]:  
   *wf-ci check-instr*  
**apply**(*rule wf-ciI*)  
   **apply**(*rule check-instr-stk-offer*)  
   **apply**(*erule check-instr-imp-check-instr'*)  
**apply**(*erule (1) check-instr-pc*)  
**done**

**end**

**lemma** *wf-ciD1*: *wf-ci ci*  $\implies$  *ci-stk-offer ci*  
**unfolding** *wf-ci-def* **by** *blast*

**lemma** *wf-ciD2*:  $\llbracket \text{wf-ci ci}; \text{ci ins } P \text{ h stk loc } C \text{ M pc frs} \rrbracket \implies \text{check-instr'} \text{ ins } P \text{ h stk loc } C \text{ M pc frs}$   
**unfolding** *wf-ci-def le-fun-def le-bool-def*  
**by** *blast*

**lemma** *wf-ciD3*:  $\llbracket \text{wf-ci ci}; \text{ci ins } P \text{ h stk loc } C \text{ M pc frs}; \text{ins-jump-ok ins pc'} \rrbracket \implies \text{ci ins } P \text{ h stk loc } C \text{ M pc' frs}$   
**unfolding** *wf-ci-def* **by** *blast*

**lemma** *check-instr'-ins-jump-ok*: *check-instr' ins P h stk loc C M pc frs*  $\implies$  *ins-jump-ok ins pc*  
**by**(*cases ins*) *auto*

**lemma** *wf-ci-ins-jump-ok*:  
   **assumes** *wf: wf-ci ci*  
   **and** *ci: ci ins P h stk loc C M pc frs*  
   **and** *pc': pc  $\leq$  pc'*  
   **shows** *ins-jump-ok ins pc'*

**proof** –  
   **from** *wf ci* **have** *check-instr' ins P h stk loc C M pc frs* **by**(*rule wf-ciD2*)  
   **with** *pc'* **have** *check-instr' ins P h stk loc C M pc' frs* **by**(*cases ins*) *auto*  
   **thus** *?thesis* **by**(*rule check-instr'-ins-jump-ok*)

**qed**

**lemma** *wf-ciD3'*:  $\llbracket \text{wf-ci ci}; \text{ci ins } P \text{ h stk loc } C \text{ M pc frs}; \text{pc} \leq \text{pc'} \rrbracket \implies \text{ci ins } P \text{ h stk loc } C \text{ M pc' frs}$   
**apply**(*frule (2) wf-ci-ins-jump-ok*)  
**apply**(*erule (2) wf-ciD3*)  
**done**

**typedef** (*'addr, 'heap*) *check-instr* = *Collect wf-ci :: ('addr, 'heap) check-instr' set*  
   **morphisms** *ci-app Abs-check-instr*  
**by** *auto*

**lemma** *ci-app-check-instr' [simp]*: *ci-app (Abs-check-instr check-instr') = check-instr'*  
**by**(*simp add: Abs-check-instr-inverse*)

**lemma** (**in** *JVM-heap-base*) *ci-app-check-instr [simp]*: *ci-app (Abs-check-instr check-instr) = check-instr*  
**by**(*simp add: Abs-check-instr-inverse*)

**lemma** *wf-ci-stk-offerD*:  
   *ci-app ci ins P h stk loc C M pc frs*  $\implies$  *ci-app ci ins P h (stk @ stk') loc C M pc frs*

**apply**(rule *ci-stk-offerD*[*OF wf-ciD1*]) **back**  
**by**(rule *ci-app* [*simplified*])

**lemma** *wf-ciD2-ci-app*:

*ci-app ci ins P h stk loc C M pc frs  $\implies$  check-instr' ins P h stk loc C M pc frs*

**apply**(cases *ci*)  
**apply**(simp add: *Abs-check-instr-inverse*)  
**apply**(erule (1) *wf-ciD2*)  
**done**

**lemma** *wf-ciD3-ci-app*:

$\llbracket ci-app ci ins P h stk loc C M pc frs; ins-jump-ok ins pc' \rrbracket \implies ci-app ci ins P h stk loc C M pc' frs$

**apply**(cases *ci*)  
**apply**(simp add: *Abs-check-instr-inverse*)  
**apply**(erule (2) *wf-ciD3*)  
**done**

**lemma** *wf-ciD3'-ci-app*:  $\llbracket ci-app ci ins P h stk loc C M pc frs; pc \leq pc' \rrbracket \implies ci-app ci ins P h stk loc C M pc' frs$

**apply**(cases *ci*)  
**apply**(simp add: *Abs-check-instr-inverse*)  
**apply**(erule (2) *wf-ciD3'*)  
**done**

**context** *JVM-heap-base* **begin**

**inductive** *exec-meth* ::

*('addr, 'heap) check-instr  $\Rightarrow$  'addr jvm-prog  $\Rightarrow$  'addr instr list  $\Rightarrow$  ex-table  $\Rightarrow$  'thread-id*  
 $\Rightarrow$  *'heap  $\Rightarrow$  ('addr val list  $\times$  'addr val list  $\times$  pc  $\times$  'addr option)  $\Rightarrow$  ('addr, 'thread-id, 'heap)*

*jvm-thread-action*

$\Rightarrow$  *'heap  $\Rightarrow$  ('addr val list  $\times$  'addr val list  $\times$  pc  $\times$  'addr option)  $\Rightarrow$  bool*

**for** *ci* :: *('addr, 'heap) check-instr* **and** *P* :: *'addr jvm-prog*

**and** *ins* :: *'addr instr list* **and** *xt* :: *ex-table* **and** *t* :: *'thread-id*

**where**

*exec-instr*:

$\llbracket (ta, xcp, h', [(stk', loc', undefined, undefined, pc')]) \in exec-instr (ins ! pc) P t h stk loc undefined undefined pc \rrbracket$ ;

*pc* < *length ins*;

*ci-app ci (ins ! pc) P h stk loc undefined undefined pc  $\llbracket \rrbracket$*

$\implies exec-meth ci P ins xt t h (stk, loc, pc, None) ta h' (stk', loc', pc', xcp)$

| *exec-catch*:

$\llbracket match-ex-table P (cname-of h xcp) pc xt = \lfloor (pc', d) \rfloor; d \leq length stk \rrbracket$

$\implies exec-meth ci P ins xt t h (stk, loc, pc, \lfloor xcp \rfloor) \varepsilon h (Addr xcp \# drop (size stk - d) stk, loc, pc', None)$

**lemma** *exec-meth-instr*:

*exec-meth ci P ins xt t h (stk, loc, pc, None) ta h' (stk', loc', pc', xcp)  $\longleftrightarrow$*

*(ta, xcp, h', [(stk', loc', undefined, undefined, pc')])  $\in exec-instr (ins ! pc) P t h stk loc undefined undefined pc \llbracket \wedge pc < length ins \wedge ci-app ci (ins ! pc) P h stk loc undefined undefined pc \llbracket$*

**by**(auto elim: *exec-meth.cases* intro: *exec-instr*)

**lemma** *exec-meth-xcpt*:

*exec-meth ci P ins xt t h (stk, loc, pc,  $\lfloor xcp \rfloor$ ) ta h (stk', loc', pc', xcp')  $\longleftrightarrow$*

$(\exists d. \text{match-ex-table } P \text{ (cname-of } h \text{ xcp) } pc \text{ xt} = \lfloor (pc', d) \rfloor \wedge ta = \varepsilon \wedge stk' = (\text{Addr } xcp \# \text{ drop } (size \text{ stk} - d) \text{ stk}) \wedge loc' = loc \wedge xcp' = \text{None} \wedge d \leq \text{length } stk)$   
**by**(*auto elim: exec-meth.cases intro: exec-catch*)

**abbreviation** *exec-meth-a*

**where** *exec-meth-a*  $\equiv$  *exec-meth* (*Abs-check-instr check-instr'*)

**abbreviation** *exec-meth-d*

**where** *exec-meth-d*  $\equiv$  *exec-meth* (*Abs-check-instr check-instr*)

**lemma** *exec-meth-length-compE2D* [*dest*]:

*exec-meth ci P (compE2 e) (compxE2 e 0 d) t h (stk, loc, pc, xcp) ta h' s'  $\implies$  pc < length (compE2 e)*

**apply**(*erule exec-meth.cases*)

**apply**(*auto dest: match-ex-table-pc-length-compE2*)

**done**

**lemma** *exec-meth-length-compEs2D* [*dest*]:

*exec-meth ci P (compEs2 es) (compxEs2 es 0 0) t h (stk, loc, pc, xcp) ta h' s'  $\implies$  pc < length (compEs2 es)*

**apply**(*erule exec-meth.cases*)

**apply**(*auto dest: match-ex-table-pc-length-compEs2*)

**done**

**lemma** *exec-instr-stk-offer*:

**assumes** *check: check-instr' (ins ! pc) P h stk loc C M pc frs*

**and** *exec: (ta', xcp', h', (stk', loc', C, M, pc') # frs)  $\in$  exec-instr (ins ! pc) P t h stk loc C M pc frs*

**shows** *(ta', xcp', h', (stk' @ stk'', loc', C, M, pc') # frs)  $\in$  exec-instr (ins ! pc) P t h (stk @ stk'')*  
*loc C M pc frs*

**using** *assms*

**proof**(*cases ins ! pc*)

**case** (*Invoke M n*)

**thus** *?thesis using exec check*

**by**(*auto split: if-split-asm extCallRet.splits split del: if-split simp add: split-beta nth-append min-def extRet2JVM-def*)

**qed**(*force simp add: nth-append is-Ref-def has-method-def nth-Cons split-beta hd-append tl-append neq-Nil-conv split: list.split if-split-asm nat.splits sum.split-asm*)**+**

**lemma** *exec-meth-stk-offer*:

**assumes** *exec: exec-meth ci P ins xt t h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')*

**shows** *exec-meth ci P ins (stack-xlift (length stk'') xt) t h (stk @ stk'', loc, pc, xcp) ta h' (stk' @ stk'', loc', pc', xcp')*

**using** *exec*

**proof**(*cases*)

**case** (*exec-catch xcp d*)

**from**  $\langle \text{match-ex-table } P \text{ (cname-of } h \text{ xcp) } pc \text{ xt} = \lfloor (pc', d) \rfloor \rangle$

**have** *match-ex-table P (cname-of h xcp) pc (stack-xlift (length stk'') xt) =  $\lfloor (pc', \text{length } stk'' + d) \rfloor$*

**by**(*simp add: match-ex-table-stack-xlift*)

**moreover have** *length stk'' + d  $\leq$  length (stk @ stk'')* **using**  $\langle d \leq \text{length } stk \rangle$  **by** *simp*

**ultimately have** *exec-meth ci P ins (stack-xlift (length stk'') xt) t h ((stk @ stk''), loc, pc,  $\lfloor xcp \rfloor$ )  $\varepsilon$  h ((Addr xcp # drop (length (stk @ stk'') - (length stk'' + d)) (stk @ stk'')), loc, pc', None)*

**by**(*rule exec-meth.exec-catch*)

**with** *exec-catch show ?thesis by (simp)*

**next**

**case** *exec-instr*  
**note** *ciins* =  $\langle ci\text{-app } ci \ (ins ! pc) \ P \ h \ stk \ loc \ undefined \ undefined \ pc \ [] \rangle$   
**hence** *ci-app ci (ins ! pc) P h (stk @ stk'') loc undefined undefined pc []*  
**by**(rule *wf-ci-stk-offerD*)  
**moreover from** *ciins*  
**have** *check-instr' (ins ! pc) P h stk loc undefined undefined pc []*  
**by**(rule *wf-ciD2-ci-app*)  
**hence**  $(ta, xcp', h', [(stk' @ stk'', loc', undefined, undefined, pc')]) \in exec\text{-instr} \ (ins ! pc) \ P \ t \ h \ (stk @ stk'') \ loc \ undefined \ undefined \ pc \ []$   
**using**  $\langle (ta, xcp', h', [(stk', loc', undefined, undefined, pc')]) \in exec\text{-instr} \ (ins ! pc) \ P \ t \ h \ stk \ loc \ undefined \ undefined \ pc \ [] \rangle$   
**by**(rule *exec-instr-stk-offer*)  
**ultimately show** ?thesis **using** *exec-instr* **by**(auto intro: *exec-meth.exec-instr*)  
**qed**

**lemma** *exec-meth-append-xt* [intro]:  
*exec-meth ci P ins xt t h s ta h' s'*  
 $\implies exec\text{-meth } ci \ P \ (ins @ ins') \ (xt @ xt') \ t \ h \ s \ ta \ h' \ s'$   
**apply**(erule *exec-meth.cases*)  
**apply**(auto)  
**apply**(rule *exec-instr*)  
**apply**(clarsimp simp add: *nth-append*)  
**apply**(simp)  
**apply**(simp add: *nth-append*)  
**apply**(rule *exec-catch*)  
**by**(simp)

**lemma** *exec-meth-append* [intro]:  
*exec-meth ci P ins xt t h s ta h' s'  $\implies exec\text{-meth } ci \ P \ (ins @ ins') \ xt \ t \ h \ s \ ta \ h' \ s'$*   
**by**(rule *exec-meth-append-xt*[**where** *xt'=[]*, *simplified*])

**lemma** *append-exec-meth-xt*:  
**assumes** *exec: exec-meth ci P ins xt t h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')*  
**and** *jump: jump-ok ins 0 n*  
**and** *pcs: pcs xt'  $\subseteq \{0..<length \ ins'\}$*   
**shows** *exec-meth ci P (ins' @ ins) (xt' @ shift (length ins') xt) t h (stk, loc, (length ins' + pc), xcp)*  
*ta h' (stk', loc', (length ins' + pc'), xcp')*  
**using** *exec*  
**proof**(cases)  
**case** (*exec-catch xcp d*)  
**from**  $\langle match\text{-ex-table } P \ (cname\text{-of } h \ xcp) \ pc \ xt = [(pc', d)] \rangle$   
**have** *match-ex-table P (cname-of h xcp) (length ins' + pc) (shift (length ins') xt) = [(length ins' + pc', d)]*  
**by**(simp add: *match-ex-table-shift*)  
**moreover from** *pcs* **have** *length ins' + pc  $\notin$  pcs xt'* **by**(auto)  
**ultimately have** *match-ex-table P (cname-of h xcp) (length ins' + pc) (xt' @ shift (length ins') xt)*  
 $= [(length \ ins' + pc', d)]$   
**by**(simp add: *match-ex-table-append-not-pcs*)  
**with** *exec-catch* **show** ?thesis **by**(auto dest: *exec-meth.exec-catch*)  
**next**  
**case** *exec-instr*  
**note** *exec* =  $\langle (ta, xcp', h', [(stk', loc', undefined, undefined, pc')]) \in exec\text{-instr} \ (ins ! pc) \ P \ t \ h \ stk \ loc \ undefined \ undefined \ pc \ [] \rangle$   
**hence**  $(ta, xcp', h', [(stk', loc', undefined, undefined, length \ ins' + pc')]) \in exec\text{-instr} \ (ins ! pc) \ P \ t$

$h \text{ stk } loc \text{ undefined undefined } (length \text{ ins}' + pc) \square$   
**proof**(cases ins ! pc)  
   **case** (Goto i)  
     **with** jump  $\langle pc < length \text{ ins} \rangle$  **have**  $- int \text{ pc} \leq i \text{ } i < int (length \text{ ins} - pc + n)$   
       **by**(auto dest: jump-ok-GotoD)  
     **with** exec Goto **show** ?thesis **by**(auto)  
**next**  
   **case** (IfFalse i)  
     **with** jump  $\langle pc < length \text{ ins} \rangle$  **have**  $- int \text{ pc} \leq i \text{ } i < int (length \text{ ins} - pc + n)$   
       **by**(auto dest: jump-ok-IfFalseD)  
     **with** exec IfFalse **show** ?thesis **by**(auto)  
**next**  
   **case** (Invoke M n)  
     **with** exec **show** ?thesis  
     **by**(auto split: if-split-asm extCallRet.splits split del: if-split simp add: split-beta nth-append min-def  
 extRet2JVM-def)  
   **qed**(auto simp add: split-beta split: if-split-asm sum.split-asm)  
   **moreover from**  $\langle ci\text{-app } ci (ins ! pc) P h \text{ stk } loc \text{ undefined undefined } pc \square \rangle$   
   **have**  $ci\text{-app } ci (ins ! pc) P h \text{ stk } loc \text{ undefined undefined } (length \text{ ins}' + pc) \square$   
     **by**(rule wf-ciD3'-ci-app) simp  
   **ultimately have** exec-meth ci P (ins' @ ins) (xt' @ shift (length ins') xt) t h (stk, loc, (length ins' + pc), None) ta h' (stk', loc', (length ins' + pc'), xcp')  
     **using**  $\langle pc < length \text{ ins} \rangle$  **by**  $-(rule \text{ exec-meth.exec-instr, simp-all})$   
   **thus** ?thesis **using** exec-instr **by**(auto)  
**qed**

**lemma** append-exec-meth:

**assumes** exec: exec-meth ci P ins xt t h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')  
**and** jump: jump-ok ins 0 n  
**shows** exec-meth ci P (ins' @ ins) (shift (length ins') xt) t h (stk, loc, (length ins' + pc), xcp) ta h' (stk', loc', (length ins' + pc'), xcp')  
**using** assms **by**(rule append-exec-meth-xt [where xt'= $\square$ , simplified])

**lemma** exec-meth-take-xt':

$\llbracket \text{exec-meth } ci \text{ } P (ins @ ins') (xt' @ xt) t h (stk, loc, pc, xcp) ta h' s';$   
 $pc < length \text{ ins}; pc \notin pcs \text{ } xt \rrbracket$   
 $\implies \text{exec-meth } ci \text{ } P ins \text{ } xt' t h (stk, loc, pc, xcp) ta h' s'$   
**apply**(erule exec-meth.cases)  
**apply**(auto intro: exec-meth.intros simp add: match-ex-table-append nth-append dest: match-ex-table-pcsD)  
**done**

**lemma** exec-meth-take-xt:

$\llbracket \text{exec-meth } ci \text{ } P (ins @ ins') (xt' @ shift (length \text{ ins}) xt) t h (stk, loc, pc, xcp) ta h' s';$   
 $pc < length \text{ ins} \rrbracket$   
 $\implies \text{exec-meth } ci \text{ } P ins \text{ } xt' t h (stk, loc, pc, xcp) ta h' s'$   
**by**(auto intro: exec-meth-take-xt')

**lemma** exec-meth-take:

$\llbracket \text{exec-meth } ci \text{ } P (ins @ ins') xt t h (stk, loc, pc, xcp) ta h' s';$   
 $pc < length \text{ ins} \rrbracket$   
 $\implies \text{exec-meth } ci \text{ } P ins \text{ } xt t h (stk, loc, pc, xcp) ta h' s'$   
**by**(auto intro: exec-meth-take-xt[where xt =  $\square$ ])



**lemma** *exec-meth-drop-xt*:

**assumes** *exec*: *exec-meth* *ci* *P* (*ins* @ *ins'*) (*xt* @ *shift* (*length ins*) *xt'*) *t* *h* (*stk*, *loc*, (*length ins* + *pc*), *xcp*) *ta* *h'* (*stk'*, *loc'*, *pc'*, *xcp'*)

**and** *xt*: *pcs* *xt*  $\subseteq \{..$

**and** *jump*: *jump-ok* *ins'* 0 *n*

**shows** *exec-meth* *ci* *P* *ins'* *xt'* *t* *h* (*stk*, *loc*, *pc*, *xcp*) *ta* *h'* (*stk'*, *loc'*, (*pc'* - *length ins*), *xcp'*)

**using** *exec*

**proof**(*cases* *rule*: *exec-meth.cases*)

**case** *exec-instr*

**let** *?PC* = *length ins* + *pc*

**note** [*simp*] =  $\langle xcp = \text{None} \rangle$

**from**  $\langle ?PC < \text{length } (ins @ ins') \rangle$  **have** *pc*: *pc* < *length ins'* **by** *simp*

**moreover with**  $\langle (ta, xcp', h', [(stk', loc', undefined, undefined, pc')]) \in \text{exec-instr } ((ins @ ins') ! ?PC) \ P \ t \ h \ stk \ loc \ undefined \ undefined \ ?PC \ [] \rangle$

**have**  $(ta, xcp', h', [(stk', loc', undefined, undefined, pc' - \text{length ins})]) \in \text{exec-instr } (ins' ! pc) \ P \ t \ h \ stk \ loc \ undefined \ undefined \ pc \ []$

**apply**(*cases* *ins'* ! *pc*)

**apply**(*simp-all* *add*: *split-beta* *split*: *if-split-asm* *sum.split-asm* *split* *del*: *if-split*)

**apply**(*force* *split*: *extCallRet.splits* *simp* *add*: *min-def* *extRet2JVM-def*) +

**done**

**moreover from**  $\langle ci\text{-app } ci \ ((ins @ ins') ! ?PC) \ P \ h \ stk \ loc \ undefined \ undefined \ ?PC \ [] \rangle$  *jump* *pc*

**have** *ci-app* *ci* (*ins'* ! *pc*) *P* *h* *stk* *loc* *undefined* *undefined* *pc* []

**by**(*fastforce* *elim*: *wf-ciD3-ci-app* *dest*: *jump-ok-ins-jump-ok*)

**ultimately show** *?thesis* **by**(*auto* *intro*: *exec-meth.intros*)

**next**

**case** (*exec-catch* *XCP* *D*)

**let** *?PC* = *length ins* + *pc*

**note** [*simp*] =  $\langle xcp = \lfloor XCP \rfloor \rangle$

$\langle ta = \varepsilon \rangle \langle h' = h \rangle \langle stk' = \text{Addr } XCP \ \# \ \text{drop } (\text{length } stk - D) \ stk \rangle \langle loc' = loc \rangle \langle xcp' = \text{None} \rangle$

**from**  $\langle \text{match-ex-table } P \ (cname\text{-of } h \ XCP) \ ?PC \ (xt @ \text{shift } (\text{length ins}) \ xt') = \lfloor (pc', D) \rfloor \rangle \ xt$

**have** *match-ex-table* *P* (*cname-of* *h* *XCP*) *pc* *xt'* =  $\lfloor (pc' - \text{length ins}, D) \rfloor$

**by**(*auto* *simp* *add*: *match-ex-table-append* *dest*: *match-ex-table-shift-pcD* *match-ex-table-pcsD*)

**with**  $\langle D \leq \text{length } stk \rangle$  **show** *?thesis* **by**(*auto* *intro*: *exec-meth.intros*)

**qed**

**lemma** *exec-meth-drop*:

$\llbracket \text{exec-meth } ci \ P \ (ins @ ins') \ (\text{shift } (\text{length ins}) \ xt) \ t \ h \ (stk, loc, (\text{length ins} + pc), xcp) \ ta \ h' \ (stk', loc', pc', xcp') ;$

*jump-ok* *ins'* 0 *b*  $\rrbracket$

$\implies \text{exec-meth } ci \ P \ ins' \ xt \ t \ h \ (stk, loc, pc, xcp) \ ta \ h' \ (stk', loc', (pc' - \text{length ins}), xcp')$

**by**(*auto* *intro*: *exec-meth-drop-xt* **where** *xt* = [])

**lemma** *exec-meth-drop-xt-pc*:

**assumes** *exec*: *exec-meth* *ci* *P* (*ins* @ *ins'*) (*xt* @ *shift* (*length ins*) *xt'*) *t* *h* (*stk*, *loc*, *pc*, *xcp*) *ta* *h'* (*stk'*, *loc'*, *pc'*, *xcp'*)

**and** *pc*: *pc*  $\geq \text{length ins}$

**and** *pcs*: *pcs* *xt*  $\subseteq \{..$

**and** *jump*: *jump-ok* *ins'* 0 *n'*

**shows** *pc'*  $\geq \text{length ins}$

**using** *exec*

**proof**(*cases* *rule*: *exec-meth.cases*)

**case** *exec-instr* **thus** *?thesis* **using** *jump* *pc*

**apply**(*cases* *ins'* ! (*pc* - *length ins*))

**apply**(*simp-all* *add*: *split-beta* *nth-append* *split*: *if-split-asm* *sum.split-asm*)

```

    apply(force split: extCallRet.splits simp add: min-def extRet2JVM-def dest: jump-ok-GotoD jump-ok-IfFalseD)+
    done
next
  case exec-catch thus ?thesis using pcs pc
    by(auto dest: match-ex-table-pcsD match-ex-table-shift-pcD simp add: match-ex-table-append)
qed

```

**lemmas** *exec-meth-drop-pc* = *exec-meth-drop-xt-pc*[**where** *xt*=[], *simplified*]

**definition** *exec-move* ::

```

('addr, 'heap) check-instr  $\Rightarrow$  'addr J1-prog  $\Rightarrow$  'thread-id  $\Rightarrow$  'addr expr1
 $\Rightarrow$  'heap  $\Rightarrow$  ('addr val list  $\times$  'addr val list  $\times$  pc  $\times$  'addr option)
 $\Rightarrow$  ('addr, 'thread-id, 'heap) jvm-thread-action
 $\Rightarrow$  'heap  $\Rightarrow$  ('addr val list  $\times$  'addr val list  $\times$  pc  $\times$  'addr option)  $\Rightarrow$  bool

```

**where** *exec-move* *ci* *P* *t* *e*  $\equiv$  *exec-meth* *ci* (*compP2* *P*) (*compE2* *e*) (*compxE2* *e* 0 0) *t*

**definition** *exec-moves* ::

```

('addr, 'heap) check-instr  $\Rightarrow$  'addr J1-prog  $\Rightarrow$  'thread-id  $\Rightarrow$  'addr expr1 list
 $\Rightarrow$  'heap  $\Rightarrow$  ('addr val list  $\times$  'addr val list  $\times$  pc  $\times$  'addr option)
 $\Rightarrow$  ('addr, 'thread-id, 'heap) jvm-thread-action
 $\Rightarrow$  'heap  $\Rightarrow$  ('addr val list  $\times$  'addr val list  $\times$  pc  $\times$  'addr option)  $\Rightarrow$  bool

```

**where** *exec-moves* *ci* *P* *t* *es*  $\equiv$  *exec-meth* *ci* (*compP2* *P*) (*compEs2* *es*) (*compxEs2* *es* 0 0) *t*

**abbreviation** *exec-move-a*

**where** *exec-move-a*  $\equiv$  *exec-move* (*Abs-check-instr* *check-instr'*)

**abbreviation** *exec-move-d*

**where** *exec-move-d*  $\equiv$  *exec-move* (*Abs-check-instr* *check-instr*)

**abbreviation** *exec-moves-a*

**where** *exec-moves-a*  $\equiv$  *exec-moves* (*Abs-check-instr* *check-instr'*)

**abbreviation** *exec-moves-d*

**where** *exec-moves-d*  $\equiv$  *exec-moves* (*Abs-check-instr* *check-instr*)

**lemma** *exec-move-newArrayI*:

*exec-move* *ci* *P* *t* *e* *h* *s* *ta* *h'* *s'*  $\Longrightarrow$  *exec-move* *ci* *P* *t* (*newA* *T*[*e*]) *h* *s* *ta* *h'* *s'*

**unfolding** *exec-move-def* **by** *auto*

**lemma** *exec-move-newArray*:

*pc* < *length* (*compE2* *e*)  $\Longrightarrow$  *exec-move* *ci* *P* *t* (*newA* *T*[*e*]) *h* (*stk*, *loc*, *pc*, *xcp*) = *exec-move* *ci* *P* *t* *e* *h* (*stk*, *loc*, *pc*, *xcp*)

**unfolding** *exec-move-def* **by**(*auto* *intro!*: *ext* *intro*: *exec-meth-take*)

**lemma** *exec-move-CastI*:

*exec-move* *ci* *P* *t* *e* *h* *s* *ta* *h'* *s'*  $\Longrightarrow$  *exec-move* *ci* *P* *t* (*Cast* *T* *e*) *h* *s* *ta* *h'* *s'*

**unfolding** *exec-move-def* **by** *auto*

**lemma** *exec-move-Cast*:

*pc* < *length* (*compE2* *e*)  $\Longrightarrow$  *exec-move* *ci* *P* *t* (*Cast* *T* *e*) *h* (*stk*, *loc*, *pc*, *xcp*) = *exec-move* *ci* *P* *t* *e* *h* (*stk*, *loc*, *pc*, *xcp*)

**unfolding** *exec-move-def* **by**(*auto* *intro!*: *ext* *intro*: *exec-meth-take*)

**lemma** *exec-move-InstanceOfI*:

$exec-move\ ci\ P\ t\ e\ h\ s\ ta\ h'\ s' \implies exec-move\ ci\ P\ t\ (e\ instanceof\ T)\ h\ s\ ta\ h'\ s'$   
**unfolding**  $exec-move-def$  **by**  $auto$

**lemma**  $exec-move-InstanceOf$ :

$pc < length\ (compE2\ e) \implies exec-move\ ci\ P\ t\ (e\ instanceof\ T)\ h\ (stk, loc, pc, xcp) = exec-move\ ci\ P\ t\ e\ h\ (stk, loc, pc, xcp)$

**unfolding**  $exec-move-def$  **by**  $(auto\ intro!:\ ext\ intro:\ exec-meth-take)$

**lemma**  $exec-move-BinOpI1$ :

$exec-move\ ci\ P\ t\ e\ h\ s\ ta\ h'\ s' \implies exec-move\ ci\ P\ t\ (e\ \ll bop \gg\ e')\ h\ s\ ta\ h'\ s'$

**unfolding**  $exec-move-def$  **by**  $auto$

**lemma**  $exec-move-BinOp1$ :

$pc < length\ (compE2\ e) \implies exec-move\ ci\ P\ t\ (e\ \ll bop \gg\ e')\ h\ (stk, loc, pc, xcp) = exec-move\ ci\ P\ t\ e\ h\ (stk, loc, pc, xcp)$

**unfolding**  $exec-move-def$

**by**  $(auto\ intro!:\ ext\ intro:\ exec-meth-take-xt\ simp\ add:\ compxE2-size-convs)$

**lemma**  $exec-move-BinOpI2$ :

**assumes**  $exec: exec-move\ ci\ P\ t\ e2\ h\ (stk, loc, pc, xcp)\ ta\ h'\ (stk', loc', pc', xcp')$

**shows**  $exec-move\ ci\ P\ t\ (e1\ \ll bop \gg\ e2)\ h\ (stk\ @\ [v], loc, length\ (compE2\ e1) + pc, xcp)\ ta\ h'\ (stk'\ @\ [v], loc', length\ (compE2\ e1) + pc', xcp')$

**proof** –

**from**  $exec$  **have**  $exec-meth\ ci\ (compP2\ P)\ (compE2\ e2)\ (compxE2\ e2\ 0\ 0)\ t\ h\ (stk, loc, pc, xcp)\ ta\ h'\ (stk', loc', pc', xcp')$

**unfolding**  $exec-move-def$  .

**from**  $exec-meth-stk-offer[OF\ this,\ where\ stk''=[v]]$  **show**  $?thesis$

**by**  $(fastforce\ split:\ bop.splits\ intro:\ append-exec-meth-xt\ simp\ add:\ exec-move-def\ compxE2-size-convs\ compxE2-stack-xlift-convs)$

**qed**

**lemma**  $exec-move-LAssI$ :

$exec-move\ ci\ P\ t\ e\ h\ s\ ta\ h'\ s' \implies exec-move\ ci\ P\ t\ (V\ :=\ e)\ h\ s\ ta\ h'\ s'$

**unfolding**  $exec-move-def$  **by**  $auto$

**lemma**  $exec-move-LAss$ :

$pc < length\ (compE2\ e) \implies exec-move\ ci\ P\ t\ (V\ :=\ e)\ h\ (stk, loc, pc, xcp) = exec-move\ ci\ P\ t\ e\ h\ (stk, loc, pc, xcp)$

**unfolding**  $exec-move-def$  **by**  $(auto\ intro!:\ ext\ intro:\ exec-meth-take)$

**lemma**  $exec-move-AAccI1$ :

$exec-move\ ci\ P\ t\ e\ h\ s\ ta\ h'\ s' \implies exec-move\ ci\ P\ t\ (e[e'])\ h\ s\ ta\ h'\ s'$

**unfolding**  $exec-move-def$  **by**  $auto$

**lemma**  $exec-move-AAcc1$ :

$pc < length\ (compE2\ e) \implies exec-move\ ci\ P\ t\ (e[e'])\ h\ (stk, loc, pc, xcp) = exec-move\ ci\ P\ t\ e\ h\ (stk, loc, pc, xcp)$

**unfolding**  $exec-move-def$

**by**  $(auto\ intro!:\ ext\ intro:\ exec-meth-take-xt\ simp\ add:\ compxE2-size-convs)$

**lemma**  $exec-move-AAccI2$ :

**assumes**  $exec: exec-move\ ci\ P\ t\ e2\ h\ (stk, loc, pc, xcp)\ ta\ h'\ (stk', loc', pc', xcp')$

**shows**  $exec-move\ ci\ P\ t\ (e1[e2])\ h\ (stk\ @\ [v], loc, length\ (compE2\ e1) + pc, xcp)\ ta\ h'\ (stk'\ @\ [v], loc', length\ (compE2\ e1) + pc', xcp')$

**proof** –

**from** *exec* **have** *exec-meth* *ci* (*compP2* *P*) (*compE2* *e2*) (*compxE2* *e2* 0 0) *t* *h* (*stk*, *loc*, *pc*, *xcp*) *ta* *h'* (*stk'*, *loc'*, *pc'*, *xcp'*)

**unfolding** *exec-move-def* .

**from** *exec-meth-stk-offer*[*OF this*, **where** *stk''*=[*v*]] **show** *?thesis*

**by**(*fastforce* *intro*: *append-exec-meth-xt simp add: exec-move-def compxE2-size-convs compxE2-stack-xlift-convs*)  
**qed**

**lemma** *exec-move-AAssI1*:

*exec-move* *ci* *P* *t* *e* *h* *s* *ta* *h'* *s'*  $\implies$  *exec-move* *ci* *P* *t* (*e*[*e'*] := *e''*) *h* *s* *ta* *h'* *s'*

**unfolding** *exec-move-def* **by** *auto*

**lemma** *exec-move-AAss1*:

**assumes** *pc*: *pc* < *length* (*compE2* *e*)

**shows** *exec-move* *ci* *P* *t* (*e*[*e'*] := *e''*) *h* (*stk*, *loc*, *pc*, *xcp*) = *exec-move* *ci* *P* *t* *e* *h* (*stk*, *loc*, *pc*, *xcp*)  
(**is** *?lhs* = *?rhs*)

**proof**(*rule* *ext iffI*)+

**fix** *ta* *h'* *s'* **assume** *?rhs* *ta* *h'* *s'*

**thus** *?lhs* *ta* *h'* *s'* **by**(*rule* *exec-move-AAssI1*)

**next**

**fix** *ta* *h'* *s'* **assume** *?lhs* *ta* *h'* *s'*

**hence** *exec-meth* *ci* (*compP2* *P*) (*compE2* *e* @ *compE2* *e'* @ *compE2* *e''* @ [*AStore*, *Push Unit*])

(*compxE2* *e* 0 0 @ *shift* (*length* (*compE2* *e*)) (*compxE2* *e'* 0 (*Suc* 0) @ *compxE2* *e''* (*length* (*compE2* *e'*)) (*Suc* (*Suc* 0)))) *t*

*h* (*stk*, *loc*, *pc*, *xcp*) *ta* *h'* *s'* **by**(*simp add: exec-move-def shift-compxE2 ac-simps*)

**thus** *?rhs* *ta* *h'* *s'* **unfolding** *exec-move-def* **using** *pc* **by**(*rule* *exec-meth-take-xt*)

**qed**

**lemma** *exec-move-AAssI2*:

**assumes** *exec*: *exec-move* *ci* *P* *t* *e2* *h* (*stk*, *loc*, *pc*, *xcp*) *ta* *h'* (*stk'*, *loc'*, *pc'*, *xcp'*)

**shows** *exec-move* *ci* *P* *t* (*e1*[*e2*] := *e3*) *h* (*stk* @ [*v*], *loc*, *length* (*compE2* *e1*) + *pc*, *xcp*) *ta* *h'* (*stk'* @ [*v*], *loc'*, *length* (*compE2* *e1*) + *pc'*, *xcp'*)

**proof** –

**from** *exec* **have** *exec-meth* *ci* (*compP2* *P*) (*compE2* *e2*) (*compxE2* *e2* 0 0) *t* *h* (*stk*, *loc*, *pc*, *xcp*) *ta* *h'* (*stk'*, *loc'*, *pc'*, *xcp'*)

**unfolding** *exec-move-def* .

**from** *exec-meth-stk-offer*[*OF this*, **where** *stk''*=[*v*], *simplified stack-xlift-compxE2*, *simplified*]

**have** *exec-meth* *ci* (*compP2* *P*) (*compE2* *e2* @ *compE2* *e3* @ [*AStore*, *Push Unit*]) (*compxE2* *e2* 0 (*Suc* 0) @ *shift* (*length* (*compE2* *e2*)) (*compxE2* *e3* 0 (*Suc* (*Suc* 0)))) *t* *h* (*stk* @ [*v*], *loc*, *pc*, *xcp*) *ta* *h'* (*stk'* @ [*v*], *loc'*, *pc'*, *xcp'*)

**by**(*rule* *exec-meth-append-xt*)

**hence** *exec-meth* *ci* (*compP2* *P*) (*compE2* *e1* @ *compE2* *e2* @ *compE2* *e3* @ [*AStore*, *Push Unit*]) (*compxE2* *e1* 0 0 @ *shift* (*length* (*compE2* *e1*)) (*compxE2* *e2* 0 (*Suc* 0) @ *shift* (*length* (*compE2* *e2*)) (*compxE2* *e3* 0 (*Suc* (*Suc* 0)))) *t* *h* (*stk* @ [*v*], *loc*, *length* (*compE2* *e1*) + *pc*, *xcp*) *ta* *h'* (*stk'* @ [*v*], *loc'*, *length* (*compE2* *e1*) + *pc'*, *xcp'*)

**by**(*rule* *append-exec-meth-xt*) *auto*

**thus** *?thesis* **by**(*auto simp add: exec-move-def shift-compxE2 ac-simps*)

**qed**

**lemma** *exec-move-AAssI3*:

**assumes** *exec*: *exec-move* *ci* *P* *t* *e3* *h* (*stk*, *loc*, *pc*, *xcp*) *ta* *h'* (*stk'*, *loc'*, *pc'*, *xcp'*)

**shows** *exec-move* *ci* *P* *t* (*e1*[*e2*] := *e3*) *h* (*stk* @ [*v'*, *v*], *loc*, *length* (*compE2* *e1*) + *length* (*compE2* *e2*) + *pc*, *xcp*) *ta* *h'* (*stk'* @ [*v'*, *v*], *loc'*, *length* (*compE2* *e1*) + *length* (*compE2* *e2*) + *pc'*, *xcp'*)

**proof** –

**from** *exec* **have** *exec-meth* *ci* (*compP2* *P*) (*compE2* *e3*) (*compxE2* *e3* 0 0) *t h* (*stk*, *loc*, *pc*, *xcp*) *ta* *h'* (*stk'*, *loc'*, *pc'*, *xcp'*)

**unfolding** *exec-move-def* .

**from** *exec-meth-stk-offer*[*OF this*, **where** *stk''*=[*v'*, *v*], *simplified stack-xlift-compxE2*, *simplified*]  
**have** *exec-meth* *ci* (*compP2* *P*) (*compE2* *e3* @ [*AStore*, *Push Unit*]) (*compxE2* *e3* 0 (*Suc* (*Suc* 0)))  
*t h* (*stk* @ [*v'*, *v*], *loc*, *pc*, *xcp*) *ta* *h'* (*stk'* @ [*v'*, *v*], *loc'*, *pc'*, *xcp'*)

**by**(*rule exec-meth-append*)

**hence** *exec-meth* *ci* (*compP2* *P*) ((*compE2* *e1* @ *compE2* *e2*) @ *compE2* *e3* @ [*AStore*, *Push Unit*])

((*compxE2* *e1* 0 0 @ *compxE2* *e2* (*length* (*compE2* *e1*)) (*Suc* 0)) @ *shift* (*length* (*compE2* *e1* @ *compE2* *e2*)) (*compxE2* *e3* 0 (*Suc* (*Suc* 0)))) *t h* (*stk* @ [*v'*, *v*], *loc*, *length* (*compE2* *e1* @ *compE2* *e2*) + *pc*, *xcp*) *ta* *h'* (*stk'* @ [*v'*, *v*], *loc'*, *length* (*compE2* *e1* @ *compE2* *e2*) + *pc'*, *xcp'*)

**by**(*rule append-exec-meth-xt*) *auto*

**thus** ?thesis **by**(*auto simp add: exec-move-def shift-compxE2 ac-simps*)

**qed**

**lemma** *exec-move-ALengthI*:

*exec-move* *ci* *P t e h s ta h' s'*  $\implies$  *exec-move* *ci* *P t* (*e*·*length*) *h s ta h' s'*

**unfolding** *exec-move-def* **by** *auto*

**lemma** *exec-move-ALength*:

*pc* < *length* (*compE2* *e*)  $\implies$  *exec-move* *ci* *P t* (*e*·*length*) *h* (*stk*, *loc*, *pc*, *xcp*) = *exec-move* *ci* *P t e h* (*stk*, *loc*, *pc*, *xcp*)

**unfolding** *exec-move-def* **by**(*auto intro!: ext intro: exec-meth-take*)

**lemma** *exec-move-FAccI*:

*exec-move* *ci* *P t e h s ta h' s'*  $\implies$  *exec-move* *ci* *P t* (*e*·*F*{*D*}) *h s ta h' s'*

**unfolding** *exec-move-def* **by** *auto*

**lemma** *exec-move-FAcc*:

*pc* < *length* (*compE2* *e*)  $\implies$  *exec-move* *ci* *P t* (*e*·*F*{*D*}) *h* (*stk*, *loc*, *pc*, *xcp*) = *exec-move* *ci* *P t e h* (*stk*, *loc*, *pc*, *xcp*)

**unfolding** *exec-move-def* **by**(*auto intro!: ext intro: exec-meth-take*)

**lemma** *exec-move-FAssI1*:

*exec-move* *ci* *P t e h s ta h' s'*  $\implies$  *exec-move* *ci* *P t* (*e*·*F*{*D*} := *e'*) *h s ta h' s'*

**unfolding** *exec-move-def* **by** *auto*

**lemma** *exec-move-FAss1*:

*pc* < *length* (*compE2* *e*)  $\implies$  *exec-move* *ci* *P t* (*e*·*F*{*D*} := *e'*) *h* (*stk*, *loc*, *pc*, *xcp*) = *exec-move* *ci* *P t e h* (*stk*, *loc*, *pc*, *xcp*)

**unfolding** *exec-move-def*

**by**(*auto intro!: ext intro: exec-meth-take-xt simp add: compxE2-size-convs*)

**lemma** *exec-move-FAssI2*:

**assumes** *exec: exec-move* *ci* *P t e2 h* (*stk*, *loc*, *pc*, *xcp*) *ta h'* (*stk'*, *loc'*, *pc'*, *xcp'*)

**shows** *exec-move* *ci* *P t* (*e1*·*F*{*D*} := *e2*) *h* (*stk* @ [*v*], *loc*, *length* (*compE2* *e1*) + *pc*, *xcp*) *ta h'* (*stk'* @ [*v*], *loc'*, *length* (*compE2* *e1*) + *pc'*, *xcp'*)

**proof** –

**from** *exec* **have** *exec-meth* *ci* (*compP2* *P*) (*compE2* *e2*) (*compxE2* *e2* 0 0) *t h* (*stk*, *loc*, *pc*, *xcp*) *ta* *h'* (*stk'*, *loc'*, *pc'*, *xcp'*)

**unfolding** *exec-move-def* .

**from** *exec-meth-stk-offer*[*OF this*, **where** *stk''*=[*v*]] **show** ?thesis

**by**(*fastforce intro: append-exec-meth-xt simp add: exec-move-def compxE2-size-convs compxE2-stack-xlift-convs*)

qed

**lemma** *exec-move-CASI1*:

*exec-move* *ci P t e h s ta h' s'  $\implies$  exec-move ci P t (e·compareAndSwap(D·F, e', e'')) h s ta h' s'*  
**unfolding** *exec-move-def* **by** *auto*

**lemma** *exec-move-CASI1*:

**assumes** *pc*: *pc* < *length (compE2 e)*  
**shows** *exec-meth ci (compP2 P) (compE2 e @ compE2 e' @ compE2 e'' @ [CAS F D]) h (stk, loc, pc, xcp) = exec-move ci P t e h (stk, loc, pc, xcp)*  
**(is ?lhs = ?rhs)**  
**proof**(*rule ext iffI*)+  
**fix** *ta h' s' assume ?rhs ta h' s'*  
**thus** *?lhs ta h' s' by(rule exec-move-CASI1)*  
**next**  
**fix** *ta h' s' assume ?lhs ta h' s'*  
**hence** *exec-meth ci (compP2 P) (compE2 e @ compE2 e' @ compE2 e'' @ [CAS F D]) (compxE2 e 0 0 @ shift (length (compE2 e)) (compxE2 e' 0 (Suc 0) @ compxE2 e'' (length (compE2 e')) (Suc (Suc 0)))) t h (stk, loc, pc, xcp) ta h' s' by(simp add: exec-move-def shift-compxE2 ac-simps)*  
**thus** *?rhs ta h' s' unfolding exec-move-def using pc by(rule exec-meth-take-xt)*  
**qed**

**lemma** *exec-move-CASI2*:

**assumes** *exec*: *exec-move ci P t e2 h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')*  
**shows** *exec-move ci P t (e1·compareAndSwap(D·F, e2, e3)) h (stk @ [v], loc, length (compE2 e1) + pc, xcp) ta h' (stk' @ [v], loc', length (compE2 e1) + pc', xcp')*  
**proof** –  
**from** *exec* **have** *exec-meth ci (compP2 P) (compE2 e2) (compxE2 e2 0 0) t h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')*  
**unfolding** *exec-move-def* .  
**from** *exec-meth-stk-offer[OF this, where stk''=[v], simplified stack-xlift-compxE2, simplified]*  
**have** *exec-meth ci (compP2 P) (compE2 e2 @ compE2 e3 @ [CAS F D]) (compxE2 e2 0 (Suc 0) @ shift (length (compE2 e2)) (compxE2 e3 0 (Suc (Suc 0)))) t h (stk @ [v], loc, pc, xcp) ta h' (stk' @ [v], loc', pc', xcp')*  
**by**(*rule exec-meth-append-xt*)  
**hence** *exec-meth ci (compP2 P) (compE2 e1 @ compE2 e2 @ compE2 e3 @ [CAS F D]) (compxE2 e1 0 0 @ shift (length (compE2 e1)) (compxE2 e2 0 (Suc 0) @ shift (length (compE2 e2)) (compxE2 e3 0 (Suc (Suc 0)))) t h (stk @ [v], loc, length (compE2 e1) + pc, xcp) ta h' (stk' @ [v], loc', length (compE2 e1) + pc', xcp')*  
**by**(*rule append-exec-meth-xt*) *auto*  
**thus** *?thesis by(auto simp add: exec-move-def shift-compxE2 ac-simps)*  
**qed**

**lemma** *exec-move-CASI3*:

**assumes** *exec*: *exec-move ci P t e3 h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')*  
**shows** *exec-move ci P t (e1·compareAndSwap(D·F, e2, e3)) h (stk @ [v', v], loc, length (compE2 e1) + length (compE2 e2) + pc, xcp) ta h' (stk' @ [v', v], loc', length (compE2 e1) + length (compE2 e2) + pc', xcp')*  
**proof** –  
**from** *exec* **have** *exec-meth ci (compP2 P) (compE2 e3) (compxE2 e3 0 0) t h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')*  
**unfolding** *exec-move-def* .  
**from** *exec-meth-stk-offer[OF this, where stk''=[v', v], simplified stack-xlift-compxE2, simplified]*

**have** *exec-meth* *ci* (*compP2* *P*) (*compE2* *e3* @ [*CAS F D*]) (*compxE2* *e3* 0 (*Suc* (*Suc* 0))) *t h* (*stk* @ [*v'*, *v*], *loc*, *pc*, *xcp*) *ta h'* (*stk'* @ [*v'*, *v*], *loc'*, *pc'*, *xcp'*)  
**by**(*rule exec-meth-append*)  
**hence** *exec-meth* *ci* (*compP2* *P*) ((*compE2* *e1* @ *compE2* *e2*) @ *compE2* *e3* @ [*CAS F D*])  
((*compxE2* *e1* 0 0 @ *compxE2* *e2* (*length* (*compE2* *e1*)) (*Suc* 0)) @ *shift* (*length* (*compE2* *e1* @ *compE2* *e2*)) (*compxE2* *e3* 0 (*Suc* (*Suc* 0)))) *t h* (*stk* @ [*v'*, *v*], *loc*, *length* (*compE2* *e1* @ *compE2* *e2*) + *pc*, *xcp*) *ta h'* (*stk'* @ [*v'*, *v*], *loc'*, *length* (*compE2* *e1* @ *compE2* *e2*) + *pc'*, *xcp'*)  
**by**(*rule append-exec-meth-xt*) *auto*  
**thus** ?thesis **by**(*auto simp add: exec-move-def shift-compxE2 ac-simps*)  
**qed**

**lemma** *exec-move-CallI1*:

*exec-move* *ci P t e h s ta h' s'  $\implies$  exec-move* *ci P t (e.M(es)) h s ta h' s'*

**unfolding** *exec-move-def* **by** *auto*

**lemma** *exec-move-Call1*:

*pc* < *length* (*compE2* *e*)  $\implies$  *exec-move* *ci P t (e.M(es)) h* (*stk*, *loc*, *pc*, *xcp*) = *exec-move* *ci P t e h* (*stk*, *loc*, *pc*, *xcp*)

**unfolding** *exec-move-def*

**by**(*auto intro!: ext intro: exec-meth-take-xt simp add: compxEs2-size-convs*)

**lemma** *exec-move-CallI2*:

**assumes** *exec: exec-moves* *ci P t es h* (*stk*, *loc*, *pc*, *xcp*) *ta h'* (*stk'*, *loc'*, *pc'*, *xcp'*)

**shows** *exec-move* *ci P t (e.M(es)) h* (*stk* @ [*v*], *loc*, *length* (*compE2* *e*) + *pc*, *xcp*) *ta h'* (*stk'* @ [*v*], *loc'*, *length* (*compE2* *e*) + *pc'*, *xcp'*)

**proof** –

**from** *exec* **have** *exec-meth* *ci* (*compP2* *P*) (*compEs2* *es*) (*compxEs2* *es* 0 0) *t h* (*stk*, *loc*, *pc*, *xcp*) *ta h'* (*stk'*, *loc'*, *pc'*, *xcp'*)

**unfolding** *exec-moves-def* .

**from** *exec-meth-stk-offer*[*OF this*, **where** *stk''*=[*v*]] **show** ?thesis

**by**(*fastforce intro: append-exec-meth-xt simp add: exec-move-def compxEs2-size-convs compxEs2-stack-xlift-convs*)  
**qed**

**lemma** *exec-move-BlockNoneI*:

*exec-move* *ci P t e h s ta h' s'  $\implies$  exec-move* *ci P t { V:T=None; e } h s ta h' s'*

**unfolding** *exec-move-def* **by** *simp*

**lemma** *exec-move-BlockNone*:

*exec-move* *ci P t { V:T=None; e } = exec-move* *ci P t e*

**unfolding** *exec-move-def* **by**(*simp*)

**lemma** *exec-move-BlockSomeI*:

**assumes** *exec: exec-move* *ci P t e h* (*stk*, *loc*, *pc*, *xcp*) *ta h'* (*stk'*, *loc'*, *pc'*, *xcp'*)

**shows** *exec-move* *ci P t { V:T=[v]; e } h* (*stk*, *loc*, *Suc* (*Suc* *pc*), *xcp*) *ta h'* (*stk'*, *loc'*, *Suc* (*Suc* *pc'*), *xcp'*)

**proof** –

**let** ?ins = [*Push v*, *Store V*]

**from** *exec* **have** *exec-meth* *ci* (*compP2* *P*) (*compE2* *e*) (*compxE2* *e* 0 0) *t h* (*stk*, *loc*, *pc*, *xcp*) *ta h'* (*stk'*, *loc'*, *pc'*, *xcp'*)

**by**(*simp add: exec-move-def*)

**hence** *exec-meth* *ci* (*compP2* *P*) (?ins @ *compE2* *e*) (*shift* (*length* ?ins) (*compxE2* *e* 0 0)) *t h* (*stk*, *loc*, *length* ?ins + *pc*, *xcp*) *ta h'* (*stk'*, *loc'*, *length* ?ins + *pc'*, *xcp'*)

**by**(*rule append-exec-meth*) *auto*

**thus** ?thesis **by**(*simp add: exec-move-def shift-compxE2*)

qed

**lemma** *exec-move-BlockSome*:

*exec-move* ci  $P$   $t$   $\{V:T=[v]; e\}$   $h$  ( $stk$ ,  $loc$ ,  $Suc$  ( $Suc$   $pc$ ),  $xcp$ )  $ta$   $h'$  ( $stk'$ ,  $loc'$ ,  $Suc$  ( $Suc$   $pc'$ ),  $xcp'$ )  
 $=$   
*exec-move* ci  $P$   $t$   $e$   $h$  ( $stk$ ,  $loc$ ,  $pc$ ,  $xcp$ )  $ta$   $h'$  ( $stk'$ ,  $loc'$ ,  $pc'$ ,  $xcp'$ ) (**is**  $?lhs = ?rhs$ )

**proof**

**assume**  $?rhs$  **thus**  $?lhs$  **by**(rule *exec-move-BlockSomeI*)

**next**

**let**  $?ins = [Push\ v, Store\ V]$

**assume**  $?lhs$

**hence** *exec-meth* ci (*compP2*  $P$ ) ( $?ins @ compE2\ e$ ) (*shift* (*length*  $?ins$ ) (*compxE2*  $e\ 0\ 0$ ))  $t\ h$  ( $stk$ ,  $loc$ , *length*  $?ins + pc$ ,  $xcp$ )  $ta$   $h'$  ( $stk'$ ,  $loc'$ , *length*  $?ins + pc'$ ,  $xcp'$ )

**by**(*simp add: exec-move-def shift-compxE2*)

**hence** *exec-meth* ci (*compP2*  $P$ ) (*compE2*  $e$ ) (*compxE2*  $e\ 0\ 0$ )  $t\ h$  ( $stk$ ,  $loc$ ,  $pc$ ,  $xcp$ )  $ta$   $h'$  ( $stk'$ ,  $loc'$ , *length*  $?ins + pc' - \text{length } ?ins$ ,  $xcp'$ )

**by**(rule *exec-meth-drop*) *auto*

**thus**  $?rhs$  **by**(*simp add: exec-move-def*)

qed

**lemma** *exec-move-SyncI1*:

*exec-move* ci  $P$   $t$   $e$   $h\ s\ ta\ h'\ s' \implies \text{exec-move}\ ci\ P\ t\ (\text{sync}_V\ (e)\ e')\ h\ s\ ta\ h'\ s'$

**unfolding** *exec-move-def* **by** *auto*

**lemma** *exec-move-Sync1*:

**assumes**  $pc: pc < \text{length}\ (compE2\ e)$

**shows** *exec-move* ci  $P$   $t$  ( $\text{sync}_V\ (e)\ e'$ )  $h$  ( $stk$ ,  $loc$ ,  $pc$ ,  $xcp$ )  $=$  *exec-move* ci  $P$   $t$   $e$   $h$  ( $stk$ ,  $loc$ ,  $pc$ ,  $xcp$ )  
**(is**  $?lhs = ?rhs$ )

**proof**(rule *ext iffI*)**+**

**fix**  $ta\ h'\ s'$

**assume**  $?lhs\ ta\ h'\ s'$

**hence** *exec-meth* ci (*compP2*  $P$ ) (*compE2*  $e @ Dup \# Store\ V \# MEnter \# compE2\ e' @ [Load\ V,$   $MExit,$   $Goto\ 4,$   $Load\ V,$   $MExit,$   $ThrowExc]$ )

(*compxE2*  $e\ 0\ 0 @ \text{shift}\ (\text{length}\ (compE2\ e))\ (compxE2\ e'\ 3\ 0 @ [(3, 3 + \text{length}\ (compE2\ e'), None, 6 + \text{length}\ (compE2\ e'), 0)]))$ )

$t\ h$  ( $stk$ ,  $loc$ ,  $pc$ ,  $xcp$ )  $ta\ h'\ s'$

**by**(*simp add: shift-compxE2 ac-simps exec-move-def*)

**thus**  $?rhs\ ta\ h'\ s'$  **unfolding** *exec-move-def* **using**  $pc$  **by**(rule *exec-meth-take-xt*)

qed(rule *exec-move-SyncI1*)

**lemma** *exec-move-SyncI2*:

**assumes** *exec*: *exec-move* ci  $P$   $t$   $e$   $h$  ( $stk$ ,  $loc$ ,  $pc$ ,  $xcp$ )  $ta\ h'$  ( $stk'$ ,  $loc'$ ,  $pc'$ ,  $xcp'$ )

**shows** *exec-move* ci  $P$   $t$  ( $\text{sync}_V\ (o')\ e$ )  $h$  ( $stk$ ,  $loc$ , ( $Suc$  ( $Suc$  ( $Suc$  ( $\text{length}\ (compE2\ o') + pc$ ))))),  $xcp$ )  $ta\ h'$  ( $stk'$ ,  $loc'$ , ( $Suc$  ( $Suc$  ( $Suc$  ( $\text{length}\ (compE2\ o') + pc'$ ))))),  $xcp'$ )

**proof**  $-$

**let**  $?e = compE2\ o' @ [Dup, Store\ V, MEnter]$

**let**  $?e' = [Load\ V, MExit, Goto\ 4, Load\ V, MExit, ThrowExc]$

**from** *exec* **have** *exec-meth* ci (*compP2*  $P$ ) (*compE2*  $e$ ) (*compxE2*  $e\ 0\ 0$ )  $t\ h$  ( $stk$ ,  $loc$ ,  $pc$ ,  $xcp$ )  $ta\ h'$  ( $stk'$ ,  $loc'$ ,  $pc'$ ,  $xcp'$ )

**by**(*simp add: exec-move-def*)

**hence** *exec-meth* ci (*compP2*  $P$ ) ( $(?e @ compE2\ e) @ ?e'$ ) ( $(compxE2\ o'\ 0\ 0 @ \text{shift}\ (\text{length}\ ?e)\ (compxE2\ e\ 0\ 0)) @ [(\text{length}\ ?e, \text{length}\ ?e + \text{length}\ (compE2\ e), None, \text{length}\ ?e + \text{length}\ (compE2\ e) + 3, 0)]$ )  $t\ h$  ( $stk$ ,  $loc$ , ( $\text{length}\ ?e + pc$ ),  $xcp$ )  $ta\ h'$  ( $stk'$ ,  $loc'$ , ( $\text{length}\ ?e + pc'$ ),  $xcp'$ )

**by**(rule *exec-meth-append-xt[OF append-exec-meth-xt]*) *auto*



**thus** *?thesis* **by**(*simp add: eval-nat-numeral shift-compxE2 exec-move-def*)  
**qed**

**lemma** *exec-move-SeqI1*:

*exec-move ci P t e h s ta h' s'  $\implies$  exec-move ci P t (e;;e') h s ta h' s'*

**unfolding** *exec-move-def* **by** *auto*

**lemma** *exec-move-Seq1*:

**assumes** *pc*: *pc* < *length (compE2 e)*

**shows** *exec-move ci P t (e;;e') h (stk, loc, pc, xcp) = exec-move ci P t e h (stk, loc, pc, xcp)*

(**is** *?lhs = ?rhs*)

**proof**(*rule ext iffI*)+

**fix** *ta h' s'*

**assume** *?lhs ta h' s'*

**hence** *exec-meth ci (compP2 P) (compE2 e @ Pop # compE2 e') (compxE2 e 0 0 @ shift (length (compE2 e)) (compxE2 e' (Suc 0) 0)) t h (stk, loc, pc, xcp) ta h' s'*

**by**(*simp add: exec-move-def shift-compxE2*)

**thus** *?rhs ta h' s'* **unfolding** *exec-move-def* **using** *pc* **by**(*rule exec-meth-take-xt*)

**qed**(*rule exec-move-SeqI1*)

**lemma** *exec-move-SeqI2*:

**assumes** *exec*: *exec-move ci P t e h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')*

**shows** *exec-move ci P t (e';e) h (stk, loc, (Suc (length (compE2 e') + pc)), xcp) ta h' (stk', loc', (Suc (length (compE2 e') + pc')), xcp')*

**proof** –

**from** *exec* **have** *exec-meth ci (compP2 P) (compE2 e) (compxE2 e 0 0) t h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')*

**by**(*simp add: exec-move-def*)

**hence** *exec-meth ci (compP2 P) ((compE2 e' @ [Pop]) @ compE2 e) (compxE2 e' 0 0 @ shift (length (compE2 e' @ [Pop])) (compxE2 e 0 0)) t h (stk, loc, (length ((compE2 e') @ [Pop]) + pc), xcp) ta h' (stk', loc', (length ((compE2 e') @ [Pop]) + pc'), xcp')*

**by**(*rule append-exec-meth-xt*) *auto*

**thus** *?thesis* **by**(*simp add: shift-compxE2 exec-move-def*)

**qed**

**lemma** *exec-move-Seq2*:

**assumes** *pc*: *pc* < *length (compE2 e)*

**shows** *exec-move ci P t (e';e) h (stk, loc, Suc (length (compE2 e') + pc), xcp) ta*

*h' (stk', loc', Suc (length (compE2 e') + pc'), xcp') =*

*exec-move ci P t e h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')*

(**is** *?lhs = ?rhs*)

**proof**

**let** *?E = compE2 e' @ [Pop]*

**assume** *?lhs*

**hence** *exec-meth ci (compP2 P) (?E @ compE2 e) (compxE2 e' 0 0 @ shift (length ?E) (compxE2 e 0 0)) t h (stk, loc, length ?E + pc, xcp) ta h' (stk', loc', length ?E + pc', xcp')*

**by**(*simp add: exec-move-def shift-compxE2*)

**from** *exec-meth-drop-xt[OF this]* **show** *?rhs* **unfolding** *exec-move-def* **by** *fastforce*

**qed**(*rule exec-move-SeqI2*)

**lemma** *exec-move-CondI1*:

*exec-move ci P t e h s ta h' s'  $\implies$  exec-move ci P t (if (e) e1 else e2) h s ta h' s'*

**unfolding** *exec-move-def* **by** *auto*

**lemma** *exec-move-Cond1*:

**assumes** *pc*:  $pc < \text{length}(\text{compE2 } e)$   
**shows**  $\text{exec-move } ci \ P \ t \ (\text{if } (e) \ e1 \ \text{else } e2) \ h \ (stk, loc, pc, xcp) = \text{exec-move } ci \ P \ t \ e \ h \ (stk, loc, pc, xcp)$   
**(is ?lhs = ?rhs)**  
**proof**(*rule ext iffI*)+  
**let**  $?E = \text{IfFalse } (2 + \text{int}(\text{length}(\text{compE2 } e1))) \ \# \ \text{compE2 } e1 \ @ \ \text{Goto } (1 + \text{int}(\text{length}(\text{compE2 } e2))) \ \# \ \text{compE2 } e2$   
**let**  $?xt = \text{compxE2 } e1 \ (\text{Suc } 0) \ 0 \ @ \ \text{compxE2 } e2 \ (\text{Suc } (\text{Suc } (\text{length}(\text{compE2 } e1)))) \ 0$   
**fix**  $ta \ h' \ s'$   
**assume**  $?lhs \ ta \ h' \ s'$   
**hence**  $\text{exec-meth } ci \ (\text{compP2 } P) \ (\text{compE2 } e \ @ \ ?E) \ (\text{compxE2 } e \ 0 \ 0 \ @ \ \text{shift}(\text{length}(\text{compE2 } e)) \ ?xt) \ t \ h \ (stk, loc, pc, xcp) \ ta \ h' \ s'$   
**by**(*simp add: exec-move-def shift-compxE2 ac-simps*)  
**thus**  $?rhs \ ta \ h' \ s' \ \text{unfolding } \text{exec-move-def} \ \text{using } pc \ \text{by}(\text{rule } \text{exec-meth-take-xt})$   
**qed**(*rule exec-move-CondI1*)

**lemma** *exec-move-CondI2*:

**assumes** *exec*:  $\text{exec-move } ci \ P \ t \ e1 \ h \ (stk, loc, pc, xcp) \ ta \ h' \ (stk', loc', pc', xcp')$   
**shows**  $\text{exec-move } ci \ P \ t \ (\text{if } (e) \ e1 \ \text{else } e2) \ h \ (stk, loc, (\text{Suc } (\text{length}(\text{compE2 } e) + pc)), xcp) \ ta \ h' \ (stk', loc', (\text{Suc } (\text{length}(\text{compE2 } e) + pc')), xcp')$   
**proof** –  
**from** *exec* **have**  $\text{exec-meth } ci \ (\text{compP2 } P) \ (\text{compE2 } e1) \ (\text{compxE2 } e1 \ 0 \ 0) \ t \ h \ (stk, loc, pc, xcp) \ ta \ h' \ (stk', loc', pc', xcp')$   
**by**(*simp add: exec-move-def*)  
**hence**  $\text{exec-meth } ci \ (\text{compP2 } P) \ (((\text{compE2 } e \ @ \ [\text{IfFalse } (2 + \text{int}(\text{length}(\text{compE2 } e1))]) \ @ \ \text{compE2 } e1) \ @ \ \text{Goto } (1 + \text{int}(\text{length}(\text{compE2 } e2))) \ \# \ \text{compE2 } e2) \ (((\text{compxE2 } e \ 0 \ 0 \ @ \ \text{shift}(\text{length}(\text{compE2 } e \ @ \ [\text{IfFalse } (2 + \text{int}(\text{length}(\text{compE2 } e1))])]) \ (\text{compxE2 } e1 \ 0 \ 0)) \ @ \ (\text{compxE2 } e2 \ (\text{Suc } (\text{Suc } (\text{length}(\text{compE2 } e) + \text{length}(\text{compE2 } e1)))) \ 0)) \ t \ h \ (stk, loc, (\text{length}(\text{compE2 } e \ @ \ [\text{IfFalse } (2 + \text{int}(\text{length}(\text{compE2 } e1))]) + pc), xcp) \ ta \ h' \ (stk', loc', (\text{length}(\text{compE2 } e \ @ \ [\text{IfFalse } (2 + \text{int}(\text{length}(\text{compE2 } e1))]) + pc')), xcp')$   
**by** –(*rule exec-meth-append-xt, rule append-exec-meth-xt, auto*)  
**thus**  $?thesis \ \text{by}(\text{simp add: shift-compxE2 exec-move-def})$   
**qed**

**lemma** *exec-move-Cond2*:

**assumes** *pc*:  $pc < \text{length}(\text{compE2 } e1)$   
**shows**  $\text{exec-move } ci \ P \ t \ (\text{if } (e) \ e1 \ \text{else } e2) \ h \ (stk, loc, (\text{Suc } (\text{length}(\text{compE2 } e) + pc)), xcp) \ ta \ h' \ (stk', loc', (\text{Suc } (\text{length}(\text{compE2 } e) + pc')), xcp') = \text{exec-move } ci \ P \ t \ e1 \ h \ (stk, loc, pc, xcp) \ ta \ h' \ (stk', loc', pc', xcp')$   
**(is ?lhs = ?rhs)**  
**proof**  
**let**  $?E1 = \text{compE2 } e \ @ \ [\text{IfFalse } (2 + \text{int}(\text{length}(\text{compE2 } e1)))]$   
**let**  $?E2 = \text{Goto } (1 + \text{int}(\text{length}(\text{compE2 } e2))) \ \# \ \text{compE2 } e2$   
**assume**  $?lhs$   
**hence**  $\text{exec-meth } ci \ (\text{compP2 } P) \ (?E1 \ @ \ \text{compE2 } e1 \ @ \ ?E2) \ (\text{compxE2 } e \ 0 \ 0 \ @ \ \text{shift}(\text{length } ?E1) \ (\text{compxE2 } e1 \ 0 \ 0 \ @ \ \text{shift}(\text{length}(\text{compE2 } e1)) \ (\text{compxE2 } e2 \ (\text{Suc } 0) \ 0))) \ t \ h \ (stk, loc, \text{length } ?E1 + pc, xcp) \ ta \ h' \ (stk', loc', \text{length } ?E1 + pc', xcp')$   
**by**(*simp add: exec-move-def shift-compxE2 ac-simps*)  
**thus**  $?rhs \ \text{unfolding } \text{exec-move-def}$   
**by** –(*rule exec-meth-take-xt, drule exec-meth-drop-xt, auto simp add: pc*)  
**qed**(*rule exec-move-CondI2*)

**lemma** *exec-move-CondI3*:

**assumes** *exec*: *exec-move ci P t e2 h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')*  
**shows** *exec-move ci P t (if (e) e1 else e2) h (stk, loc, Suc (Suc (length (compE2 e) + length (compE2 e1) + pc)), xcp) ta h' (stk', loc', Suc (Suc (length (compE2 e) + length (compE2 e1) + pc')), xcp')*  
**proof** –  
**let** *?E* = *compE2 e @ IfFalse (2 + int (length (compE2 e1))) # compE2 e1 @ [Goto (1 + int (length (compE2 e2)))]*  
**let** *?xt* = *compxE2 e 0 0 @ compxE2 e1 (Suc (length (compE2 e))) 0*  
**from** *exec* **have** *exec-meth ci (compP2 P) (compE2 e2) (compxE2 e2 0 0) t h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')*  
**by**(*simp add: exec-move-def*)  
**hence** *exec-meth ci (compP2 P) (?E @ compE2 e2) (?xt @ shift (length ?E) (compxE2 e2 0 0)) t h (stk, loc, length ?E + pc, xcp) ta h' (stk', loc', length ?E + pc', xcp')*  
**by**(*rule append-exec-meth-xt*) *auto*  
**thus** *?thesis* **by**(*simp add: shift-compxE2 exec-move-def*)  
**qed**

**lemma** *exec-move-Cond3*:

*exec-move ci P t (if (e) e1 else e2) h (stk, loc, Suc (Suc (length (compE2 e) + length (compE2 e1) + pc)), xcp) ta*  
*h' (stk', loc', Suc (Suc (length (compE2 e) + length (compE2 e1) + pc')), xcp')* =  
*exec-move ci P t e2 h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')*  
*(is ?lhs = ?rhs)*

**proof**

**let** *?E* = *compE2 e @ IfFalse (2 + int (length (compE2 e1))) # compE2 e1 @ [Goto (1 + int (length (compE2 e2)))]*  
**let** *?xt* = *compxE2 e 0 0 @ compxE2 e1 (Suc (length (compE2 e))) 0*  
**assume** *?lhs*  
**hence** *exec-meth ci (compP2 P) (?E @ compE2 e2) (?xt @ shift (length ?E) (compxE2 e2 0 0)) t h (stk, loc, length ?E + pc, xcp) ta h' (stk', loc', length ?E + pc', xcp')*  
**by**(*simp add: shift-compxE2 exec-move-def*)  
**thus** *?rhs* **unfolding** *exec-move-def* **by** –(*drule exec-meth-drop-xt, auto*)  
**qed**(*rule exec-move-CondI3*)

**lemma** *exec-move-WhileI1*:

*exec-move ci P t e h s ta h' s'  $\implies$  exec-move ci P t (while (e) e') h s ta h' s'*  
**unfolding** *exec-move-def* **by** *auto*

**lemma** (*in ab-group-add*) *uminus-minus-left-commute*:

– *a – (b + c) = – b – (a + c)*  
**by** (*simp add: algebra-simps*)

**lemma** *exec-move-While1*:

**assumes** *pc*: *pc < length (compE2 e)*  
**shows** *exec-move ci P t (while (e) e') h (stk, loc, pc, xcp) = exec-move ci P t e h (stk, loc, pc, xcp)*  
*(is ?lhs = ?rhs)*  
**proof**(*rule ext iffI*) +  
**let** *?E* = *IfFalse (3 + int (length (compE2 e'))) # compE2 e' @ [Pop, Goto (– int (length (compE2 e)) + (–2 – int (length (compE2 e')))), Push Unit]*  
**let** *?xt* = *compxE2 e' (Suc 0) 0*  
**fix** *ta h' s'*  
**assume** *?lhs ta h' s'*  
**then** **have** *exec-meth ci (compP2 P) (compE2 e @ ?E) (compxE2 e 0 0 @ shift (length (compE2 e)) ?xt) t h (stk, loc, pc, xcp) ta h' s'*

by (simp add: exec-move-def shift-compxE2 algebra-simps uminus-minus-left-commute)  
 thus ?rhs ta h' s' **unfolding** exec-move-def **using** pc **by**(rule exec-meth-take-xt)  
**qed**(rule exec-move-WhileI1)

**lemma** exec-move-WhileI2:

**assumes** exec: exec-move ci P t e1 h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')  
**shows** exec-move ci P t (while (e) e1) h (stk, loc, (Suc (length (compE2 e) + pc)), xcp) ta h' (stk', loc', (Suc (length (compE2 e) + pc')), xcp')  
**proof** –  
 let ?E = compE2 e @ [IfFalse (3 + int (length (compE2 e1)))]  
 let ?E' = [Pop, Goto (- int (length (compE2 e)) + (-2 - int (length (compE2 e1)))), Push Unit]  
**from** exec **have** exec-meth ci (compP2 P) (compE2 e1) (compxE2 e1 0 0) t h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')  
**by**(simp add: exec-move-def)  
**hence** exec-meth ci (compP2 P) ((?E @ compE2 e1) @ ?E') (compxE2 e 0 0 @ shift (length ?E) (compxE2 e1 0 0)) t h (stk, loc, length ?E + pc, xcp) ta h' (stk', loc', length ?E + pc', xcp')  
**by** -(rule exec-meth-append, rule append-exec-meth-xt, auto)  
**thus** ?thesis **by** (simp add: shift-compxE2 exec-move-def algebra-simps uminus-minus-left-commute)  
**qed**

**lemma** exec-move-While2:

**assumes** pc: pc < length (compE2 e')  
**shows** exec-move ci P t (while (e) e') h (stk, loc, (Suc (length (compE2 e) + pc)), xcp) ta h' (stk', loc', (Suc (length (compE2 e) + pc')), xcp') =  
 exec-move ci P t e' h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')  
**(is ?lhs = ?rhs)**  
**proof**  
 let ?E = compE2 e @ [IfFalse (3 + int (length (compE2 e')))]  
 let ?E' = [Pop, Goto (- int (length (compE2 e)) + (-2 - int (length (compE2 e')))), Push Unit]  
**assume** ?lhs  
**hence** exec-meth ci (compP2 P) ((?E @ compE2 e') @ ?E') (compxE2 e 0 0 @ shift (length ?E) (compxE2 e' 0 0)) t h (stk, loc, length ?E + pc, xcp) ta h' (stk', loc', length ?E + pc', xcp')  
**by**(simp add: exec-move-def shift-compxE2 algebra-simps uminus-minus-left-commute)  
**thus** ?rhs **unfolding** exec-move-def **using** pc  
**by** -(drule exec-meth-take, simp, drule exec-meth-drop-xt, auto)  
**qed**(rule exec-move-WhileI2)

**lemma** exec-move-ThrowI:

exec-move ci P t e h s ta h' s'  $\implies$  exec-move ci P t (throw e) h s ta h' s'  
**unfolding** exec-move-def **by** auto

**lemma** exec-move-Throw:

pc < length (compE2 e)  $\implies$  exec-move ci P t (throw e) h (stk, loc, pc, xcp) = exec-move ci P t e h (stk, loc, pc, xcp)  
**unfolding** exec-move-def **by**(auto intro!: ext intro: exec-meth-take)

**lemma** exec-move-TryI1:

exec-move ci P t e h s ta h' s'  $\implies$  exec-move ci P t (try e catch(C V) e') h s ta h' s'  
**unfolding** exec-move-def **by** auto

**lemma** exec-move-TryI2:

**assumes** exec: exec-move ci P t e h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')  
**shows** exec-move ci P t (try e' catch(C V) e) h (stk, loc, Suc (Suc (length (compE2 e') + pc)), xcp) ta h' (stk', loc', Suc (Suc (length (compE2 e') + pc')), xcp')

**proof** –

```

let ?e = compE2 e' @ [Goto (int(size (compE2 e))+2), Store V]
from exec have exec-meth ci (compP2 P) (compE2 e) (compxE2 e 0 0) t h (stk, loc, pc, xcp) ta h'
(stk', loc', pc', xcp')
  by(simp add: exec-move-def)
  hence exec-meth ci (compP2 P) ((?e @ compE2 e) @ []) ((compxE2 e' 0 0 @ shift (length ?e)
(compxE2 e 0 0)) @ [(0, length (compE2 e'), ⌊C⌋, Suc (length (compE2 e')), 0)]) t h (stk, loc, (length
?e + pc), xcp) ta h' (stk', loc', (length ?e + pc'), xcp')
  by(rule exec-meth-append-xt[OF append-exec-meth-xt]) auto
thus ?thesis by(simp add: eval-nat-numeral shift-compxE2 exec-move-def)
qed

```

**lemma** exec-move-Try2:

```

exec-move ci P t (try e catch (C V) e') h (stk, loc, Suc (Suc (length (compE2 e) + pc)), xcp) ta
h' (stk', loc', Suc (Suc (length (compE2 e) + pc')), xcp') =
exec-move ci P t e' h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')
(is ?lhs = ?rhs)

```

**proof**

```

let ?E = compE2 e @ [Goto (int(size (compE2 e')+2), Store V]
let ?xt = [(0, length (compE2 e), ⌊C⌋, Suc (length (compE2 e)), 0)]
assume lhs: ?lhs
hence pc: pc < length (compE2 e')
  by(fastforce elim!: exec-meth.cases simp add: exec-move-def match-ex-table-append match-ex-entry
dest: match-ex-table-pcsD)
  from lhs have exec-meth ci (compP2 P) ((?E @ compE2 e') @ []) ((compxE2 e' 0 0 @ shift (length
?E) (compxE2 e' 0 0)) @ ?xt) t h (stk, loc, length ?E + pc, xcp) ta h' (stk', loc', length ?E + pc',
xcp')
  by(simp add: exec-move-def shift-compxE2 ac-simps)
  thus ?rhs unfolding exec-move-def using pc
  by-(drule exec-meth-drop-xt[OF exec-meth-take-xt], auto)
qed(rule exec-move-TryI2)

```

**lemma** exec-move-raise-xcp-pcD:

```

exec-move ci P t E h (stk, loc, pc, None) ta h' (stk', loc', pc', Some a) ==> pc' = pc
apply(cases compE2 E ! pc)
apply(auto simp add: exec-move-def elim!: exec-meth.cases split: if-split-asm sum.split-asm)
apply(auto split: extCallRet.split-asm simp add: split-beta)
done

```

**definition**  $\tau_{exec-meth} ::$

```

('addr, 'heap) check-instr => 'addr jvm-prog => 'addr instr list => ex-table => 'thread-id => 'heap
=> ('addr val list × 'addr val list × pc × 'addr option)
=> ('addr val list × 'addr val list × pc × 'addr option) => bool

```

**where**

```

 $\tau_{exec-meth} ci P ins xt t h s s' \longleftrightarrow$ 
 $exec-meth ci P ins xt t h s \in h s' \wedge (snd (snd (snd s)) = None \longrightarrow \tau_{instr} P h (fst s) (ins ! fst (snd$ 
 $(snd s))))$ 

```

**abbreviation**  $\tau_{exec-meth-a}$

**where**  $\tau_{exec-meth-a} \equiv \tau_{exec-meth} (Abs-check-instr check-instr')$

**abbreviation**  $\tau_{exec-meth-d}$

**where**  $\tau_{exec-meth-d} \equiv \tau_{exec-meth} (Abs-check-instr check-instr)$

**lemma**  $\tau exec\text{-}methI$  [intro]:

$\llbracket exec\text{-}meth\ ci\ P\ ins\ xt\ t\ h\ (stk, loc, pc, xcp) \varepsilon h\ s'; xcp = None \implies \tau instr\ P\ h\ stk\ (ins\ !\ pc) \rrbracket$   
 $\implies \tau exec\text{-}meth\ ci\ P\ ins\ xt\ t\ h\ (stk, loc, pc, xcp)\ s'$

**by**(simp add:  $\tau exec\text{-}meth\text{-}def$ )

**lemma**  $\tau exec\text{-}methE$  [elim]:

**assumes**  $\tau exec\text{-}meth\ ci\ P\ ins\ xt\ t\ h\ s\ s'$

**obtains**  $stk\ loc\ pc\ xcp$

**where**  $s = (stk, loc, pc, xcp)$

**and**  $exec\text{-}meth\ ci\ P\ ins\ xt\ t\ h\ (stk, loc, pc, xcp) \varepsilon h\ s'$

**and**  $xcp = None \implies \tau instr\ P\ h\ stk\ (ins\ !\ pc)$

**using**  $assms$

**by**(cases  $s$ )(auto simp add:  $\tau exec\text{-}meth\text{-}def$ )

**abbreviation**  $\tau Exec\text{-}methr ::$

$(addr, heap) check\text{-}instr \Rightarrow addr\ jvm\text{-}prog \Rightarrow addr\ instr\ list \Rightarrow ex\text{-}table \Rightarrow thread\text{-}id \Rightarrow heap$   
 $\Rightarrow (addr\ val\ list \times addr\ val\ list \times pc \times addr\ option)$   
 $\Rightarrow (addr\ val\ list \times addr\ val\ list \times pc \times addr\ option) \Rightarrow bool$

**where**

$\tau Exec\text{-}methr\ ci\ P\ ins\ xt\ t\ h == (\tau exec\text{-}meth\ ci\ P\ ins\ xt\ t\ h)^{**}$

**abbreviation**  $\tau Exec\text{-}methht ::$

$(addr, heap) check\text{-}instr \Rightarrow addr\ jvm\text{-}prog \Rightarrow addr\ instr\ list \Rightarrow ex\text{-}table \Rightarrow thread\text{-}id \Rightarrow heap$   
 $\Rightarrow (addr\ val\ list \times addr\ val\ list \times pc \times addr\ option)$   
 $\Rightarrow (addr\ val\ list \times addr\ val\ list \times pc \times addr\ option) \Rightarrow bool$

**where**

$\tau Exec\text{-}methht\ ci\ P\ ins\ xt\ t\ h == (\tau exec\text{-}meth\ ci\ P\ ins\ xt\ t\ h)^{++}$

**abbreviation**  $\tau Exec\text{-}methr\text{-}a$

**where**  $\tau Exec\text{-}methr\text{-}a \equiv \tau Exec\text{-}methr\ (Abs\text{-}check\text{-}instr\ check\text{-}instr')$

**abbreviation**  $\tau Exec\text{-}methr\text{-}d$

**where**  $\tau Exec\text{-}methr\text{-}d \equiv \tau Exec\text{-}methr\ (Abs\text{-}check\text{-}instr\ check\text{-}instr)$

**abbreviation**  $\tau Exec\text{-}methht\text{-}a$

**where**  $\tau Exec\text{-}methht\text{-}a \equiv \tau Exec\text{-}methht\ (Abs\text{-}check\text{-}instr\ check\text{-}instr')$

**abbreviation**  $\tau Exec\text{-}methht\text{-}d$

**where**  $\tau Exec\text{-}methht\text{-}d \equiv \tau Exec\text{-}methht\ (Abs\text{-}check\text{-}instr\ check\text{-}instr)$

**lemma**  $\tau Exec\text{-}methr\text{-}refl$ :  $\tau Exec\text{-}methr\ ci\ P\ ins\ xt\ t\ h\ s\ s\ ..$

**lemma**  $\tau Exec\text{-}methr\text{-}step'$ :

$\llbracket \tau Exec\text{-}methr\ ci\ P\ ins\ xt\ t\ h\ s\ (stk', loc', pc', xcp');$   
 $\tau exec\text{-}meth\ ci\ P\ ins\ xt\ t\ h\ (stk', loc', pc', xcp')\ s' \rrbracket$   
 $\implies \tau Exec\text{-}methr\ ci\ P\ ins\ xt\ t\ h\ s\ s'$

**by**(rule  $rtranclp.rtrancl\text{-}into\text{-}rtrancl$ )

**lemma**  $\tau Exec\text{-}methr\text{-}step$ :

$\llbracket \tau Exec\text{-}methr\ ci\ P\ ins\ xt\ t\ h\ s\ (stk', loc', pc', xcp');$   
 $exec\text{-}meth\ ci\ P\ ins\ xt\ t\ h\ (stk', loc', pc', xcp') \varepsilon h\ s';$   
 $xcp' = None \implies \tau instr\ P\ h\ stk'\ (ins\ !\ pc') \rrbracket$   
 $\implies \tau Exec\text{-}methr\ ci\ P\ ins\ xt\ t\ h\ s\ s'$

**by**(erule  $\tau Exec\text{-methr}\text{-step}$ ')(rule  $\tau exec\text{-meth}I$ )

**lemmas**  $\tau Exec\text{-methr}\text{-intros} = \tau Exec\text{-methr}\text{-refl } \tau Exec\text{-methr}\text{-step}$

**lemmas**  $\tau Exec\text{-methr}1step = \tau Exec\text{-methr}\text{-step}[OF \ \tau Exec\text{-methr}\text{-refl}]$

**lemmas**  $\tau Exec\text{-methr}2step = \tau Exec\text{-methr}\text{-step}[OF \ \tau Exec\text{-methr}\text{-step}, OF \ \tau Exec\text{-methr}\text{-refl}]$

**lemmas**  $\tau Exec\text{-methr}3step = \tau Exec\text{-methr}\text{-step}[OF \ \tau Exec\text{-methr}\text{-step}, OF \ \tau Exec\text{-methr}\text{-step}, OF \ \tau Exec\text{-methr}\text{-refl}]$

**lemma**  $\tau Exec\text{-methr}\text{-cases}$  [consumes 1, case-names refl step]:

**assumes**  $\tau Exec\text{-methr } ci \ P \ ins \ xt \ t \ h \ s \ s'$

**obtains**  $s = s'$

|  $stk' \ loc' \ pc' \ xcp'$

**where**  $\tau Exec\text{-methr } ci \ P \ ins \ xt \ t \ h \ s \ (stk', loc', pc', xcp')$

$exec\text{-meth } ci \ P \ ins \ xt \ t \ h \ (stk', loc', pc', xcp') \in h \ s'$

$xcp' = None \implies \tau instr \ P \ h \ stk' \ (ins ! pc')$

**using** *assms*

**by**(rule *rtranclp.cases*)(auto elim!:  $\tau exec\text{-meth}E$ )

**lemma**  $\tau Exec\text{-methr}\text{-induct}$  [consumes 1, case-names refl step]:

$\llbracket \tau Exec\text{-methr } ci \ P \ ins \ xt \ t \ h \ s \ s';$

$Q \ s;$

$\bigwedge stk \ loc \ pc \ xcp \ s'. \llbracket \tau Exec\text{-methr } ci \ P \ ins \ xt \ t \ h \ s \ (stk, loc, pc, xcp); exec\text{-meth } ci \ P \ ins \ xt \ t \ h \ (stk, loc, pc, xcp) \in h \ s';$

$xcp = None \implies \tau instr \ P \ h \ stk \ (ins ! pc); Q \ (stk, loc, pc, xcp) \rrbracket \implies Q \ s' \rrbracket$

$\implies Q \ s'$

**by**(erule (1) *rtranclp-induct*)(blast elim:  $\tau exec\text{-meth}E$ )

**lemma**  $\tau Exec\text{-methr}\text{-trans}$ :

$\llbracket \tau Exec\text{-methr } ci \ P \ ins \ xt \ t \ h \ s \ s'; \tau Exec\text{-methr } ci \ P \ ins \ xt \ t \ h \ s' \ s'' \rrbracket \implies \tau Exec\text{-methr } ci \ P \ ins \ xt \ t \ h \ s \ s''$

**by**(rule *rtranclp-trans*)

**lemmas**  $\tau Exec\text{-meth}\text{-induct}\text{-split} = \tau Exec\text{-methr}\text{-induct}[split\text{-format} \ (complete), consumes \ 1, case\text{-names} \ \tau Exec\text{-refl } \tau Exec\text{-step}]$

**lemma**  $\tau Exec\text{-methr}\text{-converse}\text{-cases}$  [consumes 1, case-names refl step]:

**assumes**  $\tau Exec\text{-methr } ci \ P \ ins \ xt \ t \ h \ s \ s'$

**obtains**  $s = s'$

|  $stk \ loc \ pc \ xcp \ s''$

**where**  $s = (stk, loc, pc, xcp)$

$exec\text{-meth } ci \ P \ ins \ xt \ t \ h \ (stk, loc, pc, xcp) \in h \ s''$

$xcp = None \implies \tau instr \ P \ h \ stk \ (ins ! pc)$

$\tau Exec\text{-methr } ci \ P \ ins \ xt \ t \ h \ s'' \ s'$

**using** *assms*

**by**(erule *converse-rtranclpE*)(blast elim:  $\tau exec\text{-meth}E$ )

**definition**  $\tau exec\text{-move} ::$

$(addr, heap) \ check\text{-instr} \Rightarrow addr \ J1\text{-prog} \Rightarrow thread\text{-id} \Rightarrow addr \ expr1 \Rightarrow heap$

$\Rightarrow (addr \ val \ list \times addr \ val \ list \times pc \times addr \ option)$

$\Rightarrow (addr \ val \ list \times addr \ val \ list \times pc \times addr \ option) \Rightarrow bool$

**where**

$\tau exec\text{-move } ci \ P \ t \ e \ h =$

$(\lambda(stk, loc, pc, xcp) \ s'. \ exec\text{-move } ci \ P \ t \ e \ h \ (stk, loc, pc, xcp) \in h \ s' \wedge \tau move2 \ P \ h \ stk \ e \ pc \ xcp)$

**definition**  $\tau exec\text{-moves} ::$

$(\text{'addr}, \text{'heap}) \text{ check-instr} \Rightarrow \text{'addr J1-prog} \Rightarrow \text{'thread-id} \Rightarrow \text{'addr expr1 list} \Rightarrow \text{'heap}$   
 $\Rightarrow (\text{'addr val list} \times \text{'addr val list} \times \text{pc} \times \text{'addr option})$   
 $\Rightarrow (\text{'addr val list} \times \text{'addr val list} \times \text{pc} \times \text{'addr option}) \Rightarrow \text{bool}$

**where**

$\tau\text{exec-moves ci } P \text{ t es } h =$   
 $(\lambda(\text{stk}, \text{loc}, \text{pc}, \text{xcp}) s'. \text{exec-moves ci } P \text{ t es } h (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \varepsilon h s' \wedge \tau\text{moves2 } P \text{ h stk es pc xcp})$

**lemma**  $\tau\text{exec-moveI}$ :

$\llbracket \text{exec-move ci } P \text{ t e } h (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \varepsilon h s'; \tau\text{move2 } P \text{ h stk e pc xcp} \rrbracket$   
 $\implies \tau\text{exec-move ci } P \text{ t e } h (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) s'$

**by**(*simp add:  $\tau\text{exec-move-def}$* )

**lemma**  $\tau\text{exec-moveE}$ :

**assumes**  $\tau\text{exec-move ci } P \text{ t e } h (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) s'$   
**obtains**  $\text{exec-move ci } P \text{ t e } h (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \varepsilon h s' \tau\text{move2 } P \text{ h stk e pc xcp}$

**using** *assms by*(*simp add:  $\tau\text{exec-move-def}$* )

**lemma**  $\tau\text{exec-movesI}$ :

$\llbracket \text{exec-moves ci } P \text{ t es } h (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \varepsilon h s'; \tau\text{moves2 } P \text{ h stk es pc xcp} \rrbracket$   
 $\implies \tau\text{exec-moves ci } P \text{ t es } h (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) s'$

**by**(*simp add:  $\tau\text{exec-moves-def}$* )

**lemma**  $\tau\text{exec-movesE}$ :

**assumes**  $\tau\text{exec-moves ci } P \text{ t es } h (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) s'$   
**obtains**  $\text{exec-moves ci } P \text{ t es } h (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \varepsilon h s' \tau\text{moves2 } P \text{ h stk es pc xcp}$

**using** *assms by*(*simp add:  $\tau\text{exec-moves-def}$* )

**lemma**  $\tau\text{exec-move-conv-}\tau\text{exec-meth}$ :

$\tau\text{exec-move ci } P \text{ t e} = \tau\text{exec-meth ci } (\text{compP2 } P) (\text{compE2 } e) (\text{compxE2 } e \text{ 0 } 0) t$

**by**(*auto simp add:  $\tau\text{exec-move-def}$   $\text{exec-move-def}$   $\tau\text{move2-iff}$   $\text{compP2-def}$   $\text{intro!}$ : ext  $\tau\text{exec-methI}$  elim!:  $\tau\text{exec-methE}$* )

**lemma**  $\tau\text{exec-moves-conv-}\tau\text{exec-meth}$ :

$\tau\text{exec-moves ci } P \text{ t es} = \tau\text{exec-meth ci } (\text{compP2 } P) (\text{compEs2 } es) (\text{compxEs2 } es \text{ 0 } 0) t$

**by**(*auto simp add:  $\tau\text{exec-moves-def}$   $\text{exec-moves-def}$   $\tau\text{moves2-iff}$   $\text{compP2-def}$   $\text{intro!}$ : ext  $\tau\text{exec-methI}$  elim!:  $\tau\text{exec-methE}$* )

**abbreviation**  $\tau\text{Exec-mover}$

**where**  $\tau\text{Exec-mover ci } P \text{ t e } h == (\tau\text{exec-move ci } P \text{ t e } h)^{\wedge**}$

**abbreviation**  $\tau\text{Exec-movet}$

**where**  $\tau\text{Exec-movet ci } P \text{ t e } h == (\tau\text{exec-move ci } P \text{ t e } h)^{\wedge++}$

**abbreviation**  $\tau\text{Exec-mover-a}$

**where**  $\tau\text{Exec-mover-a} \equiv \tau\text{Exec-mover } (\text{Abs-check-instr check-instr}')$

**abbreviation**  $\tau\text{Exec-mover-d}$

**where**  $\tau\text{Exec-mover-d} \equiv \tau\text{Exec-mover } (\text{Abs-check-instr check-instr})$

**abbreviation**  $\tau\text{Exec-movet-a}$

**where**  $\tau\text{Exec-movet-a} \equiv \tau\text{Exec-movet } (\text{Abs-check-instr check-instr}')$

**abbreviation**  $\tau\text{Exec-movet-d}$

**where**  $\tau\text{Exec-movet-d} \equiv \tau\text{Exec-movet } (\text{Abs-check-instr check-instr})$



**abbreviation**  $\tau Exec\text{-}movesr$

**where**  $\tau Exec\text{-}movesr\ ci\ P\ t\ e\ h == (\tau exec\text{-}moves\ ci\ P\ t\ e\ h)^{\wedge**}$

**abbreviation**  $\tau Exec\text{-}movest$

**where**  $\tau Exec\text{-}movest\ ci\ P\ t\ e\ h == (\tau exec\text{-}moves\ ci\ P\ t\ e\ h)^{\wedge++}$

**abbreviation**  $\tau Exec\text{-}movesr\text{-}a$

**where**  $\tau Exec\text{-}movesr\text{-}a \equiv \tau Exec\text{-}movesr\ (Abs\text{-}check\text{-}instr\ check\text{-}instr')$

**abbreviation**  $\tau Exec\text{-}movesr\text{-}d$

**where**  $\tau Exec\text{-}movesr\text{-}d \equiv \tau Exec\text{-}movesr\ (Abs\text{-}check\text{-}instr\ check\text{-}instr)$

**abbreviation**  $\tau Exec\text{-}movest\text{-}a$

**where**  $\tau Exec\text{-}movest\text{-}a \equiv \tau Exec\text{-}movest\ (Abs\text{-}check\text{-}instr\ check\text{-}instr')$

**abbreviation**  $\tau Exec\text{-}movest\text{-}d$

**where**  $\tau Exec\text{-}movest\text{-}d \equiv \tau Exec\text{-}movest\ (Abs\text{-}check\text{-}instr\ check\text{-}instr)$

**lemma**  $\tau Execr\text{-}refl: \tau Exec\text{-}mover\ ci\ P\ t\ e\ h\ s\ s$

**by**(rule  $rtranclp.rtrancl\text{-}refl$ )

**lemma**  $\tau Execsr\text{-}refl: \tau Exec\text{-}movesr\ ci\ P\ t\ e\ h\ s\ s$

**by**(rule  $rtranclp.rtrancl\text{-}refl$ )

**lemma**  $\tau Execr\text{-}step:$

$\llbracket \tau Exec\text{-}mover\ ci\ P\ t\ e\ h\ s\ (stk', loc', pc', xcp');$   
 $exec\text{-}move\ ci\ P\ t\ e\ h\ (stk', loc', pc', xcp') \in h\ s';$   
 $\tau move2\ P\ h\ stk'\ e\ pc'\ xcp' \rrbracket$   
 $\implies \tau Exec\text{-}mover\ ci\ P\ t\ e\ h\ s\ s'$

**by**(rule  $rtranclp.rtrancl\text{-}into\text{-}rtrancl$ )(auto elim:  $\tau exec\text{-}moveI$ )

**lemma**  $\tau Execsr\text{-}step:$

$\llbracket \tau Exec\text{-}movesr\ ci\ P\ t\ e\ s\ h\ s\ (stk', loc', pc', xcp');$   
 $exec\text{-}moves\ ci\ P\ t\ e\ s\ h\ (stk', loc', pc', xcp') \in h\ s';$   
 $\tau moves2\ P\ h\ stk'\ e\ s\ pc'\ xcp' \rrbracket$   
 $\implies \tau Exec\text{-}movesr\ ci\ P\ t\ e\ s\ h\ s\ s'$

**by**(rule  $rtranclp.rtrancl\text{-}into\text{-}rtrancl$ )(auto elim:  $\tau exec\text{-}movesI$ )

**lemma**  $\tau Exec\text{-}step:$

$\llbracket \tau Exec\text{-}mover\ ci\ P\ t\ e\ h\ s\ (stk', loc', pc', xcp');$   
 $exec\text{-}move\ ci\ P\ t\ e\ h\ (stk', loc', pc', xcp') \in h\ s';$   
 $\tau move2\ P\ h\ stk'\ e\ pc'\ xcp' \rrbracket$   
 $\implies \tau Exec\text{-}mover\ ci\ P\ t\ e\ h\ s\ s'$

**by**(rule  $tranclp.trancl\text{-}into\text{-}trancl$ )(auto intro:  $\tau exec\text{-}moveI$ )

**lemma**  $\tau Execst\text{-}step:$

$\llbracket \tau Exec\text{-}movest\ ci\ P\ t\ e\ s\ h\ s\ (stk', loc', pc', xcp');$   
 $exec\text{-}moves\ ci\ P\ t\ e\ s\ h\ (stk', loc', pc', xcp') \in h\ s';$   
 $\tau moves2\ P\ h\ stk'\ e\ s\ pc'\ xcp' \rrbracket$   
 $\implies \tau Exec\text{-}movest\ ci\ P\ t\ e\ s\ h\ s\ s'$

**by**(rule  $tranclp.trancl\text{-}into\text{-}trancl$ )(auto intro:  $\tau exec\text{-}movesI$ )

**lemmas**  $\tau Execr1step = \tau Execr\text{-}step[OF\ \tau Execr\text{-}refl]$

**lemmas**  $\tau Execr2step = \tau Execr\text{-}step[OF \tau Execr\text{-}step, OF \tau Execr\text{-}refl]$

**lemmas**  $\tau Execr3step = \tau Execr\text{-}step[OF \tau Execr\text{-}step, OF \tau Execr\text{-}step, OF \tau Execr\text{-}refl]$

**lemmas**  $\tau Execsr1step = \tau Execsr\text{-}step[OF \tau Execsr\text{-}refl]$

**lemmas**  $\tau Execsr2step = \tau Execsr\text{-}step[OF \tau Execsr\text{-}step, OF \tau Execsr\text{-}refl]$

**lemmas**  $\tau Execsr3step = \tau Execsr\text{-}step[OF \tau Execsr\text{-}step, OF \tau Execsr\text{-}step, OF \tau Execsr\text{-}refl]$

**lemma**  $\tau Exec1step$ :

$\llbracket exec\text{-}move \text{ ci } P \text{ t e h } s \in h \text{ s' ;}$   
 $\quad \tau move2 \text{ P h (fst s) e (fst (snd (snd s))) (snd (snd (snd s))) } \rrbracket$   
 $\implies \tau Exec\text{-}movet \text{ ci } P \text{ t e h } s \text{ s'}$

**by**(rule tranclp.r-into-trancl)(cases s, auto intro:  $\tau exec\text{-}moveI$ )

**lemmas**  $\tau Exec2step = \tau Exec\text{-}step[OF \tau Exec1step]$

**lemmas**  $\tau Exec3step = \tau Exec\text{-}step[OF \tau Exec\text{-}step, OF \tau Exec1step]$

**lemma**  $\tau Execst1step$ :

$\llbracket exec\text{-}moves \text{ ci } P \text{ t es h } s \in h \text{ s' ;}$   
 $\quad \tau moves2 \text{ P h (fst s) es (fst (snd (snd s))) (snd (snd (snd s))) } \rrbracket$   
 $\implies \tau Exec\text{-}movest \text{ ci } P \text{ t es h } s \text{ s'}$

**by**(rule tranclp.r-into-trancl)(cases s, auto intro:  $\tau exec\text{-}movesI$ )

**lemmas**  $\tau Execst2step = \tau Execst\text{-}step[OF \tau Execst1step]$

**lemmas**  $\tau Execst3step = \tau Execst\text{-}step[OF \tau Execst\text{-}step, OF \tau Execst1step]$

**lemma**  $\tau Execr\text{-}induct$  [consumes 1, case-names refl step]:

**assumes** major:  $\tau Exec\text{-}mover \text{ ci } P \text{ t e h (stk, loc, pc, xcp) (stk'', loc'', pc'', xcp')}$

**and** refl:  $Q \text{ stk loc pc xcp}$

**and** step:  $\bigwedge \text{stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \text{ stk}'' \text{ loc}'' \text{ pc}'' \text{ xcp}''.$

$\llbracket \tau Exec\text{-}mover \text{ ci } P \text{ t e h (stk, loc, pc, xcp) (stk', loc', pc', xcp') ;}$   
 $\quad \tau exec\text{-}move \text{ ci } P \text{ t e h (stk', loc', pc', xcp') (stk'', loc'', pc'', xcp'') ; } Q \text{ stk' loc' pc' xcp' } \rrbracket$   
 $\implies Q \text{ stk'' loc'' pc'' xcp''}$

**shows**  $Q \text{ stk'' loc'' pc'' xcp''}$

**using** major refl

**by**(rule rtranclp-induct4)(rule step)

**lemma**  $\tau Execsr\text{-}induct$  [consumes 1, case-names refl step]:

**assumes** major:  $\tau Exec\text{-}movesr \text{ ci } P \text{ t es h (stk, loc, pc, xcp) (stk'', loc'', pc'', xcp')}$

**and** refl:  $Q \text{ stk loc pc xcp}$

**and** step:  $\bigwedge \text{stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \text{ stk}'' \text{ loc}'' \text{ pc}'' \text{ xcp}''.$

$\llbracket \tau Exec\text{-}movesr \text{ ci } P \text{ t es h (stk, loc, pc, xcp) (stk', loc', pc', xcp') ;}$   
 $\quad \tau exec\text{-}moves \text{ ci } P \text{ t es h (stk', loc', pc', xcp') (stk'', loc'', pc'', xcp'') ; } Q \text{ stk' loc' pc' xcp' } \rrbracket$   
 $\implies Q \text{ stk'' loc'' pc'' xcp''}$

**shows**  $Q \text{ stk'' loc'' pc'' xcp''}$

**using** major refl

**by**(rule rtranclp-induct4)(rule step)

**lemma**  $\tau Exec\text{-}induct$  [consumes 1, case-names base step]:

**assumes** major:  $\tau Exec\text{-}movet \text{ ci } P \text{ t e h (stk, loc, pc, xcp) (stk'', loc'', pc'', xcp')}$

**and** base:  $\bigwedge \text{stk}' \text{ loc}' \text{ pc}' \text{ xcp}'. \tau exec\text{-}move \text{ ci } P \text{ t e h (stk, loc, pc, xcp) (stk', loc', pc', xcp')} \implies Q \text{ stk' loc' pc' xcp'}$

**and** step:  $\bigwedge \text{stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \text{ stk}'' \text{ loc}'' \text{ pc}'' \text{ xcp}''.$

$\llbracket \tau Exec\text{-}movet \text{ ci } P \text{ t e h (stk, loc, pc, xcp) (stk', loc', pc', xcp') ;}$   
 $\quad \tau exec\text{-}move \text{ ci } P \text{ t e h (stk', loc', pc', xcp') (stk'', loc'', pc'', xcp'') ; } Q \text{ stk' loc' pc' xcp' } \rrbracket$

$\implies Q \text{ stk}'' \text{ loc}'' \text{ pc}'' \text{ xcp}''$   
**shows**  $Q \text{ stk}'' \text{ loc}'' \text{ pc}'' \text{ xcp}''$   
**using** *major*  
**by**(rule tranclp-induct4)(erule base step)+

**lemma**  $\tau\text{Execst-induct}$  [consumes 1, case-names base step]:  
**assumes** *major*:  $\tau\text{Exec-movest } ci \ P \ t \ es \ h \ (stk, loc, pc, xcp) \ (stk'', loc'', pc'', xcp'')$   
**and** *base*:  $\bigwedge \text{stk}' \text{ loc}' \text{ pc}' \text{ xcp}' . \tau\text{exec-moves } ci \ P \ t \ es \ h \ (stk, loc, pc, xcp) \ (stk', loc', pc', xcp') \implies Q \text{ stk}' \text{ loc}' \text{ pc}' \text{ xcp}'$   
**and** *step*:  $\bigwedge \text{stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \text{ stk}'' \text{ loc}'' \text{ pc}'' \text{ xcp}'' .$   
 $\llbracket \tau\text{Exec-movest } ci \ P \ t \ es \ h \ (stk, loc, pc, xcp) \ (stk', loc', pc', xcp');$   
 $\tau\text{exec-moves } ci \ P \ t \ es \ h \ (stk', loc', pc', xcp') \ (stk'', loc'', pc'', xcp''); Q \text{ stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rrbracket$   
 $\implies Q \text{ stk}'' \text{ loc}'' \text{ pc}'' \text{ xcp}''$   
**shows**  $Q \text{ stk}'' \text{ loc}'' \text{ pc}'' \text{ xcp}''$   
**using** *major*  
**by**(rule tranclp-induct4)(erule base step)+

**lemma**  $\tau\text{Exec-mover-}\tau\text{Exec-methr}$ :  
 $\tau\text{Exec-mover } ci \ P \ t \ e = \tau\text{Exec-methr } ci \ (compP2 \ P) \ (compE2 \ e) \ (compxE2 \ e \ 0 \ 0) \ t$   
**by**(simp only:  $\tau\text{exec-move-conv-}\tau\text{exec-meth}$ )

**lemma**  $\tau\text{Exec-movesr-}\tau\text{Exec-methr}$ :  
 $\tau\text{Exec-movesr } ci \ P \ t \ es = \tau\text{Exec-methr } ci \ (compP2 \ P) \ (compEs2 \ es) \ (compxEs2 \ es \ 0 \ 0) \ t$   
**by**(simp only:  $\tau\text{exec-moves-conv-}\tau\text{exec-meth}$ )

**lemma**  $\tau\text{Exec-movet-}\tau\text{Exec-metht}$ :  
 $\tau\text{Exec-movet } ci \ P \ t \ e = \tau\text{Exec-metht } ci \ (compP2 \ P) \ (compE2 \ e) \ (compxE2 \ e \ 0 \ 0) \ t$   
**by**(simp only:  $\tau\text{exec-move-conv-}\tau\text{exec-meth}$ )

**lemma**  $\tau\text{Exec-movest-}\tau\text{Exec-metht}$ :  
 $\tau\text{Exec-movest } ci \ P \ t \ es = \tau\text{Exec-metht } ci \ (compP2 \ P) \ (compEs2 \ es) \ (compxEs2 \ es \ 0 \ 0) \ t$   
**by**(simp only:  $\tau\text{exec-moves-conv-}\tau\text{exec-meth}$ )

**lemma**  $\tau\text{Exec-mover-trans}$ :  
 $\llbracket \tau\text{Exec-mover } ci \ P \ t \ e \ h \ s \ s'; \tau\text{Exec-mover } ci \ P \ t \ e \ h \ s' \ s'' \rrbracket \implies \tau\text{Exec-mover } ci \ P \ t \ e \ h \ s \ s''$   
**by**(rule rtranclp-trans)

**lemma**  $\tau\text{Exec-movesr-trans}$ :  
 $\llbracket \tau\text{Exec-movesr } ci \ P \ t \ es \ h \ s \ s'; \tau\text{Exec-movesr } ci \ P \ t \ es \ h \ s' \ s'' \rrbracket \implies \tau\text{Exec-movesr } ci \ P \ t \ es \ h \ s \ s''$   
**by**(rule rtranclp-trans)

**lemma**  $\tau\text{Exec-movet-trans}$ :  
 $\llbracket \tau\text{Exec-movet } ci \ P \ t \ e \ h \ s \ s'; \tau\text{Exec-movet } ci \ P \ t \ e \ h \ s' \ s'' \rrbracket \implies \tau\text{Exec-movet } ci \ P \ t \ e \ h \ s \ s''$   
**by**(rule tranclp-trans)

**lemma**  $\tau\text{Exec-movest-trans}$ :  
 $\llbracket \tau\text{Exec-movest } ci \ P \ t \ es \ h \ s \ s'; \tau\text{Exec-movest } ci \ P \ t \ es \ h \ s' \ s'' \rrbracket \implies \tau\text{Exec-movest } ci \ P \ t \ es \ h \ s \ s''$   
**by**(rule tranclp-trans)

**lemma**  $\tau\text{exec-move-into-}\tau\text{exec-moves}$ :  
 $\tau\text{exec-move } ci \ P \ t \ e \ h \ s \ s' \implies \tau\text{exec-moves } ci \ P \ t \ (e \ \# \ es) \ h \ s'$   
**by**(cases s)(auto elim!:  $\tau\text{exec-moveE intro!}$ :  $\tau\text{exec-movesI simp add: exec-move-def exec-moves-def intro: } \tau\text{moves2Hd}$ )

**lemma**  $\tau Exec\text{-}mover\text{-}\tau Exec\text{-}movesr$ :

$\tau Exec\text{-}mover\ ci\ P\ t\ e\ h\ s\ s' \implies \tau Exec\text{-}movesr\ ci\ P\ t\ (e\ \# \ es)\ h\ s\ s'$

**by**(*induct rule: rtranclp-induct*)(*blast intro: rtranclp.rtrancl-into-rtrancl*  $\tau exec\text{-}move\text{-}into\text{-}\tau exec\text{-}moves$ ) +

**lemma**  $\tau Exec\text{-}movet\text{-}\tau Exec\text{-}movest$ :

$\tau Exec\text{-}movet\ ci\ P\ t\ e\ h\ s\ s' \implies \tau Exec\text{-}movest\ ci\ P\ t\ (e\ \# \ es)\ h\ s\ s'$

**by**(*induct rule: tranclp-induct*)(*blast intro: tranclp.trancl-into-trancl*  $\tau exec\text{-}move\text{-}into\text{-}\tau exec\text{-}moves$ ) +

**lemma** *exec-moves-append*:  $exec\text{-}moves\ ci\ P\ t\ es\ h\ s\ ta\ h'\ s' \implies exec\text{-}moves\ ci\ P\ t\ (es\ @\ es')\ h\ s\ ta\ h'\ s'$

**by**(*auto simp add: exec-moves-def*)

**lemma**  $\tau exec\text{-}moves\text{-}append$ :  $\tau exec\text{-}moves\ ci\ P\ t\ es\ h\ s\ s' \implies \tau exec\text{-}moves\ ci\ P\ t\ (es\ @\ es')\ h\ s\ s'$

**by**(*cases s*)(*auto elim!:  $\tau exec\text{-}movesE$  intro!:  $\tau exec\text{-}movesI$  exec-moves-append*)

**lemma**  $\tau Exec\text{-}movesr\text{-}append$  [*intro*]:

$\tau Exec\text{-}movesr\ ci\ P\ t\ es\ h\ s\ s' \implies \tau Exec\text{-}movesr\ ci\ P\ t\ (es\ @\ es')\ h\ s\ s'$

**by**(*induct rule: rtranclp-induct*)(*blast intro: rtranclp.rtrancl-into-rtrancl*  $\tau exec\text{-}moves\text{-}append$ ) +

**lemma**  $\tau Exec\text{-}movest\text{-}append$  [*intro*]:

$\tau Exec\text{-}movest\ ci\ P\ t\ es\ h\ s\ s' \implies \tau Exec\text{-}movest\ ci\ P\ t\ (es\ @\ es')\ h\ s\ s'$

**by**(*induct rule: tranclp-induct*)(*blast intro: tranclp.trancl-into-trancl*  $\tau exec\text{-}moves\text{-}append$ ) +

**lemma** *append-exec-moves*:

**assumes** *len*:  $length\ vs = length\ es'$

**and** *exec*:  $exec\text{-}moves\ ci\ P\ t\ es\ h\ (stk, loc, pc, xcp)\ ta\ h'\ (stk', loc', pc', xcp')$

**shows**  $exec\text{-}moves\ ci\ P\ t\ (es' @\ es)\ h\ ((stk @\ vs), loc, (length\ (compEs2\ es') + pc), xcp)\ ta\ h'\ ((stk' @\ vs), loc', (length\ (compEs2\ es') + pc'), xcp')$

**proof** –

**from** *exec* **have**  $exec\text{-}meth\ ci\ (compP2\ P)\ (compEs2\ es)\ (compEs2\ es\ 0\ 0)\ t\ h\ (stk, loc, pc, xcp)\ ta\ h'\ (stk', loc', pc', xcp')$

**unfolding** *exec-moves-def* .

**hence**  $exec\text{-}meth\ ci\ (compP2\ P)\ (compEs2\ es)\ (stack\text{-}xlift\ (length\ vs)\ (compEs2\ es\ 0\ 0))\ t\ h\ ((stk @\ vs), loc, pc, xcp)\ ta\ h'\ ((stk' @\ vs), loc', pc', xcp')$  **by**(*rule exec-meth-stk-offer*)

**hence**  $exec\text{-}meth\ ci\ (compP2\ P)\ (compEs2\ es' @\ compEs2\ es)\ (compEs2\ es' 0\ 0 @\ shift\ (length\ (compEs2\ es'))\ (stack\text{-}xlift\ (length\ vs))\ (compEs2\ es\ 0\ 0))\ t\ h\ ((stk @\ vs), loc, (length\ (compEs2\ es') + pc), xcp)\ ta\ h'\ ((stk' @\ vs), loc', (length\ (compEs2\ es') + pc'), xcp')$

**by**(*rule append-exec-meth-xt*) *auto*

**thus** *?thesis* **by**(*simp add: exec-moves-def stack-xlift-compEs2 shift-compEs2 len*)

**qed**

**lemma** *append- $\tau exec\text{-}moves$* :

$\llbracket length\ vs = length\ es';$

$\tau exec\text{-}moves\ ci\ P\ t\ es\ h\ (stk, loc, pc, xcp)\ (stk', loc', pc', xcp') \rrbracket$

$\implies \tau exec\text{-}moves\ ci\ P\ t\ (es' @\ es)\ h\ ((stk @\ vs), loc, (length\ (compEs2\ es') + pc), xcp)\ ((stk' @\ vs), loc', (length\ (compEs2\ es') + pc'), xcp')$

**by**(*auto elim!:  $\tau exec\text{-}movesE$  intro:  $\tau exec\text{-}movesI$  append-exec-moves  $\tau moves2\text{-}stk\text{-}append$  append- $\tau moves2$* )

**lemma** *append- $\tau Exec\text{-}movesr$* :

**assumes** *len*:  $length\ vs = length\ es'$

**shows**  $\tau Exec\text{-}movesr\ ci\ P\ t\ es\ h\ (stk, loc, pc, xcp)\ (stk', loc', pc', xcp')$

$\implies \tau Exec\text{-}movesr\ ci\ P\ t\ (es' @\ es)\ h\ ((stk @\ vs), loc, (length\ (compEs2\ es') + pc), xcp)\ ((stk' @\ vs), loc', (length\ (compEs2\ es') + pc'), xcp')$

**by**(*induct rule*: *rtranclp-induct4*)(*blast intro*: *rtranclp.rtrancl-into-rtrancl append-τexec-moves*[*OF len*])+

**lemma** *append-τExec-movest*:

**assumes** *len*:  $\text{length } vs = \text{length } es'$

**shows**  $\tau\text{Exec-movest } ci \ P \ t \ es \ h \ (stk, loc, pc, xcp) \ (stk', loc', pc', xcp')$

$\implies \tau\text{Exec-movest } ci \ P \ t \ (es' @ es) \ h \ ((stk @ vs), loc, (\text{length } (compEs2 \ es') + pc), xcp) \ ((stk' @ vs), loc', (\text{length } (compEs2 \ es') + pc'), xcp')$

**by**(*induct rule*: *tranclp-induct4*)(*blast intro*: *tranclp.trancl-into-trancl append-τexec-moves*[*OF len*])+

**lemma** *NewArray-τexecI*:

$\tau\text{exec-move } ci \ P \ t \ e \ h \ s \ s' \implies \tau\text{exec-move } ci \ P \ t \ (newA \ T[e]) \ h \ s \ s'$

**by**(*cases s*)(*blast elim*:  $\tau\text{exec-moveE}$  *intro*:  $\tau\text{exec-moveI}$   $\tau\text{move2-}\tau\text{moves2.intros exec-move-newArrayI}$ )

**lemma** *Cast-τexecI*:

$\tau\text{exec-move } ci \ P \ t \ e \ h \ s \ s' \implies \tau\text{exec-move } ci \ P \ t \ (Cast \ T \ e) \ h \ s \ s'$

**by**(*cases s*)(*blast elim*:  $\tau\text{exec-moveE}$  *intro*:  $\tau\text{exec-moveI}$   $\tau\text{move2-}\tau\text{moves2.intros exec-move-CastI}$ )

**lemma** *InstanceOf-τexecI*:

$\tau\text{exec-move } ci \ P \ t \ e \ h \ s \ s' \implies \tau\text{exec-move } ci \ P \ t \ (e \text{ instanceof } T) \ h \ s \ s'$

**by**(*cases s*)(*blast elim*:  $\tau\text{exec-moveE}$  *intro*:  $\tau\text{exec-moveI}$   $\tau\text{move2-}\tau\text{moves2.intros exec-move-InstanceOfI}$ )

**lemma** *BinOp-τexecI1*:

$\tau\text{exec-move } ci \ P \ t \ e \ h \ s \ s' \implies \tau\text{exec-move } ci \ P \ t \ (e \llbracket \text{bop} \rrbracket e') \ h \ s \ s'$

**by**(*cases s*)(*blast elim*:  $\tau\text{exec-moveE}$  *intro*:  $\tau\text{exec-moveI}$   $\tau\text{move2-}\tau\text{moves2.intros exec-move-BinOpI1}$ )

**lemma** *BinOp-τexecI2*:

$\tau\text{exec-move } ci \ P \ t \ e' \ h \ (stk, loc, pc, xcp) \ (stk', loc', pc', xcp')$

$\implies \tau\text{exec-move } ci \ P \ t \ (e \llbracket \text{bop} \rrbracket e') \ h \ ((stk @ [v]), loc, (\text{length } (compE2 \ e) + pc), xcp) \ ((stk' @ [v]), loc', (\text{length } (compE2 \ e) + pc'), xcp')$

**by**(*blast elim*:  $\tau\text{exec-moveE}$  *intro*:  $\tau\text{exec-moveI}$   $\tau\text{move2-}\tau\text{moves2.intros exec-move-BinOpI2}$   $\tau\text{move2-stk-append}$ )

**lemma** *LAss-τexecI*:

$\tau\text{exec-move } ci \ P \ t \ e \ h \ s \ s' \implies \tau\text{exec-move } ci \ P \ t \ (V := e) \ h \ s \ s'$

**by**(*cases s*)(*blast elim*:  $\tau\text{exec-moveE}$  *intro*:  $\tau\text{exec-moveI}$   $\tau\text{move2-}\tau\text{moves2.intros exec-move-LAssI}$ )

**lemma** *AAcc-τexecI1*:

$\tau\text{exec-move } ci \ P \ t \ e \ h \ s \ s' \implies \tau\text{exec-move } ci \ P \ t \ (e[i]) \ h \ s \ s'$

**by**(*cases s*)(*blast elim*:  $\tau\text{exec-moveE}$  *intro*:  $\tau\text{exec-moveI}$   $\tau\text{move2-}\tau\text{moves2.intros exec-move-AAccI1}$ )

**lemma** *AAcc-τexecI2*:

$\tau\text{exec-move } ci \ P \ t \ e' \ h \ (stk, loc, pc, xcp) \ (stk', loc', pc', xcp')$

$\implies \tau\text{exec-move } ci \ P \ t \ (e[e']) \ h \ ((stk @ [v]), loc, (\text{length } (compE2 \ e) + pc), xcp) \ ((stk' @ [v]), loc', (\text{length } (compE2 \ e) + pc'), xcp')$

**by**(*blast elim*:  $\tau\text{exec-moveE}$  *intro*:  $\tau\text{exec-moveI}$   $\tau\text{move2-}\tau\text{moves2.intros exec-move-AAccI2}$   $\tau\text{move2-stk-append}$ )

**lemma** *AAss-τexecI1*:

$\tau\text{exec-move } ci \ P \ t \ e \ h \ s \ s' \implies \tau\text{exec-move } ci \ P \ t \ (e[i] := e') \ h \ s \ s'$

**by**(*cases s*)(*blast elim*:  $\tau\text{exec-moveE}$  *intro*:  $\tau\text{exec-moveI}$   $\tau\text{move2-}\tau\text{moves2.intros exec-move-AAssI1}$ )

**lemma** *AAss-τexecI2*:

$\tau\text{exec-move } ci \ P \ t \ e' \ h \ (stk, loc, pc, xcp) \ (stk', loc', pc', xcp')$

$\implies \tau\text{exec-move } ci \ P \ t \ (e[e'] := e') \ h \ ((stk @ [v]), loc, (\text{length } (compE2 \ e) + pc), xcp) \ ((stk' @ [v]), loc', (\text{length } (compE2 \ e) + pc'), xcp')$

**by**(blast elim:  $\tau\text{exec-move}E$  intro:  $\tau\text{exec-move}I$   $\tau\text{move2-}\tau\text{moves2.intros exec-move-AAssI2}$   $\tau\text{move2-stk-append}$ )

**lemma** *AAss- $\tau\text{execI3}$* :

$\tau\text{exec-move ci } P \text{ t } e'' \text{ h } (stk, loc, pc, xcp) (stk', loc', pc', xcp')$   
 $\implies \tau\text{exec-move ci } P \text{ t } (e[e'] := e'') \text{ h } ((stk @ [v, v']), loc, (length (compE2 e) + length (compE2 e') + pc), xcp) ((stk' @ [v, v']), loc', (length (compE2 e) + length (compE2 e') + pc'), xcp')$   
**by**(blast elim:  $\tau\text{exec-move}E$  intro:  $\tau\text{exec-move}I$   $\tau\text{move2-}\tau\text{moves2.intros exec-move-AAssI3}$   $\tau\text{move2-stk-append}$ )

**lemma** *ALength- $\tau\text{execI}$* :

$\tau\text{exec-move ci } P \text{ t } e \text{ h } s \text{ s}' \implies \tau\text{exec-move ci } P \text{ t } (e \cdot \text{length}) \text{ h } s \text{ s}'$   
**by**(cases s)(blast elim:  $\tau\text{exec-move}E$  intro:  $\tau\text{exec-move}I$   $\tau\text{move2-}\tau\text{moves2.intros exec-move-ALengthI}$ )

**lemma** *FAcc- $\tau\text{execI}$* :

$\tau\text{exec-move ci } P \text{ t } e \text{ h } s \text{ s}' \implies \tau\text{exec-move ci } P \text{ t } (e \cdot F\{D\}) \text{ h } s \text{ s}'$   
**by**(cases s)(blast elim:  $\tau\text{exec-move}E$  intro:  $\tau\text{exec-move}I$   $\tau\text{move2-}\tau\text{moves2.intros exec-move-FAccI}$ )

**lemma** *FAss- $\tau\text{execI1}$* :

$\tau\text{exec-move ci } P \text{ t } e \text{ h } s \text{ s}' \implies \tau\text{exec-move ci } P \text{ t } (e \cdot F\{D\} := e') \text{ h } s \text{ s}'$   
**by**(cases s)(blast elim:  $\tau\text{exec-move}E$  intro:  $\tau\text{exec-move}I$   $\tau\text{move2-}\tau\text{moves2.intros exec-move-FAssI1}$ )

**lemma** *FAss- $\tau\text{execI2}$* :

$\tau\text{exec-move ci } P \text{ t } e' \text{ h } (stk, loc, pc, xcp) (stk', loc', pc', xcp')$   
 $\implies \tau\text{exec-move ci } P \text{ t } (e \cdot F\{D\} := e') \text{ h } ((stk @ [v]), loc, (length (compE2 e) + pc), xcp) ((stk' @ [v]), loc', (length (compE2 e) + pc'), xcp')$   
**by**(blast elim:  $\tau\text{exec-move}E$  intro:  $\tau\text{exec-move}I$   $\tau\text{move2-}\tau\text{moves2.intros exec-move-FAssI2}$   $\tau\text{move2-stk-append}$ )

**lemma** *CAS- $\tau\text{execI1}$* :

$\tau\text{exec-move ci } P \text{ t } e \text{ h } s \text{ s}' \implies \tau\text{exec-move ci } P \text{ t } (e \cdot \text{compareAndSwap}(D \cdot F, e', e'')) \text{ h } s \text{ s}'$   
**by**(cases s)(blast elim:  $\tau\text{exec-move}E$  intro:  $\tau\text{exec-move}I$   $\tau\text{move2-}\tau\text{moves2.intros exec-move-CASI1}$ )

**lemma** *CAS- $\tau\text{execI2}$* :

$\tau\text{exec-move ci } P \text{ t } e' \text{ h } (stk, loc, pc, xcp) (stk', loc', pc', xcp')$   
 $\implies \tau\text{exec-move ci } P \text{ t } (e \cdot \text{compareAndSwap}(D \cdot F, e', e'')) \text{ h } ((stk @ [v]), loc, (length (compE2 e) + pc), xcp) ((stk' @ [v]), loc', (length (compE2 e) + pc'), xcp')$   
**by**(blast elim:  $\tau\text{exec-move}E$  intro:  $\tau\text{exec-move}I$   $\tau\text{move2-}\tau\text{moves2.intros exec-move-CASI2}$   $\tau\text{move2-stk-append}$ )

**lemma** *CAS- $\tau\text{execI3}$* :

$\tau\text{exec-move ci } P \text{ t } e'' \text{ h } (stk, loc, pc, xcp) (stk', loc', pc', xcp')$   
 $\implies \tau\text{exec-move ci } P \text{ t } (e \cdot \text{compareAndSwap}(D \cdot F, e', e'')) \text{ h } ((stk @ [v, v']), loc, (length (compE2 e) + length (compE2 e') + pc), xcp) ((stk' @ [v, v']), loc', (length (compE2 e) + length (compE2 e') + pc'), xcp')$   
**by**(blast elim:  $\tau\text{exec-move}E$  intro:  $\tau\text{exec-move}I$   $\tau\text{move2-}\tau\text{moves2.intros exec-move-CASI3}$   $\tau\text{move2-stk-append}$ )

**lemma** *Call- $\tau\text{execI1}$* :

$\tau\text{exec-move ci } P \text{ t } e \text{ h } s \text{ s}' \implies \tau\text{exec-move ci } P \text{ t } (e \cdot M(es)) \text{ h } s \text{ s}'$   
**by**(cases s)(blast elim:  $\tau\text{exec-move}E$  intro:  $\tau\text{exec-move}I$   $\tau\text{move2-}\tau\text{moves2.intros exec-move-CallI1}$ )

**lemma** *Call- $\tau\text{execI2}$* :

$\tau\text{exec-moves ci } P \text{ t } es \text{ h } (stk, loc, pc, xcp) (stk', loc', pc', xcp')$   
 $\implies \tau\text{exec-move ci } P \text{ t } (e \cdot M(es)) \text{ h } ((stk @ [v]), loc, (length (compE2 e) + pc), xcp) ((stk' @ [v]), loc', (length (compE2 e) + pc'), xcp')$   
**by**(blast elim:  $\tau\text{exec-moves}E$  intro:  $\tau\text{exec-move}I$   $\tau\text{move2-}\tau\text{moves2.intros exec-move-CallI2}$   $\tau\text{moves2-stk-append}$ )

**lemma** *Block- $\tau\text{execI-Some}$* :

$\tau_{\text{exec-move}} ci P t e h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$   
 $\implies \tau_{\text{exec-move}} ci P t \{V:T=[v]; e\} h (stk, loc, Suc (Suc pc), xcp) (stk', loc', Suc (Suc pc'), xcp')$   
**by**(blast elim:  $\tau_{\text{exec-moveE}}$  intro:  $\tau_{\text{exec-moveI}} \tau_{\text{move2-}\tau_{\text{moves2}}.intros$   $\text{exec-move-BlockSomeI}$ )

**lemma** *Block- $\tau_{\text{execI-None}}$ :*

$\tau_{\text{exec-move}} ci P t e h s s' \implies \tau_{\text{exec-move}} ci P t \{V:T=None; e\} h s s'$   
**by**(cases s)(blast elim:  $\tau_{\text{exec-moveE}}$  intro:  $\tau_{\text{exec-moveI}} \tau_{\text{move2-}\tau_{\text{moves2}}.intros$   $\text{exec-move-BlockNoneI}$ )

**lemma** *Sync- $\tau_{\text{execI}}$ :*

$\tau_{\text{exec-move}} ci P t e h s s' \implies \tau_{\text{exec-move}} ci P t (sync_V (e) e') h s s'$   
**by**(cases s)(blast elim:  $\tau_{\text{exec-moveE}}$  intro:  $\tau_{\text{exec-moveI}} \tau_{\text{move2-}\tau_{\text{moves2}}.intros$   $\text{exec-move-SyncI1}$ )

**lemma** *Insync- $\tau_{\text{execI}}$ :*

$\tau_{\text{exec-move}} ci P t e' h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$   
 $\implies \tau_{\text{exec-move}} ci P t (sync_V (e) e') h (stk, loc, Suc (Suc (Suc (length (compE2 e) + pc))), xcp)$   
 $(stk', loc', Suc (Suc (Suc (length (compE2 e) + pc'))), xcp')$   
**by**(blast elim:  $\tau_{\text{exec-moveE}}$  intro:  $\tau_{\text{exec-moveI}} \tau_{\text{move2-}\tau_{\text{moves2}}.intros$   $\text{exec-move-SyncI2}$ )

**lemma** *Seq- $\tau_{\text{execI1}}$ :*

$\tau_{\text{exec-move}} ci P t e h s s' \implies \tau_{\text{exec-move}} ci P t (e;; e') h s s'$   
**by**(cases s)(blast elim:  $\tau_{\text{exec-moveE}}$  intro:  $\tau_{\text{exec-moveI}} \tau_{\text{move2-}\tau_{\text{moves2}}.intros$   $\text{exec-move-SeqI1}$ )

**lemma** *Seq- $\tau_{\text{execI2}}$ :*

$\tau_{\text{exec-move}} ci P t e' h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$   
 $\implies \tau_{\text{exec-move}} ci P t (e;; e') h (stk, loc, Suc (length (compE2 e) + pc), xcp) (stk', loc', Suc (length (compE2 e) + pc'), xcp')$   
**by**(blast elim:  $\tau_{\text{exec-moveE}}$  intro:  $\tau_{\text{exec-moveI}} \tau_{\text{move2-}\tau_{\text{moves2}}.intros$   $\text{exec-move-SeqI2}$ )

**lemma** *Cond- $\tau_{\text{execI1}}$ :*

$\tau_{\text{exec-move}} ci P t e h s s' \implies \tau_{\text{exec-move}} ci P t (if (e) e' else e') h s s'$   
**by**(cases s)(blast elim:  $\tau_{\text{exec-moveE}}$  intro:  $\tau_{\text{exec-moveI}} \tau_{\text{move2-}\tau_{\text{moves2}}.intros$   $\text{exec-move-CondI1}$ )

**lemma** *Cond- $\tau_{\text{execI2}}$ :*

$\tau_{\text{exec-move}} ci P t e' h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$   
 $\implies \tau_{\text{exec-move}} ci P t (if (e) e' else e'') h (stk, loc, Suc (length (compE2 e) + pc), xcp) (stk', loc', Suc (length (compE2 e) + pc'), xcp')$   
**by**(blast elim:  $\tau_{\text{exec-moveE}}$  intro:  $\tau_{\text{exec-moveI}} \tau_{\text{move2-}\tau_{\text{moves2}}.intros$   $\text{exec-move-CondI2}$ )

**lemma** *Cond- $\tau_{\text{execI3}}$ :*

$\tau_{\text{exec-move}} ci P t e'' h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$   
 $\implies \tau_{\text{exec-move}} ci P t (if (e) e' else e'') h (stk, loc, Suc (Suc (length (compE2 e) + length (compE2 e') + pc)), xcp) (stk', loc', Suc (Suc (length (compE2 e) + length (compE2 e') + pc'), xcp'))$   
**by**(blast elim:  $\tau_{\text{exec-moveE}}$  intro:  $\tau_{\text{exec-moveI}} \tau_{\text{move2-}\tau_{\text{moves2}}.intros$   $\text{exec-move-CondI3}$ )

**lemma** *While- $\tau_{\text{execI1}}$ :*

$\tau_{\text{exec-move}} ci P t e h s s' \implies \tau_{\text{exec-move}} ci P t (while (e) e') h s s'$   
**by**(cases s)(blast elim:  $\tau_{\text{exec-moveE}}$  intro:  $\tau_{\text{exec-moveI}} \tau_{\text{move2-}\tau_{\text{moves2}}.intros$   $\text{exec-move-WhileI1}$ )

**lemma** *While- $\tau_{\text{execI2}}$ :*

$\tau_{\text{exec-move}} ci P t e' h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$   
 $\implies \tau_{\text{exec-move}} ci P t (while (e) e') h (stk, loc, Suc (length (compE2 e) + pc), xcp) (stk', loc', Suc (length (compE2 e) + pc'), xcp')$   
**by**(blast elim:  $\tau_{\text{exec-moveE}}$  intro:  $\tau_{\text{exec-moveI}} \tau_{\text{move2-}\tau_{\text{moves2}}.intros$   $\text{exec-move-WhileI2}$ )

**lemma** *Throw- $\tau$ execI*:

$$\tau\text{exec-move } ci \ P \ t \ e \ h \ s \ s' \Longrightarrow \tau\text{exec-move } ci \ P \ t \ (\text{throw } e) \ h \ s \ s'$$

**by**(cases  $s$ )(blast elim:  $\tau\text{exec-moveE}$  intro:  $\tau\text{exec-moveI}$   $\tau\text{move2-}\tau\text{moves2.intros}$   $\text{exec-move-ThrowI}$ )

**lemma** *Try- $\tau$ execI1*:

$$\tau\text{exec-move } ci \ P \ t \ e \ h \ s \ s' \Longrightarrow \tau\text{exec-move } ci \ P \ t \ (\text{try } e \ \text{catch}(C \ V) \ e') \ h \ s \ s'$$

**by**(cases  $s$ )(blast elim:  $\tau\text{exec-moveE}$  intro:  $\tau\text{exec-moveI}$   $\tau\text{move2-}\tau\text{moves2.intros}$   $\text{exec-move-TryI1}$ )

**lemma** *Try- $\tau$ execI2*:

$$\tau\text{exec-move } ci \ P \ t \ e' \ h \ (stk, loc, pc, xcp) \ (stk', loc', pc', xcp')$$

$$\Longrightarrow \tau\text{exec-move } ci \ P \ t \ (\text{try } e \ \text{catch}(C \ V) \ e') \ h \ (stk, loc, \text{Suc}(\text{Suc}(\text{length}(\text{compE2 } e) + pc)), xcp) \\ (stk', loc', \text{Suc}(\text{Suc}(\text{length}(\text{compE2 } e) + pc')), xcp')$$

**by**(blast elim:  $\tau\text{exec-moveE}$  intro:  $\tau\text{exec-moveI}$   $\tau\text{move2-}\tau\text{moves2.intros}$   $\text{exec-move-TryI2}$ )

**lemma** *NewArray- $\tau$ ExecrI*:

$$\tau\text{Exec-mover } ci \ P \ t \ e \ h \ s \ s' \Longrightarrow \tau\text{Exec-mover } ci \ P \ t \ (\text{newA } T[e]) \ h \ s \ s'$$

**by**(induct rule:  $r\text{tranclp-induct}$ )(blast intro:  $r\text{tranclp.rtrancl-into-rtrancl}$   $\text{NewArray-}\tau\text{execI}$ ) +

**lemma** *Cast- $\tau$ ExecrI*:

$$\tau\text{Exec-mover } ci \ P \ t \ e \ h \ s \ s' \Longrightarrow \tau\text{Exec-mover } ci \ P \ t \ (\text{Cast } T \ e) \ h \ s \ s'$$

**by**(induct rule:  $r\text{tranclp-induct}$ )(blast intro:  $r\text{tranclp.rtrancl-into-rtrancl}$   $\text{Cast-}\tau\text{execI}$ ) +

**lemma** *InstanceOf- $\tau$ ExecrI*:

$$\tau\text{Exec-mover } ci \ P \ t \ e \ h \ s \ s' \Longrightarrow \tau\text{Exec-mover } ci \ P \ t \ (e \ \text{instanceof } T) \ h \ s \ s'$$

**by**(induct rule:  $r\text{tranclp-induct}$ )(blast intro:  $r\text{tranclp.rtrancl-into-rtrancl}$   $\text{InstanceOf-}\tau\text{execI}$ ) +

**lemma** *BinOp- $\tau$ ExecrI1*:

$$\tau\text{Exec-mover } ci \ P \ t \ e1 \ h \ s \ s' \Longrightarrow \tau\text{Exec-mover } ci \ P \ t \ (e1 \ \llbracket \text{bop} \rrbracket \ e2) \ h \ s \ s'$$

**by**(induct rule:  $r\text{tranclp-induct}$ )(blast intro:  $r\text{tranclp.rtrancl-into-rtrancl}$   $\text{BinOp-}\tau\text{execI1}$ ) +

**lemma** *BinOp- $\tau$ ExecrI2*:

$$\tau\text{Exec-mover } ci \ P \ t \ e2 \ h \ (stk, loc, pc, xcp) \ (stk', loc', pc', xcp')$$

$$\Longrightarrow \tau\text{Exec-mover } ci \ P \ t \ (e \ \llbracket \text{bop} \rrbracket \ e2) \ h \ ((stk \ @ \ [v]), loc, (\text{length}(\text{compE2 } e) + pc), xcp) \ ((stk' \ @ \ [v]), loc', (\text{length}(\text{compE2 } e) + pc'), xcp')$$

**by**(induct rule:  $\tau\text{Execr-induct}$ )(blast intro:  $r\text{tranclp.rtrancl-into-rtrancl}$   $\text{BinOp-}\tau\text{execI2}$ ) +

**lemma** *LAss- $\tau$ ExecrI*:

$$\tau\text{Exec-mover } ci \ P \ t \ e \ h \ s \ s' \Longrightarrow \tau\text{Exec-mover } ci \ P \ t \ (V := e) \ h \ s \ s'$$

**by**(induct rule:  $r\text{tranclp-induct}$ )(blast intro:  $r\text{tranclp.rtrancl-into-rtrancl}$   $\text{LAss-}\tau\text{execI}$ ) +

**lemma** *AAcc- $\tau$ ExecrI1*:

$$\tau\text{Exec-mover } ci \ P \ t \ e \ h \ s \ s' \Longrightarrow \tau\text{Exec-mover } ci \ P \ t \ (e[i]) \ h \ s \ s'$$

**by**(induct rule:  $r\text{tranclp-induct}$ )(blast intro:  $r\text{tranclp.rtrancl-into-rtrancl}$   $\text{AAcc-}\tau\text{execI1}$ ) +

**lemma** *AAcc- $\tau$ ExecrI2*:

$$\tau\text{Exec-mover } ci \ P \ t \ i \ h \ (stk, loc, pc, xcp) \ (stk', loc', pc', xcp')$$

$$\Longrightarrow \tau\text{Exec-mover } ci \ P \ t \ (a[i]) \ h \ ((stk \ @ \ [v]), loc, (\text{length}(\text{compE2 } a) + pc), xcp) \ ((stk' \ @ \ [v]), loc', (\text{length}(\text{compE2 } a) + pc'), xcp')$$

**by**(induct rule:  $\tau\text{Execr-induct}$ )(blast intro:  $r\text{tranclp.rtrancl-into-rtrancl}$   $\text{AAcc-}\tau\text{execI2}$ ) +

**lemma** *AAss- $\tau$ ExecrI1*:

$$\tau\text{Exec-mover } ci \ P \ t \ e \ h \ s \ s' \Longrightarrow \tau\text{Exec-mover } ci \ P \ t \ (e[i] := e') \ h \ s \ s'$$



**by**(*induct rule*: *rtranclp-induct*)(*blast intro*: *rtranclp.rtrancl-into-rtrancl AAss- $\tau$ execI1*)+

**lemma** *AAss- $\tau$ ExecrI2*:

$\tau$ Exec-mover *ci P t i h* (*stk*, *loc*, *pc*, *xcp*) (*stk'*, *loc'*, *pc'*, *xcp'*)  
 $\implies \tau$ Exec-mover *ci P t* (*a*[*i*] := *e*) *h* ((*stk* @ [*v*]), *loc*, (*length* (*compE2* *a*) + *pc*), *xcp*) ((*stk'* @ [*v*]),  
*loc'*, (*length* (*compE2* *a*) + *pc'*), *xcp'*)

**by**(*induct rule*:  $\tau$ Execr-induct)(*blast intro*: *rtranclp.rtrancl-into-rtrancl AAss- $\tau$ execI2*)+

**lemma** *AAss- $\tau$ ExecrI3*:

$\tau$ Exec-mover *ci P t e h* (*stk*, *loc*, *pc*, *xcp*) (*stk'*, *loc'*, *pc'*, *xcp'*)  
 $\implies \tau$ Exec-mover *ci P t* (*a*[*i*] := *e*) *h* ((*stk* @ [*v*, *v'*]), *loc*, (*length* (*compE2* *a*) + *length* (*compE2* *i*) + *pc*), *xcp*) ((*stk'* @ [*v*, *v'*]), *loc'*, (*length* (*compE2* *a*) + *length* (*compE2* *i*) + *pc'*), *xcp'*)

**by**(*induct rule*:  $\tau$ Execr-induct)(*blast intro*: *rtranclp.rtrancl-into-rtrancl AAss- $\tau$ execI3*)+

**lemma** *ALength- $\tau$ ExecrI*:

$\tau$ Exec-mover *ci P t e h s s'*  $\implies \tau$ Exec-mover *ci P t* (*e*·*length*) *h s s'*

**by**(*induct rule*: *rtranclp-induct*)(*blast intro*: *rtranclp.rtrancl-into-rtrancl ALength- $\tau$ execI*)+

**lemma** *FAcc- $\tau$ ExecrI*:

$\tau$ Exec-mover *ci P t e h s s'*  $\implies \tau$ Exec-mover *ci P t* (*e*·*F*{*D*}) *h s s'*

**by**(*induct rule*: *rtranclp-induct*)(*blast intro*: *rtranclp.rtrancl-into-rtrancl FAcc- $\tau$ execI*)+

**lemma** *FAss- $\tau$ ExecrI1*:

$\tau$ Exec-mover *ci P t e h s s'*  $\implies \tau$ Exec-mover *ci P t* (*e*·*F*{*D*} := *e'*) *h s s'*

**by**(*induct rule*: *rtranclp-induct*)(*blast intro*: *rtranclp.rtrancl-into-rtrancl FAss- $\tau$ execI1*)+

**lemma** *FAss- $\tau$ ExecrI2*:

$\tau$ Exec-mover *ci P t e' h* (*stk*, *loc*, *pc*, *xcp*) (*stk'*, *loc'*, *pc'*, *xcp'*)  
 $\implies \tau$ Exec-mover *ci P t* (*e*·*F*{*D*} := *e'*) *h* ((*stk* @ [*v*]), *loc*, (*length* (*compE2* *e*) + *pc*), *xcp*) ((*stk'* @ [*v*]), *loc'*, (*length* (*compE2* *e*) + *pc'*), *xcp'*)

**by**(*induct rule*:  $\tau$ Execr-induct)(*blast intro*: *rtranclp.rtrancl-into-rtrancl FAss- $\tau$ execI2*)+

**lemma** *CAS- $\tau$ ExecrI1*:

$\tau$ Exec-mover *ci P t e h s s'*  $\implies \tau$ Exec-mover *ci P t* (*e*·*compareAndSwap*(*D*·*F*, *e'*, *e''*)) *h s s'*

**by**(*induct rule*: *rtranclp-induct*)(*blast intro*: *rtranclp.rtrancl-into-rtrancl CAS- $\tau$ execI1*)+

**lemma** *CAS- $\tau$ ExecrI2*:

$\tau$ Exec-mover *ci P t e' h* (*stk*, *loc*, *pc*, *xcp*) (*stk'*, *loc'*, *pc'*, *xcp'*)  
 $\implies \tau$ Exec-mover *ci P t* (*e*·*compareAndSwap*(*D*·*F*, *e'*, *e''*)) *h* ((*stk* @ [*v*]), *loc*, (*length* (*compE2* *e*) + *pc*), *xcp*) ((*stk'* @ [*v*]), *loc'*, (*length* (*compE2* *e*) + *pc'*), *xcp'*)

**by**(*induct rule*:  $\tau$ Execr-induct)(*blast intro*: *rtranclp.rtrancl-into-rtrancl CAS- $\tau$ execI2*)+

**lemma** *CAS- $\tau$ ExecrI3*:

$\tau$ Exec-mover *ci P t e'' h* (*stk*, *loc*, *pc*, *xcp*) (*stk'*, *loc'*, *pc'*, *xcp'*)  
 $\implies \tau$ Exec-mover *ci P t* (*e*·*compareAndSwap*(*D*·*F*, *e'*, *e''*)) *h* ((*stk* @ [*v*, *v'*]), *loc*, (*length* (*compE2* *e*) + *length* (*compE2* *e'*) + *pc*), *xcp*) ((*stk'* @ [*v*, *v'*]), *loc'*, (*length* (*compE2* *e*) + *length* (*compE2* *e'*) + *pc'*), *xcp'*)

**by**(*induct rule*:  $\tau$ Execr-induct)(*blast intro*: *rtranclp.rtrancl-into-rtrancl CAS- $\tau$ execI3*)+

**lemma** *Call- $\tau$ ExecrI1*:

$\tau$ Exec-mover *ci P t obj h s s'*  $\implies \tau$ Exec-mover *ci P t* (*obj*·*M'*(*es*)) *h s s'*

**by**(*induct rule*: *rtranclp-induct*)(*blast intro*: *rtranclp.rtrancl-into-rtrancl Call- $\tau$ execI1*)+

**lemma** *Call- $\tau$ ExecrI2*:

$\tau\text{Exec-movesr } ci P t es h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$   
 $\implies \tau\text{Exec-mover } ci P t (obj.M'(es)) h ((stk @ [v]), loc, (length (compE2 obj) + pc), xcp) ((stk' @ [v]), loc', (length (compE2 obj) + pc'), xcp')$   
**by**(induct rule:  $\tau\text{Execsr-induct}$ )(blast intro:  $rtranclp.rtrancl\text{-into-}rtrancl \text{ Call-}\tau\text{execI2}$ )+

**lemma** *Block- $\tau\text{ExecrI-Some}$ :*

$\tau\text{Exec-mover } ci P t e h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$   
 $\implies \tau\text{Exec-mover } ci P t \{V:T=[v]; e\} h (stk, loc, (Suc (Suc pc)), xcp) (stk', loc', (Suc (Suc pc')), xcp')$   
**by**(induct rule:  $\tau\text{Execr-induct}$ )(blast intro:  $rtranclp.rtrancl\text{-into-}rtrancl \text{ Block-}\tau\text{execI-Some}$ )+

**lemma** *Block- $\tau\text{ExecrI-None}$ :*

$\tau\text{Exec-mover } ci P t e h s s' \implies \tau\text{Exec-mover } ci P t \{V:T=None; e\} h s s'$   
**by**(induct rule:  $rtranclp\text{-induct}$ )(blast intro:  $rtranclp.rtrancl\text{-into-}rtrancl \text{ Block-}\tau\text{execI-None}$ )+

**lemma** *Sync- $\tau\text{ExecrI}$ :*

$\tau\text{Exec-mover } ci P t e h s s' \implies \tau\text{Exec-mover } ci P t (sync_V (e) e') h s s'$   
**by**(induct rule:  $rtranclp\text{-induct}$ )(blast intro:  $rtranclp.rtrancl\text{-into-}rtrancl \text{ Sync-}\tau\text{execI}$ )+

**lemma** *Insync- $\tau\text{ExecrI}$ :*

$\tau\text{Exec-mover } ci P t e' h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$   
 $\implies \tau\text{Exec-mover } ci P t (sync_V (e) e') h (stk, loc, (Suc (Suc (Suc (length (compE2 e) + pc)))))$   
 $(xcp) (stk', loc', (Suc (Suc (Suc (length (compE2 e) + pc')))))$   
 $(xcp')$   
**by**(induct rule:  $\tau\text{Execr-induct}$ )(blast intro:  $rtranclp.rtrancl\text{-into-}rtrancl \text{ Insync-}\tau\text{execI}$ )+

**lemma** *Seq- $\tau\text{ExecrI1}$ :*

$\tau\text{Exec-mover } ci P t e h s s' \implies \tau\text{Exec-mover } ci P t (e;;e') h s s'$   
**by**(induct rule:  $rtranclp\text{-induct}$ )(blast intro:  $rtranclp.rtrancl\text{-into-}rtrancl \text{ Seq-}\tau\text{execI1}$ )+

**lemma** *Seq- $\tau\text{ExecrI2}$ :*

$\tau\text{Exec-mover } ci P t e h (stk, loc, pc, xcp) (stk', loc', pc', xcp') \implies$   
 $\tau\text{Exec-mover } ci P t (e';e) h (stk, loc, (Suc (length (compE2 e') + pc)), xcp) (stk', loc', (Suc (length (compE2 e') + pc')), xcp')$   
**by**(induct rule:  $\tau\text{Execr-induct}$ )(blast intro:  $rtranclp.rtrancl\text{-into-}rtrancl \text{ Seq-}\tau\text{execI2}$ )+

**lemma** *Cond- $\tau\text{ExecrI1}$ :*

$\tau\text{Exec-mover } ci P t e h s s' \implies \tau\text{Exec-mover } ci P t (\text{if } (e) e1 \text{ else } e2) h s s'$   
**by**(induct rule:  $rtranclp\text{-induct}$ )(blast intro:  $rtranclp.rtrancl\text{-into-}rtrancl \text{ Cond-}\tau\text{execI1}$ )+

**lemma** *Cond- $\tau\text{ExecrI2}$ :*

$\tau\text{Exec-mover } ci P t e1 h (stk, loc, pc, xcp) (stk', loc', pc', xcp') \implies$   
 $\tau\text{Exec-mover } ci P t (\text{if } (e) e1 \text{ else } e2) h (stk, loc, (Suc (length (compE2 e) + pc)), xcp) (stk', loc', (Suc (length (compE2 e) + pc')), xcp')$   
**by**(induct rule:  $\tau\text{Execr-induct}$ )(blast intro:  $rtranclp.rtrancl\text{-into-}rtrancl \text{ Cond-}\tau\text{execI2}$ )+

**lemma** *Cond- $\tau\text{ExecrI3}$ :*

$\tau\text{Exec-mover } ci P t e2 h (stk, loc, pc, xcp) (stk', loc', pc', xcp') \implies$   
 $\tau\text{Exec-mover } ci P t (\text{if } (e) e1 \text{ else } e2) h (stk, loc, (Suc (Suc (length (compE2 e) + length (compE2 e1) + pc))), xcp) (stk', loc', (Suc (Suc (length (compE2 e) + length (compE2 e1) + pc')), xcp'))$   
**by**(induct rule:  $\tau\text{Execr-induct}$ )(blast intro:  $rtranclp.rtrancl\text{-into-}rtrancl \text{ Cond-}\tau\text{execI3}$ )+

**lemma** *While- $\tau\text{ExecrI1}$ :*

$\tau\text{Exec-mover } ci P t c h s s' \implies \tau\text{Exec-mover } ci P t (\text{while } (c) e) h s s'$   
**by**(induct rule:  $rtranclp\text{-induct}$ )(blast intro:  $rtranclp.rtrancl\text{-into-}rtrancl \text{ While-}\tau\text{execI1}$ )+

**lemma** *While- $\tau$ ExecrI2*:

$\tau\text{Exec-mover } ci \ P \ t \ E \ h \ (stk, loc, pc, xcp) \ (stk', loc', pc', xcp')$   
 $\implies \tau\text{Exec-mover } ci \ P \ t \ (\text{while } (c) \ E) \ h \ (stk, loc, (Suc \ (\text{length} \ (\text{compE2} \ c) + pc)), xcp) \ (stk', loc',$   
 $(Suc \ (\text{length} \ (\text{compE2} \ c) + pc')), xcp')$   
**by**(*induct rule*:  $\tau\text{Execr-induct}$ )(*blast intro*:  $rtranclp.rtrancl\text{-into-}rtrancl \ \text{While-}\tau\text{execI2}$ )+

**lemma** *Throw- $\tau$ ExecrI*:

$\tau\text{Exec-mover } ci \ P \ t \ e \ h \ s \ s' \implies \tau\text{Exec-mover } ci \ P \ t \ (\text{throw } e) \ h \ s \ s'$   
**by**(*induct rule*:  $rtranclp\text{-induct}$ )(*blast intro*:  $rtranclp.rtrancl\text{-into-}rtrancl \ \text{Throw-}\tau\text{execI}$ )+

**lemma** *Try- $\tau$ ExecrI1*:

$\tau\text{Exec-mover } ci \ P \ t \ E \ h \ s \ s' \implies \tau\text{Exec-mover } ci \ P \ t \ (\text{try } E \ \text{catch}(C' \ V) \ e) \ h \ s \ s'$   
**by**(*induct rule*:  $rtranclp\text{-induct}$ )(*blast intro*:  $rtranclp.rtrancl\text{-into-}rtrancl \ \text{Try-}\tau\text{execI1}$ )+

**lemma** *Try- $\tau$ ExecrI2*:

$\tau\text{Exec-mover } ci \ P \ t \ e \ h \ (stk, loc, pc, xcp) \ (stk', loc', pc', xcp')$   
 $\implies \tau\text{Exec-mover } ci \ P \ t \ (\text{try } E \ \text{catch}(C' \ V) \ e) \ h \ (stk, loc, (Suc \ (Suc \ (\text{length} \ (\text{compE2} \ E) + pc))),$   
 $xcp) \ (stk', loc', (Suc \ (Suc \ (\text{length} \ (\text{compE2} \ E) + pc')), xcp')$   
**by**(*induct rule*:  $\tau\text{Execr-induct}$ )(*blast intro*:  $rtranclp.rtrancl\text{-into-}rtrancl \ \text{Try-}\tau\text{execI2}$ )+

**lemma** *NewArray- $\tau$ ExecI*:

$\tau\text{Exec-movet } ci \ P \ t \ e \ h \ s \ s' \implies \tau\text{Exec-movet } ci \ P \ t \ (\text{newA } T[e]) \ h \ s \ s'$   
**by**(*induct rule*:  $tranclp\text{-induct}$ )(*blast intro*:  $tranclp.trancl\text{-into-}trancl \ \text{NewArray-}\tau\text{execI}$ )+

**lemma** *Cast- $\tau$ ExecI*:

$\tau\text{Exec-movet } ci \ P \ t \ e \ h \ s \ s' \implies \tau\text{Exec-movet } ci \ P \ t \ (\text{Cast } T \ e) \ h \ s \ s'$   
**by**(*induct rule*:  $tranclp\text{-induct}$ )(*blast intro*:  $tranclp.trancl\text{-into-}trancl \ \text{Cast-}\tau\text{execI}$ )+

**lemma** *InstanceOf- $\tau$ ExecI*:

$\tau\text{Exec-movet } ci \ P \ t \ e \ h \ s \ s' \implies \tau\text{Exec-movet } ci \ P \ t \ (e \ \text{instanceof } T) \ h \ s \ s'$   
**by**(*induct rule*:  $tranclp\text{-induct}$ )(*blast intro*:  $tranclp.trancl\text{-into-}trancl \ \text{InstanceOf-}\tau\text{execI}$ )+

**lemma** *BinOp- $\tau$ ExecI1*:

$\tau\text{Exec-movet } ci \ P \ t \ e1 \ h \ s \ s' \implies \tau\text{Exec-movet } ci \ P \ t \ (e1 \ \ll bop \gg \ e2) \ h \ s \ s'$   
**by**(*induct rule*:  $tranclp\text{-induct}$ )(*blast intro*:  $tranclp.trancl\text{-into-}trancl \ \text{BinOp-}\tau\text{execI1}$ )+

**lemma** *BinOp- $\tau$ ExecI2*:

$\tau\text{Exec-movet } ci \ P \ t \ e2 \ h \ (stk, loc, pc, xcp) \ (stk', loc', pc', xcp')$   
 $\implies \tau\text{Exec-movet } ci \ P \ t \ (e \ \ll bop \gg \ e2) \ h \ ((stk \ @ \ [v]), loc, (\text{length} \ (\text{compE2} \ e) + pc), xcp) \ ((stk' \ @$   
 $[v]), loc', (\text{length} \ (\text{compE2} \ e) + pc'), xcp')$   
**by**(*induct rule*:  $\tau\text{ExecI-induct}$ )(*blast intro*:  $tranclp.trancl\text{-into-}trancl \ \text{BinOp-}\tau\text{execI2}$ )+

**lemma** *LAss- $\tau$ ExecI*:

$\tau\text{Exec-movet } ci \ P \ t \ e \ h \ s \ s' \implies \tau\text{Exec-movet } ci \ P \ t \ (V := e) \ h \ s \ s'$   
**by**(*induct rule*:  $tranclp\text{-induct}$ )(*blast intro*:  $tranclp.trancl\text{-into-}trancl \ \text{LAss-}\tau\text{execI}$ )+

**lemma** *AAcc- $\tau$ ExecI1*:

$\tau\text{Exec-movet } ci \ P \ t \ e \ h \ s \ s' \implies \tau\text{Exec-movet } ci \ P \ t \ (e[i]) \ h \ s \ s'$   
**by**(*induct rule*:  $tranclp\text{-induct}$ )(*blast intro*:  $tranclp.trancl\text{-into-}trancl \ \text{AAcc-}\tau\text{execI1}$ )+

**lemma** *AAcc- $\tau$ ExecI2*:

$\tau\text{Exec-movet } ci \ P \ t \ i \ h \ (stk, loc, pc, xcp) \ (stk', loc', pc', xcp')$

$\implies \tau Exec\text{-}movet\ ci\ P\ t\ (a[i])\ h\ ((stk\ @\ [v]),\ loc,\ (length\ (compE2\ a) + pc),\ xcp)\ ((stk'\ @\ [v]),\ loc',\ (length\ (compE2\ a) + pc'),\ xcp')$   
**by**(*induct rule*:  $\tau Exec\text{-}induct$ )(*blast intro*:  $tranclp.trancl\text{-}into\text{-}trancl\ AAcc\text{-}\tau execI2$ )+

**lemma**  $AAss\text{-}\tau ExecI1$ :

$\tau Exec\text{-}movet\ ci\ P\ t\ e\ h\ s\ s' \implies \tau Exec\text{-}movet\ ci\ P\ t\ (e[i] := e')\ h\ s\ s'$   
**by**(*induct rule*:  $tranclp\text{-}induct$ )(*blast intro*:  $tranclp.trancl\text{-}into\text{-}trancl\ AAss\text{-}\tau execI1$ )+

**lemma**  $AAss\text{-}\tau ExecI2$ :

$\tau Exec\text{-}movet\ ci\ P\ t\ i\ h\ (stk,\ loc,\ pc,\ xcp)\ (stk',\ loc',\ pc',\ xcp')$   
 $\implies \tau Exec\text{-}movet\ ci\ P\ t\ (a[i] := e)\ h\ ((stk\ @\ [v]),\ loc,\ (length\ (compE2\ a) + pc),\ xcp)\ ((stk'\ @\ [v]),\ loc',\ (length\ (compE2\ a) + pc'),\ xcp')$   
**by**(*induct rule*:  $\tau Exec\text{-}induct$ )(*blast intro*:  $tranclp.trancl\text{-}into\text{-}trancl\ AAss\text{-}\tau execI2$ )+

**lemma**  $AAss\text{-}\tau ExecI3$ :

$\tau Exec\text{-}movet\ ci\ P\ t\ e\ h\ (stk,\ loc,\ pc,\ xcp)\ (stk',\ loc',\ pc',\ xcp')$   
 $\implies \tau Exec\text{-}movet\ ci\ P\ t\ (a[i] := e)\ h\ ((stk\ @\ [v,\ v']),\ loc,\ (length\ (compE2\ a) + length\ (compE2\ i) + pc),\ xcp)\ ((stk'\ @\ [v,\ v']),\ loc',\ (length\ (compE2\ a) + length\ (compE2\ i) + pc'),\ xcp')$   
**by**(*induct rule*:  $\tau Exec\text{-}induct$ )(*blast intro*:  $tranclp.trancl\text{-}into\text{-}trancl\ AAss\text{-}\tau execI3$ )+

**lemma**  $ALength\text{-}\tau ExecI$ :

$\tau Exec\text{-}movet\ ci\ P\ t\ e\ h\ s\ s' \implies \tau Exec\text{-}movet\ ci\ P\ t\ (e.length)\ h\ s\ s'$   
**by**(*induct rule*:  $tranclp\text{-}induct$ )(*blast intro*:  $tranclp.trancl\text{-}into\text{-}trancl\ ALength\text{-}\tau execI$ )+

**lemma**  $FAcc\text{-}\tau ExecI$ :

$\tau Exec\text{-}movet\ ci\ P\ t\ e\ h\ s\ s' \implies \tau Exec\text{-}movet\ ci\ P\ t\ (e.F\{D\})\ h\ s\ s'$   
**by**(*induct rule*:  $tranclp\text{-}induct$ )(*blast intro*:  $tranclp.trancl\text{-}into\text{-}trancl\ FAcc\text{-}\tau execI$ )+

**lemma**  $FAss\text{-}\tau ExecI1$ :

$\tau Exec\text{-}movet\ ci\ P\ t\ e\ h\ s\ s' \implies \tau Exec\text{-}movet\ ci\ P\ t\ (e.F\{D\} := e')\ h\ s\ s'$   
**by**(*induct rule*:  $tranclp\text{-}induct$ )(*blast intro*:  $tranclp.trancl\text{-}into\text{-}trancl\ FAss\text{-}\tau execI1$ )+

**lemma**  $FAss\text{-}\tau ExecI2$ :

$\tau Exec\text{-}movet\ ci\ P\ t\ e'\ h\ (stk,\ loc,\ pc,\ xcp)\ (stk',\ loc',\ pc',\ xcp')$   
 $\implies \tau Exec\text{-}movet\ ci\ P\ t\ (e.F\{D\} := e')\ h\ ((stk\ @\ [v]),\ loc,\ (length\ (compE2\ e) + pc),\ xcp)\ ((stk'\ @\ [v]),\ loc',\ (length\ (compE2\ e) + pc'),\ xcp')$   
**by**(*induct rule*:  $\tau Exec\text{-}induct$ )(*blast intro*:  $tranclp.trancl\text{-}into\text{-}trancl\ FAss\text{-}\tau execI2$ )+

**lemma**  $CAS\text{-}\tau ExecI1$ :

$\tau Exec\text{-}movet\ ci\ P\ t\ e\ h\ s\ s' \implies \tau Exec\text{-}movet\ ci\ P\ t\ (e.compareAndSwap(D.F,\ e',\ e''))\ h\ s\ s'$   
**by**(*induct rule*:  $tranclp\text{-}induct$ )(*blast intro*:  $tranclp.trancl\text{-}into\text{-}trancl\ CAS\text{-}\tau execI1$ )+

**lemma**  $CAS\text{-}\tau ExecI2$ :

$\tau Exec\text{-}movet\ ci\ P\ t\ e'\ h\ (stk,\ loc,\ pc,\ xcp)\ (stk',\ loc',\ pc',\ xcp')$   
 $\implies \tau Exec\text{-}movet\ ci\ P\ t\ (e.compareAndSwap(D.F,\ e',\ e''))\ h\ ((stk\ @\ [v]),\ loc,\ (length\ (compE2\ e) + pc),\ xcp)\ ((stk'\ @\ [v]),\ loc',\ (length\ (compE2\ e) + pc'),\ xcp')$   
**by**(*induct rule*:  $\tau Exec\text{-}induct$ )(*blast intro*:  $tranclp.trancl\text{-}into\text{-}trancl\ CAS\text{-}\tau execI2$ )+

**lemma**  $CAS\text{-}\tau ExecI3$ :

$\tau Exec\text{-}movet\ ci\ P\ t\ e''\ h\ (stk,\ loc,\ pc,\ xcp)\ (stk',\ loc',\ pc',\ xcp')$   
 $\implies \tau Exec\text{-}movet\ ci\ P\ t\ (e.compareAndSwap(D.F,\ e',\ e''))\ h\ ((stk\ @\ [v,\ v']),\ loc,\ (length\ (compE2\ e) + length\ (compE2\ e') + pc),\ xcp)\ ((stk'\ @\ [v,\ v']),\ loc',\ (length\ (compE2\ e) + length\ (compE2\ e') + pc'),\ xcp')$   
**by**(*induct rule*:  $\tau Exec\text{-}induct$ )(*blast intro*:  $tranclp.trancl\text{-}into\text{-}trancl\ CAS\text{-}\tau execI3$ )+

**lemma** *Call- $\tau$ ExecI1*:

$\tau Exec\text{-}movet\ ci\ P\ t\ obj\ h\ s\ s' \implies \tau Exec\text{-}movet\ ci\ P\ t\ (obj.M'(es))\ h\ s\ s'$   
**by**(*induct rule*: *tranclp-induct*)(*blast intro*: *tranclp.trancl-into-trancl Call- $\tau$ execI1*)+

**lemma** *Call- $\tau$ ExecI2*:

$\tau Exec\text{-}movest\ ci\ P\ t\ es\ h\ (stk, loc, pc, xcp)\ (stk', loc', pc', xcp') \implies \tau Exec\text{-}movet\ ci\ P\ t\ (obj.M'(es))\ h\ ((stk\ @\ [v]), loc, (length\ (compE2\ obj) + pc), xcp)\ ((stk'\ @\ [v]), loc', (length\ (compE2\ obj) + pc'), xcp')$   
**by**(*induct rule*:  $\tau Execst\text{-}induct$ )(*blast intro*: *tranclp.trancl-into-trancl Call- $\tau$ execI2*)+

**lemma** *Block- $\tau$ ExecI-Some*:

$\tau Exec\text{-}movet\ ci\ P\ t\ e\ h\ (stk, loc, pc, xcp)\ (stk', loc', pc', xcp') \implies \tau Exec\text{-}movet\ ci\ P\ t\ \{V:T=[v];\ e\}\ h\ (stk, loc, (Suc\ (Suc\ pc)), xcp)\ (stk', loc', (Suc\ (Suc\ pc'))), xcp')$   
**by**(*induct rule*:  $\tau Execst\text{-}induct$ )(*blast intro*: *tranclp.trancl-into-trancl Block- $\tau$ execI-Some*)+

**lemma** *Block- $\tau$ ExecI-None*:

$\tau Exec\text{-}movet\ ci\ P\ t\ e\ h\ s\ s' \implies \tau Exec\text{-}movet\ ci\ P\ t\ \{V:T=None;\ e\}\ h\ s\ s'$   
**by**(*induct rule*: *tranclp-induct*)(*blast intro*: *tranclp.trancl-into-trancl Block- $\tau$ execI-None*)+

**lemma** *Sync- $\tau$ ExecI*:

$\tau Exec\text{-}movet\ ci\ P\ t\ e\ h\ s\ s' \implies \tau Exec\text{-}movet\ ci\ P\ t\ (sync_V\ (e)\ e')\ h\ s\ s'$   
**by**(*induct rule*: *tranclp-induct*)(*blast intro*: *tranclp.trancl-into-trancl Sync- $\tau$ execI*)+

**lemma** *Insync- $\tau$ ExecI*:

$\tau Exec\text{-}movet\ ci\ P\ t\ e'\ h\ (stk, loc, pc, xcp)\ (stk', loc', pc', xcp') \implies \tau Exec\text{-}movet\ ci\ P\ t\ (sync_V\ (e)\ e')\ h\ (stk, loc, (Suc\ (Suc\ (Suc\ (length\ (compE2\ e) + pc))))), xcp)\ (stk', loc', (Suc\ (Suc\ (Suc\ (length\ (compE2\ e) + pc')))), xcp')$   
**by**(*induct rule*:  $\tau Execst\text{-}induct$ )(*blast intro*: *tranclp.trancl-into-trancl Insync- $\tau$ execI*)+

**lemma** *Seq- $\tau$ ExecI1*:

$\tau Exec\text{-}movet\ ci\ P\ t\ e\ h\ s\ s' \implies \tau Exec\text{-}movet\ ci\ P\ t\ (e;;e')\ h\ s\ s'$   
**by**(*induct rule*: *tranclp-induct*)(*blast intro*: *tranclp.trancl-into-trancl Seq- $\tau$ execI1*)+

**lemma** *Seq- $\tau$ ExecI2*:

$\tau Exec\text{-}movet\ ci\ P\ t\ e\ h\ (stk, loc, pc, xcp)\ (stk', loc', pc', xcp') \implies \tau Exec\text{-}movet\ ci\ P\ t\ (e';;e)\ h\ (stk, loc, (Suc\ (length\ (compE2\ e') + pc)), xcp)\ (stk', loc', (Suc\ (length\ (compE2\ e') + pc'))), xcp')$   
**by**(*induct rule*:  $\tau Execst\text{-}induct$ )(*blast intro*: *tranclp.trancl-into-trancl Seq- $\tau$ execI2*)+

**lemma** *Cond- $\tau$ ExecI1*:

$\tau Exec\text{-}movet\ ci\ P\ t\ e\ h\ s\ s' \implies \tau Exec\text{-}movet\ ci\ P\ t\ (if\ (e)\ e1\ else\ e2)\ h\ s\ s'$   
**by**(*induct rule*: *tranclp-induct*)(*blast intro*: *tranclp.trancl-into-trancl Cond- $\tau$ execI1*)+

**lemma** *Cond- $\tau$ ExecI2*:

$\tau Exec\text{-}movet\ ci\ P\ t\ e1\ h\ (stk, loc, pc, xcp)\ (stk', loc', pc', xcp') \implies \tau Exec\text{-}movet\ ci\ P\ t\ (if\ (e)\ e1\ else\ e2)\ h\ (stk, loc, (Suc\ (length\ (compE2\ e) + pc)), xcp)\ (stk', loc', (Suc\ (length\ (compE2\ e) + pc'))), xcp')$   
**by**(*induct rule*:  $\tau Execst\text{-}induct$ )(*blast intro*: *tranclp.trancl-into-trancl Cond- $\tau$ execI2*)+

**lemma** *Cond- $\tau$ ExecI3*:

$\tau Exec\text{-}movet\ ci\ P\ t\ e2\ h\ (stk, loc, pc, xcp)\ (stk', loc', pc', xcp') \implies \tau Exec\text{-}movet\ ci\ P\ t\ (if\ (e)\ e1\ else\ e2)\ h\ (stk, loc, (Suc\ (Suc\ (length\ (compE2\ e) + length\ (compE2\ e2))))), xcp)$

$e1) + pc)))$ ,  $xcp)$   $(stk', loc', (Suc (Suc (length (compE2 e) + length (compE2 e1) + pc'))), xcp')$   
**by**(*induct rule:  $\tau$ ExecI-induct*)(*blast intro: tranclp.trancl-into-trancl Cond- $\tau$ execI3*)+

**lemma** *While- $\tau$ ExecI1*:

$\tau$ Exec-movet  $ci P t c h s s' \implies \tau$ Exec-movet  $ci P t (while (c) e) h s s'$   
**by**(*induct rule: tranclp-induct*)(*blast intro: tranclp.trancl-into-trancl While- $\tau$ execI1*)+

**lemma** *While- $\tau$ ExecI2*:

$\tau$ Exec-movet  $ci P t E h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$   
 $\implies \tau$ Exec-movet  $ci P t (while (c) E) h (stk, loc, (Suc (length (compE2 c) + pc)), xcp) (stk', loc',$   
 $(Suc (length (compE2 c) + pc'))$ ,  $xcp')$   
**by**(*induct rule:  $\tau$ ExecI-induct*)(*blast intro: tranclp.trancl-into-trancl While- $\tau$ execI2*)+

**lemma** *Throw- $\tau$ ExecI*:

$\tau$ Exec-movet  $ci P t e h s s' \implies \tau$ Exec-movet  $ci P t (throw e) h s s'$   
**by**(*induct rule: tranclp-induct*)(*blast intro: tranclp.trancl-into-trancl Throw- $\tau$ execI*)+

**lemma** *Try- $\tau$ ExecI1*:

$\tau$ Exec-movet  $ci P t E h s s' \implies \tau$ Exec-movet  $ci P t (try E catch (C' V) e) h s s'$   
**by**(*induct rule: tranclp-induct*)(*blast intro: tranclp.trancl-into-trancl Try- $\tau$ execI1*)+

**lemma** *Try- $\tau$ ExecI2*:

$\tau$ Exec-movet  $ci P t e h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$   
 $\implies \tau$ Exec-movet  $ci P t (try E catch (C' V) e) h (stk, loc, (Suc (Suc (length (compE2 E) + pc))),$   
 $xcp) (stk', loc', (Suc (Suc (length (compE2 E) + pc'))$ ,  $xcp')$   
**by**(*induct rule:  $\tau$ ExecI-induct*)(*blast intro: tranclp.trancl-into-trancl Try- $\tau$ execI2*)+

**lemma**  *$\tau$ Exec-movesr-map-Val*:

$\tau$ Exec-movesr-a  $P t (map Val vs) h ([], xs, 0, None) ((rev vs), xs, (length (compEs2 (map Val vs))),$   
 $None)$

**proof**(*induct vs arbitrary: pc stk Ts rule: rev-induct*)

**case Nil thus** ?case **by**(*auto*)

**next**

**case** (snoc  $v vs'$ )

**let** ?E = compEs2 (map Val  $vs'$ )

**from** snoc **have**  $\tau$ Exec-movesr-a  $P t (map Val (vs' @ [v])) h ([], xs, 0, None) ((rev vs'), xs, (length$   
 $?E), None)$

**by** *auto*

**also** {

**have** *exec-meth-a* (compP2 P) (?E @ [Push v]) (compEs2 (map Val  $vs'$ ) 0 0 @ shift (length ?E))  
 $[] t h ((rev vs'), xs, (length ?E + 0), None) \varepsilon h ((v \# rev vs'), xs, (length ?E + Suc 0), None)$

**by**  $-(rule \text{append-exec-meth-xt, auto simp add: exec-meth-instr})$

**moreover** **have**  $\tau$ moves2 (compP2 P)  $h (rev vs') (map Val vs' @ [Val v]) (length (compEs2 (map$   
 $Val vs')) + 0) None$

**by**(*rule append- $\tau$ moves2  $\tau$ moves2Hd  $\tau$ move2Val*)+

**ultimately** **have**  $\tau$ Exec-movesr-a  $P t (map Val (vs' @ [v])) h ((rev vs'), xs, (length ?E), None)$   
 $((rev (vs' @ [v])), xs, (length (compEs2 (map Val (vs' @ [v]))), None)$

**by**  $-(rule \tau$ Execsr1step, auto simp add: *exec-moves-def compP2-def*) }

**finally** **show** ?case .

**qed**

**lemma**  *$\tau$ Exec-mover-blocks1 [simp]*:

$\tau$ Exec-mover  $ci P t (blocks1 n Ts body) h s s' = \tau$ Exec-mover  $ci P t body h s s'$   
**by**(*simp add:  $\tau$ exec-move-conv- $\tau$ exec-meth*)

**lemma**  $\tau Exec\text{-}movet\text{-}blocks1$  [simp]:

$\tau Exec\text{-}movet\ ci\ P\ t\ (blocks1\ n\ Ts\ body)\ h\ s\ s' = \tau Exec\text{-}movet\ ci\ P\ t\ body\ h\ s\ s'$   
**by**(simp add:  $\tau exec\text{-}move\text{-}conv\text{-}\tau exec\text{-}meth$ )

**definition**  $\tau exec\text{-}1 :: 'addr\ jvm\text{-}prog \Rightarrow 'thread\text{-}id \Rightarrow ('addr, 'heap)\ jvm\text{-}state \Rightarrow ('addr, 'heap)\ jvm\text{-}state \Rightarrow bool$

**where**  $\tau exec\text{-}1\ P\ t\ \sigma\ \sigma' \longleftrightarrow exec\text{-}1\ P\ t\ \sigma\ \varepsilon\ \sigma' \wedge \tau Move2\ P\ \sigma$

**lemma**  $\tau exec\text{-}1I$  [intro]:

$\llbracket exec\text{-}1\ P\ t\ \sigma\ \varepsilon\ \sigma'; \tau Move2\ P\ \sigma \rrbracket \Longrightarrow \tau exec\text{-}1\ P\ t\ \sigma\ \sigma'$   
**by**(simp add:  $\tau exec\text{-}1\text{-}def$ )

**lemma**  $\tau exec\text{-}1E$  [elim]:

**assumes**  $\tau exec\text{-}1\ P\ t\ \sigma\ \sigma'$   
**obtains**  $exec\text{-}1\ P\ t\ \sigma\ \varepsilon\ \sigma' \tau Move2\ P\ \sigma$   
**using** *assms* **by**(auto simp add:  $\tau exec\text{-}1\text{-}def$ )

**abbreviation**  $\tau Exec\text{-}1r :: 'addr\ jvm\text{-}prog \Rightarrow 'thread\text{-}id \Rightarrow ('addr, 'heap)\ jvm\text{-}state \Rightarrow ('addr, 'heap)\ jvm\text{-}state \Rightarrow bool$

**where**  $\tau Exec\text{-}1r\ P\ t == (\tau exec\text{-}1\ P\ t)^{**}$

**abbreviation**  $\tau Exec\text{-}1t :: 'addr\ jvm\text{-}prog \Rightarrow 'thread\text{-}id \Rightarrow ('addr, 'heap)\ jvm\text{-}state \Rightarrow ('addr, 'heap)\ jvm\text{-}state \Rightarrow bool$

**where**  $\tau Exec\text{-}1t\ P\ t == (\tau exec\text{-}1\ P\ t)^{++}$

**definition**  $\tau exec\text{-}1\text{-}d :: 'addr\ jvm\text{-}prog \Rightarrow 'thread\text{-}id \Rightarrow ('addr, 'heap)\ jvm\text{-}state \Rightarrow ('addr, 'heap)\ jvm\text{-}state \Rightarrow bool$

**where**  $\tau exec\text{-}1\text{-}d\ P\ t\ \sigma\ \sigma' \longleftrightarrow exec\text{-}1\ P\ t\ \sigma\ \varepsilon\ \sigma' \wedge \tau Move2\ P\ \sigma \wedge check\ P\ \sigma$

**lemma**  $\tau exec\text{-}1\text{-}dI$  [intro]:

$\llbracket exec\text{-}1\ P\ t\ \sigma\ \varepsilon\ \sigma'; check\ P\ \sigma; \tau Move2\ P\ \sigma \rrbracket \Longrightarrow \tau exec\text{-}1\text{-}d\ P\ t\ \sigma\ \sigma'$   
**by**(simp add:  $\tau exec\text{-}1\text{-}d\text{-}def$ )

**lemma**  $\tau exec\text{-}1\text{-}dE$  [elim]:

**assumes**  $\tau exec\text{-}1\text{-}d\ P\ t\ \sigma\ \sigma'$   
**obtains**  $exec\text{-}1\ P\ t\ \sigma\ \varepsilon\ \sigma' check\ P\ \sigma \tau Move2\ P\ \sigma$   
**using** *assms* **by**(auto simp add:  $\tau exec\text{-}1\text{-}d\text{-}def$ )

**abbreviation**  $\tau Exec\text{-}1\text{-}dr :: 'addr\ jvm\text{-}prog \Rightarrow 'thread\text{-}id \Rightarrow ('addr, 'heap)\ jvm\text{-}state \Rightarrow ('addr, 'heap)\ jvm\text{-}state \Rightarrow bool$

**where**  $\tau Exec\text{-}1\text{-}dr\ P\ t == (\tau exec\text{-}1\text{-}d\ P\ t)^{**}$

**abbreviation**  $\tau Exec\text{-}1\text{-}dt :: 'addr\ jvm\text{-}prog \Rightarrow 'thread\text{-}id \Rightarrow ('addr, 'heap)\ jvm\text{-}state \Rightarrow ('addr, 'heap)\ jvm\text{-}state \Rightarrow bool$

**where**  $\tau Exec\text{-}1\text{-}dt\ P\ t == (\tau exec\text{-}1\text{-}d\ P\ t)^{++}$

**declare**  $compxE2\text{-}size\text{-}conv[simp\ del]\ compxEs2\text{-}size\text{-}conv[simp\ del]$

**declare**  $compxE2\text{-}stack\text{-}xlift\text{-}conv[simp\ del]\ compxEs2\text{-}stack\text{-}xlift\text{-}conv[simp\ del]$

**lemma**  $exec\text{-}instr\text{-}frs\text{-}offer$ :

$(ta, xcp', h', (stk', loc', C, M, pc') \# frs) \in exec\text{-}instr\ ins\ P\ t\ h\ stk\ loc\ C\ M\ pc\ frs$   
 $\Longrightarrow (ta, xcp', h', (stk', loc', C, M, pc') \# frs @ frs') \in exec\text{-}instr\ ins\ P\ t\ h\ stk\ loc\ C\ M\ pc\ (frs @ frs')$

```

frs')
apply(cases ins)
apply(simp-all add: nth-append split-beta split: if-split-asm sum.split-asm)
apply(force split: extCallRet.split-asm simp add: extRet2JVM-def)+
done

```

**lemma** *check-instr-frs-offer*:

```

[[ check-instr ins P h stk loc C M pc frs; ins ≠ Return ]]
  ⇒ check-instr ins P h stk loc C M pc (frs @ frs')
by(cases ins)(simp-all split: if-split-asm)

```

**lemma** *exec-instr-CM-change*:

```

(ta, xcp', h', (stk', loc', C, M, pc') # frs) ∈ exec-instr ins P t h stk loc C M pc frs
  ⇒ (ta, xcp', h', (stk', loc', C', M', pc') # frs) ∈ exec-instr ins P t h stk loc C' M' pc frs
apply(cases ins)
apply(simp-all add: nth-append split-beta neq-Nil-conv split: if-split-asm sum.split-asm)
apply(force split: extCallRet.split-asm simp add: extRet2JVM-def)+
done

```

**lemma** *check-instr-CM-change*:

```

[[ check-instr ins P h stk loc C M pc frs; ins ≠ Return ]]
  ⇒ check-instr ins P h stk loc C' M' pc frs
by(cases ins)(simp-all split: if-split-asm)

```

**lemma** *exec-move-exec-1*:

```

assumes exec: exec-move ci P t body h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')
and sees: P ⊢ C sees M : Ts → T = [body] in D
shows exec-1 (compP2 P) t (xcp, h, (stk, loc, C, M, pc) # frs) ta (xcp', h', (stk', loc', C, M, pc') # frs)
using exec unfolding exec-move-def
proof(cases)
  case exec-instr
    note [simp] = ⟨xcp = None⟩
    and exec = ⟨(ta, xcp', h', [(stk', loc', undefined, undefined, pc')])
      ∈ exec-instr (compE2 body ! pc) (compP2 P) t h stk loc undefined undefined pc []⟩
    from exec have (ta, xcp', h', [(stk', loc', C, M, pc')])
      ∈ exec-instr (compE2 body ! pc) (compP2 P) t h stk loc C M pc []
    by(rule exec-instr-CM-change)
    from exec-instr-frs-offer[OF this, of frs]
    have (ta, xcp', h', (stk', loc', C, M, pc') # frs)
      ∈ exec-instr (compE2 body ! pc) (compP2 P) t h stk loc C M pc frs by simp
    with sees ⟨pc < length (compE2 body)⟩ show ?thesis
    by(simp add: exec-1-iff compP2-def compMb2-def nth-append)
  next
    case exec-catch
    thus ?thesis using sees-method-compP[OF sees, of λC M Ts T. compMb2]
    by(simp add: exec-1-iff compMb2-def compP2-def)
qed

```

**lemma** *τexec-move-τexec-1*:

```

assumes exec: τexec-move ci P t body h (stk, loc, pc, xcp) (stk', loc', pc', xcp')
and sees: P ⊢ C sees M : Ts → T = [body] in D
shows τexec-1 (compP2 P) t (xcp, h, (stk, loc, C, M, pc) # frs) (xcp', h, (stk', loc', C, M, pc') # frs)

```



**proof**(rule  $\tau\text{exec-1I}$ )  
**from**  $\text{exec}$  **obtain**  $\text{exec}'$ :  $\text{exec-move } ci \ P \ t \ \text{body } h \ (stk, loc, pc, xcp) \in h \ (stk', loc', pc', xcp')$   
**and**  $\tau$ :  $\tau\text{move2 } P \ h \ stk \ \text{body } pc \ xcp \ \text{by}(\text{rule } \tau\text{exec-moveE})$   
**have**  $\text{exec-1 } (compP2 \ P) \ t \ (xcp, h, (stk, loc, C, M, pc) \# \text{frs}) \in (xcp', h, (stk', loc', C, M, pc') \# \text{frs})$   
**using**  $\text{exec}' \text{ sees}$  **by**(rule  $\text{exec-move-exec-1}$ )  
**thus**  $compP2 \ P, t \vdash (xcp, h, (stk, loc, C, M, pc) \# \text{frs}) \rightarrow_{\varepsilon-jvm} (xcp', h, (stk', loc', C, M, pc') \# \text{frs})$  **by** *auto*  
**{ fix**  $a$   
**assume**  $[simp]: xcp = [a]$   
**from**  $\text{sees-method-compP}[OF \ \text{sees}, \text{ of } \lambda C \ M \ Ts \ T. \ compMb2]$   
**have**  $\text{ex-table-of } (compP2 \ P) \ C \ M = \text{compxE2 } \text{body } 0 \ 0 \ \text{by}(\text{simp add: compP2-def compMb2-def})$   
**hence**  $\text{match-ex-table } (compP2 \ P) \ (\text{cname-of } h \ a) \ pc \ (\text{ex-table-of } (compP2 \ P) \ C \ M) \neq \text{None } pc < \text{length } (compE2 \ \text{body})$   
**using**  $\text{exec}' \text{ sees}$  **by**(*auto simp add: exec-move-def elim: exec-meth.cases*) **}**  
**with**  $\tau \text{ sees sees-method-compP}[OF \ \text{sees}, \text{ of } \lambda C \ M \ Ts \ T. \ compMb2]$   
**show**  $\tau\text{Move2 } (compP2 \ P) \ (xcp, h, (stk, loc, C, M, pc) \# \text{frs})$   
**unfolding**  $\tau\text{Move2-compP2}[OF \ \text{sees}]$  **by**(*fastforce simp add: compP2-def compMb2-def*)  
**qed**

**lemma**  $\tau\text{Exec-mover-}\tau\text{Exec-1r}$ :

**assumes**  $\text{move: } \tau\text{Exec-mover } ci \ P \ t \ \text{body } h \ (stk, loc, pc, xcp) \ (stk', loc', pc', xcp')$   
**and**  $\text{sees: } P \vdash C \ \text{sees } M : Ts \rightarrow T = [body] \text{ in } D$   
**shows**  $\tau\text{Exec-1r } (compP2 \ P) \ t \ (xcp, h, (stk, loc, C, M, pc) \# \text{frs}') \ (xcp', h, (stk', loc', C, M, pc') \# \text{frs}')$   
**using** *move*  
**by**(*induct rule:  $\tau\text{Execr-induct}$* )(*blast intro: rtranclp.rtrancl-into-rtrancl  $\tau\text{exec-move-}\tau\text{exec-1}[OF - \text{sees}]$* )+

**lemma**  $\tau\text{Exec-movet-}\tau\text{Exec-1t}$ :

**assumes**  $\text{move: } \tau\text{Exec-movet } ci \ P \ t \ \text{body } h \ (stk, loc, pc, xcp) \ (stk', loc', pc', xcp')$   
**and**  $\text{sees: } P \vdash C \ \text{sees } M : Ts \rightarrow T = [body] \text{ in } D$   
**shows**  $\tau\text{Exec-1t } (compP2 \ P) \ t \ (xcp, h, (stk, loc, C, M, pc) \# \text{frs}') \ (xcp', h, (stk', loc', C, M, pc') \# \text{frs}')$   
**using** *move*  
**by**(*induct rule:  $\tau\text{Exect-induct}$* )(*blast intro: tranclp.trancl-into-trancl  $\tau\text{exec-move-}\tau\text{exec-1}[OF - \text{sees}]$* )+

**lemma**  $\tau\text{Exec-1r-rtranclpD}$ :

$\tau\text{Exec-1r } P \ t \ (xcp, h, \text{frs}) \ (xcp', h', \text{frs}')$   
 $\implies (\lambda((xcp, \text{frs}), h) ((xcp', \text{frs}'), h'). \text{exec-1 } P \ t \ (xcp, h, \text{frs}) \in (xcp', h', \text{frs}') \wedge \tau\text{Move2 } P \ (xcp, h, \text{frs}))^{\wedge**} ((xcp, \text{frs}), h) ((xcp', \text{frs}'), h')$   
**by**(*induct rule: rtranclp-induct3*)(*fastforce intro: rtranclp.rtrancl-into-rtrancl*)+

**lemma**  $\tau\text{Exec-1t-rtranclpD}$ :

$\tau\text{Exec-1t } P \ t \ (xcp, h, \text{frs}) \ (xcp', h', \text{frs}')$   
 $\implies (\lambda((xcp, \text{frs}), h) ((xcp', \text{frs}'), h'). \text{exec-1 } P \ t \ (xcp, h, \text{frs}) \in (xcp', h', \text{frs}') \wedge \tau\text{Move2 } P \ (xcp, h, \text{frs}))^{\wedge++} ((xcp, \text{frs}), h) ((xcp', \text{frs}'), h')$   
**by**(*induct rule: tranclp-induct3*)(*fastforce intro: tranclp.trancl-into-trancl*)+

**lemma**  $\text{exec-meth-length-compE2-stack-xliftD}$ :

$\text{exec-meth } ci \ P \ (compE2 \ e) \ (\text{stack-xlift } d \ (compxE2 \ e \ 0 \ 0)) \ t \ h \ (stk, loc, pc, xcp) \ ta \ h' \ s'$   
 $\implies pc < \text{length } (compE2 \ e)$   
**by**(*cases s'*)(*auto simp add: stack-xlift-compxE2*)

**lemma**  $\text{exec-meth-length-pc-xt-Nil}$ :

$exec\text{-}meth\ ci\ P\ ins\ []\ t\ h\ (stk, loc, pc, xcp)\ ta\ h'\ s' \implies pc < length\ ins$   
**apply**(erule  $exec\text{-}meth.cases$ )  
**apply**(auto dest:  $match\text{-}ex\text{-}table\text{-}pc\text{-}length\text{-}compE2$ )  
**done**

**lemma** *BinOp-exec2D*:

**assumes**  $exec: exec\text{-}meth\ ci\ (compP2\ P)\ (compE2\ (e1\ \llbracket bop \rrbracket\ e2))\ (compxE2\ (e1\ \llbracket bop \rrbracket\ e2)\ 0\ 0)\ t\ h$   
 $(stk\ @\ [v1], loc, length\ (compE2\ e1) + pc, xcp)\ ta\ h'\ (stk', loc', pc', xcp')$   
**and**  $pc: pc < length\ (compE2\ e2)$   
**shows**  $exec\text{-}meth\ ci\ (compP2\ P)\ (compE2\ e2)\ (stack\text{-}xlift\ (length\ [v1])\ (compxE2\ e2\ 0\ 0))\ t\ h\ (stk$   
 $@\ [v1], loc, pc, xcp)\ ta\ h'\ (stk', loc', pc' - length\ (compE2\ e1), xcp') \wedge pc' \geq length\ (compE2\ e1)$

**proof**

**from**  $exec$  **have**  $exec\text{-}meth\ ci\ (compP2\ P)\ ((compE2\ e1\ @\ compE2\ e2)\ @\ [BinOpInstr\ bop])$   
 $(compxE2\ e1\ 0\ 0\ @\ shift\ (length\ (compE2\ e1))\ (stack\text{-}xlift\ (length\ [v1])\ (compxE2\ e2\ 0\ 0)))\ t\ h$   
 $(stk\ @\ [v1], loc, length\ (compE2\ e1) + pc, xcp)\ ta\ h'\ (stk', loc', pc', xcp')$   
**by**(simp add:  $compxE2\text{-}size\text{-}conv\ compxE2\text{-}stack\text{-}xlift\text{-}conv$ )  
**hence**  $exec': exec\text{-}meth\ ci\ (compP2\ P)\ (compE2\ e1\ @\ compE2\ e2)\ (compxE2\ e1\ 0\ 0\ @\ shift\ (length$   
 $(compE2\ e1))\ (stack\text{-}xlift\ (length\ [v1])\ (compxE2\ e2\ 0\ 0)))\ t\ h$   
 $(stk\ @\ [v1], loc, length\ (compE2\ e1) + pc, xcp)\ ta\ h'\ (stk', loc', pc', xcp')$   
**by**(rule  $exec\text{-}meth\text{-}take$ ) (simp add:  $pc$ )  
**thus**  $exec\text{-}meth\ ci\ (compP2\ P)\ (compE2\ e2)\ (stack\text{-}xlift\ (length\ [v1])\ (compxE2\ e2\ 0\ 0))\ t\ h$   
 $(stk\ @\ [v1], loc, pc, xcp)\ ta\ h'\ (stk', loc', pc' - length\ (compE2\ e1), xcp')$   
**by**(rule  $exec\text{-}meth\text{-}drop\text{-}xt$ ) auto  
**from**  $exec'$  **show**  $pc' \geq length\ (compE2\ e1)$   
**by**(rule  $exec\text{-}meth\text{-}drop\text{-}xt\text{-}pc$ )(auto)

**qed**

**lemma** *Call-execParamD*:

**assumes**  $exec: exec\text{-}meth\ ci\ (compP2\ P)\ (compE2\ (obj.M'(ps)))\ (compxE2\ (obj.M'(ps))\ 0\ 0)\ t\ h$   
 $(stk\ @\ [v], loc, length\ (compE2\ obj) + pc, xcp)\ ta\ h'\ (stk', loc', pc', xcp')$   
**and**  $pc: pc < length\ (compEs2\ ps)$   
**shows**  $exec\text{-}meth\ ci\ (compP2\ P)\ (compEs2\ ps)\ (stack\text{-}xlift\ (length\ [v])\ (compxEs2\ ps\ 0\ 0))\ t\ h\ (stk$   
 $@\ [v], loc, pc, xcp)\ ta\ h'\ (stk', loc', pc' - length\ (compE2\ obj), xcp') \wedge pc' \geq length\ (compE2\ obj)$

**proof**

**from**  $exec$  **have**  $exec\text{-}meth\ ci\ (compP2\ P)\ ((compE2\ obj\ @\ compEs2\ ps)\ @\ [Invoke\ M'\ (length\ ps)])$   
 $(compxE2\ obj\ 0\ 0\ @\ shift\ (length\ (compE2\ obj))\ (stack\text{-}xlift\ (length\ [v])\ (compxEs2\ ps\ 0\ 0)))\ t\ h$   
 $(stk\ @\ [v], loc, length\ (compE2\ obj) + pc, xcp)\ ta\ h'\ (stk', loc', pc', xcp')$   
**by**(simp add:  $compxEs2\text{-}size\text{-}conv\ compxEs2\text{-}stack\text{-}xlift\text{-}conv$ )  
**hence**  $exec': exec\text{-}meth\ ci\ (compP2\ P)\ (compE2\ obj\ @\ compEs2\ ps)\ (compxE2\ obj\ 0\ 0\ @\ shift\ (length$   
 $(compE2\ obj))\ (stack\text{-}xlift\ (length\ [v])\ (compxEs2\ ps\ 0\ 0)))\ t\ h$   
 $(stk\ @\ [v], loc, length\ (compE2\ obj) + pc, xcp)\ ta\ h'\ (stk', loc', pc', xcp')$   
**by**(rule  $exec\text{-}meth\text{-}take$ )(simp add:  $pc$ )  
**thus**  $exec\text{-}meth\ ci\ (compP2\ P)\ (compEs2\ ps)\ (stack\text{-}xlift\ (length\ [v])\ (compxEs2\ ps\ 0\ 0))\ t\ h\ (stk$   
 $@\ [v], loc, pc, xcp)\ ta\ h'\ (stk', loc', pc' - length\ (compE2\ obj), xcp')$   
**by**(rule  $exec\text{-}meth\text{-}drop\text{-}xt$ ) auto  
**from**  $exec'$  **show**  $pc' \geq length\ (compE2\ obj)$   
**by**(rule  $exec\text{-}meth\text{-}drop\text{-}xt\text{-}pc$ )(auto)

**qed**

**lemma** *exec-move-length-compE2D* [dest]:

$exec\text{-}move\ ci\ P\ t\ e\ h\ (stk, loc, pc, xcp)\ ta\ h'\ s' \implies pc < length\ (compE2\ e)$   
**by**(cases  $s'$ )(auto simp add:  $exec\text{-}move\text{-}def$ )

**lemma** *exec-moves-length-compEs2D* [dest]:

$exec\_moves\ ci\ P\ t\ es\ h\ (stk, loc, pc, xcp)\ ta\ h'\ s' \implies pc < length\ (compEs2\ es)$   
**by**(cases  $s'$ )(auto simp add:  $exec\_moves\_def$ )

**lemma**  $exec\_meth\_ci\_appD$ :

$\llbracket exec\_meth\ ci\ P\ ins\ xt\ t\ h\ (stk, loc, pc, None)\ ta\ h'\ fr' \rrbracket$   
 $\implies ci\_app\ ci\ (ins\ !\ pc)\ P\ h\ stk\ loc\ undefined\ undefined\ pc\ []$   
**by**(cases  $fr'$ )(simp add:  $exec\_meth\_instr$ )

**lemma**  $exec\_move\_ci\_appD$ :

$exec\_move\ ci\ P\ t\ E\ h\ (stk, loc, pc, None)\ ta\ h'\ fr'$   
 $\implies ci\_app\ ci\ (compE2\ E\ !\ pc)\ (compP2\ P)\ h\ stk\ loc\ undefined\ undefined\ pc\ []$   
**unfolding**  $exec\_move\_def$  **by**(rule  $exec\_meth\_ci\_appD$ )

**lemma**  $exec\_moves\_ci\_appD$ :

$exec\_moves\ ci\ P\ t\ Es\ h\ (stk, loc, pc, None)\ ta\ h'\ fr'$   
 $\implies ci\_app\ ci\ (compEs2\ Es\ !\ pc)\ (compP2\ P)\ h\ stk\ loc\ undefined\ undefined\ pc\ []$   
**unfolding**  $exec\_moves\_def$  **by**(rule  $exec\_meth\_ci\_appD$ )

**lemma**  $\tau instr\_stk\_append\_check$ :

$check\_instr'\ i\ P\ h\ stk\ loc\ C\ M\ pc\ frs \implies \tau instr\ P\ h\ (stk\ @\ vs)\ i = \tau instr\ P\ h\ stk\ i$   
**by**(cases  $i$ )(simp-all add:  $nth\_append$ )

**lemma**  $\tau instr\_stk\_drop\_exec\_move$ :

$exec\_move\ ci\ P\ t\ e\ h\ (stk, loc, pc, None)\ ta\ h'\ fr'$   
 $\implies \tau instr\ (compP2\ P)\ h\ (stk\ @\ vs)\ (compE2\ e\ !\ pc) = \tau instr\ (compP2\ P)\ h\ stk\ (compE2\ e\ !\ pc)$   
**apply**(drule  $exec\_move\_ci\_appD$ )  
**apply**(drule  $wf\_ciD2\_ci\_app$ )  
**apply**(erule  $\tau instr\_stk\_append\_check$ )  
**done**

**lemma**  $\tau instr\_stk\_drop\_exec\_moves$ :

$exec\_moves\ ci\ P\ t\ es\ h\ (stk, loc, pc, None)\ ta\ h'\ fr'$   
 $\implies \tau instr\ (compP2\ P)\ h\ (stk\ @\ vs)\ (compEs2\ es\ !\ pc) = \tau instr\ (compP2\ P)\ h\ stk\ (compEs2\ es\ !\ pc)$   
**apply**(drule  $exec\_moves\_ci\_appD$ )  
**apply**(drule  $wf\_ciD2\_ci\_app$ )  
**apply**(erule  $\tau instr\_stk\_append\_check$ )  
**done**

**end**

**end**

## 7.16 The delay bisimulation between intermediate language and JVM

**theory**  $J1JVMBisim$  **imports**

$Execs$

$../BV/BVNoTypeError$

$J1$

**begin**

**declare**  $Listn.lesub\_list\_impl\_same\_size[simp\ del]$

**lemma** (in *JVM-heap-conf-base'*)  $\tau exec-1$ - $\tau exec-1-d$ :

$\llbracket wf-jvm-prog_{\Phi} P; \tau exec-1 P t \sigma \sigma'; \Phi \mid - t:\sigma [ok] \rrbracket \implies \tau exec-1-d P t \sigma \sigma'$   
**by**(*auto simp add:  $\tau exec-1-def$   $\tau exec-1-d-def$  welltyped-commute[symmetric] elim: jvmd-NormalE*)

**context** *JVM-conf-read* **begin**

**lemma**  $\tau Exec-1r$ -preserves-correct-state:

**assumes**  $wf: wf-jvm-prog_{\Phi} P$   
**and**  $exec: \tau Exec-1r P t \sigma \sigma'$   
**shows**  $\Phi \mid - t:\sigma [ok] \implies \Phi \mid - t:\sigma' [ok]$

**using** *exec*

**by**(*induct*)(*blast intro: BV-correct-1[OF wf]*)**+**

**lemma**  $\tau Exec-1t$ -preserves-correct-state:

**assumes**  $wf: wf-jvm-prog_{\Phi} P$   
**and**  $exec: \tau Exec-1t P t \sigma \sigma'$   
**shows**  $\Phi \mid - t:\sigma [ok] \implies \Phi \mid - t:\sigma' [ok]$

**using** *exec*

**by**(*induct*)(*blast intro: BV-correct-1[OF wf]*)**+**

**lemma**  $\tau Exec-1r$ - $\tau Exec-1-dr$ :

**assumes**  $wf: wf-jvm-prog_{\Phi} P$   
**shows**  $\llbracket \tau Exec-1r P t \sigma \sigma'; \Phi \mid - t:\sigma [ok] \rrbracket \implies \tau Exec-1-dr P t \sigma \sigma'$

**apply**(*induct rule: rtranclp-induct*)

**apply**(*blast intro: rtranclp.rtrancl-into-rtrancl  $\tau exec-1$ - $\tau exec-1-d$ [OF wf]  $\tau Exec-1r$ -preserves-correct-state[OF wf]*)**+**

**done**

**lemma**  $\tau Exec-1t$ - $\tau Exec-1-dt$ :

**assumes**  $wf: wf-jvm-prog_{\Phi} P$   
**shows**  $\llbracket \tau Exec-1t P t \sigma \sigma'; \Phi \mid - t:\sigma [ok] \rrbracket \implies \tau Exec-1-dt P t \sigma \sigma'$

**apply**(*induct rule: tranclp-induct*)

**apply**(*blast intro: tranclp.trancl-into-trancl  $\tau exec-1$ - $\tau exec-1-d$ [OF wf]  $\tau Exec-1t$ -preserves-correct-state[OF wf]*)**+**

**done**

**lemma**  $\tau Exec-1-dr$ -preserves-correct-state:

**assumes**  $wf: wf-jvm-prog_{\Phi} P$   
**and**  $exec: \tau Exec-1-dr P t \sigma \sigma'$   
**shows**  $\Phi \mid - t:\sigma [ok] \implies \Phi \mid - t:\sigma' [ok]$

**using** *exec*

**by**(*induct*)(*blast intro: BV-correct-1[OF wf]*)**+**

**lemma**  $\tau Exec-1-dt$ -preserves-correct-state:

**assumes**  $wf: wf-jvm-prog_{\Phi} P$   
**and**  $exec: \tau Exec-1-dt P t \sigma \sigma'$   
**shows**  $\Phi \mid - t:\sigma [ok] \implies \Phi \mid - t:\sigma' [ok]$

**using** *exec*

**by**(*induct*)(*blast intro: BV-correct-1[OF wf]*)**+**

**end**

**locale** *J1-JVM-heap-base* =

```

J1-heap-base +
JVM-heap-base +
constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
begin

inductive bisim1 ::
  'm prog ⇒ 'heap ⇒ 'addr expr1 ⇒ ('addr expr1 × 'addr locals1)
  ⇒ ('addr val list × 'addr val list × pc × 'addr option) ⇒ bool

and bisims1 ::
  'm prog ⇒ 'heap ⇒ 'addr expr1 list ⇒ ('addr expr1 list × 'addr locals1)
  ⇒ ('addr val list × 'addr val list × pc × 'addr option) ⇒ bool

and bisim1-syntax ::
  'm prog ⇒ 'addr expr1 ⇒ 'heap ⇒ ('addr expr1 × 'addr locals1)
  ⇒ ('addr val list × 'addr val list × pc × 'addr option) ⇒ bool
  (·, ·, · ⊢ · ↔ · [50, 0, 0, 0, 50] 100)

and bisims1-syntax ::
  'm prog ⇒ 'addr expr1 list ⇒ 'heap ⇒ ('addr expr1 list × 'addr locals1)
  ⇒ ('addr val list × 'addr val list × pc × 'addr option) ⇒ bool
  (·, ·, · ⊢ · [↔] · [50, 0, 0, 0, 50] 100)
for P :: 'm prog and h :: 'heap
where
  P, e, h ⊢ exs ↔ s ≡ bisim1 P h e exs s
  | P, es, h ⊢ esxs [↔] s ≡ bisims1 P h es esxs s

  | bisim1Val2:
    pc = length (compE2 e) ⇒ P, e, h ⊢ (Val v, xs) ↔ (v # [], xs, pc, None)

  | bisim1New:
    P, new C, h ⊢ (new C, xs) ↔ ([], xs, 0, None)

  | bisim1NewThrow:
    P, new C, h ⊢ (THROW OutOfMemory, xs) ↔ ([], xs, 0, [addr-of-sys-xcpt OutOfMemory])

  | bisim1NewArray:
    P, e, h ⊢ (e', xs) ↔ (stk, loc, pc, xcp) ⇒ P, newA T[e], h ⊢ (newA T[e'], xs) ↔ (stk, loc, pc,
    xcp)

  | bisim1NewArrayThrow:
    P, e, h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a]) ⇒ P, newA T[e], h ⊢ (Throw a, xs) ↔ (stk, loc, pc,
    [a])

  | bisim1NewArrayFail:
    P, newA T[e], h ⊢ (Throw a, xs) ↔ ([v], xs, length (compE2 e), [a])

```

- | *bisim1Cast*:  

$$P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \implies P, \text{Cast } T \ e, h \vdash (\text{Cast } T \ e', xs) \leftrightarrow (stk, loc, pc, xcp)$$
- | *bisim1CastThrow*:  

$$P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \implies P, \text{Cast } T \ e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a])$$
- | *bisim1CastFail*:  

$$P, \text{Cast } T \ e, h \vdash (\text{THROW } \text{ClassCast}, xs) \leftrightarrow ([v], xs, \text{length } (\text{compE2 } e), [\text{addr-of-sys-xcpt } \text{ClassCast}])$$
- | *bisim1InstanceOf*:  

$$P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \implies P, e \text{ instanceof } T, h \vdash (e' \text{ instanceof } T, xs) \leftrightarrow (stk, loc, pc, xcp)$$
- | *bisim1InstanceOfThrow*:  

$$P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \implies P, e \text{ instanceof } T, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a])$$
- | *bisim1Val*:  $P, \text{Val } v, h \vdash (\text{Val } v, xs) \leftrightarrow ([], xs, 0, \text{None})$
- | *bisim1Var*:  $P, \text{Var } V, h \vdash (\text{Var } V, xs) \leftrightarrow ([], xs, 0, \text{None})$
- | *bisim1BinOp1*:  

$$P, e1, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \implies P, e1 \llbracket \text{bop} \rrbracket e2, h \vdash (e' \llbracket \text{bop} \rrbracket e2, xs) \leftrightarrow (stk, loc, pc, xcp)$$
- | *bisim1BinOp2*:  

$$P, e2, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \implies P, e1 \llbracket \text{bop} \rrbracket e2, h \vdash (\text{Val } v1 \llbracket \text{bop} \rrbracket e', xs) \leftrightarrow (stk @ [v1], loc, \text{length } (\text{compE2 } e1) + pc, xcp)$$
- | *bisim1BinOpThrow1*:  

$$P, e1, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \implies P, e1 \llbracket \text{bop} \rrbracket e2, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a])$$
- | *bisim1BinOpThrow2*:  

$$P, e2, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \implies P, e1 \llbracket \text{bop} \rrbracket e2, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk @ [v1], loc, \text{length } (\text{compE2 } e1) + pc, [a])$$
- | *bisim1BinOpThrow*:  

$$P, e1 \llbracket \text{bop} \rrbracket e2, h \vdash (\text{Throw } a, xs) \leftrightarrow ([v1, v2], xs, \text{length } (\text{compE2 } e1) + \text{length } (\text{compE2 } e2), [a])$$
- | *bisim1LAss1*:  

$$P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \implies P, V := e, h \vdash (V := e', xs) \leftrightarrow (stk, loc, pc, xcp)$$
- | *bisim1LAss2*:  

$$P, V := e, h \vdash (\text{unit}, xs) \leftrightarrow ([], xs, \text{Suc } (\text{length } (\text{compE2 } e)), \text{None})$$
- | *bisim1LAssThrow*:  

$$P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \implies P, V := e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a])$$

| *bisim1AAcc1*:  
 $P, a, h \vdash (a', xs) \leftrightarrow (stk, loc, pc, xcp)$   
 $\implies P, a[i], h \vdash (a'[i], xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1AAcc2*:  
 $P, i, h \vdash (i', xs) \leftrightarrow (stk, loc, pc, xcp)$   
 $\implies P, a[i], h \vdash (Val\ v[i'], xs) \leftrightarrow (stk\ @\ [v], loc, length\ (compE2\ a) + pc, xcp)$

| *bisim1AAccThrow1*:  
 $P, a, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$   
 $\implies P, a[i], h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$

| *bisim1AAccThrow2*:  
 $P, i, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$   
 $\implies P, a[i], h \vdash (Throw\ ad, xs) \leftrightarrow (stk\ @\ [v], loc, length\ (compE2\ a) + pc, [ad])$

| *bisim1AAccFail*:  
 $P, a[i], h \vdash (Throw\ ad, xs) \leftrightarrow ([v, v'], xs, length\ (compE2\ a) + length\ (compE2\ i), [ad])$

| *bisim1AAss1*:  
 $P, a, h \vdash (a', xs) \leftrightarrow (stk, loc, pc, xcp)$   
 $\implies P, a[i] := e, h \vdash (a'[i] := e, xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1AAss2*:  
 $P, i, h \vdash (i', xs) \leftrightarrow (stk, loc, pc, xcp)$   
 $\implies P, a[i] := e, h \vdash (Val\ v[i'] := e, xs) \leftrightarrow (stk\ @\ [v], loc, length\ (compE2\ a) + pc, xcp)$

| *bisim1AAss3*:  
 $P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp)$   
 $\implies P, a[i] := e, h \vdash (Val\ v[Val\ v'] := e', xs) \leftrightarrow (stk\ @\ [v', v], loc, length\ (compE2\ a) + length\ (compE2\ i) + pc, xcp)$

| *bisim1AAssThrow1*:  
 $P, a, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$   
 $\implies P, a[i] := e, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$

| *bisim1AAssThrow2*:  
 $P, i, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$   
 $\implies P, a[i] := e, h \vdash (Throw\ ad, xs) \leftrightarrow (stk\ @\ [v], loc, length\ (compE2\ a) + pc, [ad])$

| *bisim1AAssThrow3*:  
 $P, e, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$   
 $\implies P, a[i] := e, h \vdash (Throw\ ad, xs) \leftrightarrow (stk\ @\ [v', v], loc, length\ (compE2\ a) + length\ (compE2\ i) + pc, [ad])$

| *bisim1AAssFail*:  
 $P, a[i] := e, h \vdash (Throw\ ad, xs) \leftrightarrow ([v', v, v''], xs, length\ (compE2\ a) + length\ (compE2\ i) + length\ (compE2\ e), [ad])$

| *bisim1AAss4*:  
 $P, a[i] := e, h \vdash (unit, xs) \leftrightarrow ([], xs, Suc\ (length\ (compE2\ a) + length\ (compE2\ i) + length\ (compE2\ e)), None)$

| *bisim1ALength*:

$$P, a, h \vdash (a', xs) \leftrightarrow (stk, loc, pc, xcp) \\ \implies P, a \cdot length, h \vdash (a' \cdot length, xs) \leftrightarrow (stk, loc, pc, xcp)$$

| *bisim1ALengthThrow*:

$$P, a, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor) \\ \implies P, a \cdot length, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor)$$

| *bisim1ALengthNull*:

$$P, a \cdot length, h \vdash (THROW\ NullPointer, xs) \leftrightarrow ([Null], xs, length\ (compE2\ a), \lfloor addr-of-sys-xcpt\ NullPointer \rfloor)$$

| *bisim1FAcc*:

$$P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \\ \implies P, e \cdot F\{D\}, h \vdash (e' \cdot F\{D\}, xs) \leftrightarrow (stk, loc, pc, xcp)$$

| *bisim1FAccThrow*:

$$P, e, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor) \\ \implies P, e \cdot F\{D\}, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor)$$

| *bisim1FAccNull*:

$$P, e \cdot F\{D\}, h \vdash (THROW\ NullPointer, xs) \leftrightarrow ([Null], xs, length\ (compE2\ e), \lfloor addr-of-sys-xcpt\ NullPointer \rfloor)$$

| *bisim1FAss1*:

$$P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \\ \implies P, e \cdot F\{D\} := e2, h \vdash (e' \cdot F\{D\} := e2, xs) \leftrightarrow (stk, loc, pc, xcp)$$

| *bisim1FAss2*:

$$P, e2, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \\ \implies P, e \cdot F\{D\} := e2, h \vdash (Val\ v \cdot F\{D\} := e', xs) \leftrightarrow (stk\ @\ [v], loc, length\ (compE2\ e) + pc, xcp)$$

| *bisim1FAssThrow1*:

$$P, e, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor) \\ \implies P, e \cdot F\{D\} := e2, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor)$$

| *bisim1FAssThrow2*:

$$P, e2, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor) \\ \implies P, e \cdot F\{D\} := e2, h \vdash (Throw\ ad, xs) \leftrightarrow (stk\ @\ [v], loc, length\ (compE2\ e) + pc, \lfloor ad \rfloor)$$

| *bisim1FAssNull*:

$$P, e \cdot F\{D\} := e2, h \vdash (THROW\ NullPointer, xs) \leftrightarrow ([v, Null], xs, length\ (compE2\ e) + length\ (compE2\ e2), \lfloor addr-of-sys-xcpt\ NullPointer \rfloor)$$

| *bisim1FAss3*:

$$P, e \cdot F\{D\} := e2, h \vdash (unit, xs) \leftrightarrow ([], xs, Suc\ (length\ (compE2\ e) + length\ (compE2\ e2)), None)$$

| *bisim1CAS1*:



$P, e1, h \vdash (e1', xs) \leftrightarrow (stk, loc, pc, xcp)$   
 $\implies P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h \vdash (e1' \cdot compareAndSwap(D \cdot F, e2, e3), xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1CAS2*:

$P, e2, h \vdash (e2', xs) \leftrightarrow (stk, loc, pc, xcp)$   
 $\implies P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h \vdash (Val\ v \cdot compareAndSwap(D \cdot F, e2', e3), xs) \leftrightarrow (stk @ [v], loc, length\ (compE2\ e1) + pc, xcp)$

| *bisim1CAS3*:

$P, e3, h \vdash (e3', xs) \leftrightarrow (stk, loc, pc, xcp)$   
 $\implies P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h \vdash (Val\ v \cdot compareAndSwap(D \cdot F, Val\ v', e3'), xs) \leftrightarrow (stk @ [v', v], loc, length\ (compE2\ e1) + length\ (compE2\ e2) + pc, xcp)$

| *bisim1CASThrow1*:

$P, e1, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$   
 $\implies P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$

| *bisim1CASThrow2*:

$P, e2, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$   
 $\implies P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h \vdash (Throw\ ad, xs) \leftrightarrow (stk @ [v], loc, length\ (compE2\ e1) + pc, [ad])$

| *bisim1CASThrow3*:

$P, e3, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, [ad])$   
 $\implies P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h \vdash (Throw\ ad, xs) \leftrightarrow (stk @ [v', v], loc, length\ (compE2\ e1) + length\ (compE2\ e2) + pc, [ad])$

| *bisim1CASFail*:

$P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h \vdash (Throw\ ad, xs) \leftrightarrow ([v', v, v'], xs, length\ (compE2\ e1) + length\ (compE2\ e2) + length\ (compE2\ e3), [ad])$

| *bisim1Call1*:

$P, obj, h \vdash (obj', xs) \leftrightarrow (stk, loc, pc, xcp)$   
 $\implies P, obj \cdot M(ps), h \vdash (obj' \cdot M(ps), xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1CallParams*:

$P, ps, h \vdash (ps', xs) [\leftrightarrow] (stk, loc, pc, xcp)$   
 $\implies P, obj \cdot M(ps), h \vdash (Val\ v \cdot M(ps'), xs) \leftrightarrow (stk @ [v], loc, length\ (compE2\ obj) + pc, xcp)$

| *bisim1CallThrowObj*:

$P, obj, h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a])$   
 $\implies P, obj \cdot M(ps), h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a])$

| *bisim1CallThrowParams*:

$P, ps, h \vdash (map\ Val\ vs\ @\ Throw\ a\ \# \ ps', xs) [\leftrightarrow] (stk, loc, pc, [a])$   
 $\implies P, obj \cdot M(ps), h \vdash (Throw\ a, xs) \leftrightarrow (stk @ [v], loc, length\ (compE2\ obj) + pc, [a])$

| *bisim1CallThrow*:

$length\ ps = length\ vs$   
 $\implies P, obj \cdot M(ps), h \vdash (Throw\ a, xs) \leftrightarrow (vs @ [v], xs, length\ (compE2\ obj) + length\ (compEs2\ ps), [a])$

- | *bisim1BlockSome1*:  
 $P, \{V:T=[v]; e\}, h \vdash (\{V:T=[v]; e\}, xs) \leftrightarrow ([], xs, 0, None)$
- | *bisim1BlockSome2*:  
 $P, \{V:T=[v]; e\}, h \vdash (\{V:T=[v]; e\}, xs) \leftrightarrow ([v], xs, Suc\ 0, None)$
- | *bisim1BlockSome4*:  
 $P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp)$   
 $\implies P, \{V:T=[v]; e\}, h \vdash (\{V:T=None; e'\}, xs) \leftrightarrow (stk, loc, Suc\ (Suc\ pc), xcp)$
- | *bisim1BlockThrowSome*:  
 $P, e, h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a])$   
 $\implies P, \{V:T=[v]; e\}, h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, Suc\ (Suc\ pc), [a])$
- | *bisim1BlockNone*:  
 $P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp)$   
 $\implies P, \{V:T=None; e\}, h \vdash (\{V:T=None; e'\}, xs) \leftrightarrow (stk, loc, pc, xcp)$
- | *bisim1BlockThrowNone*:  
 $P, e, h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a])$   
 $\implies P, \{V:T=None; e\}, h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a])$
- | *bisim1Sync1*:  
 $P, e1, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp)$   
 $\implies P, sync_V\ (e1)\ e2, h \vdash (sync_V\ (e')\ e2, xs) \leftrightarrow (stk, loc, pc, xcp)$
- | *bisim1Sync2*:  
 $P, sync_V\ (e1)\ e2, h \vdash (sync_V\ (Val\ v)\ e2, xs) \leftrightarrow ([v, v], xs, Suc\ (length\ (compE2\ e1)), None)$
- | *bisim1Sync3*:  
 $P, sync_V\ (e1)\ e2, h \vdash (sync_V\ (Val\ v)\ e2, xs) \leftrightarrow ([v], xs[V := v], Suc\ (Suc\ (length\ (compE2\ e1))), None)$
- | *bisim1Sync4*:  
 $P, e2, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp)$   
 $\implies P, sync_V\ (e1)\ e2, h \vdash (insync_V\ (a)\ e', xs) \leftrightarrow (stk, loc, Suc\ (Suc\ (Suc\ (length\ (compE2\ e1) + pc))), xcp)$
- | *bisim1Sync5*:  
 $P, sync_V\ (e1)\ e2, h \vdash (insync_V\ (a)\ Val\ v, xs) \leftrightarrow ([xs ! V, v], xs, 4 + length\ (compE2\ e1) + length\ (compE2\ e2), None)$
- | *bisim1Sync6*:  
 $P, sync_V\ (e1)\ e2, h \vdash (Val\ v, xs) \leftrightarrow ([v], xs, 5 + length\ (compE2\ e1) + length\ (compE2\ e2), None)$
- | *bisim1Sync7*:  
 $P, sync_V\ (e1)\ e2, h \vdash (insync_V\ (a)\ Throw\ a', xs) \leftrightarrow ([Addr\ a'], xs, 6 + length\ (compE2\ e1) + length\ (compE2\ e2), None)$
- | *bisim1Sync8*:  
 $P, sync_V\ (e1)\ e2, h \vdash (insync_V\ (a)\ Throw\ a', xs) \leftrightarrow$   
 $\quad ([xs ! V, Addr\ a'], xs, 7 + length\ (compE2\ e1) + length\ (compE2\ e2), None)$

| *bisim1Sync9*:  
 $P, \text{sync}_V(e1) e2, h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], xs, 8 + \text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2), \text{None})$

| *bisim1Sync10*:  
 $P, \text{sync}_V(e1) e2, h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], xs, 8 + \text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2), [a])$

| *bisim1Sync11*:  
 $P, \text{sync}_V(e1) e2, h \vdash (\text{THROW } \text{NullPointer}, xs) \leftrightarrow ([\text{Null}], xs, \text{Suc}(\text{Suc}(\text{length}(\text{compE2 } e1))), [\text{addr-of-sys-xcpt } \text{NullPointer}])$

| *bisim1Sync12*:  
 $P, \text{sync}_V(e1) e2, h \vdash (\text{Throw } a, xs) \leftrightarrow ([v, v'], xs, 4 + \text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2), [a])$

| *bisim1Sync14*:  
 $P, \text{sync}_V(e1) e2, h \vdash (\text{Throw } a, xs) \leftrightarrow ([v, \text{Addr } a'], xs, 7 + \text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2), [a])$

| *bisim1SyncThrow*:  
 $P, e1, h \vdash (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a])$   
 $\implies P, \text{sync}_V(e1) e2, h \vdash (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a])$

| *bisim1InSync*: — This rule only exists such that  $P, e, h \vdash (e, xs) \leftrightarrow ([], xs, 0, \text{None})$  holds for all  $e$   
 $P, \text{insync}_V(a) e, h \vdash (\text{insync}_V(a) e, xs) \leftrightarrow ([], xs, 0, \text{None})$

| *bisim1Seq1*:  
 $P, e1, h \vdash (e', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \implies P, e1;;e2, h \vdash (e';;e2, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp})$

| *bisim1SeqThrow1*:  
 $P, e1, h \vdash (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a]) \implies P, e1;;e2, h \vdash (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a])$

| *bisim1Seq2*:  
 $P, e2, h \vdash \text{exs} \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp})$   
 $\implies P, e1;;e2, h \vdash \text{exs} \leftrightarrow (\text{stk}, \text{loc}, \text{Suc}(\text{length}(\text{compE2 } e1) + \text{pc}), \text{xcp})$

| *bisim1Cond1*:  
 $P, e, h \vdash (e', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp})$   
 $\implies P, \text{if } (e) e1 \text{ else } e2, h \vdash (\text{if } (e') e1 \text{ else } e2, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp})$

| *bisim1CondThen*:  
 $P, e1, h \vdash \text{exs} \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp})$   
 $\implies P, \text{if } (e) e1 \text{ else } e2, h \vdash \text{exs} \leftrightarrow (\text{stk}, \text{loc}, \text{Suc}(\text{length}(\text{compE2 } e) + \text{pc}), \text{xcp})$

| *bisim1CondElse*:  
 $P, e2, h \vdash \text{exs} \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp})$   
 $\implies P, \text{if } (e) e1 \text{ else } e2, h \vdash \text{exs} \leftrightarrow (\text{stk}, \text{loc}, \text{Suc}(\text{Suc}(\text{length}(\text{compE2 } e) + \text{length}(\text{compE2 } e1) + \text{pc})), \text{xcp})$

| *bisim1CondThrow*:

$P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$   
 $\implies P, \text{if } (e) \ e1 \ \text{else } e2, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$

| *bisim1While1*:  
 $P, \text{while } (c) \ e, h \vdash (\text{while } (c) \ e, xs) \leftrightarrow ([], xs, 0, \text{None})$

| *bisim1While3*:  
 $P, c, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp)$   
 $\implies P, \text{while } (c) \ e, h \vdash (\text{if } (e') \ (e;; \text{while } (c) \ e) \ \text{else unit}, xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1While4*:  
 $P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp)$   
 $\implies P, \text{while } (c) \ e, h \vdash (e';; \text{while } (c) \ e, xs) \leftrightarrow (stk, loc, \text{Suc } (\text{length } (\text{compE2 } c) + pc), xcp)$

| *bisim1While6*:  
 $P, \text{while } (c) \ e, h \vdash (\text{while } (c) \ e, xs) \leftrightarrow ([], xs, \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } c) + \text{length } (\text{compE2 } e))), \text{None})$

| *bisim1While7*:  
 $P, \text{while } (c) \ e, h \vdash (\text{unit}, xs) \leftrightarrow ([], xs, \text{Suc } (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } c) + \text{length } (\text{compE2 } e)))), \text{None})$

| *bisim1WhileThrow1*:  
 $P, c, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$   
 $\implies P, \text{while } (c) \ e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$

| *bisim1WhileThrow2*:  
 $P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$   
 $\implies P, \text{while } (c) \ e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, \text{Suc } (\text{length } (\text{compE2 } c) + pc), \lfloor a \rfloor)$

| *bisim1Throw1*:  
 $P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \implies P, \text{throw } e, h \vdash (\text{throw } e', xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1Throw2*:  
 $P, \text{throw } e, h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], xs, \text{length } (\text{compE2 } e), \lfloor a \rfloor)$

| *bisim1ThrowNull*:  
 $P, \text{throw } e, h \vdash (\text{THROW } \text{NullPointer}, xs) \leftrightarrow ([\text{Null}], xs, \text{length } (\text{compE2 } e), \lfloor \text{addr-of-sys-xcpt NullPointer} \rfloor)$

| *bisim1ThrowThrow*:  
 $P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor) \implies P, \text{throw } e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$

| *bisim1Try*:  
 $P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp)$   
 $\implies P, \text{try } e \ \text{catch}(C \ V) \ e2, h \vdash (\text{try } e' \ \text{catch}(C \ V) \ e2, xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1TryCatch1*:  
 $\llbracket P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor); \text{typeof-addr } h \ a = \lfloor \text{Class-type } C' \rfloor; P \vdash C' \preceq^* C \rrbracket$   
 $\implies P, \text{try } e \ \text{catch}(C \ V) \ e2, h \vdash (\{V:\text{Class } C=\text{None}; e2\}, xs[V := \text{Addr } a]) \leftrightarrow ([\text{Addr } a], loc, \text{Suc } (\text{length } (\text{compE2 } e)), \text{None})$

| *bisim1TryCatch2*:  
 $P, e2, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp)$   
 $\implies P, \text{try } e \text{ catch}(C \ V) \ e2, h \vdash (\{V:Class \ C=None; e'\}, xs) \leftrightarrow (stk, loc, Suc \ (Suc \ (length \ (compE2 \ e) + pc)), xcp)$

| *bisim1TryFail*:  
 $\llbracket P, e, h \vdash (Throw \ a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor); \text{typeof-addr } h \ a = \lfloor Class\text{-type } C' \rfloor; \neg P \vdash C' \preceq^* C \rrbracket$   
 $\implies P, \text{try } e \text{ catch}(C \ V) \ e2, h \vdash (Throw \ a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$

| *bisim1TryCatchThrow*:  
 $P, e2, h \vdash (Throw \ a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$   
 $\implies P, \text{try } e \text{ catch}(C \ V) \ e2, h \vdash (Throw \ a, xs) \leftrightarrow (stk, loc, Suc \ (Suc \ (length \ (compE2 \ e) + pc)), \lfloor a \rfloor)$

| *bisims1Nil*:  $P, [], h \vdash ([], xs) [\leftrightarrow] ([], xs, 0, None)$

| *bisims1List1*:  
 $P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \implies P, e\#es, h \vdash (e'\#es, xs) [\leftrightarrow] (stk, loc, pc, xcp)$

| *bisims1List2*:  
 $P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, xcp)$   
 $\implies P, e\#es, h \vdash (Val \ v \ \# \ es', xs) [\leftrightarrow] (stk \ @ \ [v], loc, length \ (compE2 \ e) + pc, xcp)$

**inductive-cases** *bisim1-cases*:

$P, e, h \vdash (Val \ v, xs) \leftrightarrow (stk, loc, pc, xcp)$

**lemma** *bisim1-refl*:  $P, e, h \vdash (e, xs) \leftrightarrow ([], xs, 0, None)$   
**and** *bisims1-refl*:  $P, es, h \vdash (es, xs) [\leftrightarrow] ([], xs, 0, None)$   
**apply**(*induct e and es rule: call.induct calls.induct*)  
**apply**(*auto intro: bisim1-bisims1.intros simp add: nat-fun-sum-eq-conv*)  
**apply**(*rename-tac option a*)  
**apply**(*case-tac option*)  
**apply**(*auto intro: bisim1-bisims1.intros split: if-split-asm*)  
**done**

**lemma** *bisims1-lengthD*:  $P, es, h \vdash (es', xs) [\leftrightarrow] s \implies length \ es = length \ es'$   
**apply**(*induct es arbitrary: es' s*)  
**apply**(*auto elim: bisims1.cases*)  
**done**

Derive an alternative induction rule for *bisim1* such that (i) induction hypothesis are generated for all subexpressions and (ii) the number of surrounding blocks is passed through.

**inductive** *bisim1'* ::

$'m \text{ prog} \Rightarrow 'heap \Rightarrow 'addr \ expr1 \Rightarrow nat \Rightarrow ('addr \ expr1 \times 'addr \ locals1)$   
 $\Rightarrow ('addr \ val \ list \times 'addr \ val \ list \times pc \times 'addr \ option) \Rightarrow bool$

**and** *bisims1'* ::

$'m \text{ prog} \Rightarrow 'heap \Rightarrow 'addr \ expr1 \ list \Rightarrow nat \Rightarrow ('addr \ expr1 \ list \times 'addr \ locals1)$   
 $\Rightarrow ('addr \ val \ list \times 'addr \ val \ list \times pc \times 'addr \ option) \Rightarrow bool$

**and** *bisim1'-syntax* ::

'm prog  $\Rightarrow$  'addr expr1  $\Rightarrow$  nat  $\Rightarrow$  'heap  $\Rightarrow$  ('addr expr1  $\times$  'addr locals1)  
 $\Rightarrow$  ('addr val list  $\times$  'addr val list  $\times$  pc  $\times$  'addr option)  $\Rightarrow$  bool  
 (-, -, -,  $\vdash''$  -  $\leftrightarrow$  - [50, 0, 0, 0, 0, 50] 100)

**and** bisims1'-syntax ::

'm prog  $\Rightarrow$  'addr expr1 list  $\Rightarrow$  nat  $\Rightarrow$  'heap  $\Rightarrow$  ('addr expr1 list  $\times$  'addr val list)  
 $\Rightarrow$  ('addr val list  $\times$  'addr val list  $\times$  pc  $\times$  'addr option)  $\Rightarrow$  bool  
 (-, -, -,  $\vdash''$  - [ $\leftrightarrow$ ] - [50, 0, 0, 0, 0, 50] 100)

**for** P :: 'm prog **and** h :: 'heap

**where**

P, e, n, h  $\vdash'$  exs  $\leftrightarrow$  s  $\equiv$  bisim1' P h e n exs s

| P, es, n, h  $\vdash'$  esxs [ $\leftrightarrow$ ] s  $\equiv$  bisims1' P h es n esxs s

| bisim1Val2':

P, e, n, h  $\vdash'$  (Val v, xs)  $\leftrightarrow$  (v # [], xs, length (compE2 e), None)

| bisim1New':

P, new C, n, h  $\vdash'$  (new C, xs)  $\leftrightarrow$  ([], xs, 0, None)

| bisim1NewThrow':

P, new C, n, h  $\vdash'$  (THROW OutOfMemory, xs)  $\leftrightarrow$  ([], xs, 0, [addr-of-sys-xcpt OutOfMemory])

| bisim1NewArray':

P, e, n, h  $\vdash'$  (e', xs)  $\leftrightarrow$  (stk, loc, pc, xcp)  
 $\Rightarrow$  P, newA T[e], n, h  $\vdash'$  (newA T[e'], xs)  $\leftrightarrow$  (stk, loc, pc, xcp)

| bisim1NewArrayThrow':

P, e, n, h  $\vdash'$  (Throw a, xs)  $\leftrightarrow$  (stk, loc, pc, [a])  
 $\Rightarrow$  P, newA T[e], n, h  $\vdash'$  (Throw a, xs)  $\leftrightarrow$  (stk, loc, pc, [a])

| bisim1NewArrayFail':

( $\bigwedge$ xs. P, e, n, h  $\vdash'$  (e, xs)  $\leftrightarrow$  ([], xs, 0, None))  
 $\Rightarrow$  P, newA T[e], n, h  $\vdash'$  (Throw a, xs)  $\leftrightarrow$  ([v], xs, length (compE2 e), [a])

| bisim1Cast':

P, e, n, h  $\vdash'$  (e', xs)  $\leftrightarrow$  (stk, loc, pc, xcp)  
 $\Rightarrow$  P, Cast T e, n, h  $\vdash'$  (Cast T e', xs)  $\leftrightarrow$  (stk, loc, pc, xcp)

| bisim1CastThrow':

P, e, n, h  $\vdash'$  (Throw a, xs)  $\leftrightarrow$  (stk, loc, pc, [a])  
 $\Rightarrow$  P, Cast T e, n, h  $\vdash'$  (Throw a, xs)  $\leftrightarrow$  (stk, loc, pc, [a])

| bisim1CastFail':

( $\bigwedge$ xs. P, e, n, h  $\vdash'$  (e, xs)  $\leftrightarrow$  ([], xs, 0, None))  
 $\Rightarrow$  P, Cast T e, n, h  $\vdash'$  (THROW ClassCast, xs)  $\leftrightarrow$  ([v], xs, length (compE2 e), [addr-of-sys-xcpt ClassCast])

| bisim1InstanceOf':

P, e, n, h  $\vdash'$  (e', xs)  $\leftrightarrow$  (stk, loc, pc, xcp)  
 $\Rightarrow$  P, e instanceof T, n, h  $\vdash'$  (e' instanceof T, xs)  $\leftrightarrow$  (stk, loc, pc, xcp)

- | *bisim1InstanceOfThrow'*:  
 $P, e, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$   
 $\implies P, e\ instanceof\ T, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$
- | *bisim1Val'*:  $P, Val\ v, n, h \vdash' (Val\ v, xs) \leftrightarrow ([], xs, 0, None)$
- | *bisim1Var'*:  $P, Var\ V, n, h \vdash' (Var\ V, xs) \leftrightarrow ([], xs, 0, None)$
- | *bisim1BinOp1'*:  
 $\llbracket P, e1, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp);$   
 $\bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$   
 $\implies P, e1 \llbracket bop \rrbracket e2, n, h \vdash' (e' \llbracket bop \rrbracket e2, xs) \leftrightarrow (stk, loc, pc, xcp)$
- | *bisim1BinOp2'*:  
 $\llbracket P, e2, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp);$   
 $\bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$   
 $\implies P, e1 \llbracket bop \rrbracket e2, n, h \vdash' (Val\ v1 \llbracket bop \rrbracket e', xs) \leftrightarrow (stk\ @\ [v1], loc, length\ (compE2\ e1) + pc, xcp)$
- | *bisim1BinOpThrow1'*:  
 $\llbracket P, e1, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor);$   
 $\bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$   
 $\implies P, e1 \llbracket bop \rrbracket e2, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$
- | *bisim1BinOpThrow2'*:  
 $\llbracket P, e2, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor);$   
 $\bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$   
 $\implies P, e1 \llbracket bop \rrbracket e2, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk\ @\ [v1], loc, length\ (compE2\ e1) + pc, \lfloor a \rfloor)$
- | *bisim1BinOpThrow'*:  
 $\llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None);$   
 $\bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$   
 $\implies P, e1 \llbracket bop \rrbracket e2, n, h \vdash' (Throw\ a, xs) \leftrightarrow ([v1, v2], xs, length\ (compE2\ e1) + length\ (compE2\ e2), \lfloor a \rfloor)$
- | *bisim1LAss1'*:  
 $P, e, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp)$   
 $\implies P, V := e, n, h \vdash' (V := e', xs) \leftrightarrow (stk, loc, pc, xcp)$
- | *bisim1LAss2'*:  
 $(\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None))$   
 $\implies P, V := e, n, h \vdash' (unit, xs) \leftrightarrow ([], xs, Suc\ (length\ (compE2\ e)), None)$
- | *bisim1LAssThrow'*:  
 $P, e, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$   
 $\implies P, V := e, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$
- | *bisim1AAcc1'*:  
 $\llbracket P, a, n, h \vdash' (a', xs) \leftrightarrow (stk, loc, pc, xcp); \bigwedge xs. P, i, n, h \vdash' (i, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$   
 $\implies P, a[i], n, h \vdash' (a'[i], xs) \leftrightarrow (stk, loc, pc, xcp)$
- | *bisim1AAcc2'*:

$$\begin{aligned} & \llbracket P, i, n, h \vdash' (i', xs) \leftrightarrow (stk, loc, pc, xcp); \bigwedge xs. P, a, n, h \vdash' (a, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket \\ & \implies P, a[i], n, h \vdash' (Val v[i'], xs) \leftrightarrow (stk @ [v], loc, length (compE2 a) + pc, xcp) \end{aligned}$$

| *bisim1AAccThrow1'*:

$$\begin{aligned} & \llbracket P, a, n, h \vdash' (Throw ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor); \\ & \quad \bigwedge xs. P, i, n, h \vdash' (i, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket \\ & \implies P, a[i], n, h \vdash' (Throw ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor) \end{aligned}$$

| *bisim1AAccThrow2'*:

$$\begin{aligned} & \llbracket P, i, n, h \vdash' (Throw ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor); \\ & \quad \bigwedge xs. P, a, n, h \vdash' (a, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket \\ & \implies P, a[i], n, h \vdash' (Throw ad, xs) \leftrightarrow (stk @ [v], loc, length (compE2 a) + pc, \lfloor ad \rfloor) \end{aligned}$$

| *bisim1AAccFail'*:

$$\begin{aligned} & \llbracket \bigwedge xs. P, a, n, h \vdash' (a, xs) \leftrightarrow (\llbracket, xs, 0, None); \bigwedge xs. P, i, n, h \vdash' (i, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket \\ & \implies P, a[i], n, h \vdash' (Throw ad, xs) \leftrightarrow ([v, v'], xs, length (compE2 a) + length (compE2 i), \lfloor ad \rfloor) \end{aligned}$$

| *bisim1AAss1'*:

$$\begin{aligned} & \llbracket P, a, n, h \vdash' (a', xs) \leftrightarrow (stk, loc, pc, xcp); \\ & \quad \bigwedge xs. P, i, n, h \vdash' (i, xs) \leftrightarrow (\llbracket, xs, 0, None); \\ & \quad \bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket \\ & \implies P, a[i] := e, n, h \vdash' (a'[i] := e, xs) \leftrightarrow (stk, loc, pc, xcp) \end{aligned}$$

| *bisim1AAss2'*:

$$\begin{aligned} & \llbracket P, i, n, h \vdash' (i', xs) \leftrightarrow (stk, loc, pc, xcp); \\ & \quad \bigwedge xs. P, a, n, h \vdash' (a, xs) \leftrightarrow (\llbracket, xs, 0, None); \\ & \quad \bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket \\ & \implies P, a[i] := e, n, h \vdash' (Val v[i'] := e, xs) \leftrightarrow (stk @ [v], loc, length (compE2 a) + pc, xcp) \end{aligned}$$

| *bisim1AAss3'*:

$$\begin{aligned} & \llbracket P, e, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp); \\ & \quad \bigwedge xs. P, a, n, h \vdash' (a, xs) \leftrightarrow (\llbracket, xs, 0, None); \\ & \quad \bigwedge xs. P, i, n, h \vdash' (i, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket \\ & \implies P, a[i] := e, n, h \vdash' (Val v[Val v'] := e', xs) \leftrightarrow (stk @ [v', v], loc, length (compE2 a) + length (compE2 i) + pc, xcp) \end{aligned}$$

| *bisim1AAssThrow1'*:

$$\begin{aligned} & \llbracket P, a, n, h \vdash' (Throw ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor); \\ & \quad \bigwedge xs. P, i, n, h \vdash' (i, xs) \leftrightarrow (\llbracket, xs, 0, None); \\ & \quad \bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket \\ & \implies P, a[i] := e, n, h \vdash' (Throw ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor) \end{aligned}$$

| *bisim1AAssThrow2'*:

$$\begin{aligned} & \llbracket P, i, n, h \vdash' (Throw ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor); \\ & \quad \bigwedge xs. P, a, n, h \vdash' (a, xs) \leftrightarrow (\llbracket, xs, 0, None); \\ & \quad \bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket \\ & \implies P, a[i] := e, n, h \vdash' (Throw ad, xs) \leftrightarrow (stk @ [v], loc, length (compE2 a) + pc, \lfloor ad \rfloor) \end{aligned}$$

| *bisim1AAssThrow3'*:

$$\begin{aligned} & \llbracket P, e, n, h \vdash' (Throw ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor); \\ & \quad \bigwedge xs. P, a, n, h \vdash' (a, xs) \leftrightarrow (\llbracket, xs, 0, None); \\ & \quad \bigwedge xs. P, i, n, h \vdash' (i, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket \\ & \implies P, a[i] := e, n, h \vdash' (Throw ad, xs) \leftrightarrow (stk @ [v', v], loc, length (compE2 a) + length (compE2 i) + pc, \lfloor ad \rfloor) \end{aligned}$$



$i) + pc, \lfloor ad \rfloor$ )

| *bisim1AssFail'*:

$\llbracket \bigwedge xs. P, a, n, h \vdash' (a, xs) \leftrightarrow ([], xs, 0, None);$   
 $\bigwedge xs. P, i, n, h \vdash' (i, xs) \leftrightarrow ([], xs, 0, None);$   
 $\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$   
 $\implies P, a[i] := e, n, h \vdash' (Throw\ ad, xs) \leftrightarrow ([v', v, v''], xs, length\ (compE2\ a) + length\ (compE2\ i)$   
 $+ length\ (compE2\ e), \lfloor ad \rfloor)$

| *bisim1Ass4'*:

$\llbracket \bigwedge xs. P, a, n, h \vdash' (a, xs) \leftrightarrow ([], xs, 0, None);$   
 $\bigwedge xs. P, i, n, h \vdash' (i, xs) \leftrightarrow ([], xs, 0, None);$   
 $\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$   
 $\implies P, a[i] := e, n, h \vdash' (unit, xs) \leftrightarrow ([], xs, Suc\ (length\ (compE2\ a) + length\ (compE2\ i) + length$   
 $(compE2\ e)), None)$

| *bisim1ALength'*:

$P, a, n, h \vdash' (a', xs) \leftrightarrow (stk, loc, pc, xcp)$   
 $\implies P, a \cdot length, n, h \vdash' (a' \cdot length, xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1ALengthThrow'*:

$P, a, n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor)$   
 $\implies P, a \cdot length, n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor)$

| *bisim1ALengthNull'*:

$(\bigwedge xs. P, a, n, h \vdash' (a, xs) \leftrightarrow ([], xs, 0, None))$   
 $\implies P, a \cdot length, n, h \vdash' (THROW\ NullPointer, xs) \leftrightarrow ([Null], xs, length\ (compE2\ a), \lfloor addr-of-sys-xcpt$   
 $NullPointer \rfloor)$

| *bisim1FAcc'*:

$P, e, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp)$   
 $\implies P, e \cdot F\{D\}, n, h \vdash' (e' \cdot F\{D\}, xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1FAccThrow'*:

$P, e, n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor)$   
 $\implies P, e \cdot F\{D\}, n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor)$

| *bisim1FAccNull'*:

$(\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None))$   
 $\implies P, e \cdot F\{D\}, n, h \vdash' (THROW\ NullPointer, xs) \leftrightarrow ([Null], xs, length\ (compE2\ e), \lfloor addr-of-sys-xcpt$   
 $NullPointer \rfloor)$

| *bisim1FAss1'*:

$\llbracket P, e, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp);$   
 $\bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$   
 $\implies P, e \cdot F\{D\} := e2, n, h \vdash' (e' \cdot F\{D\} := e2, xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1FAss2'*:

$\llbracket P, e2, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp);$   
 $\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$

$\Rightarrow P, e \cdot F\{D\} := e2, n, h \vdash' (Val\ v \cdot F\{D\} := e', xs) \leftrightarrow (stk \ @ \ [v], loc, length\ (compE2\ e) + pc, xcp)$

| *bisim1FAssThrow1'*:

$\llbracket P, e, n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor);$   
 $\bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket$   
 $\Rightarrow P, e \cdot F\{D\} := e2, n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor)$

| *bisim1FAssThrow2'*:

$\llbracket P, e2, n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor);$   
 $\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket$   
 $\Rightarrow P, e \cdot F\{D\} := e2, n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk \ @ \ [v], loc, length\ (compE2\ e) + pc, \lfloor ad \rfloor)$

| *bisim1FAssNull'*:

$\llbracket \bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow (\llbracket, xs, 0, None);$   
 $\bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket$   
 $\Rightarrow P, e \cdot F\{D\} := e2, n, h \vdash' (THROW\ NullPointer, xs) \leftrightarrow ([v, Null], xs, length\ (compE2\ e) + length\ (compE2\ e2), \lfloor addr-of-sys-xcpt\ NullPointer \rfloor)$

| *bisim1FAss3'*:

$\llbracket \bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow (\llbracket, xs, 0, None);$   
 $\bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket$   
 $\Rightarrow P, e \cdot F\{D\} := e2, n, h \vdash' (unit, xs) \leftrightarrow (\llbracket, xs, Suc\ (length\ (compE2\ e) + length\ (compE2\ e2)), None)$

| *bisim1CAS1'*:

$\llbracket P, e1, n, h \vdash' (e1', xs) \leftrightarrow (stk, loc, pc, xcp);$   
 $\bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow (\llbracket, xs, 0, None);$   
 $\bigwedge xs. P, e3, n, h \vdash' (e3, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket$   
 $\Rightarrow P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), n, h \vdash' (e1' \cdot compareAndSwap(D \cdot F, e2, e3), xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1CAS2'*:

$\llbracket P, e2, n, h \vdash' (e2', xs) \leftrightarrow (stk, loc, pc, xcp);$   
 $\bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow (\llbracket, xs, 0, None);$   
 $\bigwedge xs. P, e3, n, h \vdash' (e3, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket$   
 $\Rightarrow P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), n, h \vdash' (Val\ v \cdot compareAndSwap(D \cdot F, e2', e3), xs) \leftrightarrow (stk \ @ \ [v], loc, length\ (compE2\ e1) + pc, xcp)$

| *bisim1CAS3'*:

$\llbracket P, e3, n, h \vdash' (e3', xs) \leftrightarrow (stk, loc, pc, xcp);$   
 $\bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow (\llbracket, xs, 0, None);$   
 $\bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket$   
 $\Rightarrow P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), n, h \vdash' (Val\ v \cdot compareAndSwap(D \cdot F, Val\ v', e3'), xs) \leftrightarrow (stk \ @ \ [v', v], loc, length\ (compE2\ e1) + length\ (compE2\ e2) + pc, xcp)$

| *bisim1CASThrow1'*:

$\llbracket P, e1, n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor);$   
 $\bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow (\llbracket, xs, 0, None);$   
 $\bigwedge xs. P, e3, n, h \vdash' (e3, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket$   
 $\Rightarrow P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor)$

| *bisim1CASThrow2'*:

$\llbracket P, e2, n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor);$   
 $\bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow (\llbracket, xs, 0, None \rrbracket);$   
 $\bigwedge xs. P, e3, n, h \vdash' (e3, xs) \leftrightarrow (\llbracket, xs, 0, None \rrbracket) \rrbracket$   
 $\implies P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk\ @\ [v], loc, length\ (compE2\ e1) + pc, \lfloor ad \rfloor)$

$| \text{bisim1CASThrow3}':$   
 $\llbracket P, e3, n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, \lfloor ad \rfloor);$   
 $\bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow (\llbracket, xs, 0, None \rrbracket);$   
 $\bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow (\llbracket, xs, 0, None \rrbracket) \rrbracket$   
 $\implies P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), n, h \vdash' (Throw\ ad, xs) \leftrightarrow (stk\ @\ [v', v], loc, length\ (compE2\ e1) + length\ (compE2\ e2) + pc, \lfloor ad \rfloor)$

$| \text{bisim1CASFail}':$   
 $\llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow (\llbracket, xs, 0, None \rrbracket);$   
 $\bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow (\llbracket, xs, 0, None \rrbracket);$   
 $\bigwedge xs. P, e3, n, h \vdash' (e3, xs) \leftrightarrow (\llbracket, xs, 0, None \rrbracket) \rrbracket$   
 $\implies P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), n, h \vdash' (Throw\ ad, xs) \leftrightarrow ([v', v, v'], xs, length\ (compE2\ e1) + length\ (compE2\ e2) + length\ (compE2\ e3), \lfloor ad \rfloor)$

$| \text{bisim1Call1}':$   
 $\llbracket P, obj, n, h \vdash' (obj', xs) \leftrightarrow (stk, loc, pc, xcp);$   
 $\bigwedge xs. P, ps, n, h \vdash' (ps, xs) \leftrightarrow (\llbracket, xs, 0, None \rrbracket) \rrbracket$   
 $\implies P, obj \cdot M(ps), n, h \vdash' (obj' \cdot M(ps), xs) \leftrightarrow (stk, loc, pc, xcp)$

$| \text{bisim1CallParams}':$   
 $\llbracket P, ps, n, h \vdash' (ps', xs) \leftrightarrow (\llbracket, xs, 0, None \rrbracket) \rrbracket (stk, loc, pc, xcp); ps \neq \llbracket;$   
 $\bigwedge xs. P, obj, n, h \vdash' (obj, xs) \leftrightarrow (\llbracket, xs, 0, None \rrbracket) \rrbracket$   
 $\implies P, obj \cdot M(ps), n, h \vdash' (Val\ v \cdot M(ps'), xs) \leftrightarrow (stk\ @\ [v], loc, length\ (compE2\ obj) + pc, xcp)$

$| \text{bisim1CallThrowObj}':$   
 $\llbracket P, obj, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor);$   
 $\bigwedge xs. P, ps, n, h \vdash' (ps, xs) \leftrightarrow (\llbracket, xs, 0, None \rrbracket) \rrbracket$   
 $\implies P, obj \cdot M(ps), n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$

$| \text{bisim1CallThrowParams}':$   
 $\llbracket P, ps, n, h \vdash' (map\ Val\ vs\ @\ Throw\ a\ \# \ ps', xs) \leftrightarrow (\llbracket, xs, 0, None \rrbracket) \rrbracket (stk, loc, pc, \lfloor a \rfloor);$   
 $\bigwedge xs. P, obj, n, h \vdash' (obj, xs) \leftrightarrow (\llbracket, xs, 0, None \rrbracket) \rrbracket$   
 $\implies P, obj \cdot M(ps), n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk\ @\ [v], loc, length\ (compE2\ obj) + pc, \lfloor a \rfloor)$

$| \text{bisim1CallThrow}':$   
 $\llbracket length\ ps = length\ vs;$   
 $\bigwedge xs. P, obj, n, h \vdash' (obj, xs) \leftrightarrow (\llbracket, xs, 0, None \rrbracket); \bigwedge xs. P, ps, n, h \vdash' (ps, xs) \leftrightarrow (\llbracket, xs, 0, None \rrbracket) \rrbracket$   
 $\implies P, obj \cdot M(ps), n, h \vdash' (Throw\ a, xs) \leftrightarrow (vs\ @\ [v], xs, length\ (compE2\ obj) + length\ (compEs2\ ps), \lfloor a \rfloor)$

$| \text{bisim1BlockSome1}':$   
 $(\bigwedge xs. P, e, Suc\ n, h \vdash' (e, xs) \leftrightarrow (\llbracket, xs, 0, None \rrbracket))$   
 $\implies P, \{V:T=\lfloor v \rfloor; e\}, n, h \vdash' (\{V:T=\lfloor v \rfloor; e\}, xs) \leftrightarrow (\llbracket, xs, 0, None \rrbracket)$

$| \text{bisim1BlockSome2}':$   
 $(\bigwedge xs. P, e, Suc\ n, h \vdash' (e, xs) \leftrightarrow (\llbracket, xs, 0, None \rrbracket))$

$$\implies P, \{V:T=[v]; e\}, n, h \vdash' (\{V:T=[v]; e\}, xs) \leftrightarrow ([v], xs, \text{Suc } 0, \text{None})$$

| *bisim1BlockSome4'*:

$$P, e, \text{Suc } n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp)$$

$$\implies P, \{V:T=[v]; e\}, n, h \vdash' (\{V:T=\text{None}; e'\}, xs) \leftrightarrow (stk, loc, \text{Suc } (\text{Suc } pc), xcp)$$

| *bisim1BlockThrowSome'*:

$$P, e, \text{Suc } n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$$

$$\implies P, \{V:T=[v]; e\}, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (stk, loc, \text{Suc } (\text{Suc } pc), \lfloor a \rfloor)$$

| *bisim1BlockNone'*:

$$P, e, \text{Suc } n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp)$$

$$\implies P, \{V:T=\text{None}; e\}, n, h \vdash' (\{V:T=\text{None}; e'\}, xs) \leftrightarrow (stk, loc, pc, xcp)$$

| *bisim1BlockThrowNone'*:

$$P, e, \text{Suc } n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$$

$$\implies P, \{V:T=\text{None}; e\}, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$$

| *bisim1Sync1'*:

$$\llbracket P, e1, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp);$$

$$\bigwedge xs. P, e2, \text{Suc } n, h \vdash' (e2, xs) \leftrightarrow (\llbracket, xs, 0, \text{None} \rrbracket)$$

$$\implies P, \text{sync}_V (e1) e2, n, h \vdash' (\text{sync}_V (e') e2, xs) \leftrightarrow (stk, loc, pc, xcp)$$

| *bisim1Sync2'*:

$$\llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow (\llbracket, xs, 0, \text{None} \rrbracket);$$

$$\bigwedge xs. P, e2, \text{Suc } n, h \vdash' (e2, xs) \leftrightarrow (\llbracket, xs, 0, \text{None} \rrbracket)$$

$$\implies P, \text{sync}_V (e1) e2, n, h \vdash' (\text{sync}_V (\text{Val } v) e2, xs) \leftrightarrow ([v, v], xs, \text{Suc } (\text{length } (\text{compE2 } e1)), \text{None})$$

| *bisim1Sync3'*:

$$\llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow (\llbracket, xs, 0, \text{None} \rrbracket);$$

$$\bigwedge xs. P, e2, \text{Suc } n, h \vdash' (e2, xs) \leftrightarrow (\llbracket, xs, 0, \text{None} \rrbracket)$$

$$\implies P, \text{sync}_V (e1) e2, n, h \vdash' (\text{sync}_V (\text{Val } v) e2, xs) \leftrightarrow ([v], xs[V := v], \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e1)))), \text{None})$$

| *bisim1Sync4'*:

$$\llbracket P, e2, \text{Suc } n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp);$$

$$\bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow (\llbracket, xs, 0, \text{None} \rrbracket)$$

$$\implies P, \text{sync}_V (e1) e2, n, h \vdash' (\text{insync}_V (a) e', xs) \leftrightarrow (stk, loc, \text{Suc } (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e1) + pc))), xcp)$$

| *bisim1Sync5'*:

$$\llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow (\llbracket, xs, 0, \text{None} \rrbracket);$$

$$\bigwedge xs. P, e2, \text{Suc } n, h \vdash' (e2, xs) \leftrightarrow (\llbracket, xs, 0, \text{None} \rrbracket)$$

$$\implies P, \text{sync}_V (e1) e2, n, h \vdash' (\text{insync}_V (a) \text{Val } v, xs) \leftrightarrow ([xs ! V, v], xs, 4 + \text{length } (\text{compE2 } e1) + \text{length } (\text{compE2 } e2), \text{None})$$

| *bisim1Sync6'*:

$$\llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow (\llbracket, xs, 0, \text{None} \rrbracket);$$

$$\bigwedge xs. P, e2, \text{Suc } n, h \vdash' (e2, xs) \leftrightarrow (\llbracket, xs, 0, \text{None} \rrbracket)$$

$$\implies P, \text{sync}_V (e1) e2, n, h \vdash' (\text{Val } v, xs) \leftrightarrow ([v], xs, 5 + \text{length } (\text{compE2 } e1) + \text{length } (\text{compE2 } e2), \text{None})$$

| *bisim1Sync7'*:

$\llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None);$   
 $\bigwedge xs. P, e2, Suc\ n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$   
 $\implies P, sync_V (e1)\ e2, n, h \vdash' (insync_V (a)\ Throw\ a', xs) \leftrightarrow ([Addr\ a'], xs, 6 + length\ (compE2\ e1) + length\ (compE2\ e2), None)$

$| bisim1Sync8':$   
 $\llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None);$   
 $\bigwedge xs. P, e2, Suc\ n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$   
 $\implies P, sync_V (e1)\ e2, n, h \vdash' (insync_V (a)\ Throw\ a', xs) \leftrightarrow$   
 $([xs!\ V, Addr\ a'], xs, 7 + length\ (compE2\ e1) + length\ (compE2\ e2), None)$

$| bisim1Sync9':$   
 $\llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None);$   
 $\bigwedge xs. P, e2, Suc\ n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$   
 $\implies P, sync_V (e1)\ e2, n, h \vdash' (Throw\ a, xs) \leftrightarrow ([Addr\ a], xs, 8 + length\ (compE2\ e1) + length\ (compE2\ e2), None)$

$| bisim1Sync10':$   
 $\llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None);$   
 $\bigwedge xs. P, e2, Suc\ n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$   
 $\implies P, sync_V (e1)\ e2, n, h \vdash' (Throw\ a, xs) \leftrightarrow ([Addr\ a], xs, 8 + length\ (compE2\ e1) + length\ (compE2\ e2), [a])$

$| bisim1Sync11':$   
 $\llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None);$   
 $\bigwedge xs. P, e2, Suc\ n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$   
 $\implies P, sync_V (e1)\ e2, n, h \vdash' (THROW\ NullPointer, xs) \leftrightarrow ([Null], xs, Suc\ (Suc\ (length\ (compE2\ e1))), [addr-of-sys-xcpt\ NullPointer])$

$| bisim1Sync12':$   
 $\llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None);$   
 $\bigwedge xs. P, e2, Suc\ n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$   
 $\implies P, sync_V (e1)\ e2, n, h \vdash' (Throw\ a, xs) \leftrightarrow ([v, v'], xs, 4 + length\ (compE2\ e1) + length\ (compE2\ e2), [a])$

$| bisim1Sync14':$   
 $\llbracket \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow ([], xs, 0, None);$   
 $\bigwedge xs. P, e2, Suc\ n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$   
 $\implies P, sync_V (e1)\ e2, n, h \vdash' (Throw\ a, xs) \leftrightarrow$   
 $([v, Addr\ a'], xs, 7 + length\ (compE2\ e1) + length\ (compE2\ e2), [a])$

$| bisim1SyncThrow':$   
 $\llbracket P, e1, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a]);$   
 $\bigwedge xs. P, e2, Suc\ n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$   
 $\implies P, sync_V (e1)\ e2, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a])$

$| bisim1InSync':$   
 $P, insync_V (a)\ e, n, h \vdash' (insync_V (a)\ e, xs) \leftrightarrow ([], xs, 0, None)$

$| bisim1Seq1':$   
 $\llbracket P, e1, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp);$   
 $\bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$

$$\implies P, e1;;e2, n, h \vdash' (e';;e2, xs) \leftrightarrow (stk, loc, pc, xcp)$$

| *bisim1SeqThrow1'*:

$$\begin{aligned} & \llbracket P, e1, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor); \\ & \quad \bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket \\ & \implies P, e1;;e2, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor) \end{aligned}$$

| *bisim1Seq2'*:

$$\begin{aligned} & \llbracket P, e2, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp); \\ & \quad \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket \\ & \implies P, e1;;e2, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, Suc\ (length\ (compE2\ e1) + pc), xcp) \end{aligned}$$

| *bisim1Cond1'*:

$$\begin{aligned} & \llbracket P, e, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp); \\ & \quad \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow (\llbracket, xs, 0, None); \\ & \quad \bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket \\ & \implies P, \text{if } (e)\ e1\ \text{else } e2, n, h \vdash' (\text{if } (e')\ e1\ \text{else } e2, xs) \leftrightarrow (stk, loc, pc, xcp) \end{aligned}$$

| *bisim1CondThen'*:

$$\begin{aligned} & \llbracket P, e1, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp); \\ & \quad \bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow (\llbracket, xs, 0, None); \\ & \quad \bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket \\ & \implies P, \text{if } (e)\ e1\ \text{else } e2, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, Suc\ (length\ (compE2\ e) + pc), xcp) \end{aligned}$$

| *bisim1CondElse'*:

$$\begin{aligned} & \llbracket P, e2, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp); \\ & \quad \bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow (\llbracket, xs, 0, None); \\ & \quad \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket \\ & \implies P, \text{if } (e)\ e1\ \text{else } e2, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, Suc\ (Suc\ (length\ (compE2\ e) + length\ (compE2\ e1) + pc)), xcp) \end{aligned}$$

| *bisim1CondThrow'*:

$$\begin{aligned} & \llbracket P, e, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor); \\ & \quad \bigwedge xs. P, e1, n, h \vdash' (e1, xs) \leftrightarrow (\llbracket, xs, 0, None); \\ & \quad \bigwedge xs. P, e2, n, h \vdash' (e2, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket \\ & \implies P, \text{if } (e)\ e1\ \text{else } e2, n, h \vdash' (Throw\ a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor) \end{aligned}$$

| *bisim1While1'*:

$$\begin{aligned} & \llbracket \bigwedge xs. P, c, n, h \vdash' (c, xs) \leftrightarrow (\llbracket, xs, 0, None); \\ & \quad \bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket \\ & \implies P, \text{while } (c)\ e, n, h \vdash' (\text{while } (c)\ e, xs) \leftrightarrow (\llbracket, xs, 0, None) \end{aligned}$$

| *bisim1While3'*:

$$\begin{aligned} & \llbracket P, c, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp); \\ & \quad \bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket \\ & \implies P, \text{while } (c)\ e, n, h \vdash' (\text{if } (e')\ (e;;\ \text{while } (c)\ e)\ \text{else } \text{unit}, xs) \leftrightarrow (stk, loc, pc, xcp) \end{aligned}$$

| *bisim1While4'*:

$$\begin{aligned} & \llbracket P, e, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp); \\ & \quad \bigwedge xs. P, c, n, h \vdash' (c, xs) \leftrightarrow (\llbracket, xs, 0, None) \rrbracket \\ & \implies P, \text{while } (c)\ e, n, h \vdash' (e';;\ \text{while } (c)\ e, xs) \leftrightarrow (stk, loc, Suc\ (length\ (compE2\ c) + pc), xcp) \end{aligned}$$

| *bisim1While6'*:  
 $\llbracket \bigwedge xs. P, c, n, h \vdash' (c, xs) \leftrightarrow ([], xs, 0, None);$   
 $\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \implies$   
 $P, \text{while } (c) \ e, n, h \vdash' (\text{while } (c) \ e, xs) \leftrightarrow ([], xs, \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } c) + \text{length } (\text{compE2 } e))), None)$

| *bisim1While7'*:  
 $\llbracket \bigwedge xs. P, c, n, h \vdash' (c, xs) \leftrightarrow ([], xs, 0, None);$   
 $\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None) \rrbracket \implies$   
 $P, \text{while } (c) \ e, n, h \vdash' (\text{unit}, xs) \leftrightarrow ([], xs, \text{Suc } (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } c) + \text{length } (\text{compE2 } e))))), None)$

| *bisim1WhileThrow1'*:  
 $\llbracket P, c, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor);$   
 $\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$   
 $\implies P, \text{while } (c) \ e, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$

| *bisim1WhileThrow2'*:  
 $\llbracket P, e, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor);$   
 $\bigwedge xs. P, c, n, h \vdash' (c, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$   
 $\implies P, \text{while } (c) \ e, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (stk, loc, \text{Suc } (\text{length } (\text{compE2 } c) + pc), \lfloor a \rfloor)$

| *bisim1Throw1'*:  
 $P, e, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp)$   
 $\implies P, \text{throw } e, n, h \vdash' (\text{throw } e', xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1Throw2'*:  
 $(\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None))$   
 $\implies P, \text{throw } e, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], xs, \text{length } (\text{compE2 } e), \lfloor a \rfloor)$

| *bisim1ThrowNull'*:  
 $(\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None))$   
 $\implies P, \text{throw } e, n, h \vdash' (\text{THROW } \text{NullPointer}, xs) \leftrightarrow ([\text{Null}], xs, \text{length } (\text{compE2 } e), \lfloor \text{addr-of-sys-xcpt NullPointer} \rfloor)$

| *bisim1ThrowThrow'*:  
 $P, e, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$   
 $\implies P, \text{throw } e, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$

| *bisim1Try'*:  
 $\llbracket P, e, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp);$   
 $\bigwedge xs. P, e2, \text{Suc } n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$   
 $\implies P, \text{try } e \text{ catch } (C \ V) \ e2, n, h \vdash' (\text{try } e' \text{ catch } (C \ V) \ e2, xs) \leftrightarrow (stk, loc, pc, xcp)$

| *bisim1TryCatch1'*:  
 $\llbracket P, e, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor); \text{typeof-addr } h \ a = \lfloor \text{Class-type } C' \rfloor; P \vdash C' \preceq^* C;$   
 $\bigwedge xs. P, e2, \text{Suc } n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \rrbracket$   
 $\implies P, \text{try } e \text{ catch } (C \ V) \ e2, n, h \vdash' (\{V:\text{Class } C=\text{None}; e2\}, xs[V := \text{Addr } a]) \leftrightarrow ([\text{Addr } a], loc, \text{Suc } (\text{length } (\text{compE2 } e)), None)$

| *bisim1TryCatch2'*:  
 $\llbracket P, e2, \text{Suc } n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp);$

$\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None) \parallel$   
 $\implies P, \text{try } e \text{ catch}(C \ V) \ e2, n, h \vdash' (\{V:\text{Class } C=\text{None}; e'\}, xs) \leftrightarrow (stk, loc, \text{Suc} (\text{Suc} (\text{length} (\text{compE2 } e) + pc)), xcp)$

| *bisim1TryFail'*:

$\parallel P, e, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]); \text{typeof-addr } h \ a = \lfloor \text{Class-type } C' \rfloor; \neg P \vdash C' \preceq^* C;$

$\bigwedge xs. P, e2, \text{Suc } n, h \vdash' (e2, xs) \leftrightarrow ([], xs, 0, None) \parallel$   
 $\implies P, \text{try } e \text{ catch}(C \ V) \ e2, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a])$

| *bisim1TryCatchThrow'*:

$\parallel P, e2, \text{Suc } n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]);$   
 $\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None) \parallel$   
 $\implies P, \text{try } e \text{ catch}(C \ V) \ e2, n, h \vdash' (\text{Throw } a, xs) \leftrightarrow (stk, loc, \text{Suc} (\text{Suc} (\text{length} (\text{compE2 } e) + pc)), [a])$

| *bisims1Nil'*:  $P, [], n, h \vdash' ([], xs) [\leftrightarrow] ([], xs, 0, None)$

| *bisims1List1'*:

$\parallel P, e, n, h \vdash' (e', xs) \leftrightarrow (stk, loc, pc, xcp);$   
 $\bigwedge xs. P, es, n, h \vdash' (es, xs) [\leftrightarrow] ([], xs, 0, None) \parallel$   
 $\implies P, e \# es, n, h \vdash' (e' \# es, xs) [\leftrightarrow] (stk, loc, pc, xcp)$

| *bisims1List2'*:

$\parallel P, es, n, h \vdash' (es', xs) [\leftrightarrow] (stk, loc, pc, xcp);$   
 $\bigwedge xs. P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None) \parallel$   
 $\implies P, e \# es, n, h \vdash' (\text{Val } v \# es', xs) [\leftrightarrow] (stk @ [v], loc, \text{length} (\text{compE2 } e) + pc, xcp)$

**lemma** *bisim1'-refl*:  $P, e, n, h \vdash' (e, xs) \leftrightarrow ([], xs, 0, None)$

**and** *bisims1'-refl*:  $P, es, n, h \vdash' (es, xs) [\leftrightarrow] ([], xs, 0, None)$

**apply**(*induct e and es arbitrary: n xs and n xs rule: call.induct calls.induct*)

**apply**(*auto intro: bisim1'-bisims1'.intros simp add: nat-fun-sum-eq-conv*)

**apply**(*rename-tac option a b c*)

**apply**(*case-tac option*)

**apply**(*auto intro: bisim1'-bisims1'.intros simp add: fun-eq-iff split: if-split-asm*)

**done**

**lemma** *bisim1-imp-bisim1'*:  $P, e, h \vdash \text{exs} \leftrightarrow s \implies P, e, n, h \vdash' \text{exs} \leftrightarrow s$

**and** *bisims1-imp-bisims1'*:  $P, es, h \vdash \text{esxs} [\leftrightarrow] s \implies P, es, n, h \vdash' \text{esxs} [\leftrightarrow] s$

**proof**(*induct arbitrary: n and n rule: bisim1-bisims1.inducts*)

**case** (*bisim1CallParams ps ps' xs stk loc pc xcp obj M v*)

**show** ?*case*

**proof**(*cases ps = []*)

**case** *True*

**with**  $\langle P, ps, h \vdash (ps', xs) [\leftrightarrow] (stk, loc, pc, xcp) \rangle$  **have**  $ps' = [] \ pc = 0 \ stk = [] \ loc = xs \ xcp = None$

**by**(*auto elim: bisims1.cases*)

**moreover** **have**  $P, obj, n, h \vdash' (\text{Val } v, xs) \leftrightarrow ([v], xs, \text{length} (\text{compE2 } obj), None)$

**by**(*blast intro: bisim1Val2' bisim1'-refl*)

**hence**  $P, obj \cdot M([], n, h \vdash' (\text{Val } v \cdot M([], xs) \leftrightarrow ([v], xs, \text{length} (\text{compE2 } obj), None)$

**by**—(*rule bisim1Call1', auto intro!: bisims1Nil' simp add: bsoks-def*)

**ultimately show** ?*thesis* **using** *True* **by** *simp*

**next**

**case** *False* **with** *bisim1CallParams* **show** ?*thesis*

**by**(*auto intro: bisim1CallParams' bisims1'-refl bisim1'-refl*)



```

qed
qed(auto intro: bisim1'-bisims1'.intros bisim1'-refl bisims1'-refl)

lemma bisim1'-imp-bisim1: P, e, n, h ⊢' exs ↔ s ⇒ P, e, h ⊢ exs ↔ s
  and bisims1'-imp-bisims1: P, es, n, h ⊢' esxs [↔] s ⇒ P, es, h ⊢ esxs [↔] s
apply(induct rule: bisim1'-bisims1'.inducts)
apply(blast intro: bisim1-bisims1.intros)+
done

lemma bisim1'-eq-bisim1: bisim1' P h e n = bisim1 P h e
  and bisims1'-eq-bisims1: bisims1' P h es n = bisims1 P h es
by(blast intro!: ext bisim1-imp-bisim1' bisims1-imp-bisims1' bisim1'-imp-bisim1 bisims1'-imp-bisims1)+

end

lemmas bisim1-bisims1-inducts =
  J1-JVM-heap-base.bisim1'-bisims1'.inducts
[simplified J1-JVM-heap-base.bisim1'-eq-bisim1 J1-JVM-heap-base.bisims1'-eq-bisims1,
consumes 1,
case-names bisim1Val2 bisim1New bisim1NewThrow
bisim1NewArray bisim1NewArrayThrow bisim1NewArrayFail bisim1Cast bisim1CastThrow bisim1Cast-
Fail
bisim1InstanceOf bisim1InstanceOfThrow
bisim1Val bisim1Var bisim1BinOp1 bisim1BinOp2 bisim1BinOpThrow1 bisim1BinOpThrow2 bisim1BinOpThrow
bisim1LAss1 bisim1LAss2 bisim1LAssThrow
bisim1AAcc1 bisim1AAcc2 bisim1AAccThrow1 bisim1AAccThrow2 bisim1AAccFail
bisim1AAss1 bisim1AAss2 bisim1AAss3 bisim1AAssThrow1 bisim1AAssThrow2
bisim1AAssThrow3 bisim1AAssFail bisim1AAss4
bisim1ALength bisim1ALengthThrow bisim1ALengthNull
bisim1FAcc bisim1FAccThrow bisim1FAccNull
bisim1FAss1 bisim1FAss2 bisim1FAssThrow1 bisim1FAssThrow2 bisim1FAssNull bisim1FAss3
bisim1CAS1 bisim1CAS2 bisim1CAS3 bisim1CASThrow1 bisim1CASThrow2
bisim1CASThrow3 bisim1CASFail
bisim1Call1 bisim1CallParams bisim1CallThrowObj bisim1CallThrowParams
bisim1CallThrow
bisim1BlockSome1 bisim1BlockSome2 bisim1BlockSome4 bisim1BlockThrowSome
bisim1BlockNone bisim1BlockThrowNone
bisim1Sync1 bisim1Sync2 bisim1Sync3 bisim1Sync4 bisim1Sync5 bisim1Sync6
bisim1Sync7 bisim1Sync8 bisim1Sync9 bisim1Sync10 bisim1Sync11 bisim1Sync12
bisim1Sync14 bisim1SyncThrow bisim1InSync
bisim1Seq1 bisim1SeqThrow1 bisim1Seq2
bisim1Cond1 bisim1CondThen bisim1CondElse bisim1CondThrow
bisim1While1 bisim1While3 bisim1While4
bisim1While6 bisim1While7 bisim1WhileThrow1 bisim1WhileThrow2
bisim1Throw1 bisim1Throw2 bisim1ThrowNull bisim1ThrowThrow
bisim1Try bisim1TryCatch1 bisim1TryCatch2 bisim1TryFail bisim1TryCatchThrow
bisims1Nil bisims1List1 bisims1List2]

lemmas bisim1-bisims1-inducts-split = bisim1-bisims1-inducts[split-format (complete)]

context J1-JVM-heap-base begin

```

**lemma** *bisim1-pc-length-compE2*:  $P, E, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp) \implies pc \leq \text{length } (\text{compE2 } E)$   
**and** *bisims1-pc-length-compEs2*:  $P, Es, h \vdash (es, xs) [\leftrightarrow] (stk, loc, pc, xcp) \implies pc \leq \text{length } (\text{compEs2 } Es)$

**apply**(*induct* (*stk*, *loc*, *pc*, *xcp*) **and** (*stk*, *loc*, *pc*, *xcp*)  
*arbitrary*: *stk loc pc xcp* **and** *stk loc pc xcp* *rule*: *bisim1-bisims1.inducts*)  
**apply**(*auto*)  
**done**

**lemma** *bisim1-pc-length-compE2D*:

$P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, \text{length } (\text{compE2 } e), xcp)$   
 $\implies xcp = \text{None} \wedge \text{call1 } e' = \text{None} \wedge (\exists v. stk = [v] \wedge (\text{is-val } e' \longrightarrow e' = \text{Val } v \wedge xs = loc))$

**and** *bisims1-pc-length-compEs2D*:

$P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, \text{length } (\text{compEs2 } es), xcp)$   
 $\implies xcp = \text{None} \wedge \text{calls1 } es' = \text{None} \wedge (\exists vs. stk = \text{rev } vs \wedge \text{length } vs = \text{length } es \wedge (\text{is-vals } es' \longrightarrow es' = \text{map Val } vs \wedge xs = loc))$

**proof**(*induct* (*e'*, *xs*) (*stk*, *loc*, *length* (*compE2 e*), *xcp*)  
**and** (*es'*, *xs*) (*stk*, *loc*, *length* (*compEs2 es*), *xcp*)  
*arbitrary*: *e' xs stk loc xcp* **and** *es' xs stk loc xcp* *rule*: *bisim1-bisims1.inducts*)  
**case** (*bisims1List2 es es' xs stk loc pc xcp e v*)  
**then obtain** *vs* **where** *xcp = None calls1 es' = None*  
 $stk = \text{rev } vs \wedge \text{length } vs = \text{length } es \wedge \text{is-vals } es' \longrightarrow es' = \text{map Val } vs \wedge xs = loc$  **by** *auto*  
**thus** ?*case*  
**by**(*clarsimp*)(*rule-tac*  $x=v\#vs$  **in** *exI*, *auto*)  
**qed**(*simp-all* (*no-asm-use*), (*fastforce* *dest*: *bisim1-pc-length-compE2 bisims1-pc-length-compEs2 split*:  
*bop.split-asm if-split-asm*)+)

**corollary** *bisim1-call-pcD*:  $\llbracket P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp); \text{call1 } e' = \lfloor aMvs \rfloor \rrbracket \implies pc < \text{length } (\text{compE2 } e)$

**and** *bisims1-calls-pcD*:  $\llbracket P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, xcp); \text{calls1 } es' = \lfloor aMvs \rfloor \rrbracket \implies pc < \text{length } (\text{compEs2 } es)$

**proof** –

**assume** *bisim*:  $P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp)$   
**and** *call*:  $\text{call1 } e' = \lfloor aMvs \rfloor$

{ **assume**  $pc = \text{length } (\text{compE2 } e)$   
**with** *bisim call* **have** *False*  
**by**(*auto* *dest*: *bisim1-pc-length-compE2D*) }  
**moreover from** *bisim* **have**  $pc \leq \text{length } (\text{compE2 } e)$   
**by**(*rule* *bisim1-pc-length-compE2*)  
**ultimately show**  $pc < \text{length } (\text{compE2 } e)$   
**by**(*cases*  $pc < \text{length } (\text{compE2 } e)$ )(*auto*)

**next**

**assume** *bisim*:  $P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, xcp)$   
**and** *call*:  $\text{calls1 } es' = \lfloor aMvs \rfloor$   
{ **assume**  $pc = \text{length } (\text{compEs2 } es)$   
**with** *bisim call* **have** *False*  
**by**(*auto* *dest*: *bisims1-pc-length-compEs2D*) }  
**moreover from** *bisim* **have**  $pc \leq \text{length } (\text{compEs2 } es)$   
**by**(*rule* *bisims1-pc-length-compEs2*)  
**ultimately show**  $pc < \text{length } (\text{compEs2 } es)$   
**by**(*cases*  $pc < \text{length } (\text{compEs2 } es)$ )(*auto*)

**qed**

**lemma** *bisim1-length-xs*:  $P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \implies \text{length } xs = \text{length } loc$   
**and** *bisims1-length-xs*:  $P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, xcp) \implies \text{length } xs = \text{length } loc$   
**by**(*induct* ( $e', xs$ ) ( $stk, loc, pc, xcp$ ) **and** ( $es', xs$ ) ( $stk, loc, pc, xcp$ ))  
*arbitrary*:  $e' \text{ } xs \text{ } stk \text{ } loc \text{ } pc \text{ } xcp$  **and**  $es' \text{ } xs \text{ } stk \text{ } loc \text{ } pc \text{ } xcp$  *rule*: *bisim1-bisims1.inducts*)  
*auto*

**lemma** *bisim1-Val-length-compE2D*:

$P, e, h \vdash (Val \ v, xs) \leftrightarrow (stk, loc, \text{length } (compE2 \ e), xcp) \implies stk = [v] \wedge xs = loc \wedge xcp = None$

**and** *bisims1-Val-length-compEs2D*:

$P, es, h \vdash (\text{map } Val \ vs, xs) [\leftrightarrow] (stk, loc, \text{length } (compEs2 \ es), xcp) \implies stk = rev \ vs \wedge xs = loc \wedge xcp = None$

**by**(*auto dest*: *bisim1-pc-length-compE2D bisims1-pc-length-compEs2D*)

**lemma** *bisim-Val-loc-eq-xcp-None*:

$P, e, h \vdash (Val \ v, xs) \leftrightarrow (stk, loc, pc, xcp) \implies xs = loc \wedge xcp = None$

**and** *bisims-Val-loc-eq-xcp-None*:

$P, es, h \vdash (\text{map } Val \ vs, xs) [\leftrightarrow] (stk, loc, pc, xcp) \implies xs = loc \wedge xcp = None$

**apply**(*induct* ( $Val \ v :: 'addr \ expr1, xs$ ) ( $stk, loc, pc, xcp$ ))

**and** ( $\text{map } Val \ vs :: 'addr \ expr1 \ list, xs$ ) ( $stk, loc, pc, xcp$ )

*arbitrary*:  $v \text{ } xs \text{ } stk \text{ } loc \text{ } pc \text{ } xcp$  **and**  $vs \text{ } xs \text{ } stk \text{ } loc \text{ } pc \text{ } xcp$  *rule*: *bisim1-bisims1.inducts*)

**apply**(*auto*)

**done**

**lemma** *bisim-Val-pc-not-Invoke*:

$\llbracket P, e, h \vdash (Val \ v, xs) \leftrightarrow (stk, loc, pc, xcp); pc < \text{length } (compE2 \ e) \rrbracket \implies compE2 \ e ! pc \neq Invoke \ M \ n'$

**and** *bisims-Val-pc-not-Invoke*:

$\llbracket P, es, h \vdash (\text{map } Val \ vs, xs) [\leftrightarrow] (stk, loc, pc, xcp); pc < \text{length } (compEs2 \ es) \rrbracket \implies compEs2 \ es ! pc \neq Invoke \ M \ n'$

**apply**(*induct* ( $Val \ v :: 'addr \ expr1, xs$ ) ( $stk, loc, pc, xcp$ ))

**and** ( $\text{map } Val \ vs :: 'addr \ expr1 \ list, xs$ ) ( $stk, loc, pc, xcp$ )

*arbitrary*:  $v \text{ } xs \text{ } stk \text{ } loc \text{ } pc \text{ } xcp$  **and**  $vs \text{ } xs \text{ } stk \text{ } loc \text{ } pc \text{ } xcp$  *rule*: *bisim1-bisims1.inducts*)

**apply**(*auto simp add*: *nth-append compEs2-map-Val dest*: *bisim1-pc-length-compE2*)

**done**

**lemma** *bisim1-VarD*:  $P, E, h \vdash (Var \ V, xs) \leftrightarrow (stk, loc, pc, xcp) \implies xs = loc$

**and**  $P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, xcp) \implies True$

**by**(*induct* ( $Var \ V :: 'addr \ expr1, xs$ ) ( $stk, loc, pc, xcp$ ) **and** *arbitrary*:  $V \text{ } xs \text{ } stk \text{ } loc \text{ } pc \text{ } xcp$  **and** *rule*: *bisim1-bisims1.inducts*) *auto*

**lemma** *bisim1-ThrowD*:

$P, e, h \vdash (Throw \ a, xs) \leftrightarrow (stk, loc, pc, xcp)$

$\implies pc < \text{length } (compE2 \ e) \wedge (xcp = [a] \vee xcp = None) \wedge xs = loc$

**and** *bisims1-ThrowD*:

$P, es, h \vdash (\text{map } Val \ vs @ Throw \ a \# es', xs) [\leftrightarrow] (stk, loc, pc, xcp)$

$\implies pc < \text{length } (compEs2 \ es) \wedge (xcp = [a] \vee xcp = None) \wedge xs = loc$

**apply**(*induct* ( $Throw \ a :: 'addr \ expr1, xs$ ) ( $stk, loc, pc, xcp$ ))

**and** ( $\text{map } Val \ vs @ Throw \ a \# es', xs$ ) ( $stk, loc, pc, xcp$ )

*arbitrary*:  $xs \text{ } stk \text{ } loc \text{ } pc \text{ } xcp$  **and**  $vs \text{ } es' \text{ } xs \text{ } stk \text{ } loc \text{ } pc \text{ } xcp$  *rule*: *bisim1-bisims1.inducts*)

**apply**(*fastforce dest*: *bisim1-pc-length-compE2 bisim-Val-loc-eq-xcp-None simp add*: *Cons-eq-append-conv*) +

**done**

**lemma fixes**  $P :: 'addr\ J1\text{-}prog$

**shows**  $bisim1\text{-}Invoke\text{-}stkD$ :

$\llbracket P, e, h \vdash exs \leftrightarrow (stk, loc, pc, None); pc < length\ (compE2\ e); compE2\ e ! pc = Invoke\ M\ n' \rrbracket$   
 $\implies \exists vs\ v\ stk'.\ stk = vs @ v \# stk' \wedge length\ vs = n'$

**and**  $bisims1\text{-}Invoke\text{-}stkD$ :

$\llbracket P, es, h \vdash esxs [\leftrightarrow] (stk, loc, pc, None); pc < length\ (compEs2\ es); compEs2\ es ! pc = Invoke\ M\ n' \rrbracket$   
 $\implies \exists vs\ v\ stk'.\ stk = vs @ v \# stk' \wedge length\ vs = n'$

**proof**(*induct* ( $stk, loc, pc, None :: 'addr\ option$ ) **and** ( $stk, loc, pc, None :: 'addr\ option$ ))

*arbitrary: stk loc pc and stk loc pc rule: bisim1-bisims1.inducts*)

**case**  $bisim1Call1$

**thus**  $?case$

**apply**(*clarsimp simp add: nth-append append-eq-append-conv2 neq-Nil-conv split: if-split-asm*)

**apply**(*drule bisim1-pc-length-compE2, clarsimp simp add: neq-Nil-conv nth-append*)

**apply**(*frule bisim1-pc-length-compE2, clarsimp*)

**apply**(*drule bisim1-pc-length-compE2D, fastforce*)

**done**

**next**

**case**  $bisim1CallParams$  **thus**  $?case$

**apply**(*clarsimp simp add: nth-append append-eq-Cons-conv split: if-split-asm*)

**apply**(*fastforce simp add: append-eq-append-conv2 Cons-eq-append-conv*)

**apply**(*frule bisims1-pc-length-compEs2, clarsimp*)

**apply**(*drule bisims1-pc-length-compEs2D, fastforce simp add: append-eq-append-conv2*)

**done**

**qed**(*fastforce simp add: nth-append append-eq-append-conv2 neq-Nil-conv split: if-split-asm bop.split-asm*  
*dest: bisim1-pc-length-compE2 bisims1-pc-length-compEs2*) $+$

**lemma fixes**  $P :: 'addr\ J1\text{-}prog$

**shows**  $bisim1\text{-}call\text{-}xcpNone$ :  $P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, [a]) \implies call1\ e' = None$

**and**  $bisims1\text{-}calls\text{-}xcpNone$ :  $P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, [a]) \implies calls1\ es' = None$

**apply**(*induct* ( $e', xs$ ) ( $stk, loc, pc, [a :: 'addr]$ ) **and** ( $es', xs$ ) ( $stk, loc, pc, [a :: 'addr]$ ))

*arbitrary: e' xs stk loc pc and es' xs stk loc pc rule: bisim1-bisims1.inducts*)

**apply**(*auto dest: bisim-Val-loc-eq-xcp-None bisims-Val-loc-eq-xcp-None simp add: is-vals-conv*)

**done**

**lemma**  $bisims1\text{-}map\text{-}Val\text{-}append$ :

**assumes**  $bisim$ :  $P, es', h \vdash (es'', xs) [\leftrightarrow] (stk, loc, pc, xcp)$

**shows**  $length\ es = length\ vs$

$\implies P, es @ es', h \vdash (map\ Val\ vs @ es'', xs) [\leftrightarrow] (stk @ rev\ vs, loc, length\ (compEs2\ es) +$

$pc, xcp)$

**proof**(*induction vs arbitrary: es*)

**case**  $Nil$  **thus**  $?case$  **using**  $bisim$  **by**  $simp$

**next**

**case** ( $Cons\ v\ vs$ )

**from**  $\langle length\ es = length\ (v \# vs) \rangle$  **obtain**  $e\ es'''$  **where**  $[simp]$ :  $es = e \# es'''$  **by** (*cases es, auto*)

**with**  $\langle length\ es = length\ (v \# vs) \rangle$  **have**  $len$ :  $length\ es''' = length\ vs$  **by**  $simp$

**from**  $Cons.IH[OF\ len]$

**show**  $?case$  **by** (*simp add: add.assoc append-assoc[symmetric] del: append-assoc*)(*rule bisims1List2,*

*auto*)

**qed**

**lemma**  $bisim1\text{-}hext\text{-}mono$ :  $\llbracket P, e, h \vdash exs \leftrightarrow s; hext\ h\ h' \rrbracket \implies P, e, h' \vdash exs \leftrightarrow s$  (**is**  $PROP\ ?thesis1$ )

**and**  $bisims1\text{-}hext\text{-}mono$ :  $\llbracket P, es, h \vdash esxs [\leftrightarrow] s; hext\ h\ h' \rrbracket \implies P, es, h' \vdash esxs [\leftrightarrow] s$  (**is**  $PROP$

```

?thesis2)
proof -
  assume hext: hext h h'
  have  $P, e, h \vdash \text{exs} \leftrightarrow s \implies P, e, h' \vdash \text{exs} \leftrightarrow s$ 
    and  $P, es, h \vdash \text{esxs} [\leftrightarrow] s \implies P, es, h' \vdash \text{esxs} [\leftrightarrow] s$ 
  apply(induct rule: bisim1-bisims1.inducts)
  apply(insert hext)
  apply(auto intro: bisim1-bisims1.intros dest: hext-objD)
  done
  thus PROP ?thesis1 and PROP ?thesis2 by auto
qed

declare match-ex-table-append-not-pcs [simp]
  match-ex-table-eq-NoneI [simp]
  outside-pcs-compxE2-not-matches-entry [simp]
  outside-pcs-compxEs2-not-matches-entry [simp]

lemma bisim1-xcp-Some-not-caught:
   $P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$ 
 $\implies \text{match-ex-table } (\text{compP } f P) (\text{cname-of } h a) (pc' + pc) (\text{compxE2 } e pc' d) = \text{None}$ 

  and bisims1-xcp-Some-not-caught:
   $P, es, h \vdash (\text{map Val vs } @ \text{ Throw } a \# es', xs) [\leftrightarrow] (stk, loc, pc, \lfloor a \rfloor)$ 
 $\implies \text{match-ex-table } (\text{compP } f P) (\text{cname-of } h a) (pc' + pc) (\text{compxEs2 } es pc' d) = \text{None}$ 
proof(induct (Throw a :: 'addr expr1, xs) (stk, loc, pc,  $\lfloor a \rfloor$  :: 'addr))
  and (map Val vs @ Throw a # es' :: 'addr expr1 list, xs) (stk, loc, pc,  $\lfloor a \rfloor$  :: 'addr))
  arbitrary: xs stk loc pc pc' d and xs stk loc pc vs es' pc' d rule: bisim1-bisims1.inducts)
  case bisim1Sync10
  thus ?case by(simp add: matches-ex-entry-def)
next
  case bisim1Sync11
  thus ?case by(simp add: matches-ex-entry-def)
next
  case (bisim1SyncThrow e1 xs stk loc pc e2)
  note IH =  $\langle \text{match-ex-table } (\text{compP } f P) (\text{cname-of } h a) (pc' + pc) (\text{compxE2 } e1 pc' d) = \text{None} \rangle$ 
  from  $\langle P, e1, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor) \rangle$  have  $pc < \text{length } (\text{compE2 } e1)$  by(auto dest:
  bisim1-ThrowD)
  with IH show ?case
  by(auto simp add: match-ex-table-append matches-ex-entry-def dest: match-ex-table-pc-length-compE2
  intro: match-ex-table-not-pcs-None)
next
  case bisims1List1 thus ?case
  by(auto simp add: Cons-eq-append-conv dest: bisim1-ThrowD bisim-Val-loc-eq-xcp-None)
next
  case (bisims1List2 es es'' xs stk loc pc e v)
  hence  $\bigwedge pc'. \text{match-ex-table } (\text{compP } f P) (\text{cname-of } h a) (pc' + pc) (\text{compxEs2 } es pc' d) = \text{None}$ 
  by(auto simp add: Cons-eq-append-conv)
  from this[of  $pc' + \text{length } (\text{compE2 } e)$  Suc d] show ?case by(auto simp add: add.assoc)
next
  case (bisim1BlockThrowSome e xs stk loc pc T v)
  hence  $\bigwedge pc'. \text{match-ex-table } (\text{compP } f P) (\text{cname-of } h a) (pc' + pc) (\text{compxE2 } e pc' d) = \text{None}$  by
  auto
  from this[of  $2 + pc$ ] show ?case by(auto)
next

```

```

    case (bisim1Seq2 e2 stk loc pc e1 xs)
    hence  $\bigwedge pc'. \text{match-ex-table } (compP f P) \text{ (cname-of } h \text{ a) } (pc' + pc) (compxE2 e2 pc' d) = \text{None}$  by
    auto
    from this[of Suc (pc' + length (compE2 e1))] show ?case by (simp add: add.assoc)
next
    case (bisim1CondThen e1 stk loc pc e e2 xs)
    hence  $\bigwedge pc'. \text{match-ex-table } (compP f P) \text{ (cname-of } h \text{ a) } (pc' + pc) (compxE2 e1 pc' d) = \text{None}$  by
    auto
    note this[of Suc (pc' + length (compE2 e))]
    moreover from  $\langle P, e1, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor) \rangle$ 
    have  $pc < \text{length } (compE2 e1)$  by (auto dest: bisim1-ThrowD)
    ultimately show ?case by (simp add: add.assoc match-ex-table-eq-NoneI outside-pcs-compxE2-not-matches-entry)
next
    case (bisim1CondElse e2 stk loc pc e e1 xs)
    hence  $\bigwedge pc'. \text{match-ex-table } (compP f P) \text{ (cname-of } h \text{ a) } (pc' + pc) (compxE2 e2 pc' d) = \text{None}$  by
    auto
    note this[of Suc (Suc (pc' + (length (compE2 e) + length (compE2 e1)))]
    thus ?case by (simp add: add.assoc)
next
    case (bisim1WhileThrow2 e xs stk loc pc c)
    hence  $\bigwedge pc'. \text{match-ex-table } (compP f P) \text{ (cname-of } h \text{ a) } (pc' + pc) (compxE2 e pc' d) = \text{None}$  by
    auto
    from this[of Suc (pc' + (length (compE2 c)))]
    show ?case by (simp add: add.assoc)
next
    case (bisim1Throw1 e xs stk loc pc)
    thus ?case by (auto dest: bisim-Val-loc-eq-xcp-None)
next
    case (bisim1TryFail e xs stk loc pc C' C e2)
    hence  $\text{match-ex-table } (compP f P) \text{ (cname-of } h \text{ a) } (pc' + pc) (compxE2 e pc' d) = \text{None}$  by auto
    moreover from  $\langle P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor) \rangle$  have  $pc < \text{length } (compE2 e)$ 
    by (auto dest: bisim1-ThrowD)
    ultimately show ?case using  $\langle \text{typeof-addr } h \text{ a} = \lfloor \text{Class-type } C' \rfloor \rangle \langle \neg P \vdash C' \preceq^* C \rangle$ 
    by (simp add: matches-ex-entry-def cname-of-def)
next
    case (bisim1TryCatchThrow e2 xs stk loc pc e C)
    hence  $\bigwedge pc'. \text{match-ex-table } (compP f P) \text{ (cname-of } h \text{ a) } (pc' + pc) (compxE2 e2 pc' d) = \text{None}$  by
    auto
    from this[of Suc (Suc (pc' + (length (compE2 e)))]
    show ?case by (simp add: add.assoc matches-ex-entry-def)
next
    case bisim1Sync12 thus ?case
    by (auto dest: bisim1-ThrowD simp add: match-ex-table-append eval-nat-numeral, simp add: matches-ex-entry-def)
next
    case bisim1Sync14 thus ?case
    by (auto dest: bisim1-ThrowD simp add: match-ex-table-append eval-nat-numeral, simp add: matches-ex-entry-def)
qed(fastforce dest: bisim1-ThrowD simp add: add.assoc[symmetric])+

declare match-ex-table-append-not-pcs [simp del]
        match-ex-table-eq-NoneI [simp del]
        outside-pcs-compxE2-not-matches-entry [simp del]
        outside-pcs-compxEs2-not-matches-entry [simp del]

lemma bisim1-xcp-pcD:  $P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor) \implies pc < \text{length } (compE2 e)$ 

```

**and** *bisims1-xcp-pcD*:  $P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, [a]) \implies pc < \text{length} (\text{compEs2 } es)$   
**by** (*induct* ( $e', xs$ ) ( $stk, loc, pc, [a :: 'addr]$ )) **and** ( $es', xs$ ) ( $stk, loc, pc, [a :: 'addr]$ )  
*arbitrary*:  $e' \text{ xs } stk \text{ loc } pc$  **and**  $es' \text{ xs } stk \text{ loc } pc$  *rule*: *bisim1-bisims1.inducts*)  
*auto*

**declare** *nth-append* [*simp*]

**lemma** *bisim1-Val- $\tau$ Exec-move*:  
 $\llbracket P, E, h \vdash (\text{Val } v, xs) \leftrightarrow (stk, loc, pc, xcp); pc < \text{length} (\text{compE2 } E) \rrbracket$   
 $\implies xs = loc \wedge xcp = \text{None} \wedge$   
 $\tau\text{Exec-mover-a } P \text{ t } E \text{ h } (stk, xs, pc, \text{None}) ([v], xs, \text{length} (\text{compE2 } E), \text{None})$

**and** *bisims1-Val- $\tau$ Exec-moves*:  
 $\llbracket P, Es, h \vdash (\text{map Val } vs, xs) [\leftrightarrow] (stk, loc, pc, xcp); pc < \text{length} (\text{compEs2 } Es) \rrbracket$   
 $\implies xs = loc \wedge xcp = \text{None} \wedge$   
 $\tau\text{Exec-movesr-a } P \text{ t } Es \text{ h } (stk, xs, pc, \text{None}) (\text{rev } vs, xs, \text{length} (\text{compEs2 } Es), \text{None})$

**proof** (*induct* ( $\text{Val } v :: 'addr \text{ expr1}$ ,  $xs$ ) ( $stk, loc, pc, xcp$ )  
**and** ( $\text{map Val } vs :: 'addr \text{ expr1 list}$ ,  $xs$ ) ( $stk, loc, pc, xcp$ )  
*arbitrary*:  $v \text{ xs } stk \text{ loc } pc \text{ xcp}$  **and**  $vs \text{ xs } stk \text{ loc } pc \text{ xcp}$  *rule*: *bisim1-bisims1.inducts*)  
**case** *bisim1Val* **thus** ?*case* **by** (*auto intro!*:  $\tau\text{Execr1step exec-instr } \tau\text{move2Val simp add: exec-move-def}$ )  
**next**  
**case** (*bisim1LAss2*  $V \text{ e } xs$ )  
**have**  $\tau\text{Exec-mover-a } P \text{ t } (V := e) \text{ h } ([], xs, \text{Suc} (\text{length} (\text{compE2 } e)), \text{None}) ([Unit], xs, \text{Suc} (\text{Suc} (\text{length} (\text{compE2 } e))), \text{None})$   
**by** (*auto intro!*:  $\tau\text{Execr1step exec-instr } \tau\text{move2LAssRed2 simp add: exec-move-def}$ )  
**with** *bisim1LAss2* **show** ?*case* **by** *simp*  
**next**  
**case** (*bisim1AAss4*  $a \text{ i } e \text{ xs}$ )  
**have**  $\tau\text{Exec-mover-a } P \text{ t } (a[i] := e) \text{ h } ([], xs, \text{Suc} (\text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i) + \text{length} (\text{compE2 } e)), \text{None}) ([Unit], xs, \text{Suc} (\text{Suc} (\text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i) + \text{length} (\text{compE2 } e))), \text{None})$   
**by** (*auto intro!*:  $\tau\text{Execr1step exec-instr } \tau\text{move2AAssRed simp add: exec-move-def}$ )  
**with** *bisim1AAss4* **show** ?*case* **by** (*simp add: ac-simps*)  
**next**  
**case** (*bisim1FAss3*  $e \text{ F } D \text{ e2 } xs$ )  
**have**  $\tau\text{Exec-mover-a } P \text{ t } (e \cdot F\{D\} := e2) \text{ h } ([], xs, \text{Suc} (\text{length} (\text{compE2 } e) + \text{length} (\text{compE2 } e2)), \text{None}) ([Unit], xs, \text{Suc} (\text{Suc} (\text{length} (\text{compE2 } e) + \text{length} (\text{compE2 } e2))), \text{None})$   
**by** (*auto intro!*:  $\tau\text{Execr1step exec-instr } \tau\text{move2FAssRed simp add: exec-move-def}$ )  
**with** *bisim1FAss3* **show** ?*case* **by** *simp*  
**next**  
**case** (*bisim1Sync6*  $V \text{ e1 } e2 \text{ v } xs$ )  
**have**  $\tau\text{Exec-mover-a } P \text{ t } (\text{sync}_V (e1) \text{ e2}) \text{ h } ([v], xs, 5 + \text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2), \text{None})$   
 $([v], xs, 9 + \text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2), \text{None})$   
**by** (*rule*  $\tau\text{Execr1step}$ ) (*auto intro: exec-instr } \tau\text{move2Sync6 simp add: exec-move-def})  
**with** *bisim1Sync6* **show** ?*case* **by** (*auto simp add: eval-nat-numeral*)  
**next**  
**case** (*bisim1Seq2*  $e2 \text{ stk } loc \text{ pc } xcp \text{ e1 } v \text{ xs}$ )  
**from**  $\langle \text{Suc} (\text{length} (\text{compE2 } e1) + pc) < \text{length} (\text{compE2 } (e1;; e2)) \rangle$  **have**  $pc: pc < \text{length} (\text{compE2 } e2)$  **by** *simp*  
**with**  $\langle pc < \text{length} (\text{compE2 } e2) \implies xs = loc \wedge xcp = \text{None} \wedge \tau\text{Exec-mover-a } P \text{ t } e2 \text{ h } (stk, xs, pc, \text{None}) ([v], xs, \text{length} (\text{compE2 } e2), \text{None}) \rangle$   
**have**  $xs = loc \text{ xcp} = \text{None}$   
 $\tau\text{Exec-mover-a } P \text{ t } e2 \text{ h } (stk, xs, pc, \text{None}) ([v], xs, \text{length} (\text{compE2 } e2), \text{None})$  **by** *auto**

**moreover**  
**hence**  $\tau\text{Exec-mover-a } P \ t \ (e1;;e2) \ h \ (stk, xs, \text{Suc} \ (\text{length} \ (\text{compE2} \ e1) + pc), \text{None}) \ ([v], xs, \text{Suc} \ (\text{length} \ (\text{compE2} \ e1) + \text{length} \ (\text{compE2} \ e2)), \text{None})$   
**by**  $-(\text{rule } \text{Seq-}\tau\text{ExecrI2})$   
**ultimately show**  $?case$  **by**  $(\text{simp})$   
**next**  
**case**  $(\text{bisim1CondThen } e1 \ stk \ loc \ pc \ xcp \ e \ e2 \ v \ xs)$   
**from**  $\langle P, e1, h \vdash (\text{Val } v, xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$   
**have**  $pc \leq \text{length} \ (\text{compE2} \ e1)$  **by**  $(\text{rule } \text{bisim1-pc-length-compE2})$   
  
**have**  $e: \tau\text{Exec-mover-a } P \ t \ (\text{if } (e) \ e1 \ \text{else } e2) \ h$   
 $([v], xs, \text{Suc} \ (\text{length} \ (\text{compE2} \ e) + (\text{length} \ (\text{compE2} \ e1))), \text{None})$   
 $([v], xs, \text{length} \ (\text{compE2} \ (\text{if } (e) \ e1 \ \text{else } e2)), \text{None})$   
**by**  $(\text{rule } \tau\text{Execr1step})(\text{auto } \text{simp } \text{add: } \text{exec-move-def } \text{intro!} : \text{exec-instr } \tau\text{move2CondThenExit})$   
**show**  $?case$   
**proof**  $(\text{cases } pc < \text{length} \ (\text{compE2} \ e1))$   
**case**  $\text{True}$   
**with**  $\langle pc < \text{length} \ (\text{compE2} \ e1) \rangle$   
 $\implies xs = loc \wedge xcp = \text{None} \wedge \tau\text{Exec-mover-a } P \ t \ e1 \ h \ (stk, xs, pc, \text{None}) \ ([v], xs, \text{length} \ (\text{compE2} \ e1), \text{None})$   
**have**  $s: xs = loc \ xcp = \text{None}$   
**and**  $\tau\text{Exec-mover-a } P \ t \ e1 \ h \ (stk, xs, pc, \text{None}) \ ([v], xs, \text{length} \ (\text{compE2} \ e1), \text{None})$  **by**  $\text{auto}$   
**hence**  $\tau\text{Exec-mover-a } P \ t \ (\text{if } (e) \ e1 \ \text{else } e2) \ h$   
 $(stk, xs, \text{Suc} \ (\text{length} \ (\text{compE2} \ e) + pc), \text{None})$   
 $([v], xs, \text{Suc} \ (\text{length} \ (\text{compE2} \ e) + \text{length} \ (\text{compE2} \ e1)), \text{None})$   
**by**  $-(\text{rule } \text{Cond-}\tau\text{ExecrI2})$   
**with**  $e \ \text{True} \ s$  **show**  $?thesis$  **by**  $(\text{simp})$   
**next**  
**case**  $\text{False}$   
**with**  $\langle pc \leq \text{length} \ (\text{compE2} \ e1) \rangle$  **have**  $pc: pc = \text{length} \ (\text{compE2} \ e1)$  **by**  $\text{auto}$   
**with**  $\langle P, e1, h \vdash (\text{Val } v, xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$   
**have**  $stk = [v] \ xs = loc \ xcp = \text{None}$  **by**  $(\text{auto } \text{dest: } \text{bisim1-Val-length-compE2D})$   
**with**  $pc \ e$  **show**  $?thesis$  **by**  $(\text{simp})$   
**qed**  
**next**  
**case**  $(\text{bisim1CondElse } e2 \ stk \ loc \ pc \ xcp \ e \ e1 \ v \ xs)$   
**from**  $\langle P, e2, h \vdash (\text{Val } v, xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$   
**have**  $pc \leq \text{length} \ (\text{compE2} \ e2)$  **by**  $(\text{rule } \text{bisim1-pc-length-compE2})$   
  
**show**  $?case$   
**proof**  $(\text{cases } pc < \text{length} \ (\text{compE2} \ e2))$   
**case**  $\text{True}$   
**with**  $\langle pc < \text{length} \ (\text{compE2} \ e2) \rangle$   
 $\implies xs = loc \wedge xcp = \text{None} \wedge \tau\text{Exec-mover-a } P \ t \ e2 \ h \ (stk, xs, pc, \text{None}) \ ([v], xs, \text{length} \ (\text{compE2} \ e2), \text{None})$   
**have**  $s: xs = loc \ xcp = \text{None}$   
**and**  $e: \tau\text{Exec-mover-a } P \ t \ e2 \ h \ (stk, xs, pc, \text{None}) \ ([v], xs, \text{length} \ (\text{compE2} \ e2), \text{None})$  **by**  $\text{auto}$   
**from**  $e$  **have**  $\tau\text{Exec-mover-a } P \ t \ (\text{if } (e) \ e1 \ \text{else } e2) \ h \ (stk, xs, \text{Suc} \ (\text{Suc} \ (\text{length} \ (\text{compE2} \ e) + \text{length} \ (\text{compE2} \ e1) + pc)), \text{None}) \ ([v], xs, \text{Suc} \ (\text{Suc} \ (\text{length} \ (\text{compE2} \ e) + \text{length} \ (\text{compE2} \ e1) + \text{length} \ (\text{compE2} \ e2))), \text{None})$   
**by**  $(\text{rule } \text{Cond-}\tau\text{ExecrI3})$   
**with**  $\text{True} \ s$  **show**  $?thesis$  **by**  $(\text{simp } \text{add: } \text{add.assoc})$   
**next**  
**case**  $\text{False}$



```

  with  $\langle pc \leq \text{length}(\text{compE2 } e2) \rangle$  have  $pc: pc = \text{length}(\text{compE2 } e2)$  by auto
  with  $\langle P, e2, h \vdash (\text{Val } v, xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
  have  $stk = [v] \ xs = loc \ xcp = None$  by(auto dest: bisim1-Val-length-compE2D)
  with  $pc$  show ?thesis by(simp add: add.assoc)
qed
next
case (bisim1While7 c e xs)
have  $\tau\text{Exec-mover-a } P \ t \ (\text{while } (c) \ e) \ h$ 
  ( $[], xs, \text{Suc}(\text{Suc}(\text{Suc}(\text{length}(\text{compE2 } c) + \text{length}(\text{compE2 } e))))$ , None)
  ( $[\text{Unit}]$ , xs,  $\text{length}(\text{compE2 } (\text{while } (c) \ e))$ , None)
  by(auto intro!:  $\tau\text{Execr1step exec-instr } \tau\text{move2While4 simp add: exec-move-def}$ )
thus ?case by(simp)
next
case (bisims1List1 e e' xs stk loc pc xcp es)
from  $\langle e' \# es = \text{map Val } vs \rangle$  obtain  $v \ vs'$  where [simp]:  $vs = v \# vs' \ e' = \text{Val } v \ es = \text{map Val } vs'$ 
by auto
from  $\langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
have  $\text{length}: pc \leq \text{length}(\text{compE2 } e)$  by(auto dest: bisim1-pc-length-compE2)
hence  $xs = loc \wedge xcp = None \wedge \tau\text{Exec-mover-a } P \ t \ e \ h \ (stk, xs, pc, None) \ ([v], xs, \text{length}(\text{compE2 } e), None)$ 
proof(cases  $pc < \text{length}(\text{compE2 } e)$ )
  case True
  with  $\langle [e' = \text{Val } v; pc < \text{length}(\text{compE2 } e)] \implies xs = loc \wedge xcp = None \wedge \tau\text{Exec-mover-a } P \ t \ e \ h \ (stk, xs, pc, None) \ ([v], xs, \text{length}(\text{compE2 } e), None) \rangle$ 
  show ?thesis by auto
  next
  case False
  with  $\text{length}$  have  $pc: pc = \text{length}(\text{compE2 } e)$  by auto
  with  $\langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$  have  $stk = [v] \ xs = loc \ xcp = None$ 
  by(auto dest: bisim1-Val-length-compE2D)
  with  $pc$  show ?thesis by(auto)
qed
hence  $s: xs = loc \ xcp = None$ 
and  $\text{exec1}: \tau\text{Exec-mover-a } P \ t \ e \ h \ (stk, xs, pc, None) \ ([v], xs, \text{length}(\text{compE2 } e), None)$  by auto
from  $\text{exec1}$  have  $\tau\text{Exec-movesr-a } P \ t \ (e \# es) \ h \ (stk, xs, pc, None) \ ([v], xs, \text{length}(\text{compE2 } e), None)$ 
by(auto intro:  $\tau\text{Exec-mover-}\tau\text{Exec-movesr}$ )
moreover have  $\tau\text{Exec-movesr-a } P \ t \ (\text{map Val } vs') \ h \ ([], xs, 0, None) \ (\text{rev } vs', xs, \text{length}(\text{compEs2 } (\text{map Val } vs')), None)$ 
by(rule  $\tau\text{Exec-movesr-map-Val}$ )
hence  $\tau\text{Exec-movesr-a } P \ t \ ([e] @ \text{map Val } vs') \ h \ ([] @ [v], xs, \text{length}(\text{compEs2 } [e]) + 0, None) \ (\text{rev } vs' @ [v], xs, \text{length}(\text{compEs2 } [e]) + \text{length}(\text{compEs2 } (\text{map Val } vs')), None)$ 
by  $-(\text{rule append-}\tau\text{Exec-movesr}, \text{auto})$ 
ultimately show ?case using  $s$  by(auto)
next
case (bisims1List2 es es' xs stk loc pc xcp e v)
from  $\langle \text{Val } v \# es' = \text{map Val } vs \rangle$  obtain  $vs'$  where [simp]:  $vs = v \# vs' \ es' = \text{map Val } vs'$  by auto
from  $\langle P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, xcp) \rangle$ 
have  $\text{length}: pc \leq \text{length}(\text{compEs2 } es)$  by(auto dest: bisims1-pc-length-compEs2)
hence  $xs = loc \wedge xcp = None \wedge \tau\text{Exec-movesr-a } P \ t \ es \ h \ (stk, xs, pc, None) \ (\text{rev } vs', xs, \text{length}(\text{compEs2 } es), None)$ 
proof(cases  $pc < \text{length}(\text{compEs2 } es)$ )
  case True
  with  $\langle [es' = \text{map Val } vs'; pc < \text{length}(\text{compEs2 } es)] \implies xs = loc \wedge xcp = None \wedge \tau\text{Exec-movesr-a}$ 

```

```

P t es h (stk, xs, pc, None)
  (rev vs', xs, length (compEs2 es), None)
  show ?thesis by auto
next
case False
with length have pc: pc = length (compEs2 es) by auto
with  $\langle P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, xcp) \rangle$  have stk = rev vs' xs = loc xcp = None
  by (auto dest: bisims1-Val-length-compEs2D)
with pc show ?thesis by (auto)
qed
hence s: xs = loc xcp = None
and exec1:  $\tau Exec\text{-}movesr\text{-}a$  P t es h (stk, xs, pc, None) (rev vs', xs, length (compEs2 es), None)
by auto
from exec1 have  $\tau Exec\text{-}movesr\text{-}a$  P t ( $[e] @ es$ ) h (stk @ [v], xs, length (compEs2 [e]) + pc, None)
(rev vs' @ [v], xs, length (compEs2 [e]) + length (compEs2 es), None)
  by  $\neg(rule\ append\ \tau Exec\text{-}movesr, auto)$ 
thus ?case using s by (auto)
qed (auto)

```

**lemma** *bisim1Val2D1*:

```

assumes bisim:  $P, e, h \vdash (Val\ v, xs) \leftrightarrow (stk, loc, pc, xcp)$ 
shows  $xcp = None \wedge xs = loc \wedge \tau Exec\text{-}mover\text{-}a$  P t e h (stk, loc, pc, xcp) ( $[v], loc, length (compE2\ e), None$ )
proof -
  from bisim have xcp = None xs = loc by (auto dest: bisim-Val-loc-eq-xcp-None)
  moreover
  have  $\tau Exec\text{-}mover\text{-}a$  P t e h (stk, loc, pc, xcp) ( $[v], loc, length (compE2\ e), None$ )
  proof (cases pc < length (compE2 e))
    case True
    from bisim1-Val- $\tau Exec\text{-}move$ [OF bisim True] show ?thesis by auto
  next
    case False
    from bisim have pc ≤ length (compE2 e) by (auto dest: bisim1-pc-length-compE2)
    with False have pc = length (compE2 e) by auto
    with bisim have stk = [v] loc = xs xcp = None by (auto dest: bisim1-Val-length-compE2D)
    with  $\langle pc = length (compE2\ e) \rangle$  show ?thesis by (auto)
  qed
  ultimately show ?thesis by simp
qed

```

**lemma** *bisim1-Throw- $\tau Exec\text{-}movet$* :

```

[[  $P, e, h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, pc, None)$  ]]
 $\implies \exists pc'. \tau Exec\text{-}movet\text{-}a$  P t e h (stk, loc, pc, None) ( $[Addr\ a], loc, pc', [a]$ )  $\wedge$ 
 $P, e, h \vdash (Throw\ a, xs) \leftrightarrow ([Addr\ a], loc, pc', [a]) \wedge xs = loc$ 

```

**and** *bisims1-Throw- $\tau Exec\text{-}movest$* :

```

[[  $P, es, h \vdash (map\ Val\ vs @ Throw\ a \# es', xs) [\leftrightarrow] (stk, loc, pc, None)$  ]]
 $\implies \exists pc'. \tau Exec\text{-}movest\text{-}a$  P t es h (stk, loc, pc, None) ( $Addr\ a \# rev\ vs, loc, pc', [a]$ )  $\wedge$ 
 $P, es, h \vdash (map\ Val\ vs @ Throw\ a \# es', xs) [\leftrightarrow] (Addr\ a \# rev\ vs, loc, pc', [a]) \wedge xs = loc$ 

```

**proof**(*induct* *e n* :: *nat* *Throw a* :: '*addr expr1 xs stk loc pc None* :: '*addr option*

and *es n* :: *nat* *map Val vs @ Throw a # es'* :: '*addr expr1 list xs stk loc pc None* :: '*addr option*  
 arbitrary: **and** *vs rule*: *bisim1-bisims1-inducts-split*)

**case** (*bisim1Sync9 e1 n e2 V xs*)

**let** ?*pc* = 8 + *length (compE2 e1)* + *length (compE2 e2)*

**have**  $\tau\text{Exec-movet-a } P \ t \ (\text{sync}_V \ (e1) \ e2) \ h \ ([\text{Addr } a], \ xs, \ ?pc, \ \text{None}) \ ([\text{Addr } a], \ xs, \ ?pc, \ [a])$   
**by**(rule  $\tau\text{Exec1step}$ )(auto intro:  $\text{exec-instr } \tau\text{move2-}\tau\text{moves2.intros simp add: is-Ref-def exec-move-def}$ )  
**moreover**  
**have**  $P, \text{sync}_V \ (e1) \ e2, h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], xs, ?pc, [a])$  **by**(rule  $\text{bisim1Sync10}$ )  
**ultimately show**  $?case$  **by** auto  
**next**  
**case** ( $\text{bisim1Seq2 } e2 \ n \ xs \ stk \ loc \ pc \ e1$ )  
**then obtain**  $pc'$  **where**  $\tau\text{Exec-movet-a } P \ t \ e2 \ h \ (stk, \ loc, \ pc, \ \text{None}) \ ([\text{Addr } a], \ loc, \ pc', \ [a])$   
 $P, \ e2, \ h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], loc, pc', [a]) \ xs = loc$  **by** auto  
**thus**  $?case$  **by**(auto intro:  $\text{Seq-}\tau\text{ExecI2 bisim1-bisims1.bisim1Seq2}$ )  
**next**  
**case** ( $\text{bisim1CondThen } e1 \ n \ xs \ stk \ loc \ pc \ e2$ )  
**then obtain**  $pc'$  **where**  $\text{exec: } \tau\text{Exec-movet-a } P \ t \ e1 \ h \ (stk, \ loc, \ pc, \ \text{None}) \ ([\text{Addr } a], \ loc, \ pc', \ [a])$   
**and**  $\text{bisim: } P, \ e1, \ h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], loc, pc', [a])$  **and**  $s: xs = loc$  **by** auto  
**from**  $\text{exec}$  **have**  $\tau\text{Exec-movet-a } P \ t \ (\text{if } (e) \ e1 \ \text{else } e2) \ h \ (stk, \ loc, \ \text{Suc } (\text{length } (\text{compE2 } e) + pc), \ \text{None}) \ ([\text{Addr } a], \ loc, \ \text{Suc } (\text{length } (\text{compE2 } e) + pc'), \ [a])$   
**by**(rule  $\text{Cond-}\tau\text{ExecI2}$ )  
**moreover from**  $\text{bisim}$   
**have**  $P, \ \text{if } (e) \ e1 \ \text{else } e2, \ h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], loc, \text{Suc } (\text{length } (\text{compE2 } e) + pc'), [a])$   
**by**(rule  $\text{bisim1-bisims1.bisim1CondThen}$ )  
**ultimately show**  $?case$  **using**  $s$  **by**(auto)  
**next**  
**case** ( $\text{bisim1CondElse } e2 \ n \ xs \ stk \ loc \ pc \ e1$ )  
**then obtain**  $pc'$  **where**  $\text{exec: } \tau\text{Exec-movet-a } P \ t \ e2 \ h \ (stk, \ loc, \ pc, \ \text{None}) \ ([\text{Addr } a], \ loc, \ pc', \ [a])$   
**and**  $\text{bisim: } P, \ e2, \ h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], loc, pc', [a])$  **and**  $s: xs = loc$  **by** auto  
**let**  $?pc \ pc = \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e) + \text{length } (\text{compE2 } e1) + pc))$   
**from**  $\text{exec}$  **have**  $\tau\text{Exec-movet-a } P \ t \ (\text{if } (e) \ e1 \ \text{else } e2) \ h \ (stk, \ loc, \ (?pc \ pc), \ \text{None}) \ ([\text{Addr } a], \ loc, \ ?pc \ pc', \ [a])$   
**by**(rule  $\text{Cond-}\tau\text{ExecI3}$ )  
**moreover from**  $\text{bisim}$   
**have**  $P, \ \text{if } (e) \ e1 \ \text{else } e2, \ h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], loc, ?pc \ pc', [a])$   
**by**(rule  $\text{bisim1-bisims1.bisim1CondElse}$ )  
**ultimately show**  $?case$  **using**  $s$  **by** auto  
**next**  
**case** ( $\text{bisim1Throw1 } e \ n \ xs \ stk \ loc \ pc$ )  
**note**  $\text{bisim} = \langle P, \ e, \ h \vdash (\text{addr } a, xs) \leftrightarrow (stk, \ loc, \ pc, \ \text{None}) \rangle$   
**hence**  $s: xs = loc$   
**and**  $\text{exec: } \tau\text{Exec-mover-a } P \ t \ e \ h \ (stk, \ loc, \ pc, \ \text{None}) \ ([\text{Addr } a], \ loc, \ \text{length } (\text{compE2 } e), \ \text{None})$   
**by**(auto dest:  $\text{bisim1Val2D1}$ )  
**from**  $\text{exec}$  **have**  $\tau\text{Exec-mover-a } P \ t \ (\text{throw } e) \ h \ (stk, \ loc, \ pc, \ \text{None}) \ ([\text{Addr } a], \ loc, \ \text{length } (\text{compE2 } e), \ \text{None})$   
**by**(rule  $\text{Throw-}\tau\text{ExecrI}$ )  
**also have**  $\tau\text{Exec-movet-a } P \ t \ (\text{throw } e) \ h \ ([\text{Addr } a], \ loc, \ \text{length } (\text{compE2 } e), \ \text{None}) \ ([\text{Addr } a], \ loc, \ \text{length } (\text{compE2 } e), \ [a])$   
**by**(rule  $\tau\text{Exec1step}$ , auto intro:  $\text{exec-instr } \tau\text{move2Throw2 simp add: is-Ref-def exec-move-def}$ )  
**also have**  $P, \ \text{throw } e, \ h \vdash (\text{Throw } a, loc) \leftrightarrow ([\text{Addr } a], loc, \text{length } (\text{compE2 } e), [a])$   
**by**(rule  $\text{bisim1Throw2}$ )  
**ultimately show**  $?case$  **using**  $s$  **by** auto  
**next**  
**case** ( $\text{bisims1List1 } e \ n \ e' \ xs \ stk \ loc \ pc \ es \ vs$ )  
**note**  $\text{bisim} = \langle P, e, h \vdash (e', xs) \leftrightarrow (stk, \ loc, \ pc, \ \text{None}) \rangle$   
**show**  $?case$   
**proof**(cases  $\text{is-val } e'$ )  
**case**  $\text{True}$

**with**  $\langle e' \# es = \text{map Val } vs @ \text{Throw } a \# es' \rangle$  **obtain**  $v \# vs'$  **where**  $vs = v \# vs' \ e' = \text{Val } v$   
**and**  $es: es = \text{map Val } vs' @ \text{Throw } a \# es'$  **by**  $(\text{auto simp add: Cons-eq-append-conv})$   
**with**  $\text{bisim}$  **have**  $P, e, h \vdash (\text{Val } v, xs) \leftrightarrow (stk, loc, pc, \text{None})$  **by**  $\text{simp}$   
**from**  $\text{bisim1Val2D1}[OF \text{ this}]$  **have**  $[simp]: xs = loc$   
**and**  $\text{exec}: \tau \text{Exec-mover-a } P \ t \ e \ h \ (stk, loc, pc, \text{None}) \ ([v], loc, \text{length}(\text{compE2 } e), \text{None})$   
**by**  $\text{auto}$   
**from**  $\text{exec}$  **have**  $\tau \text{Exec-movesr-a } P \ t \ (e \# es) \ h \ (stk, loc, pc, \text{None}) \ ([v], loc, \text{length}(\text{compE2 } e), \text{None})$   
**by**  $(\text{rule } \tau \text{Exec-mover-}\tau \text{Exec-movesr})$   
**also from**  $es \ \langle es = \text{map Val } vs' @ \text{Throw } a \# es' \rangle$   
 $\implies \exists pc'. \tau \text{Exec-movest-a } P \ t \ es \ h \ ([], loc, 0, \text{None}) \ (\text{Addr } a \# \text{rev } vs', loc, pc', [a]) \wedge$   
 $P, es, h \vdash (\text{map Val } vs' @ \text{Throw } a \# es', loc) \ [\leftrightarrow] \ (\text{Addr } a \# \text{rev } vs', loc, pc', [a]) \wedge loc = loc$   
**obtain**  $pc'$  **where**  $\text{execs}: \tau \text{Exec-movest-a } P \ t \ es \ h \ ([], loc, 0, \text{None}) \ (\text{Addr } a \# \text{rev } vs', loc, pc', [a])$   
**and**  $\text{bisim}': P, es, h \vdash (\text{map Val } vs' @ \text{Throw } a \# es', loc) \ [\leftrightarrow] \ (\text{Addr } a \# \text{rev } vs', loc, pc', [a])$   
**by**  $\text{auto}$   
**from**  $\text{append-}\tau \text{Exec-movest}[OF - \text{execs}, \text{of } [v] \ [e]]$   
**have**  $\tau \text{Exec-movest-a } P \ t \ (e \# es) \ h \ ([v], loc, \text{length}(\text{compE2 } e), \text{None}) \ (\text{Addr } a \# \text{rev } vs' @ [v], loc, \text{length}(\text{compE2 } e) + pc', [a])$  **by**  $\text{simp}$   
**also from**  $\text{bisims1List2}[OF \text{ bisim}', \text{of } e \ v] \ es \ \langle e' = \text{Val } v \rangle \ \langle vs = v \# vs' \rangle$   
**have**  $P, e \# es, h \vdash (e' \# es, xs) \ [\leftrightarrow] \ ((\text{Addr } a \# \text{rev } vs), loc, \text{length}(\text{compE2 } e) + pc', [a])$  **by**  $\text{simp}$   
**ultimately show**  $?thesis$  **using**  $\langle vs = v \# vs' \rangle \ es \ \langle e' = \text{Val } v \rangle$  **by**  $\text{auto}$   
**next**  
**case**  $\text{False}$   
**with**  $\langle e' \# es = \text{map Val } vs @ \text{Throw } a \# es' \rangle$  **have**  $[simp]: e' = \text{Throw } a \ es = es' \ vs = []$   
**by**  $(\text{auto simp add: Cons-eq-append-conv})$   
**from**  $\langle e' = \text{Throw } a \implies \exists pc'. \tau \text{Exec-movet-a } P \ t \ e \ h \ (stk, loc, pc, \text{None}) \ ([\text{Addr } a], loc, pc', [a])$   
 $\wedge P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], loc, pc', [a]) \wedge xs = loc$   
**obtain**  $pc'$  **where**  $\tau \text{Exec-movet-a } P \ t \ e \ h \ (stk, loc, pc, \text{None}) \ ([\text{Addr } a], loc, pc', [a])$   
**and**  $\text{bisim}: P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], loc, pc', [a])$  **and**  $s: xs = loc$  **by**  $\text{auto}$   
**hence**  $\tau \text{Exec-movest-a } P \ t \ (e \# es) \ h \ (stk, loc, pc, \text{None}) \ ([\text{Addr } a], loc, pc', [a])$   
**by**  $-(\text{rule } \tau \text{Exec-movet-}\tau \text{Exec-movest})$   
**moreover from**  $\text{bisim}$   
**have**  $P, e \# es, h \vdash (\text{Throw } a \# es, xs) \ [\leftrightarrow] \ ([\text{Addr } a], loc, pc', [a])$  **by**  $(\text{rule } \text{bisim1-bisims1.bisims1List1})$   
**ultimately show**  $?thesis$  **using**  $s$  **by**  $\text{auto}$   
**qed**  
**next**  
**case**  $(\text{bisims1List2 } es \ n \ es'' \ xs \ stk \ loc \ pc \ e \ v)$   
**note**  $IH = \langle \bigwedge vs. es'' = \text{map Val } vs @ \text{Throw } a \# es' \rangle$   
 $\implies \exists pc'. \tau \text{Exec-movest-a } P \ t \ es \ h \ (stk, loc, pc, \text{None}) \ (\text{Addr } a \# \text{rev } vs, loc, pc', [a]) \wedge$   
 $P, es, h \vdash (\text{map Val } vs @ \text{Throw } a \# es', xs) \ [\leftrightarrow] \ (\text{Addr } a \# \text{rev } vs, loc, pc', [a]) \wedge xs = loc$   
**from**  $\langle \text{Val } v \# es'' = \text{map Val } vs @ \text{Throw } a \# es' \rangle$   
**obtain**  $vs'$  **where**  $[simp]: vs = v \# vs' \ es'' = \text{map Val } vs' @ \text{Throw } a \# es'$  **by**  $(\text{auto simp add: Cons-eq-append-conv})$   
**from**  $IH[OF \ \langle es'' = \text{map Val } vs' @ \text{Throw } a \# es' \rangle]$   
**obtain**  $pc'$  **where**  $\text{exec}: \tau \text{Exec-movest-a } P \ t \ es \ h \ (stk, loc, pc, \text{None}) \ (\text{Addr } a \# \text{rev } vs', loc, pc', [a])$   
**and**  $\text{bisim}: P, es, h \vdash (\text{map Val } vs' @ \text{Throw } a \# es', xs) \ [\leftrightarrow] \ (\text{Addr } a \# \text{rev } vs', loc, pc', [a])$   
**and**  $[simp]: xs = loc$  **by**  $\text{auto}$   
**from**  $\text{append-}\tau \text{Exec-movest}[OF - \text{exec}, \text{of } [v] \ [e]]$   
**have**  $\tau \text{Exec-movest-a } P \ t \ (e \# es) \ h \ (stk @ [v], loc, \text{length}(\text{compE2 } e) + pc, \text{None}) \ (\text{Addr } a \# \text{rev } vs, loc, \text{length}(\text{compE2 } e) + pc', [a])$  **by**  $\text{simp}$   
**moreover from**  $\text{bisim}$   
**have**  $P, e \# es, h \vdash (\text{Val } v \# \text{map Val } vs' @ \text{Throw } a \# es', xs) \ [\leftrightarrow] \ ((\text{Addr } a \# \text{rev } vs') @ [v], loc, \text{length}(\text{compE2 } e) + pc, \text{None})$

```

(compE2 e) + pc', [a])
  by(rule bisim1-bisims1.bisims1List2)
  ultimately show ?case by(auto)
qed(auto)

```

**lemma** *bisim1-Throw- $\tau$ Exec-mover*:

```

[[ P, e, h  $\vdash$  (Throw a,xs)  $\leftrightarrow$  (stk,loc,pc,None) ]]
 $\implies \exists pc'. \tau\text{Exec-mover-a } P \text{ t e h (stk, loc, pc, None) ([Addr a], loc, pc', [a])} \wedge$ 
  P, e, h  $\vdash$  (Throw a,xs)  $\leftrightarrow$  ([Addr a], loc, pc', [a])  $\wedge$  xs = loc
by(drule bisim1-Throw- $\tau$ Exec-movet)(blast intro: tranclp-into-rtranclp)

```

**lemma** *bisims1-Throw- $\tau$ Exec-movesr*:

```

[[ P, es, h  $\vdash$  (map Val vs @ Throw a # es',xs) [ $\leftrightarrow$ ] (stk,loc,pc,None) ]]
 $\implies \exists pc'. \tau\text{Exec-movesr-a } P \text{ t es h (stk, loc, pc, None) (Addr a # rev vs, loc, pc', [a])} \wedge$ 
  P, es, h  $\vdash$  (map Val vs @ Throw a # es',xs) [ $\leftrightarrow$ ] (Addr a # rev vs, loc, pc', [a])  $\wedge$  xs = loc
by(drule bisims1-Throw- $\tau$ Exec-movest)(blast intro: tranclp-into-rtranclp)

```

**declare** *split-beta* [simp]

**lemma** *bisim1-inline-call-Throw*:

```

[[ P,e,h  $\vdash$  (e', xs)  $\leftrightarrow$  (stk, loc, pc, None); call1 e' = [(a, M, vs)];
  compE2 e ! pc = Invoke M n0; pc < length (compE2 e) ]]
 $\implies n0 = \text{length vs} \wedge P,e,h \vdash (\text{inline-call (Throw A) e', xs}) \leftrightarrow (stk, loc, pc, [A])$ 
(is [[ -; -; - ]  $\implies$  ?concl e n e' xs pc stk loc)

```

**and** *bisims1-inline-calls-Throw*:

```

[[ P,es,h  $\vdash$  (es', xs) [ $\leftrightarrow$ ] (stk, loc, pc, None); calls1 es' = [(a, M, vs)];
  compEs2 es ! pc = Invoke M n0; pc < length (compEs2 es) ]]
 $\implies n0 = \text{length vs} \wedge P,es,h \vdash (\text{inline-calls (Throw A) es', xs}) [ $\leftrightarrow$ ] (stk, loc, pc, [A])$ 
(is [[ -; -; - ]  $\implies$  ?concls es n es' xs pc stk loc)

```

**proof**(induct e n :: nat e' xs stk loc pc None :: 'addr option

and es n :: nat es' xs stk loc pc None :: 'addr option

rule: bisim1-bisims1-inducts-split)

**case** (bisim1BinOp1 e1 n e' xs stk loc pc e2 bop)

**note** IH1 =  $\langle \llbracket \text{call1 e'} = [(a, M, vs)]; \text{compE2 e1} ! pc = \text{Invoke M n0}; pc < \text{length (compE2 e1)} \rrbracket$

$\implies ?\text{concl e1 n e' xs pc stk loc} \rangle$

**note** bisim1 =  $\langle P,e1,h \vdash (e', xs) \leftrightarrow (stk, loc, pc, None) \rangle$

**note** ins =  $\langle \text{compE2 (e1 «bop» e2)} ! pc = \text{Invoke M n0} \rangle$

**note** call =  $\langle \text{call1 (e' «bop» e2)} = [(a, M, vs)] \rangle$

**show** ?case

**proof**(cases is-val e')

**case** False

**with** bisim1 call **have** pc < length (compE2 e1)

**by**(auto intro: bisim1-call-pcD)

**with** call ins IH1 False **show** ?thesis

**by**(auto intro: bisim1-bisims1.bisim1BinOp1)

**next**

**case** True

**then obtain** v **where** [simp]: e' = Val v **by** auto

**from** bisim1 **have** pc  $\leq$  length (compE2 e1) **by**(auto dest: bisim1-pc-length-compE2)

**moreover** {

**assume** pc: pc < length (compE2 e1)

**with** bisim1 ins **have** False

**by**(auto dest: bisim-Val-pc-not-Invoke) }

```

    ultimately have [simp]: pc = length (compE2 e1) by(cases pc < length (compE2 e1)) auto
    from call ins show ?thesis by simp
  qed
next
  case bisim1BinOp2 thus ?case
    by(auto split: if-split-asm bop.split-asm dest: bisim1-bisims1.bisim1BinOp2)
next
  case (bisim1AAcc1 A n a' xs stk loc pc i)
  note IH1 = ⟨[call1 a' = [(a, M, vs)]; compE2 A ! pc = Invoke M n0; pc < length (compE2 A)] ⟩
    ⇒ ?concl A n a' xs pc stk loc⟩
  note bisim1 = ⟨P,A,h ⊢ (a', xs) ↔ (stk, loc, pc, None)⟩
  note ins = ⟨compE2 (A[i]) ! pc = Invoke M n0⟩
  note call = ⟨call1 (a'[i]) = [(a, M, vs)]⟩
  show ?case
  proof(cases is-val a')
    case False
    with bisim1 call have pc < length (compE2 A)
      by(auto intro: bisim1-call-pcD)
    with call ins IH1 False show ?thesis
      by(auto intro: bisim1-bisims1.bisim1AAcc1)
  next
    case True
    then obtain v where [simp]: a' = Val v by auto
    from bisim1 have pc ≤ length (compE2 A) by(auto dest: bisim1-pc-length-compE2)
    moreover {
      assume pc: pc < length (compE2 A)
      with bisim1 ins have False
        by(auto dest: bisim-Val-pc-not-Invoke) }
    ultimately have [simp]: pc = length (compE2 A) by(cases pc < length (compE2 A)) auto
    from call ins show ?thesis by simp
  qed
next
  case bisim1AAcc2 thus ?case
    by(auto split: if-split-asm dest: bisim1-bisims1.bisim1AAcc2)
next
  case (bisim1AAss1 A n a' xs stk loc pc i e)
  note IH1 = ⟨[call1 a' = [(a, M, vs)]; compE2 A ! pc = Invoke M n0; pc < length (compE2 A)] ⟩
    ⇒ ?concl A n a' xs pc stk loc⟩
  note bisim1 = ⟨P,A,h ⊢ (a', xs) ↔ (stk, loc, pc, None)⟩
  note ins = ⟨compE2 (A[i] := e) ! pc = Invoke M n0⟩
  note call = ⟨call1 (a'[i] := e) = [(a, M, vs)]⟩
  show ?case
  proof(cases is-val a')
    case False
    with bisim1 call have pc < length (compE2 A)
      by(auto intro: bisim1-call-pcD)
    with call ins IH1 False show ?thesis
      by(auto intro: bisim1-bisims1.bisim1AAss1)
  next
    case True
    then obtain v where [simp]: a' = Val v by auto
    from bisim1 have pc ≤ length (compE2 A) by(auto dest: bisim1-pc-length-compE2)
    moreover {
      assume pc: pc < length (compE2 A)

```

```

    with bisim1 ins have False
      by(auto dest: bisim-Val-pc-not-Invoke) }
    ultimately have [simp]: pc = length (compE2 A) by(cases pc < length (compE2 A)) auto
    from call ins show ?thesis by simp
qed
next
case (bisim1AAss2 i n i' xs stk loc pc A e v)
note IH1 =  $\langle \llbracket \text{call1 } i' = \lfloor (a, M, vs) \rfloor; \text{compE2 } i ! pc = \text{Invoke } M \ n0; pc < \text{length } (\text{compE2 } i) \rrbracket \Rightarrow ?\text{concl } i \ n \ i' \ xs \ pc \ stk \ loc \rangle$ 
note bisim1 =  $\langle P, i, h \vdash (i', xs) \leftrightarrow (stk, loc, pc, \text{None}) \rangle$ 
note ins =  $\langle \text{compE2 } (A[i] := e) ! (\text{length } (\text{compE2 } A) + pc) = \text{Invoke } M \ n0 \rangle$ 
note call =  $\langle \text{call1 } (\text{Val } v[i] := e) = \lfloor (a, M, vs) \rfloor \rangle$ 
show ?case
proof(cases is-val i')
  case False
    with bisim1 call have pc < length (compE2 i)
      by(auto intro: bisim1-call-pcD)
    with call ins IH1 False show ?thesis
      by(auto intro: bisim1-bisims1.bisim1AAss2)
  next
  case True
    then obtain v where [simp]: i' = Val v by auto
    from bisim1 have pc ≤ length (compE2 i) by(auto dest: bisim1-pc-length-compE2)
    moreover {
      assume pc: pc < length (compE2 i)
      with bisim1 ins have False
        by(auto dest: bisim-Val-pc-not-Invoke) }
    ultimately have [simp]: pc = length (compE2 i) by(cases pc < length (compE2 i)) auto
    from call ins show ?thesis by simp
  qed
next
case bisim1AAss3 thus ?case
  by(auto split: if-split-asm nat.split-asm simp add: nth-Cons dest: bisim1-bisims1.bisim1AAss3)
next
case (bisim1FAss1 e n e' xs stk loc pc e2 F D)
note IH1 =  $\langle \llbracket \text{call1 } e' = \lfloor (a, M, vs) \rfloor; \text{compE2 } e ! pc = \text{Invoke } M \ n0; pc < \text{length } (\text{compE2 } e) \rrbracket \Rightarrow ?\text{concl } e \ n \ e' \ xs \ pc \ stk \ loc \rangle$ 
note bisim1 =  $\langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, \text{None}) \rangle$ 
note ins =  $\langle \text{compE2 } (e \cdot F\{D\} := e2) ! pc = \text{Invoke } M \ n0 \rangle$ 
note call =  $\langle \text{call1 } (e \cdot F\{D\} := e2) = \lfloor (a, M, vs) \rfloor \rangle$ 
show ?case
proof(cases is-val e')
  case False
    with bisim1 call have pc < length (compE2 e)
      by(auto intro: bisim1-call-pcD)
    with call ins IH1 False show ?thesis
      by(auto intro: bisim1-bisims1.bisim1FAss1)
  next
  case True
    then obtain v where [simp]: e' = Val v by auto
    from bisim1 have pc ≤ length (compE2 e) by(auto dest: bisim1-pc-length-compE2)
    moreover {
      assume pc: pc < length (compE2 e)
      with bisim1 ins have False

```

```

      by(auto dest: bisim-Val-pc-not-Invoke) }
    ultimately have [simp]: pc = length (compE2 e) by(cases pc < length (compE2 e)) auto
    from call ins show ?thesis by simp
  qed
next
  case bisim1FAss2 thus ?case
    by(auto split: if-split-asm nat.split-asm simp add: nth-Cons dest: bisim1-bisims1.bisim1FAss2)
next
  case (bisim1CAS1 E n e' xs stk loc pc e2 e3 D F)
  note IH1 = ⟨[call1 e' = [(a, M, vs)]; compE2 E ! pc = Invoke M n0; pc < length (compE2 E)]⟩
    ⇒ ?concl E n e' xs pc stk loc
  note bisim1 = ⟨P,E,h ⊢ (e', xs) ↔ (stk, loc, pc, None)⟩
  note ins = ⟨compE2 - ! pc = Invoke M n0⟩
  note call = ⟨call1 - = [(a, M, vs)]⟩
  show ?case
  proof(cases is-val e')
    case False
    with bisim1 call have pc < length (compE2 E)
      by(auto intro: bisim1-call-pcD)
    with call ins IH1 False show ?thesis
      by(auto intro: bisim1-bisims1.bisim1CAS1)
  next
    case True
    then obtain v where [simp]: e' = Val v by auto
    from bisim1 have pc ≤ length (compE2 E) by(auto dest: bisim1-pc-length-compE2)
    moreover {
      assume pc: pc < length (compE2 E)
      with bisim1 ins have False
        by(auto dest: bisim-Val-pc-not-Invoke) }
    ultimately have [simp]: pc = length (compE2 E) by(cases pc < length (compE2 E)) auto
    from call ins show ?thesis by simp
  qed
next
  case (bisim1CAS2 e2 n e2' xs stk loc pc e1 e3 D F v)
  note IH1 = ⟨[call1 e2' = [(a, M, vs)]; compE2 e2 ! pc = Invoke M n0; pc < length (compE2 e2)]⟩
    ⇒ ?concl e2 n e2' xs pc stk loc
  note bisim1 = ⟨P,e2,h ⊢ (e2', xs) ↔ (stk, loc, pc, None)⟩
  note ins = ⟨compE2 - ! (length (compE2 e1) + pc) = Invoke M n0⟩
  note call = ⟨call1 - = [(a, M, vs)]⟩
  show ?case
  proof(cases is-val e2')
    case False
    with bisim1 call have pc < length (compE2 e2)
      by(auto intro: bisim1-call-pcD)
    with call ins IH1 False show ?thesis
      by(auto intro: bisim1-bisims1.bisim1CAS2)
  next
    case True
    then obtain v where [simp]: e2' = Val v by auto
    from bisim1 have pc ≤ length (compE2 e2) by(auto dest: bisim1-pc-length-compE2)
    moreover {
      assume pc: pc < length (compE2 e2)
      with bisim1 ins have False
        by(auto dest: bisim-Val-pc-not-Invoke) }
  qed

```



```

ultimately have [simp]: pc = length (compE2 e2) by(cases pc < length (compE2 e2)) auto
from call ins show ?thesis by simp
qed
next
case (bisim1Call1 obj n obj' xs stk loc pc ps M')
note IH1 = ⟨[call1 obj' = [(a, M, vs)]]; compE2 obj ! pc = Invoke M n0;
           pc < length (compE2 obj) ⟩
           ⇒ ?concl obj n obj' xs pc stk loc
note IH2 = ⟨∧xs. [calls1 ps = [(a, M, vs)]]; compEs2 ps ! 0 = Invoke M n0; 0 < length (compEs2
ps) ⟩
           ⇒ ?concls ps n ps xs 0 [] xs
note ins = ⟨compE2 (obj·M'(ps)) ! pc = Invoke M n0⟩
note bisim1 = ⟨P,obj,h ⊢ (obj', xs) ↔ (stk, loc, pc, None)⟩
note call = ⟨call1 (obj'·M'(ps)) = [(a, M, vs)]⟩
thus ?case
proof(cases rule: call1-callE)
  case CallObj
  with bisim1 call have pc < length (compE2 obj) by(auto intro: bisim1-call-pcD)
  with call ins CallObj IH1 show ?thesis
    by(auto intro: bisim1-bisims1.bisim1Call1)
next
case (CallParams v)
from bisim1 have pc ≤ length (compE2 obj) by(auto dest: bisim1-pc-length-compE2)
moreover {
  assume pc: pc < length (compE2 obj)
  with bisim1 ins CallParams have False by(auto dest: bisim-Val-pc-not-Invoke) }
ultimately have [simp]: pc = length (compE2 obj) by(cases pc < length (compE2 obj)) auto
with bisim1 CallParams have [simp]: stk = [v] loc = xs by(auto dest: bisim1-Val-length-compE2D)
from IH2[of loc] CallParams ins
show ?thesis
  apply(clarsimp simp add: compEs2-map-Val is-vals-conv split: if-split-asm)
  apply(drule bisim1-bisims1.bisim1CallParams)
  apply(auto simp add: neq-Nil-conv)
  done
next
case [simp]: Call
from bisim1 have pc ≤ length (compE2 obj) by(auto dest: bisim1-pc-length-compE2)
moreover {
  assume pc: pc < length (compE2 obj)
  with bisim1 ins have False by(auto dest: bisim-Val-pc-not-Invoke) }
ultimately have [simp]: pc = length (compE2 obj) by(cases pc < length (compE2 obj)) auto
with ins have [simp]: vs = [] by(auto simp add: compEs2-map-Val split: if-split-asm)
from bisim1 have [simp]: stk = [Addr a] xs = loc by(auto dest: bisim1-Val-length-compE2D)
from ins show ?thesis by(auto intro: bisim1CallThrow[of [] [], simplified])
qed
next
case (bisim1CallParams ps n ps' xs stk loc pc obj M' v)
note IH2 = ⟨[calls1 ps' = [(a, M, vs)]]; compEs2 ps ! pc = Invoke M n0; pc < length (compEs2 ps)
⟩
           ⇒ ?concls ps n ps' xs pc stk loc
note ins = ⟨compE2 (obj·M'(ps)) ! (length (compE2 obj) + pc) = Invoke M n0⟩
note bisim2 = ⟨P,ps,h ⊢ (ps', xs) [↔] (stk, loc, pc, None)⟩
note call = ⟨call1 (Val v·M'(ps')) = [(a, M, vs)]⟩
thus ?case

```

```

proof(cases rule: call1-callE)
  case CallObj thus ?thesis by simp
next
  case (CallParams v')
  hence [simp]:  $v' = v$  and call': calls1 ps' =  $\lfloor (a, M, vs) \rfloor$  by auto
  from bisim2 call' have pc < length (compEs2 ps) by(auto intro: bisims1-calls-pcD)
  with IH2 CallParams ins show ?thesis
    by(auto simp add: is-vals-conv split: if-split-asm intro: bisim1-bisims1.bisim1CallParams)
next
  case Call
  hence [simp]:  $v = \text{Addr } a \ M' = M \ ps' = \text{map } \text{Val } vs$  by auto
  from bisim2 have pc  $\leq$  length (compEs2 ps) by(auto dest: bisims1-pc-length-compEs2)
  moreover {
    assume pc: pc < length (compEs2 ps)
    with bisim2 ins have False by(auto dest: bisims1-Val-pc-not-Invoke) }
  ultimately have [simp]: pc = length (compEs2 ps) by(cases pc < length (compEs2 ps)) auto
  from bisim2 have [simp]: stk = rev vs xs = loc by(auto dest: bisims1-Val-length-compEs2D)
  from bisim2 have length ps = length vs by(auto dest: bisims1-lengthD)
  with ins show ?thesis by(auto intro: bisim1CallThrow)
qed
next
  case (bisims1List1 e n e' xs stk loc pc es)
  note IH1 =  $\langle \llbracket \text{call1 } e' = \lfloor (a, M, vs) \rfloor; \text{compE2 } e ! pc = \text{Invoke } M \ n0; pc < \text{length } (\text{compE2 } e) \rrbracket$ 
     $\implies ?\text{concl } e \ n \ e' \ xs \ pc \ stk \ loc \rangle$ 
  note IH2 =  $\langle \bigwedge xs. \llbracket \text{calls1 } es = \lfloor (a, M, vs) \rfloor; \text{compEs2 } es ! 0 = \text{Invoke } M \ n0; 0 < \text{length } (\text{compEs2 } es) \rrbracket$ 
     $\implies ?\text{concls } es \ n \ es \ xs \ 0 \ [] \ xs \rangle$ 
  note bisim1 =  $\langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, \text{None}) \rangle$ 
  note call =  $\langle \text{calls1 } (e' \# es) = \lfloor (a, M, vs) \rfloor \rangle$ 
  note ins =  $\langle \text{compEs2 } (e \# es) ! pc = \text{Invoke } M \ n0 \rangle$ 
  show ?case
  proof(cases is-val e')
    case False
    with bisim1 call have pc < length (compE2 e) by(auto intro: bisim1-call-pcD)
    with call ins False IH1 show ?thesis
      by(auto intro: bisim1-bisims1.bisims1List1)
    next
    case True
    then obtain v where [simp]:  $e' = \text{Val } v$  by auto
    from bisim1 have pc  $\leq$  length (compE2 e) by(auto dest: bisim1-pc-length-compE2)
    moreover {
      assume pc: pc < length (compE2 e)
      with bisim1 ins have False by(auto dest: bisim1-Val-pc-not-Invoke) }
    ultimately have [simp]: pc = length (compE2 e) by(cases pc < length (compE2 e)) auto
    with bisim1 have [simp]: stk =  $[v]$  loc = xs by(auto dest: bisim1-Val-length-compE2D)
    from call have es  $\neq []$  by(cases es) simp-all
    with IH2[of loc] call ins
    show ?thesis by(auto split: if-split-asm dest: bisims1List2)
  qed
qed(auto split: if-split-asm bop.split-asm intro: bisim1-bisims1.intros dest: bisim1-pc-length-compE2)

lemma bisim1-max-stack:  $P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \implies \text{length } stk \leq \text{max-stack } e$ 
  and bisims1-max-stacks:  $P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, xcp) \implies \text{length } stk \leq \text{max-stacks } es$ 
apply(induct (e', xs) (stk, loc, pc, xcp) and (es', xs) (stk, loc, pc, xcp))

```

arbitrary:  $e' \text{ xs stk loc pc xcp}$  and  $es' \text{ xs stk loc pc xcp}$  rule:  $\text{bisim1-bisims1.inducts}$ )  
**apply**(auto simp add:  $\text{max-stack1[simplified]}$   $\text{max-def}$   $\text{max-stacks-ge-length}$ )  
**apply**(drule  $\text{sym}$ , simp add:  $\text{max-stacks-ge-length}$ , drule  $\text{sym}$ , simp, rule  $\text{le-trans[OF max-stacks-ge-length]}$ ,  
simp)  
**done**

**inductive**  $\text{bisim1-fr} :: 'addr \text{J1-prog} \Rightarrow 'heap \Rightarrow 'addr \text{expr1} \times 'addr \text{locals1} \Rightarrow 'addr \text{frame} \Rightarrow \text{bool}$   
**for**  $P :: 'addr \text{J1-prog}$  and  $h :: 'heap$

**where**

$\llbracket P \vdash C \text{ sees } M:Ts \rightarrow T = \lfloor \text{body} \rfloor \text{ in } D;$   
 $P, \text{blocks1 } 0 \text{ (Class } D \# Ts) \text{ body, } h \vdash (e, \text{xs}) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{None});$   
 $\text{call1 } e = \lfloor (a, M', \text{vs}) \rfloor;$   
 $\text{max-vars } e \leq \text{length xs} \rrbracket$   
 $\Rightarrow \text{bisim1-fr } P \ h \ (e, \text{xs}) \ (\text{stk}, \text{loc}, C, M, \text{pc})$

**declare**  $\text{bisim1-fr.intros}$  [intro]

**declare**  $\text{bisim1-fr.cases}$  [elim]

**lemma**  $\text{bisim1-fr-hext-mono}$ :

$\llbracket \text{bisim1-fr } P \ h \ \text{exs fr}; \text{hext } h \ h' \rrbracket \Rightarrow \text{bisim1-fr } P \ h' \ \text{exs fr}$   
**by**(auto intro:  $\text{bisim1-hext-mono}$ )

**lemma**  $\text{bisim1-max-vars}$ :  $P, E, h \vdash (e, \text{xs}) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{pcp}) \Rightarrow \text{max-vars } E \geq \text{max-vars } e$   
and  $\text{bisims1-max-varss}$ :  $P, Es, h \vdash (es, \text{xs}) [\leftrightarrow] (\text{stk}, \text{loc}, \text{pc}, \text{pcp}) \Rightarrow \text{max-varss } Es \geq \text{max-varss } es$   
**apply**(induct  $E \ (e, \text{xs}) \ (\text{stk}, \text{loc}, \text{pc}, \text{pcp})$  and  $Es \ (es, \text{xs}) \ (\text{stk}, \text{loc}, \text{pc}, \text{pcp})$ )

arbitrary:  $e \ \text{xs} \ \text{stk} \ \text{loc} \ \text{pc} \ \text{pcp}$  and  $es \ \text{xs} \ \text{stk} \ \text{loc} \ \text{pc} \ \text{pcp}$  rule:  $\text{bisim1-bisims1.inducts}$ )  
**apply**(auto)  
**done**

**lemma**  $\text{bisim1-call-}\tau\text{Exec-move}$ :

$\llbracket P, e, h \vdash (e', \text{xs}) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{None}); \text{call1 } e' = \lfloor (a, M', \text{vs}) \rfloor; n + \text{max-vars } e' \leq \text{length xs}; \neg$   
 $\text{contains-insync } e \rrbracket$   
 $\Rightarrow \exists \text{pc}' \ \text{loc}' \ \text{stk}'. \ \tau\text{Exec-mover-a } P \ t \ e \ h \ (\text{stk}, \text{loc}, \text{pc}, \text{None}) \ (\text{rev vs} \ @ \ \text{Addr } a \ \# \ \text{stk}', \text{loc}', \text{pc}',$   
 $\text{None}) \wedge$

$\text{pc}' < \text{length} (\text{compE2 } e) \wedge \text{compE2 } e \ ! \ \text{pc}' = \text{Invoke } M' \ (\text{length vs}) \wedge$

$P, e, h \vdash (e', \text{xs}) \leftrightarrow (\text{rev vs} \ @ \ \text{Addr } a \ \# \ \text{stk}', \text{loc}', \text{pc}', \text{None})$

(is  $\llbracket -; -; - \rrbracket \Rightarrow ?\text{concl } e \ n \ e' \ \text{xs} \ \text{pc} \ \text{stk} \ \text{loc}$ )

and  $\text{bisims1-calls-}\tau\text{Exec-moves}$ :

$\llbracket P, es, h \vdash (es', \text{xs}) [\leftrightarrow] (\text{stk}, \text{loc}, \text{pc}, \text{None}); \text{calls1 } es' = \lfloor (a, M', \text{vs}) \rfloor;$

$n + \text{max-varss } es' \leq \text{length xs}; \neg \text{contains-insyncs } es \rrbracket$

$\Rightarrow \exists \text{pc}' \ \text{stk}' \ \text{loc}'. \ \tau\text{Exec-movesr-a } P \ t \ es \ h \ (\text{stk}, \text{loc}, \text{pc}, \text{None}) \ (\text{rev vs} \ @ \ \text{Addr } a \ \# \ \text{stk}', \text{loc}', \text{pc}',$   
 $\text{None}) \wedge$

$\text{pc}' < \text{length} (\text{compEs2 } es) \wedge \text{compEs2 } es \ ! \ \text{pc}' = \text{Invoke } M' \ (\text{length vs}) \wedge$

$P, es, h \vdash (es', \text{xs}) [\leftrightarrow] (\text{rev vs} \ @ \ \text{Addr } a \ \# \ \text{stk}', \text{loc}', \text{pc}', \text{None})$

(is  $\llbracket -; -; - \rrbracket \Rightarrow ?\text{concls } es \ n \ es' \ \text{xs} \ \text{pc} \ \text{stk} \ \text{loc}$ )

**proof**(induct  $e \ n :: \text{nat } e' \ \text{xs} \ \text{stk} \ \text{loc} \ \text{pc} \ \text{pcp} \equiv \text{None} :: 'addr \text{option}$

and  $es \ n :: \text{nat } es' \ \text{xs} \ \text{stk} \ \text{loc} \ \text{pc} \ \text{pcp} \equiv \text{None} :: 'addr \text{option}$

rule:  $\text{bisim1-bisims1-inducts-split}$ )

**case**  $\text{bisim1Val2}$  **thus**  $?case$  **by** auto

**next**

**case**  $\text{bisim1New}$  **thus**  $?case$  **by** auto

**next**

**case**  $\text{bisim1NewArray}$  **thus**  $?case$

```

  by auto (fastforce intro: bisim1-bisims1.bisim1NewArray elim!: NewArray- $\tau$ ExecrI intro!: exI)
next
  case bisim1Cast thus ?case
    by(auto)(fastforce intro: bisim1-bisims1.bisim1Cast elim!: Cast- $\tau$ ExecrI intro!: exI)+
next
  case bisim1InstanceOf thus ?case
    by(auto)(fastforce intro: bisim1-bisims1.bisim1InstanceOf elim!: InstanceOf- $\tau$ ExecrI intro!: exI)+
next
  case bisim1Val thus ?case by auto
next
  case bisim1Var thus ?case by auto
next
  case (bisim1BinOp1 e1 n e' xs stk loc pc e2 bop)
    note IH1 =  $\langle \llbracket \text{call1 } e' = \lfloor (a, M', vs) \rfloor; n + \text{max-vars } e' \leq \text{length } xs; \neg \text{contains-insync } e1 \rrbracket \implies$ 
 $\text{?concl } e1 \ n \ e' \ xs \ pc \ stk \ loc \rangle$ 
    note IH2 =  $\langle \bigwedge xs. \llbracket \text{call1 } e2 = \lfloor (a, M', vs) \rfloor; n + \text{max-vars } e2 \leq \text{length } xs; \neg \text{contains-insync } e2 \rrbracket$ 
 $\implies \text{?concl } e2 \ n \ e2 \ xs \ 0 \ \llbracket xs \rrbracket \rangle$ 
    note call =  $\langle \text{call1 } (e' \llbracket \text{bop} \rrbracket e2) = \lfloor (a, M', vs) \rfloor \rangle$ 
    note len =  $\langle n + \text{max-vars } (e' \llbracket \text{bop} \rrbracket e2) \leq \text{length } xs \rangle$ 
    note bisim1 =  $\langle P, e1, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, \text{None}) \rangle$ 
    note cs =  $\langle \neg \text{contains-insync } (e1 \llbracket \text{bop} \rrbracket e2) \rangle$ 
    show ?case
    proof(cases is-val e')
      case True
        then obtain v where [simp]:  $e' = \text{Val } v$  by auto
        from bisim1 have  $\tau \text{Exec-mover-a } P \ t \ e1 \ h \ (stk, loc, pc, \text{None}) \ ([v], loc, \text{length } (\text{compE2 } e1), \text{None})$ 
        and [simp]:  $xs = loc$  by(auto dest!: bisim1Val2D1)
        hence  $\tau \text{Exec-mover-a } P \ t \ (e1 \llbracket \text{bop} \rrbracket e2) \ h \ (stk, loc, pc, \text{None}) \ ([v], loc, \text{length } (\text{compE2 } e1), \text{None})$ 
        by-(rule BinOp- $\tau$ ExecrI1)
        also from call IH2[of loc] len cs obtain  $pc' \ stk' \ loc'$ 
        where exec:  $\tau \text{Exec-mover-a } P \ t \ e2 \ h \ (\llbracket, xs, 0, \text{None} \rrbracket) \ (\text{rev } vs \ @ \ \text{Addr } a \ \# \ stk', loc', pc', \text{None})$ 
        and ins:  $\text{compE2 } e2 \ ! \ pc' = \text{Invoke } M' \ (\text{length } vs) \ pc' < \text{length } (\text{compE2 } e2)$ 
        and bisim':  $P, e2, h \vdash (e2, xs) \leftrightarrow (\text{rev } vs \ @ \ \text{Addr } a \ \# \ stk', loc', pc', \text{None})$  by auto
        from BinOp- $\tau$ ExecrI2[OF exec, of e1 bop v]
        have  $\tau \text{Exec-mover-a } P \ t \ (e1 \llbracket \text{bop} \rrbracket e2) \ h \ ([v], loc, \text{length } (\text{compE2 } e1), \text{None}) \ (\text{rev } vs \ @ \ \text{Addr } a \ \#$ 
 $(stk' \ @ \ [v]), loc', \text{length } (\text{compE2 } e1) + pc', \text{None})$  by simp
        also (rtranclp-trans) from bisim'
        have  $P, e1 \llbracket \text{bop} \rrbracket e2, h \vdash (\text{Val } v \llbracket \text{bop} \rrbracket e2, xs) \leftrightarrow ((\text{rev } vs \ @ \ \text{Addr } a \ \# \ stk') \ @ \ [v], loc', \text{length}$ 
 $(\text{compE2 } e1) + pc', \text{None})$ 
        by(rule bisim1BinOp2)
        ultimately show ?thesis using ins by fastforce
      next
        case False with IH1 len False call cs show ?thesis
        by(clarsimp)(fastforce intro: bisim1-bisims1.bisim1BinOp1 elim!: BinOp- $\tau$ ExecrI1 intro!: exI)
    qed
next
  case (bisim1BinOp2 e2 n e' xs stk loc pc e1 bop v1)
    then obtain  $pc' \ loc' \ stk'$  where  $pc': pc' < \text{length } (\text{compE2 } e2) \ \text{compE2 } e2 \ ! \ pc' = \text{Invoke } M' \ (\text{length}$ 
 $vs)$ 
    and exec:  $\tau \text{Exec-mover-a } P \ t \ e2 \ h \ (stk, loc, pc, \text{None}) \ (\text{rev } vs \ @ \ \text{Addr } a \ \# \ stk', loc', pc', \text{None})$ 
    and bisim':  $P, e2, h \vdash (e', xs) \leftrightarrow (\text{rev } vs \ @ \ \text{Addr } a \ \# \ stk', loc', pc', \text{None})$  by fastforce
    from exec have  $\tau \text{Exec-mover-a } P \ t \ (e1 \llbracket \text{bop} \rrbracket e2) \ h \ (stk \ @ \ [v1], loc, \text{length } (\text{compE2 } e1) + pc,$ 
 $\text{None})$ 
 $((\text{rev } vs \ @ \ \text{Addr } a \ \# \ stk') \ @ \ [v1], loc', \text{length } (\text{compE2 } e1) + pc',$ 

```

None)  
 by(rule BinOp- $\tau$ ExecrI2)  
 moreover from *bisim'*  
 have  $P, e1 \llbracket \text{bop} \rrbracket e2, h \vdash (\text{Val } v1 \llbracket \text{bop} \rrbracket e', xs) \leftrightarrow ((\text{rev } vs @ \text{Addr } a \# \text{stk}') @ [v1], \text{loc}', \text{length}(\text{compE2 } e1) + \text{pc}', \text{None})$   
 by(rule *bisim1-bisims1.bisim1BinOp2*)  
 ultimately show ?case using *pc'* by(fastforce)  
 next  
 case *bisim1LAss1* thus ?case  
 by(auto)(fastforce intro: *bisim1-bisims1.bisim1LAss1 elim!*: *LAss- $\tau$ ExecrI* intro!: *exI*)  
 next  
 case *bisim1LAss2* thus ?case by *simp*  
 next  
 case (*bisim1AAcc1 A n a' xs stk loc pc i*)  
 note  $IH1 = \langle \llbracket \text{call1 } a' = \lfloor (a, M', vs) \rfloor; n + \text{max-vars } a' \leq \text{length } xs; \neg \text{contains-insync } A \rrbracket \Rightarrow ?\text{concl } A \text{ n } a' \text{ xs } \text{pc } \text{stk } \text{loc} \rangle$   
 note  $IH2 = \langle \bigwedge xs. \llbracket \text{call1 } i = \lfloor (a, M', vs) \rfloor; n + \text{max-vars } i \leq \text{length } xs; \neg \text{contains-insync } i \rrbracket \Rightarrow ?\text{concl } i \text{ n } i \text{ xs } 0 \sqcap xs \rangle$   
 note  $\text{call} = \langle \text{call1 } (a'[i]) = \lfloor (a, M', vs) \rfloor \rangle$   
 note  $\text{len} = \langle n + \text{max-vars } (a'[i]) \leq \text{length } xs \rangle$   
 note  $\text{bisim1} = \langle P, A, h \vdash (a', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{None}) \rangle$   
 note  $\text{cs} = \langle \neg \text{contains-insync } (A[i]) \rangle$   
 show ?case  
 proof(cases *is-val a'*)  
 case *True*  
 then obtain *v* where [*simp*]:  $a' = \text{Val } v$  by *auto*  
 from *bisim1* have  $\tau \text{Exec-mover-a } P \text{ t } A \text{ h } (\text{stk}, \text{loc}, \text{pc}, \text{None}) ([v], \text{loc}, \text{length}(\text{compE2 } A), \text{None})$   
 and [*simp*]:  $xs = \text{loc}$  by(auto *dest!*: *bisim1Val2D1*)  
 hence  $\tau \text{Exec-mover-a } P \text{ t } (A[i]) \text{ h } (\text{stk}, \text{loc}, \text{pc}, \text{None}) ([v], \text{loc}, \text{length}(\text{compE2 } A), \text{None})$   
 by-(rule *AAcc- $\tau$ ExecrI1*)  
 also from *call IH2[of loc] len cs* obtain  $\text{pc}' \text{stk}' \text{loc}'$   
 where *exec*:  $\tau \text{Exec-mover-a } P \text{ t } i \text{ h } ([], xs, 0, \text{None}) (\text{rev } vs @ \text{Addr } a \# \text{stk}', \text{loc}', \text{pc}', \text{None})$   
 and *ins*:  $\text{compE2 } i ! \text{pc}' = \text{Invoke } M' (\text{length } vs) \text{pc}' < \text{length}(\text{compE2 } i)$   
 and *bisim'*:  $P, i, h \vdash (i, xs) \leftrightarrow (\text{rev } vs @ \text{Addr } a \# \text{stk}', \text{loc}', \text{pc}', \text{None})$  by *auto*  
 from *AAcc- $\tau$ ExecrI2[OF exec, of A v]*  
 have  $\tau \text{Exec-mover-a } P \text{ t } (A[i]) \text{ h } ([v], \text{loc}, \text{length}(\text{compE2 } A), \text{None}) (\text{rev } vs @ \text{Addr } a \# (\text{stk}' @ [v]), \text{loc}', \text{length}(\text{compE2 } A) + \text{pc}', \text{None})$  by *simp*  
 also (rtranclp-trans) from *bisim'*  
 have  $P, A[i], h \vdash (\text{Val } v[i], xs) \leftrightarrow ((\text{rev } vs @ \text{Addr } a \# \text{stk}') @ [v], \text{loc}', \text{length}(\text{compE2 } A) + \text{pc}', \text{None})$   
 by(rule *bisim1AAcc2*)  
 ultimately show ?thesis using *ins* by *fastforce*  
 next  
 case *False* with *IH1 len False call cs* show ?thesis  
 by(*clarsimp*)(fastforce intro: *bisim1-bisims1.bisim1AAcc1 elim!*: *AAcc- $\tau$ ExecrI1* intro!: *exI*)  
 qed  
 next  
 case (*bisim1AAcc2 i n i' xs stk loc pc A v*)  
 then obtain  $\text{pc}' \text{loc}' \text{stk}'$  where  $\text{pc}': \text{pc}' < \text{length}(\text{compE2 } i) \text{compE2 } i ! \text{pc}' = \text{Invoke } M' (\text{length } vs)$   
 and *exec*:  $\tau \text{Exec-mover-a } P \text{ t } i \text{ h } (\text{stk}, \text{loc}, \text{pc}, \text{None}) (\text{rev } vs @ \text{Addr } a \# \text{stk}', \text{loc}', \text{pc}', \text{None})$   
 and *bisim'*:  $P, i, h \vdash (i', xs) \leftrightarrow (\text{rev } vs @ \text{Addr } a \# \text{stk}', \text{loc}', \text{pc}', \text{None})$  by *fastforce*  
 from *exec* have  $\tau \text{Exec-mover-a } P \text{ t } (A[i]) \text{ h } (\text{stk} @ [v], \text{loc}, \text{length}(\text{compE2 } A) + \text{pc}, \text{None})$   
 $((\text{rev } vs @ \text{Addr } a \# \text{stk}') @ [v], \text{loc}', \text{length}(\text{compE2 } A) + \text{pc}', \text{None})$

```

    by(rule AAcc-τExecrI2)
    moreover from bisim'
    have P,A[i],h ⊢ (Val v[i], xs) ↔ ((rev vs @ Addr a # stk') @ [v], loc', length (compE2 A) + pc',
    None)
    by(rule bisim1-bisims1.bisim1AAcc2)
    ultimately show ?case using pc' by(fastforce)
next
  case (bisim1AAss1 A n a' xs stk loc pc i e)
  note IH1 = ⟨[call1 a' = [(a, M', vs)]; n + max-vars a' ≤ length xs; ¬ contains-insync A] ⟩ ⇒
  ?concl A n a' xs pc stk loc⟩
  note IH2 = ⟨[call1 i = [(a, M', vs)]; n + max-vars i ≤ length xs; ¬ contains-insync i] ⟩ ⇒
  ?concl i n i xs 0 [] xs⟩
  note IH3 = ⟨[call1 e = [(a, M', vs)]; n + max-vars e ≤ length xs; ¬ contains-insync e] ⟩ ⇒
  ?concl e n e xs 0 [] xs⟩
  note call = ⟨call1 (a'[i] := e) = [(a, M', vs)]⟩
  note len = ⟨n + max-vars (a'[i] := e) ≤ length xs⟩
  note bisim1 = ⟨P,A,h ⊢ (a', xs) ↔ (stk, loc, pc, None)⟩
  note bisim2 = ⟨P,i,h ⊢ (i, loc) ↔ ([], loc, 0, None)⟩
  note cs = ⟨¬ contains-insync (A[i] := e)⟩
  show ?case
  proof(cases is-val a')
    case True
    then obtain v where [simp]: a' = Val v by auto
    from bisim1 have τExec-mover-a P t A h (stk, loc, pc, None) ([v], loc, length (compE2 A), None)
    and [simp]: xs = loc by(auto dest!: bisim1Val2D1)
    hence exec: τExec-mover-a P t (A[i] := e) h (stk, loc, pc, None) ([v], loc, length (compE2 A),
    None)
    by-(rule AAss-τExecrI1)
    show ?thesis
    proof(cases is-val i)
      case True
      then obtain v' where [simp]: i = Val v' by auto
      note exec also from bisim2
      have τExec-mover-a P t i h ([], loc, 0, None) ([v'], loc, length (compE2 i), None)
      by(auto dest!: bisim1Val2D1)
      from AAss-τExecrI2[OF this, of A e v]
      have τExec-mover-a P t (A[i] := e) h ([v], loc, length (compE2 A), None) ([v', v], loc, length
      (compE2 A) + length (compE2 i), None) by simp
      also (rtranclp-trans) from call IH3[of loc] len cs obtain pc' stk' loc'
      where exec: τExec-mover-a P t e h ([], loc, 0, None) (rev vs @ Addr a # stk', loc', pc', None)
      and ins: compE2 e ! pc' = Invoke M' (length vs) pc' < length (compE2 e)
      and bisim': P,e,h ⊢ (e, loc) ↔ (rev vs @ Addr a # stk', loc', pc', None) by auto
      from AAss-τExecrI3[OF exec, of A i v' v]
      have τExec-mover-a P t (A[i] := e) h ([v', v], loc, length (compE2 A) + length (compE2 i),
      None)
      ((rev vs @ Addr a # stk') @ [v', v], loc', length (compE2 A) + length (compE2 i)
      + pc', None) by simp
      also (rtranclp-trans) from bisim'
      have P,A[i] := e,h ⊢ (Val v[Val v'] := e, xs) ↔ ((rev vs @ Addr a # stk') @ [v', v], loc', length
      (compE2 A) + length (compE2 i) + pc', None)
      by -(rule bisim1AAss3, simp)
      ultimately show ?thesis using ins by fastforce
    next
      case False

```

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note exec also from False call IH2[of loc] len cs obtain pc' stk' loc'
  where exec:  $\tau\text{Exec-mover-a } P \ t \ i \ h \ (\llbracket \cdot \rrbracket, xs, 0, \text{None}) \ (\text{rev } vs \ @ \ \text{Addr } a \ \# \ stk', loc', pc', \text{None})$ 
  and ins:  $\text{compE2 } i \ ! \ pc' = \text{Invoke } M' \ (\text{length } vs) \ pc' < \text{length } (\text{compE2 } i)$ 
  and bisim':  $P, i, h \vdash (i, xs) \leftrightarrow (\text{rev } vs \ @ \ \text{Addr } a \ \# \ stk', loc', pc', \text{None})$  by auto
from AAss- $\tau$ ExecrI2[OF exec, of A e v]
  have  $\tau\text{Exec-mover-a } P \ t \ (A[i] := e) \ h \ ([v], loc, \text{length } (\text{compE2 } A), \text{None}) \ (\text{rev } vs \ @ \ \text{Addr } a \ \# \ (stk' \ @ \ [v]), loc', \text{length } (\text{compE2 } A) + pc', \text{None})$  by simp
  also (rtrancpl-trans) from bisim'
  have  $P, A[i] := e, h \vdash (\text{Val } v[i] := e, xs) \leftrightarrow ((\text{rev } vs \ @ \ \text{Addr } a \ \# \ stk') \ @ \ [v], loc', \text{length } (\text{compE2 } A) + pc', \text{None})$ 
  by(rule bisim1AAss2)
  ultimately show ?thesis using ins False by(fastforce intro!: exI)
qed
next
  case False with IH1 len False call cs show ?thesis
  by(clarsimp)(fastforce intro: bisim1-bisims1.bisim1AAss1 elim!: AAss- $\tau$ ExecrI1 intro!: exI)
qed
next
  case (bisim1AAss2 i n i' xs stk loc pc A e v)
  note IH2 =  $\langle \llbracket \text{call1 } i' = [(a, M', vs)] ; n + \text{max-vars } i' \leq \text{length } xs ; \neg \text{contains-insync } i \rrbracket \implies ?\text{concl } i \ n \ i' \ xs \ pc \ stk \ loc \rangle$ 
  note IH3 =  $\langle \bigwedge xs. \llbracket \text{call1 } e = [(a, M', vs)] ; n + \text{max-vars } e \leq \text{length } xs ; \neg \text{contains-insync } e \rrbracket \implies ?\text{concl } e \ n \ e \ xs \ 0 \ \llbracket \cdot \rrbracket \ xs \rangle$ 
  note call =  $\langle \text{call1 } (\text{Val } v[i'] := e) = [(a, M', vs)] \rangle$ 
  note len =  $\langle n + \text{max-vars } (\text{Val } v[i'] := e) \leq \text{length } xs \rangle$ 
  note bisim2 =  $\langle P, i, h \vdash (i', xs) \leftrightarrow (stk, loc, pc, \text{None}) \rangle$ 
  note cs =  $\langle \neg \text{contains-insync } (A[i] := e) \rangle$ 
  show ?case
  proof(cases is-val i')
    case True
    then obtain v' where [simp]: i' = Val v' by auto
    from bisim2 have exec:  $\tau\text{Exec-mover-a } P \ t \ i \ h \ (stk, loc, pc, \text{None}) \ ([v'], loc, \text{length } (\text{compE2 } i), \text{None})$ 
    and [simp]: xs = loc by(auto dest!: bisim1Val2D1)
    from AAss- $\tau$ ExecrI2[OF exec, of A e v]
    have  $\tau\text{Exec-mover-a } P \ t \ (A[i] := e) \ h \ (stk \ @ \ [v], loc, \text{length } (\text{compE2 } A) + pc, \text{None}) \ ([v', v], loc, \text{length } (\text{compE2 } A) + \text{length } (\text{compE2 } i), \text{None})$  by simp
    also from call IH3[of loc] len cs obtain pc' stk' loc'
    where exec:  $\tau\text{Exec-mover-a } P \ t \ e \ h \ (\llbracket \cdot \rrbracket, xs, 0, \text{None}) \ (\text{rev } vs \ @ \ \text{Addr } a \ \# \ stk', loc', pc', \text{None})$ 
    and ins:  $\text{compE2 } e \ ! \ pc' = \text{Invoke } M' \ (\text{length } vs) \ pc' < \text{length } (\text{compE2 } e)$ 
    and bisim':  $P, e, h \vdash (e, xs) \leftrightarrow (\text{rev } vs \ @ \ \text{Addr } a \ \# \ stk', loc', pc', \text{None})$  by auto
    from AAss- $\tau$ ExecrI3[OF exec, of A i v' v]
    have  $\tau\text{Exec-mover-a } P \ t \ (A[i] := e) \ h \ ([v', v], loc, \text{length } (\text{compE2 } A) + \text{length } (\text{compE2 } i), \text{None}) \ ((\text{rev } vs \ @ \ \text{Addr } a \ \# \ stk') \ @ \ [v', v], loc', \text{length } (\text{compE2 } A) + \text{length } (\text{compE2 } i) + pc', \text{None})$  by simp
    also (rtrancpl-trans) from bisim'
    have  $P, A[i] := e, h \vdash (\text{Val } v[\text{Val } v'] := e, xs) \leftrightarrow ((\text{rev } vs \ @ \ \text{Addr } a \ \# \ stk') \ @ \ [v', v], loc', \text{length } (\text{compE2 } A) + \text{length } (\text{compE2 } i) + pc', \text{None})$ 
    by(rule bisim1AAss3)
    ultimately show ?thesis using ins by(fastforce intro!: exI)
  next
  case False
  with IH2 len call cs obtain pc' loc' stk'
  where ins:  $pc' < \text{length } (\text{compE2 } i) \ \text{compE2 } i \ ! \ pc' = \text{Invoke } M' \ (\text{length } vs)$ 

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    and exec:  $\tau\text{Exec-mover-a } P \ t \ i \ h \ (stk, loc, pc, None) \ (rev \ vs \ @ \ Addr \ a \ \# \ stk', loc', pc', None)$ 
    and bisim':  $P, i, h \vdash (i', xs) \leftrightarrow (rev \ vs \ @ \ Addr \ a \ \# \ stk', loc', pc', None)$  by fastforce
    from bisim' have  $P, A[i] := e, h \vdash (Val \ v[i'] := e, xs) \leftrightarrow ((rev \ vs \ @ \ Addr \ a \ \# \ stk') \ @ \ [v], loc',$ 
    length (compE2 A) + pc', None)
    by(rule bisim1-bisims1.bisim1AAss2)
    with AAss- $\tau\text{ExecrI2}$ [OF exec, of A e v] ins False show ?thesis by(auto intro!: exI)
  qed
next
  case (bisim1AAss3 e n e' xs stk loc pc A i v v')
  then obtain pc' loc' stk' where pc':  $pc' < \text{length} \ (compE2 \ e) \ compE2 \ e \ ! \ pc' = \text{Invoke } M' \ (\text{length} \ vs)$ 
    and exec:  $\tau\text{Exec-mover-a } P \ t \ e \ h \ (stk, loc, pc, None) \ (rev \ vs \ @ \ Addr \ a \ \# \ stk', loc', pc', None)$ 
    and bisim':  $P, e, h \vdash (e', xs) \leftrightarrow (rev \ vs \ @ \ Addr \ a \ \# \ stk', loc', pc', None)$  by fastforce
    from exec have  $\tau\text{Exec-mover-a } P \ t \ (A[i] := e) \ h \ (stk \ @ \ [v', v], loc, \text{length} \ (compE2 \ A) + \text{length} \ (compE2 \ i) + pc, None)$ 
    ((rev vs @ Addr a # stk') @ [v', v], loc', length (compE2 A) + length (compE2 i) + pc', None)
    by(rule AAss- $\tau\text{ExecrI3}$ )
    moreover from bisim'
    have  $P, A[i] := e, h \vdash (Val \ v \ [Val \ v'] := e', xs) \leftrightarrow ((rev \ vs \ @ \ Addr \ a \ \# \ stk') \ @ \ [v', v], loc', \text{length} \ (compE2 \ A) + \text{length} \ (compE2 \ i) + pc', None)$ 
    by(rule bisim1-bisims1.bisim1AAss3)
    ultimately show ?case using pc' by(fastforce intro!: exI)
next
  case bisim1AAss4 thus ?case by simp
next
  case bisim1ALength thus ?case
    by(auto)(fastforce intro: bisim1-bisims1.bisim1ALength elim!: ALength- $\tau\text{ExecrI}$  intro!: exI)
next
  case bisim1FAcc thus ?case
    by(auto)(fastforce intro: bisim1-bisims1.bisim1FAcc elim!: FAcc- $\tau\text{ExecrI}$  intro!: exI)
next
  case (bisim1FAss1 e n e' xs stk loc pc e2 F D)
  note IH1 =  $\langle \llbracket call1 \ e' = \lfloor (a, M', vs) \rfloor; n + \text{max-vars } e' \leq \text{length } xs; \neg \text{contains-insync } e \rrbracket \implies ?concl \ e \ n \ e' \ xs \ pc \ stk \ loc \rangle$ 
  note IH2 =  $\langle \bigwedge xs. \llbracket call1 \ e2 = \lfloor (a, M', vs) \rfloor; n + \text{max-vars } e2 \leq \text{length } xs; \neg \text{contains-insync } e2 \rrbracket \implies ?concl \ e2 \ n \ e2 \ xs \ 0 \ [] \ xs \rangle$ 
  note call =  $\langle call1 \ (e' \cdot F\{D\} := e2) = \lfloor (a, M', vs) \rfloor \rangle$ 
  note len =  $\langle n + \text{max-vars } (e' \cdot F\{D\} := e2) \leq \text{length } xs \rangle$ 
  note bisim1 =  $\langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, None) \rangle$ 
  note cs =  $\langle \neg \text{contains-insync } (e' \cdot F\{D\} := e2) \rangle$ 
  show ?case
  proof(cases is-val e')
    case True
    then obtain v where [simp]:  $e' = Val \ v$  by auto
    from bisim1 have  $\tau\text{Exec-mover-a } P \ t \ e \ h \ (stk, loc, pc, None) \ ([v], loc, \text{length} \ (compE2 \ e), None)$ 
    and [simp]:  $xs = loc$  by(auto dest!: bisim1Val2D1)
    hence  $\tau\text{Exec-mover-a } P \ t \ (e' \cdot F\{D\} := e2) \ h \ (stk, loc, pc, None) \ ([v], loc, \text{length} \ (compE2 \ e), None)$ 
    by-(rule FAss- $\tau\text{ExecrI1}$ )
    also from call IH2[of loc] len cs obtain pc' stk' loc'
    where exec:  $\tau\text{Exec-mover-a } P \ t \ e2 \ h \ ([], xs, 0, None) \ (rev \ vs \ @ \ Addr \ a \ \# \ stk', loc', pc', None)$ 
    and ins:  $\text{compE2 } e2 \ ! \ pc' = \text{Invoke } M' \ (\text{length } vs) \ pc' < \text{length} \ (compE2 \ e2)$ 
    and bisim':  $P, e2, h \vdash (e2, xs) \leftrightarrow (rev \ vs \ @ \ Addr \ a \ \# \ stk', loc', pc', None)$  by auto
    from FAss- $\tau\text{ExecrI2}$ [OF exec, of e F D v]

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  have  $\tau\text{Exec-mover-a } P \ t \ (e \cdot F\{D\} := e2) \ h \ ([v], \text{loc}, \text{length} \ (\text{compE2 } e), \text{None}) \ (\text{rev } vs \ @ \ \text{Addr } a \ # \ \text{stk}' \ @ \ [v]), \text{loc}', \text{length} \ (\text{compE2 } e) + \text{pc}', \text{None}) \ \text{by } \text{simp}$ 
  also  $(\text{rtrancpl-trans}) \ \text{from } \text{bisim}'$ 
  have  $P, e \cdot F\{D\} := e2, h \vdash (\text{Val } v \cdot F\{D\} := e2, xs) \leftrightarrow ((\text{rev } vs \ @ \ \text{Addr } a \ # \ \text{stk}') \ @ \ [v], \text{loc}', \text{length} \ (\text{compE2 } e) + \text{pc}', \text{None})$ 
  by  $(\text{rule } \text{bisim1FAss2})$ 
  ultimately show  $?thesis$  using  $ins$  by  $\text{fastforce}$ 
next
  case  $\text{False}$  with  $IH1 \ \text{len } \text{False}$  call  $cs$  show  $?thesis$ 
  by  $(\text{clarsimp})(\text{fastforce } \text{intro}: \text{bisim1-bisims1}.\text{bisim1FAss1} \ \text{elim!}: \text{FAss-}\tau\text{ExecrI1} \ \text{intro!}: \text{exI})$ 
qed
next
  case  $(\text{bisim1FAss2 } e2 \ n \ e' \ xs \ \text{stk} \ \text{loc} \ \text{pc} \ e \ F \ D \ v)$ 
  then obtain  $\text{pc}' \ \text{loc}' \ \text{stk}'$  where  $\text{pc}': \text{pc}' < \text{length} \ (\text{compE2 } e2) \ \text{compE2 } e2 \ ! \ \text{pc}' = \text{Invoke } M' \ (\text{length} \ vs)$ 
  and  $\text{exec}: \tau\text{Exec-mover-a } P \ t \ e2 \ h \ (\text{stk}, \text{loc}, \text{pc}, \text{None}) \ (\text{rev } vs \ @ \ \text{Addr } a \ # \ \text{stk}', \text{loc}', \text{pc}', \text{None})$ 
  and  $\text{bisim}': P, e2, h \vdash (e', xs) \leftrightarrow (\text{rev } vs \ @ \ \text{Addr } a \ # \ \text{stk}', \text{loc}', \text{pc}', \text{None})$  by  $\text{fastforce}$ 
  from  $\text{exec}$  have  $\tau\text{Exec-mover-a } P \ t \ (e \cdot F\{D\} := e2) \ h \ (\text{stk} \ @ \ [v], \text{loc}, \text{length} \ (\text{compE2 } e) + \text{pc}, \text{None})$ 
   $((\text{rev } vs \ @ \ \text{Addr } a \ # \ \text{stk}') \ @ \ [v], \text{loc}', \text{length} \ (\text{compE2 } e) + \text{pc}', \text{None})$ 
  by  $(\text{rule } \text{FAss-}\tau\text{ExecrI2})$ 
  moreover from  $\text{bisim}'$ 
  have  $P, e \cdot F\{D\} := e2, h \vdash (\text{Val } v \cdot F\{D\} := e', xs) \leftrightarrow ((\text{rev } vs \ @ \ \text{Addr } a \ # \ \text{stk}') \ @ \ [v], \text{loc}', \text{length} \ (\text{compE2 } e) + \text{pc}', \text{None})$ 
  by  $(\text{rule } \text{bisim1-bisims1}.\text{bisim1FAss2})$ 
  ultimately show  $?case$  using  $\text{pc}'$  by  $(\text{fastforce})$ 
next
  case  $\text{bisim1FAss3}$  thus  $?case$  by  $\text{simp}$ 
next
  case  $(\text{bisim1CAS1 } e1 \ n \ e' \ xs \ \text{stk} \ \text{loc} \ \text{pc} \ e2 \ e3 \ D \ F)$ 
  note  $IH1 = \langle \llbracket \text{call1 } e' = \lfloor (a, M', vs) \rfloor; n + \text{max-vars } e' \leq \text{length } xs; \neg \text{contains-insync } e1 \rrbracket \implies ?\text{concl } e1 \ n \ e' \ xs \ \text{pc} \ \text{stk} \ \text{loc} \rangle$ 
  note  $IH2 = \langle \bigwedge xs. \llbracket \text{call1 } e2 = \lfloor (a, M', vs) \rfloor; n + \text{max-vars } e2 \leq \text{length } xs; \neg \text{contains-insync } e2 \rrbracket \implies ?\text{concl } e2 \ n \ e2 \ xs \ 0 \ \llbracket xs \rrbracket \rangle$ 
  note  $IH3 = \langle \bigwedge xs. \llbracket \text{call1 } e3 = \lfloor (a, M', vs) \rfloor; n + \text{max-vars } e3 \leq \text{length } xs; \neg \text{contains-insync } e3 \rrbracket \implies ?\text{concl } e3 \ n \ e3 \ xs \ 0 \ \llbracket xs \rrbracket \rangle$ 
  note  $\text{call} = \langle \text{call1} - = \lfloor (a, M', vs) \rfloor \rangle$ 
  note  $\text{len} = \langle n + \text{max-vars} - \leq \text{length } xs \rangle$ 
  note  $\text{bisim1} = \langle P, e1, h \vdash (e', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{None}) \rangle$ 
  note  $\text{bisim2} = \langle P, e2, h \vdash (e2, \text{loc}) \leftrightarrow (\llbracket \rrbracket, \text{loc}, 0, \text{None}) \rangle$ 
  note  $cs = \langle \neg \text{contains-insync} \rightarrow \rangle$ 
  show  $?case$ 
  proof  $(\text{cases is-val } e')$ 
  case  $\text{True}$ 
  then obtain  $v$  where  $[\text{simp}]: e' = \text{Val } v$  by  $\text{auto}$ 
  from  $\text{bisim1}$  have  $\tau\text{Exec-mover-a } P \ t \ e1 \ h \ (\text{stk}, \text{loc}, \text{pc}, \text{None}) \ ([v], \text{loc}, \text{length} \ (\text{compE2 } e1), \text{None})$ 
  and  $[\text{simp}]: xs = \text{loc}$  by  $(\text{auto } \text{dest!}: \text{bisim1Val2D1})$ 
  hence  $\text{exec}: \tau\text{Exec-mover-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ h \ (\text{stk}, \text{loc}, \text{pc}, \text{None}) \ ([v], \text{loc}, \text{length} \ (\text{compE2 } e1), \text{None})$ 
  by  $(\text{rule } \text{CAS-}\tau\text{ExecrI1})$ 
  show  $?thesis$ 
  proof  $(\text{cases is-val } e2)$ 
  case  $\text{True}$ 
  then obtain  $v'$  where  $[\text{simp}]: e2 = \text{Val } v'$  by  $\text{auto}$ 
  note  $\text{exec}$  also from  $\text{bisim2}$ 

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have  $\tau\text{Exec-mover-a } P \ t \ e2 \ h \ (\[], \text{loc}, 0, \text{None}) \ ([v], \text{loc}, \text{length} \ (\text{compE2 } e2), \text{None})$ 
  by (auto dest!: bisim1Val2D1)
from CAS- $\tau\text{ExecrI2}$  [OF this, of  $e1 \ D \ F \ e3$ ]
  have  $\tau\text{Exec-mover-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ h \ ([v], \text{loc}, \text{length} \ (\text{compE2 } e1),$ 
     $\text{None}) \ ([v', v], \text{loc}, \text{length} \ (\text{compE2 } e1) + \text{length} \ (\text{compE2 } e2), \text{None})$  by simp
  also (rtrancpl-trans) from call IH3 [of loc] len cs obtain  $pc' \ stk' \ loc'$ 
  where  $\text{exec}: \tau\text{Exec-mover-a } P \ t \ e3 \ h \ (\[], \text{loc}, 0, \text{None}) \ (\text{rev } vs \ @ \ \text{Addr } a \ \# \ stk', \text{loc}', pc', \text{None})$ 
    and  $\text{ins}: \text{compE2 } e3 \ ! \ pc' = \text{Invoke } M' \ (\text{length } vs) \ pc' < \text{length} \ (\text{compE2 } e3)$ 
    and  $\text{bisim}' : P, e3, h \vdash (e3, \text{loc}) \leftrightarrow (\text{rev } vs \ @ \ \text{Addr } a \ \# \ stk', \text{loc}', pc', \text{None})$  by auto
  from CAS- $\tau\text{ExecrI3}$  [OF exec, of  $e1 \ D \ F \ e2 \ v' \ v$ ]
  have  $\tau\text{Exec-mover-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ h \ ([v', v], \text{loc}, \text{length} \ (\text{compE2 } e1)$ 
     $+ \text{length} \ (\text{compE2 } e2), \text{None})$ 
     $((\text{rev } vs \ @ \ \text{Addr } a \ \# \ stk') \ @ \ [v', v], \text{loc}', \text{length} \ (\text{compE2 } e1) + \text{length} \ (\text{compE2 } e2) + pc', \text{None})$  by simp
  also (rtrancpl-trans) from bisim'
  have  $P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), h \vdash (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', e3), xs) \leftrightarrow$ 
     $((\text{rev } vs \ @ \ \text{Addr } a \ \# \ stk') \ @ \ [v', v], \text{loc}', \text{length} \ (\text{compE2 } e1) + \text{length} \ (\text{compE2 } e2) + pc', \text{None})$ 
    by - (rule bisim1CAS3, simp)
  ultimately show ?thesis using ins by fastforce
next
case False
note  $\text{exec}$  also from False call IH2 [of loc] len cs obtain  $pc' \ stk' \ loc'$ 
  where  $\text{exec}: \tau\text{Exec-mover-a } P \ t \ e2 \ h \ (\[], xs, 0, \text{None}) \ (\text{rev } vs \ @ \ \text{Addr } a \ \# \ stk', \text{loc}', pc', \text{None})$ 
    and  $\text{ins}: \text{compE2 } e2 \ ! \ pc' = \text{Invoke } M' \ (\text{length } vs) \ pc' < \text{length} \ (\text{compE2 } e2)$ 
    and  $\text{bisim}' : P, e2, h \vdash (e2, xs) \leftrightarrow (\text{rev } vs \ @ \ \text{Addr } a \ \# \ stk', \text{loc}', pc', \text{None})$  by auto
  from CAS- $\tau\text{ExecrI2}$  [OF exec, of  $e1 \ D \ F \ e3 \ v$ ]
  have  $\tau\text{Exec-mover-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ h \ ([v], \text{loc}, \text{length} \ (\text{compE2 } e1),$ 
     $\text{None}) \ (\text{rev } vs \ @ \ \text{Addr } a \ \# \ (stk' \ @ \ [v]), \text{loc}', \text{length} \ (\text{compE2 } e1) + pc', \text{None})$  by simp
  also (rtrancpl-trans) from bisim'
  have  $P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), h \vdash (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, e2, e3), xs) \leftrightarrow$ 
     $((\text{rev } vs \ @ \ \text{Addr } a \ \# \ stk') \ @ \ [v], \text{loc}', \text{length} \ (\text{compE2 } e1) + pc', \text{None})$ 
    by (rule bisim1CAS2)
  ultimately show ?thesis using ins False by (fastforce intro!: exI)
qed
next
case False with IH1 len False call cs show ?thesis
  by (clarsimp)(fastforce intro: bisim1-bisims1.bisim1CAS1 elim!: CAS- $\tau\text{ExecrI1}$  intro!: exI)
qed
next
case (bisim1CAS2  $e2 \ n \ e2' \ xs \ stk \ loc \ pc \ e1 \ e3 \ D \ F \ v$ )
  note  $\text{IH2} = \langle \llbracket \text{call1 } e2' = \lfloor (a, M', vs) \rfloor; n + \text{max-vars } e2' \leq \text{length } xs; \neg \text{contains-insync } e2 \rrbracket \Rightarrow$ 
     $?concl \ e2 \ n \ e2' \ xs \ pc \ stk \ loc \rangle$ 
  note  $\text{IH3} = \langle \bigwedge xs. \llbracket \text{call1 } e3 = \lfloor (a, M', vs) \rfloor; n + \text{max-vars } e3 \leq \text{length } xs; \neg \text{contains-insync } e3 \rrbracket \Rightarrow$ 
     $?concl \ e3 \ n \ e3 \ xs \ 0 \ [] \ xs \rangle$ 
  note  $\text{call} = \langle \text{call1} - = \lfloor (a, M', vs) \rfloor \rangle$ 
  note  $\text{len} = \langle n + \text{max-vars} - \leq \text{length } xs \rangle$ 
  note  $\text{bisim2} = \langle P, e2, h \vdash (e2', xs) \leftrightarrow (stk, loc, pc, \text{None}) \rangle$ 
  note  $\text{cs} = \langle \neg \text{contains-insync} \rightarrow \rangle$ 
  show ?case
  proof (cases is-val  $e2'$ )
    case True
    then obtain  $v'$  where [simp]:  $e2' = \text{Val } v'$  by auto
    from bisim2 have  $\text{exec}: \tau\text{Exec-mover-a } P \ t \ e2 \ h \ (stk, \text{loc}, pc, \text{None}) \ ([v], \text{loc}, \text{length} \ (\text{compE2 } e2),$ 
       $\text{None})$ 

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    and [simp]:  $xs = loc$  by(auto dest!: bisim1Val2D1)
  from CAS-ExecrI2[OF exec, of e1 D F e3 v]
  have  $\tau Exec\text{-}mover\text{-}a\ P\ t\ (e1 \cdot compareAndSwap(D \cdot F, e2, e3))\ h\ (stk\ @\ [v], loc, length\ (compE2\ e1)$ 
+  $pc, None)\ ([v', v], loc, length\ (compE2\ e1) + length\ (compE2\ e2), None)$  by simp
  also from call IH3[of loc] len cs obtain  $pc'\ stk'\ loc'$ 
    where exec:  $\tau Exec\text{-}mover\text{-}a\ P\ t\ e3\ h\ ([], xs, 0, None)\ (rev\ vs\ @\ Addr\ a\ \# stk', loc', pc', None)$ 
    and ins:  $compE2\ e3\ !\ pc' = Invoke\ M'\ (length\ vs)\ pc' < length\ (compE2\ e3)$ 
    and bisim':  $P, e3, h \vdash (e3, xs) \leftrightarrow (rev\ vs\ @\ Addr\ a\ \# stk', loc', pc', None)$  by auto
  from CAS-ExecrI3[OF exec, of e1 D F e2 v' v]
  have  $\tau Exec\text{-}mover\text{-}a\ P\ t\ (e1 \cdot compareAndSwap(D \cdot F, e2, e3))\ h\ ([v', v], loc, length\ (compE2\ e1)$ 
+  $length\ (compE2\ e2), None)$ 
     $((rev\ vs\ @\ Addr\ a\ \# stk')\ @\ [v', v], loc', length\ (compE2\ e1) + length\ (compE2\ e2)$ 
+  $pc', None)$  by simp
  also (rtranclp-trans) from bisim'
  have  $P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h \vdash (Val\ v \cdot compareAndSwap(D \cdot F, Val\ v', e3), xs) \leftrightarrow$ 
 $((rev\ vs\ @\ Addr\ a\ \# stk')\ @\ [v', v], loc', length\ (compE2\ e1) + length\ (compE2\ e2) + pc', None)$ 
    by(rule bisim1CAS3)
  ultimately show ?thesis using ins by(fastforce intro!: exI)
next
case False
with IH2 len call cs obtain  $pc'\ loc'\ stk'$ 
  where ins:  $pc' < length\ (compE2\ e2)\ compE2\ e2\ !\ pc' = Invoke\ M'\ (length\ vs)$ 
  and exec:  $\tau Exec\text{-}mover\text{-}a\ P\ t\ e2\ h\ (stk, loc, pc, None)\ (rev\ vs\ @\ Addr\ a\ \# stk', loc', pc', None)$ 
  and bisim':  $P, e2, h \vdash (e2', xs) \leftrightarrow (rev\ vs\ @\ Addr\ a\ \# stk', loc', pc', None)$  by fastforce
  from bisim' have  $P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h \vdash (Val\ v \cdot compareAndSwap(D \cdot F, e2', e3),$ 
 $xs) \leftrightarrow ((rev\ vs\ @\ Addr\ a\ \# stk')\ @\ [v], loc', length\ (compE2\ e1) + pc', None)$ 
    by(rule bisim1-bisims1.bisim1CAS2)
  with CAS-ExecrI2[OF exec, of e1 D F e3 v] ins False show ?thesis by(auto intro!: exI)
qed
next
case (bisim1CAS3 e3 n e3' xs stk loc pc e1 e2 D F v v')
  then obtain  $pc'\ loc'\ stk'$  where  $pc': pc' < length\ (compE2\ e3)\ compE2\ e3\ !\ pc' = Invoke\ M'\ (length\$ 
 $vs)$ 
    and exec:  $\tau Exec\text{-}mover\text{-}a\ P\ t\ e3\ h\ (stk, loc, pc, None)\ (rev\ vs\ @\ Addr\ a\ \# stk', loc', pc', None)$ 
    and bisim':  $P, e3, h \vdash (e3', xs) \leftrightarrow (rev\ vs\ @\ Addr\ a\ \# stk', loc', pc', None)$  by fastforce
  from exec have  $\tau Exec\text{-}mover\text{-}a\ P\ t\ (e1 \cdot compareAndSwap(D \cdot F, e2, e3))\ h\ (stk\ @\ [v', v], loc, length\$ 
 $(compE2\ e1) + length\ (compE2\ e2) + pc, None)$ 
     $((rev\ vs\ @\ Addr\ a\ \# stk')\ @\ [v', v], loc', length\ (compE2\ e1) + length\$ 
 $(compE2\ e2) + pc', None)$ 
    by(rule CAS-ExecrI3)
  moreover from bisim'
  have  $P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h \vdash (Val\ v \cdot compareAndSwap(D \cdot F, Val\ v', e3'), xs) \leftrightarrow$ 
 $((rev\ vs\ @\ Addr\ a\ \# stk')\ @\ [v', v], loc', length\ (compE2\ e1) + length\ (compE2\ e2) + pc', None)$ 
    by(rule bisim1-bisims1.bisim1CAS3)
  ultimately show ?case using  $pc'$  by(fastforce intro!: exI)
next
case (bisim1Call1 obj n obj' xs stk loc pc ps M)
  note IH1 =  $\langle \llbracket call1\ obj' = \lfloor (a, M', vs) \rfloor; n + max\text{-}vars\ obj' \leq length\ xs; \neg\ contains\text{-}insync\ obj \rrbracket \implies$ 
 $?concl\ obj\ n\ obj'\ xs\ pc\ stk\ loc \rangle$ 
  note IH2 =  $\langle \bigwedge xs. \llbracket calls1\ ps = \lfloor (a, M', vs) \rfloor; n + max\text{-}varss\ ps \leq length\ xs; \neg\ contains\text{-}insyncs\ ps \rrbracket$ 
 $\implies ?concls\ ps\ n\ ps\ xs\ 0\ \square\ xs \rangle$ 
  note len =  $\langle n + max\text{-}vars\ (obj' \cdot M(ps)) \leq length\ xs \rangle$ 
  note bisim1 =  $\langle P, obj, h \vdash (obj', xs) \leftrightarrow (stk, loc, pc, None) \rangle$ 
  note call =  $\langle call1\ (obj' \cdot M(ps)) = \lfloor (a, M', vs) \rfloor \rangle$ 

```

```

note  $cs = \langle \neg \text{contains-insync } (obj \cdot M(ps)) \rangle$ 
from call show ?case
proof(cases rule: call1-callE)
  case CallObj
    hence  $\neg \text{is-val } obj' \text{ by } auto$ 
    with CallObj IH1 len cs show ?thesis
      by(clarsimp)(fastforce intro: bisim1-bisims1.bisim1Call1 elim!: Call-ExecrI1 intro!: exI)
  next
    case (CallParams v)
      with bisim1 have  $\tau Exec\text{-mover-}a \ P \ t \ obj \ h \ (stk, loc, pc, None) \ ([v], loc, length \ (compE2 \ obj),$ 
None)
        and [simp]:  $xs = loc \text{ by } (auto \ dest! : bisim1Val2D1)$ 
        hence  $\tau Exec\text{-mover-}a \ P \ t \ (obj \cdot M(ps)) \ h \ (stk, loc, pc, None) \ ([v], loc, length \ (compE2 \ obj), None)$ 
        by-(rule Call-ExecrI1)
      also from IH2[of loc] CallParams len cs obtain  $pc' \ stk' \ loc'$ 
        where exec:  $\tau Exec\text{-movesr-}a \ P \ t \ ps \ h \ ([], loc, 0, None) \ (rev \ vs \ @ \ Addr \ a \ \# \ stk', loc', pc', None)$ 
        and ins:  $compEs2 \ ps \ ! \ pc' = Invoke \ M' \ (length \ vs) \ pc' < length \ (compEs2 \ ps)$ 
        and bisim':  $P, ps, h \vdash (ps, xs) \ [\leftrightarrow] \ (rev \ vs \ @ \ Addr \ a \ \# \ stk', loc', pc', None) \text{ by } auto$ 
        from Call-ExecrI2[OF exec, of obj M v]
        have  $\tau Exec\text{-mover-}a \ P \ t \ (obj \cdot M(ps)) \ h \ ([v], loc, length \ (compE2 \ obj), None) \ (rev \ vs \ @ \ Addr \ a \ \#$ 
 $(stk' \ @ \ [v]), loc', length \ (compE2 \ obj) + pc', None) \text{ by } simp$ 
        also (rtranclp-trans)
        have  $P, obj \cdot M(ps), h \vdash (Val \ v \cdot M(ps), xs) \leftrightarrow ((rev \ vs \ @ \ Addr \ a \ \# \ stk') \ @ \ [v], loc', length \ (compE2$ 
 $obj) + pc', None)$ 
        using bisim' by(rule bisim1CallParams)
        ultimately show ?thesis using ins CallParams by fastforce
      next
        case [simp]: Call
          from bisim1 have  $\tau Exec\text{-mover-}a \ P \ t \ obj \ h \ (stk, loc, pc, None) \ ([Addr \ a], loc, length \ (compE2$ 
 $obj), None)$ 
          and [simp]:  $xs = loc \text{ by } (auto \ dest! : bisim1Val2D1)$ 
          hence  $\tau Exec\text{-mover-}a \ P \ t \ (obj \cdot M(ps)) \ h \ (stk, loc, pc, None) \ ([Addr \ a], loc, length \ (compE2 \ obj),$ 
 $None)$ 
          by-(rule Call-ExecrI1)
          also have  $\tau Exec\text{-movesr-}a \ P \ t \ ps \ h \ ([], xs, 0, None) \ (rev \ vs, xs, length \ (compEs2 \ ps), None)$ 
          proof(cases vs)
            case Nil with Call show ?thesis by(auto)
          next
            case Cons with Call bisims1-Val-Exec-moves[OF bisims1-refl[of P h map Val vs loc]]
            show ?thesis by(auto simp add: bsoks-def)
          qed
          from Call-ExecrI2[OF this, of obj M Addr a]
          have  $\tau Exec\text{-mover-}a \ P \ t \ (obj \cdot M(ps)) \ h \ ([Addr \ a], loc, length \ (compE2 \ obj), None) \ (rev \ vs \ @ \ [Addr$ 
 $a], xs, length \ (compE2 \ obj) + length \ (compEs2 \ ps), None) \text{ by } simp$ 
          also (rtranclp-trans)
          have  $P, ps, h \vdash (map \ Val \ vs, xs) \ [\leftrightarrow] \ (rev \ vs, xs, length \ (compEs2 \ ps), None)$ 
          by(rule bisims1-map-Val-append[OF bisims1Nil, simplified])(simp-all add: bsoks-def)
          hence  $P, obj \cdot M(ps), h \vdash (addr \ a \cdot M(map \ Val \ vs), xs) \leftrightarrow (rev \ vs \ @ \ [Addr \ a], xs, length \ (compE2 \ obj)$ 
 $+ length \ (compEs2 \ ps), None)$ 
          by(rule bisim1CallParams)
          ultimately show ?thesis by fastforce
        qed
      next
        case (bisim1CallParams ps n ps' xs stk loc pc obj M v)

```

```

note IH2 =  $\langle \llbracket \text{calls1 } ps' = \lfloor (a, M', vs) \rfloor; n + \text{max-varss } ps' \leq \text{length } xs; \neg \text{contains-insyncs } ps \rrbracket \implies$ 
 $\text{?concls } ps \ n \ ps' \ xs \ pc \ stk \ loc \rangle$ 
note bisim2 =  $\langle P, ps, h \vdash (ps', xs) [\leftrightarrow] (stk, loc, pc, None) \rangle$ 
note call =  $\langle \text{call1 } (Val \ v \cdot M(ps')) = \lfloor (a, M', vs) \rfloor \rangle$ 
note len =  $\langle n + \text{max-vars } (Val \ v \cdot M(ps')) \leq \text{length } xs \rangle$ 
note cs =  $\langle \neg \text{contains-insync } (obj \cdot M(ps)) \rangle$ 
from call show ?case
proof(cases rule: call1-callE)
  case CallObj thus ?thesis by simp
next
  case (CallParams v')
  with IH2 len cs obtain pc' stk' loc'
    where exec:  $\tau \text{Exec-movesr-a } P \ t \ ps \ h \ (stk, loc, pc, None) \ (rev \ vs \ @ \ \text{Addr } a \ \# \ stk', loc', pc',$ 
    None)
    and ins:  $pc' < \text{length } (compEs2 \ ps) \ compEs2 \ ps \ ! \ pc' = \text{Invoke } M' \ (\text{length } vs)$ 
    and bisim':  $P, ps, h \vdash (ps', xs) [\leftrightarrow] (rev \ vs \ @ \ \text{Addr } a \ \# \ stk', loc', pc', None)$  by auto
  from exec have  $\tau \text{Exec-mover-a } P \ t \ (obj \cdot M(ps)) \ h \ (stk \ @ \ [v], loc, \text{length } (compE2 \ obj) + pc, None)$ 
 $((rev \ vs \ @ \ \text{Addr } a \ \# \ stk') \ @ \ [v], loc', \text{length } (compE2 \ obj) + pc', None)$ 
  by(rule Call- $\tau \text{ExecrI2}$ )
  moreover have  $P, obj \cdot M(ps), h \vdash (Val \ v \cdot M(ps'), xs) \leftrightarrow$ 
 $((rev \ vs \ @ \ \text{Addr } a \ \# \ stk') \ @ \ [v], loc', \text{length } (compE2 \ obj) + pc', None)$ 
  using bisim' by(rule bisim1-bisims1.bisim1CallParams)
  ultimately show ?thesis using ins by fastforce
next
  case Call
  hence [simp]:  $v = \text{Addr } a \ ps' = \text{map } Val \ vs \ M' = M$  by simp-all
  have  $xs = loc \wedge \tau \text{Exec-movesr-a } P \ t \ ps \ h \ (stk, loc, pc, None) \ (rev \ vs, loc, \text{length } (compEs2 \ ps),$ 
  None)
  proof(cases  $pc < \text{length } (compEs2 \ ps)$ )
    case True with bisim2 show ?thesis by(auto dest: bisims1-Val- $\tau \text{Exec-moves}$ )
  next
    case False
    from bisim2 have  $pc \leq \text{length } (compEs2 \ ps)$  by(rule bisims1-pc-length-compEs2)
    with False have  $pc = \text{length } (compEs2 \ ps)$  by simp
    with bisim2 show ?thesis by(auto dest: bisims1-Val-length-compEs2D)
  qed
  then obtain [simp]:  $xs = loc$ 
  and exec:  $\tau \text{Exec-movesr-a } P \ t \ ps \ h \ (stk, loc, pc, None) \ (rev \ vs, loc, \text{length } (compEs2 \ ps), None)$ 
..
  from exec have  $\tau \text{Exec-mover-a } P \ t \ (obj \cdot M(ps)) \ h \ (stk \ @ \ [v], loc, \text{length } (compE2 \ obj) + pc, None)$ 
 $((rev \ vs \ @ \ [v], loc, \text{length } (compE2 \ obj) + \text{length } (compEs2 \ ps), None)$ 
  by(rule Call- $\tau \text{ExecrI2}$ )
  moreover from bisim2 have  $\text{len: } \text{length } ps = \text{length } ps'$  by(auto dest: bisims1-lengthD)
  moreover have  $P, ps, h \vdash (\text{map } Val \ vs, xs) [\leftrightarrow] (rev \ vs, xs, \text{length } (compEs2 \ ps), None)$  using len
  by-(rule bisims1-map-Val-append[OF bisims1Nil, simplified], simp-all)
  hence  $P, obj \cdot M(ps), h \vdash (\text{addr } a \cdot M(\text{map } Val \ vs), xs) \leftrightarrow (rev \ vs \ @ \ [\text{Addr } a], xs, \text{length } (compE2 \ obj)$ 
 $+ \text{length } (compEs2 \ ps), None)$  by(rule bisim1-bisims1.bisim1CallParams)
  ultimately show ?thesis by fastforce
qed
next
  case bisim1BlockSome1 thus ?case by simp
next
  case bisim1BlockSome2 thus ?case by simp
next

```

```

case (bisim1BlockSome4 e n e' xs stk loc pc V T v)
then obtain pc' loc' stk' where pc': pc' < length (compE2 e) compE2 e ! pc' = Invoke M' (length
vs)
  and exec:  $\tau \text{Exec-mover-a } P \text{ t e h (stk, loc, pc, None) (rev vs @ Addr a \# stk', loc', pc', None)}$ 
  and bisim':  $P, e, h \vdash (e', xs) \leftrightarrow (\text{rev vs @ Addr a \# stk', loc', pc', None})$  by auto
  note Block- $\tau$ ExecrI-Some[OF exec, of V T v]
  moreover from bisim' have  $P, \{V:T=[v]; e\}, h \vdash (\{V:T=None; e'\}, xs) \leftrightarrow (\text{rev vs @ Addr a \#}$ 
stk', loc', Suc (Suc pc'), None)
  by(rule bisim1-bisims1.bisim1BlockSome4)
  ultimately show ?case using pc' by fastforce
next
case (bisim1BlockNone e n e' xs stk loc pc V T)
then obtain pc' loc' stk' where pc': pc' < length (compE2 e) compE2 e ! pc' = Invoke M' (length
vs)
  and exec:  $\tau \text{Exec-mover-a } P \text{ t e h (stk, loc, pc, None) (rev vs @ Addr a \# stk', loc', pc', None)}$ 
  and bisim':  $P, e, h \vdash (e', xs) \leftrightarrow (\text{rev vs @ Addr a \# stk', loc', pc', None})$  by auto
  note Block- $\tau$ ExecrI-None[OF exec, of V T]
  moreover from bisim' have  $P, \{V:T=None; e\}, h \vdash (\{V:T=None; e'\}, xs) \leftrightarrow (\text{rev vs @ Addr a \#}$ 
stk', loc', pc', None)
  by(rule bisim1-bisims1.bisim1BlockNone)
  ultimately show ?case using pc' by fastforce
next
case bisim1Sync1 thus ?case
  by (auto)(fastforce intro: bisim1-bisims1.bisim1Sync1 elim!: Sync- $\tau$ ExecrI intro!: exI)
next
case bisim1Sync2 thus ?case by simp
next
case bisim1Sync3 thus ?case by simp
next
case bisim1Sync4 thus ?case
  by (auto)(fastforce intro: bisim1-bisims1.bisim1Sync4 elim!: Insync- $\tau$ ExecrI intro!: exI)
next
case bisim1Sync5 thus ?case by simp
next
case bisim1Sync6 thus ?case by simp
next
case bisim1Sync7 thus ?case by simp
next
case bisim1Sync8 thus ?case by simp
next
case bisim1Sync9 thus ?case by simp
next
case bisim1InSync thus ?case by simp
next
case bisim1Seq1 thus ?case
  by (auto)(fastforce intro: bisim1-bisims1.bisim1Seq1 elim!: Seq- $\tau$ ExecrI1 intro!: exI)
next
case (bisim1Seq2 e2 n e' xs stk loc pc e1)
then obtain pc' loc' stk' where pc': pc' < length (compE2 e2) compE2 e2 ! pc' = Invoke M' (length
vs)
  and exec:  $\tau \text{Exec-mover-a } P \text{ t e2 h (stk, loc, pc, None) (rev vs @ Addr a \# stk', loc', pc', None)}$ 
  and bisim':  $P, e2, h \vdash (e', xs) \leftrightarrow (\text{rev vs @ Addr a \# stk', loc', pc', None})$  by auto
from Seq- $\tau$ ExecrI2[OF exec, of e1] pc' bisim'
show ?case by(fastforce intro: bisim1-bisims1.bisim1Seq2 intro!: exI)

```

```

next
  case bisim1Cond1 thus ?case
    by (auto)(fastforce intro: bisim1-bisims1.bisim1Cond1 elim!: Cond-τExecrI1 intro!: exI)+
next
  case (bisim1CondThen e1 n e' xs stk loc pc e e2)
  then obtain pc' loc' stk' where pc': pc' < length (compE2 e1) compE2 e1 ! pc' = Invoke M' (length vs)
    and exec:  $\tau\text{Exec-mover-a } P \ t \ e1 \ h \ (stk, loc, pc, None) \ (rev \ vs \ @ \ Addr \ a \ \# \ stk', loc', pc', None)$ 
    and bisim':  $P, e1, h \vdash (e', xs) \leftrightarrow (rev \ vs \ @ \ Addr \ a \ \# \ stk', loc', pc', None)$  by auto
  from Cond-τExecrI2[OF exec] pc' bisim' show ?case
  by (fastforce intro: bisim1-bisims1.bisim1CondThen intro!: exI)
next
  case (bisim1CondElse e2 n e' xs stk loc pc e e1)
  then obtain pc' loc' stk' where pc': pc' < length (compE2 e2) compE2 e2 ! pc' = Invoke M' (length vs)
    and exec:  $\tau\text{Exec-mover-a } P \ t \ e2 \ h \ (stk, loc, pc, None) \ (rev \ vs \ @ \ Addr \ a \ \# \ stk', loc', pc', None)$ 
    and bisim':  $P, e2, h \vdash (e', xs) \leftrightarrow (rev \ vs \ @ \ Addr \ a \ \# \ stk', loc', pc', None)$  by auto
  from Cond-τExecrI3[OF exec] pc' bisim' show ?case
  by (fastforce intro: bisim1-bisims1.bisim1CondElse intro!: exI)
next
  case bisim1While1 thus ?case by simp
next
  case bisim1While3 thus ?case
    by (auto)(fastforce intro: bisim1-bisims1.bisim1While3 elim!: While-τExecrI1 intro!: exI)+
next
  case bisim1While4 thus ?case
    by (auto)(fastforce intro!: While-τExecrI2 bisim1-bisims1.bisim1While4 exI)+
next
  case bisim1While6 thus ?case by simp
next
  case bisim1While7 thus ?case by simp
next
  case bisim1Throw1 thus ?case
    by (auto)(fastforce intro!: exI bisim1-bisims1.bisim1Throw1 elim!: Throw-τExecrI)+
next
  case bisim1Try thus ?case
    by (auto)(fastforce intro: bisim1-bisims1.bisim1Try elim!: Try-τExecrI1 intro!: exI)+
next
  case (bisim1TryCatch1 e n a' xs stk loc pc C' C e2 V)
  note IH2 =  $\langle \bigwedge xs. \llbracket call1 \ e2 \rrbracket = \llbracket (a, M', vs) \rrbracket; Suc \ n + max\text{-vars} \ e2 \leq length \ xs; \neg \text{contains-insync} \ e2 \rrbracket \implies ?concl \ e2 \ (Suc \ V) \ e2 \ xs \ 0 \ [] \ xs \rangle$ 
  note bisim1 =  $\langle P, e, h \vdash (Throw \ a', xs) \leftrightarrow (stk, loc, pc, \llbracket a' \rrbracket) \rangle$ 
  note bisim2 =  $\langle \bigwedge xs. P, e2, h \vdash (e2, xs) \leftrightarrow ([], xs, 0, None) \rangle$ 
  note len =  $\langle n + max\text{-vars} \ \{V:Class \ C=None; \ e2\} \leq length \ (xs[V := Addr \ a']) \rangle$ 
  note cs =  $\langle \neg \text{contains-insync} \ (try \ e \ catch(C \ V) \ e2) \rangle$ 
  from bisim1 have [simp]: xs = loc by (auto dest: bisim1-ThrowD)
  from len have  $\tau\text{Exec-mover-a } P \ t \ (try \ e \ catch(C \ V) \ e2) \ h \ ([Addr \ a'], loc, Suc \ (length \ (compE2 \ e)), None) \ ([], loc[V := Addr \ a'], Suc \ (Suc \ (length \ (compE2 \ e))), None)$ 
  by  $-(rule \ \tau\text{Execr1step}, auto \ simp \ add: \ exec\text{-move-def} \ intro: \ \tau\text{move2-}\tau\text{moves2.intros} \ exec\text{-instr})$ 
  also from IH2 [of loc[V := Addr a'] len  $\langle call1 \ \{V:Class \ C=None; \ e2\} = \llbracket (a, M', vs) \rrbracket \rangle \ cs$ ]
  obtain pc' loc' stk'
    where exec:  $\tau\text{Exec-mover-a } P \ t \ e2 \ h \ ([], loc[V := Addr \ a'], 0, None) \ (rev \ vs \ @ \ Addr \ a \ \# \ stk', loc', pc', None)$ 
    and ins: pc' < length (compE2 e2) compE2 e2 ! pc' = Invoke M' (length vs)

```

```

    and bisim': P, e2, h ⊢ (e2, loc[V := Addr a']) ↔ (rev vs @ Addr a # stk', loc', pc', None) by auto
  from Try-τExecrI2[OF exec, of e C V]
  have τExec-mover-a P t (try e catch(C V) e2) h ([], loc[V := Addr a'], Suc (Suc (length (compE2 e))), None) (rev vs @ Addr a # stk', loc', Suc (Suc (length (compE2 e) + pc')), None) by simp
  also from bisim'
  have P, try e catch(C V) e2, h ⊢ ({ V:Class C=None; e2 }, loc[V := Addr a']) ↔ (rev vs @ Addr a # stk', loc', (Suc (Suc (length (compE2 e) + pc'))), None)
  by(rule bisim1TryCatch2)
  ultimately show ?case using ins by fastforce
next
case bisim1TryCatch2 thus ?case
  by (auto)(fastforce intro!: Try-τExecrI2 bisim1-bisims1.bisim1TryCatch2 exI)+
next
case bisims1Nil thus ?case by simp
next
case (bisims1List1 e n e' xs stk loc pc es)
  note IH1 = ⟨[call1 e' = [(a, M', vs)]; n + max-vars e' ≤ length xs; ¬ contains-insync e] ⇒ ?concl
  e n e' xs pc stk loc⟩
  note IH2 = ⟨∧xs. [calls1 es = [(a, M', vs)]; n + max-varss es ≤ length xs; ¬ contains-insyncs es]
  ⇒ ?concls es n es xs 0 [] xs⟩
  note bisim1 = ⟨P, e, h ⊢ (e', xs) ↔ (stk, loc, pc, None)⟩
  note call = ⟨calls1 (e' # es) = [(a, M', vs)]⟩
  note len = ⟨n + max-varss (e' # es) ≤ length xs⟩
  note cs = ⟨¬ contains-insyncs (e # es)⟩
  show ?case
  proof(cases is-val e')
  case True
  then obtain v where [simp]: e' = Val v by auto
  with bisim1 have τExec-mover-a P t e h (stk, loc, pc, None) ([v], loc, length (compE2 e), None)
    and [simp]: xs = loc by(auto dest!: bisim1Val2D1)
  hence τExec-movesr-a P t (e # es) h (stk, loc, pc, None) ([v], loc, length (compE2 e), None)
    by-(rule τExec-mover-τExec-movesr)
  also from call IH2[of loc] len cs obtain pc' stk' loc'
    where exec: τExec-movesr-a P t es h ([], xs, 0, None) (rev vs @ Addr a # stk', loc', pc', None)
    and ins: compEs2 es ! pc' = Invoke M' (length vs) pc' < length (compEs2 es)
    and bisim': P, es, h ⊢ (es, xs) [↔] (rev vs @ Addr a # stk', loc', pc', None) by auto
  from append-τExec-movesr[OF - exec, of [v] [e]]
  have τExec-movesr-a P t (e # es) h ([v], loc, length (compE2 e), None) (rev vs @ Addr a # (stk'
  @ [v]), loc', length (compE2 e) + pc', None)
    by simp
  also (rtranclp-trans) from bisim'
  have P, e # es, h ⊢ (Val v # es, xs) [↔]
    ((rev vs @ Addr a # stk') @ [v], loc', length (compE2 e) + pc', None)
    by(rule bisim1-bisims1.bisims1List2)
  ultimately show ?thesis using ins by fastforce
next
case False
  with call IH1 len cs show ?thesis
  by (auto)(fastforce intro!: τExec-mover-τExec-movesr bisim1-bisims1.bisims1List1 exI)+
qed
next
case (bisims1List2 es n es' xs stk loc pc e v)
  then obtain pc' stk' loc' where pc': pc' < length (compEs2 es) compEs2 es ! pc' = Invoke M'
  (length vs)

```



```

and exec:  $\tau \text{Exec-movesr-a } P \text{ } t \text{ } es \text{ } h \text{ } (stk, loc, pc, None) \text{ } (rev \text{ } vs \text{ } @ \text{ } Addr \text{ } a \text{ } \# \text{ } stk', loc', pc', None)$ 
and bisim':  $P, es, h \vdash (es', xs) [\leftrightarrow] (rev \text{ } vs \text{ } @ \text{ } Addr \text{ } a \text{ } \# \text{ } stk', loc', pc', None)$  by auto
note append- $\tau$ Exec-movesr[OF - exec, of [v] [e]]
moreover from bisim'
have  $P, e \# es, h \vdash (Val \text{ } v \# es', xs) [\leftrightarrow] ((rev \text{ } vs \text{ } @ \text{ } Addr \text{ } a \text{ } \# \text{ } stk') \text{ } @ \text{ } [v], loc', length \text{ } (compE2 \text{ } e) + pc', None)$ 
by(rule bisim1-bisims1.bisims1List2)
ultimately show ?case using pc' by fastforce
qed

```

**lemma** *fixes*  $P :: 'addr \text{ } J1\text{-prog}$

**shows** *bisim1-inline-call-Val*:

```

 $\llbracket P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, None); call1 \text{ } e' = \lfloor (a, M, vs) \rfloor;$ 
 $compE2 \text{ } e \text{ } ! \text{ } pc = Invoke \text{ } M \text{ } n0 \rrbracket$ 
 $\implies length \text{ } stk \geq Suc \text{ } (length \text{ } vs) \wedge n0 = length \text{ } vs \wedge$ 
 $P, e, h \vdash (inline\text{-call} \text{ } (Val \text{ } v) \text{ } e', xs) \leftrightarrow (v \# drop \text{ } (Suc \text{ } (length \text{ } vs)) \text{ } stk, loc, Suc \text{ } pc, None)$ 
(is  $\llbracket -; -; - \rrbracket \implies ?concl \text{ } e \text{ } n \text{ } e' \text{ } xs \text{ } pc \text{ } stk \text{ } loc$  )

```

**and** *bisims1-inline-calls-Val*:

```

 $\llbracket P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, None); calls1 \text{ } es' = \lfloor (a, M, vs) \rfloor;$ 
 $compEs2 \text{ } es \text{ } ! \text{ } pc = Invoke \text{ } M \text{ } n0 \rrbracket$ 
 $\implies length \text{ } stk \geq Suc \text{ } (length \text{ } vs) \wedge n0 = length \text{ } vs \wedge$ 
 $P, es, h \vdash (inline\text{-calls} \text{ } (Val \text{ } v) \text{ } es', xs) [\leftrightarrow] (v \# drop \text{ } (Suc \text{ } (length \text{ } vs)) \text{ } stk, loc, Suc \text{ } pc, None)$ 
(is  $\llbracket -; -; - \rrbracket \implies ?concl \text{ } es \text{ } n \text{ } es' \text{ } xs \text{ } pc \text{ } stk \text{ } loc$  )

```

**proof**(*induct*  $(e', xs) \text{ } (stk, loc, pc, None :: 'addr \text{ } option)$

**and**  $(es', xs) \text{ } (stk, loc, pc, None :: 'addr \text{ } option)$

*arbitrary*:  $e' \text{ } xs \text{ } stk \text{ } loc \text{ } pc$  **and**  $es' \text{ } xs \text{ } stk \text{ } loc \text{ } pc$  *rule*: *bisim1-bisims1.inducts*)

**case** *bisim1Val2* **thus** *?case* **by** *simp*

**next**

**case** *bisim1New* **thus** *?case* **by** *simp*

**next**

**case** *bisim1NewArray* **thus** *?case*

**by**(*auto split: if-split-asm dest: bisim1-pc-length-compE2 intro: bisim1-bisims1.bisim1NewArray*)

**next**

**case** *bisim1Cast* **thus** *?case*

**by**(*auto split: if-split-asm dest: bisim1-pc-length-compE2 intro: bisim1-bisims1.bisim1Cast*)

**next**

**case** *bisim1InstanceOf* **thus** *?case*

**by**(*auto split: if-split-asm dest: bisim1-pc-length-compE2 intro: bisim1-bisims1.bisim1InstanceOf*)

**next**

**case** *bisim1Val* **thus** *?case* **by** *simp*

**next**

**case** *bisim1Var* **thus** *?case* **by** *simp*

**next**

**case**  $(bisim1BinOp1 \text{ } e1 \text{ } e' \text{ } xs \text{ } stk \text{ } loc \text{ } pc \text{ } bop \text{ } e2)$

**note**  $IH1 = \langle \llbracket call1 \text{ } e' = \lfloor (a, M, vs) \rfloor; compE2 \text{ } e1 \text{ } ! \text{ } pc = Invoke \text{ } M \text{ } n0 \rrbracket \implies ?concl \text{ } e1 \text{ } n \text{ } e' \text{ } xs \text{ } pc \text{ } stk \text{ } loc \rangle$

**note**  $bisim1 = \langle P, e1, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, None) \rangle$

**note**  $call = \langle call1 \text{ } (e' \text{ } \langle bop \rangle \text{ } e2) = \lfloor (a, M, vs) \rfloor \rangle$

**note**  $ins = \langle compE2 \text{ } (e1 \text{ } \langle bop \rangle \text{ } e2) \text{ } ! \text{ } pc = Invoke \text{ } M \text{ } n0 \rangle$

**show** *?case*

**proof**(*cases is-val e'*)

**case** *False*

**with** *bisim1 call* **have**  $pc < length \text{ } (compE2 \text{ } e1)$  **by**(*auto intro: bisim1-call-pcD*)

```

    with call ins False IH1 show ?thesis
      by(auto intro: bisim1-bisims1.bisim1BinOp1)
  next
    case True
    then obtain v where [simp]: e' = Val v by auto
    from bisim1 have pc ≤ length (compE2 e1) by(auto dest: bisim1-pc-length-compE2)
    moreover {
      assume pc: pc < length (compE2 e1)
      with bisim1 ins have False by(auto dest: bisim-Val-pc-not-Invoke) }
    ultimately have [simp]: pc = length (compE2 e1) by(cases pc < length (compE2 e1)) auto
    with ins have False by(simp)
    thus ?thesis ..
  qed
next
  case (bisim1BinOp2 e2 e' xs stk loc pc e1 bop v1)
  note IH2 = ⟨[call1 e' = [(a, M, vs)]; compE2 e2 ! pc = Invoke M n0] ⟹ ?concl e2 n e' xs pc stk
  loc⟩
  note bisim2 = ⟨P, e2, h ⊢ (e', xs) ↔ (stk, loc, pc, None)⟩
  note call = ⟨call1 (Val v1 «bop» e') = [(a, M, vs)]⟩
  note ins = ⟨compE2 (e1 «bop» e2) ! (length (compE2 e1) + pc) = Invoke M n0⟩
  from call bisim2 have pc: pc < length (compE2 e2) by(auto intro: bisim1-call-pcD)
  with ins have ins': compE2 e2 ! pc = Invoke M n0 by(simp)
  from IH2 ins' pc call show ?case by(auto dest: bisim1-bisims1.bisim1BinOp2)
next
  case bisim1LAss1 thus ?case
    by(auto split: if-split-asm dest: bisim1-pc-length-compE2 intro: bisim1-bisims1.bisim1LAss1)
next
  case bisim1LAss2 thus ?case by simp
next
  case (bisim1AAcc1 A a' xs stk loc pc i)
  note IH1 = ⟨[call1 a' = [(a, M, vs)]; compE2 A ! pc = Invoke M n0] ⟹ ?concl A n a' xs pc stk
  loc⟩
  note bisim1 = ⟨P, A, h ⊢ (a', xs) ↔ (stk, loc, pc, None)⟩
  note call = ⟨call1 (a'[i]) = [(a, M, vs)]⟩
  note ins = ⟨compE2 (A[i]) ! pc = Invoke M n0⟩
  show ?case
  proof(cases is-val a')
    case False
    with bisim1 call have pc < length (compE2 A) by(auto intro: bisim1-call-pcD)
    with call ins False IH1 show ?thesis
      by(auto intro: bisim1-bisims1.bisim1AAcc1)
  next
    case True
    then obtain v where [simp]: a' = Val v by auto
    from bisim1 have pc ≤ length (compE2 A) by(auto dest: bisim1-pc-length-compE2)
    moreover {
      assume pc: pc < length (compE2 A)
      with bisim1 ins have False by(auto dest: bisim-Val-pc-not-Invoke) }
    ultimately have [simp]: pc = length (compE2 A) by(cases pc < length (compE2 A)) auto
    with ins have False by(simp)
    thus ?thesis ..
  qed
next
  case (bisim1AAcc2 i i' xs stk loc pc A v)

```

```

note IH2 = ⟨[call1 i' = [(a, M, vs)]]; compE2 i ! pc = Invoke M n0] ⟹ ?concl i n i' xs pc stk loc⟩
note bisim2 = ⟨P, i, h ⊢ (i', xs) ↔ (stk, loc, pc, None)⟩
note call = ⟨call1 (Val v[i']) = [(a, M, vs)]⟩
note ins = ⟨compE2 (A[i]) ! (length (compE2 A) + pc) = Invoke M n0⟩
from call bisim2 have pc: pc < length (compE2 i) by(auto intro: bisim1-call-pcD)
with ins have ins': compE2 i ! pc = Invoke M n0 by(simp)
from IH2 ins' pc call show ?case
  by(auto dest: bisim1-bisims1.bisim1AAcc2)
next
  case (bisim1AAss1 A a' xs stk loc pc i e)
  note IH1 = ⟨[call1 a' = [(a, M, vs)]]; compE2 A ! pc = Invoke M n0] ⟹ ?concl A n a' xs pc stk
loc⟩
note bisim1 = ⟨P, A, h ⊢ (a', xs) ↔ (stk, loc, pc, None)⟩
note call = ⟨call1 (a'[i] := e) = [(a, M, vs)]⟩
note ins = ⟨compE2 (A[i] := e) ! pc = Invoke M n0⟩
show ?case
proof(cases is-val a')
  case False
  with bisim1 call have pc < length (compE2 A) by(auto intro: bisim1-call-pcD)
  with call ins False IH1 show ?thesis by(auto intro: bisim1-bisims1.bisim1AAss1)
next
  case True
  then obtain v where [simp]: a' = Val v by auto
  from bisim1 have pc ≤ length (compE2 A) by(auto dest: bisim1-pc-length-compE2)
  moreover {
    assume pc: pc < length (compE2 A)
    with bisim1 ins have False by(auto dest: bisim-Val-pc-not-Invoke) }
  ultimately have [simp]: pc = length (compE2 A) by(cases pc < length (compE2 A)) auto
  with ins have False by(simp)
  thus ?thesis ..
qed
next
  case (bisim1AAss2 i i' xs stk loc pc A e v)
  note IH2 = ⟨[call1 i' = [(a, M, vs)]]; compE2 i ! pc = Invoke M n0] ⟹ ?concl i n i' xs pc stk loc⟩
  note bisim2 = ⟨P, i, h ⊢ (i', xs) ↔ (stk, loc, pc, None)⟩
  note call = ⟨call1 (Val v[i'] := e) = [(a, M, vs)]⟩
  note ins = ⟨compE2 (A[i] := e) ! (length (compE2 A) + pc) = Invoke M n0⟩
  show ?case
  proof(cases is-val i')
    case False
    with bisim2 call have pc: pc < length (compE2 i) by(auto intro: bisim1-call-pcD)
    with ins have ins': compE2 i ! pc = Invoke M n0 by(simp)
    from IH2 ins' pc False call show ?thesis by(auto dest: bisim1-bisims1.bisim1AAss2)
  next
    case True
    then obtain v where [simp]: i' = Val v by auto
    from bisim2 have pc ≤ length (compE2 i) by(auto dest: bisim1-pc-length-compE2)
    moreover {
      assume pc: pc < length (compE2 i)
      with bisim2 ins have False by(auto dest: bisim-Val-pc-not-Invoke) }
    ultimately have [simp]: pc = length (compE2 i) by(cases pc < length (compE2 i)) auto
    with ins have False by(simp)
    thus ?thesis ..
  qed

```

```

next
  case (bisim1AAss3 e e' xs stk loc pc i A v v')
  note IH2 =  $\langle \llbracket \text{call1 } e' = \lfloor (a, M, vs) \rfloor; \text{compE2 } e ! pc = \text{Invoke } M \ n0 \rrbracket \implies ?\text{concl } e \ n \ e' \ xs \ pc \ stk \ loc \rangle$ 
  note bisim3 =  $\langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, \text{None}) \rangle$ 
  note call =  $\langle \text{call1 } (\text{Val } v \lfloor \text{Val } v \rfloor := e') = \lfloor (a, M, vs) \rfloor \rangle$ 
  note ins =  $\langle \text{compE2 } (i \lfloor A \rfloor := e) ! (\text{length } (\text{compE2 } i) + \text{length } (\text{compE2 } A) + pc) = \text{Invoke } M \ n0 \rangle$ 
  from call bisim3 have pc:  $pc < \text{length } (\text{compE2 } e)$  by (auto intro: bisim1-call-pcD)
  with ins have ins':  $\text{compE2 } e ! pc = \text{Invoke } M \ n0$  by (simp)
  from IH2 ins' pc call show ?case by (auto dest: bisim1-bisims1.bisim1AAss3)
next
  case bisim1AAss4 thus ?case by simp
next
  case bisim1ALength thus ?case
  by (auto split: if-split-asm dest: bisim1-pc-length-compE2 intro: bisim1-bisims1.bisim1ALength)
next
  case bisim1FAcc thus ?case
  by (auto split: if-split-asm dest: bisim1-pc-length-compE2 intro: bisim1-bisims1.bisim1FAcc)
next
  case (bisim1FAss1 e1 e' xs stk loc pc F D e2)
  note IH1 =  $\langle \llbracket \text{call1 } e' = \lfloor (a, M, vs) \rfloor; \text{compE2 } e1 ! pc = \text{Invoke } M \ n0 \rrbracket \implies ?\text{concl } e1 \ n \ e' \ xs \ pc \ stk \ loc \rangle$ 
  note bisim1 =  $\langle P, e1, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, \text{None}) \rangle$ 
  note call =  $\langle \text{call1 } (e' \cdot F \{D\} := e2) = \lfloor (a, M, vs) \rfloor \rangle$ 
  note ins =  $\langle \text{compE2 } (e1 \cdot F \{D\} := e2) ! pc = \text{Invoke } M \ n0 \rangle$ 
  show ?case
  proof (cases is-val e')
    case False
    with bisim1 call have pc < length (compE2 e1) by (auto intro: bisim1-call-pcD)
    with call ins False IH1 show ?thesis
    by (auto intro: bisim1-bisims1.bisim1FAss1)
  next
    case True
    then obtain v where [simp]:  $e' = \text{Val } v$  by auto
    from bisim1 have pc ≤ length (compE2 e1) by (auto dest: bisim1-pc-length-compE2)
    moreover {
      assume pc:  $pc < \text{length } (\text{compE2 } e1)$ 
      with bisim1 ins have False by (auto dest: bisim-Val-pc-not-Invoke) }
    ultimately have [simp]:  $pc = \text{length } (\text{compE2 } e1)$  by (cases pc < length (compE2 e1)) auto
    with ins have False by (simp)
    thus ?thesis ..
  qed
next
  case (bisim1FAss2 e2 e' xs stk loc pc e1 F D v1)
  note IH2 =  $\langle \llbracket \text{call1 } e' = \lfloor (a, M, vs) \rfloor; \text{compE2 } e2 ! pc = \text{Invoke } M \ n0 \rrbracket \implies ?\text{concl } e2 \ n \ e' \ xs \ pc \ stk \ loc \rangle$ 
  note bisim2 =  $\langle P, e2, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, \text{None}) \rangle$ 
  note call =  $\langle \text{call1 } (\text{Val } v1 \cdot F \{D\} := e') = \lfloor (a, M, vs) \rfloor \rangle$ 
  note ins =  $\langle \text{compE2 } (e1 \cdot F \{D\} := e2) ! (\text{length } (\text{compE2 } e1) + pc) = \text{Invoke } M \ n0 \rangle$ 
  from call bisim2 have pc:  $pc < \text{length } (\text{compE2 } e2)$  by (auto intro: bisim1-call-pcD)
  with ins have ins':  $\text{compE2 } e2 ! pc = \text{Invoke } M \ n0$  by (simp)
  from IH2 ins' pc call show ?case by (auto dest: bisim1-bisims1.bisim1FAss2)
next
  case bisim1FAss3 thus ?case by simp

```

```

next
  case (bisim1CAS1 e1 e' xs stk loc pc D F e2 E3)
  note IH1 = ⟨[call1 e' = [(a, M, vs)]; compE2 e1 ! pc = Invoke M n0] ⟹ ?concl e1 n e' xs pc stk
loc⟩
  note bisim1 = ⟨P, e1, h ⊢ (e', xs) ↔ (stk, loc, pc, None)⟩
  note call = ⟨call1 - = [(a, M, vs)]⟩
  note ins = ⟨compE2 - ! pc = Invoke M n0⟩
  show ?case
  proof(cases is-val e')
    case False
    with bisim1 call have pc < length (compE2 e1) by(auto intro: bisim1-call-pcD)
    with call ins False IH1 show ?thesis by(auto intro: bisim1-bisims1.bisim1CAS1)
  next
  case True
  then obtain v where [simp]: e' = Val v by auto
  from bisim1 have pc ≤ length (compE2 e1) by(auto dest: bisim1-pc-length-compE2)
  moreover {
    assume pc: pc < length (compE2 e1)
    with bisim1 ins have False by(auto dest: bisim-Val-pc-not-Invoke) }
  ultimately have [simp]: pc = length (compE2 e1) by(cases pc < length (compE2 e1)) auto
  with ins have False by(simp)
  thus ?thesis ..
qed
next
  case (bisim1CAS2 e2 e2' xs stk loc pc e1 D F e3 v)
  note IH2 = ⟨[call1 e2' = [(a, M, vs)]; compE2 e2 ! pc = Invoke M n0] ⟹ ?concl e2 n e2' xs pc
stk loc⟩
  note bisim2 = ⟨P, e2, h ⊢ (e2', xs) ↔ (stk, loc, pc, None)⟩
  note call = ⟨call1 - = [(a, M, vs)]⟩
  note ins = ⟨compE2 - ! (length (compE2 e1) + pc) = Invoke M n0⟩
  show ?case
  proof(cases is-val e2')
    case False
    with bisim2 call have pc: pc < length (compE2 e2) by(auto intro: bisim1-call-pcD)
    with ins have ins': compE2 e2 ! pc = Invoke M n0 by(simp)
    from IH2 ins' pc False call show ?thesis by(auto dest: bisim1-bisims1.bisim1CAS2)
  next
  case True
  then obtain v where [simp]: e2' = Val v by auto
  from bisim2 have pc ≤ length (compE2 e2) by(auto dest: bisim1-pc-length-compE2)
  moreover {
    assume pc: pc < length (compE2 e2)
    with bisim2 ins have False by(auto dest: bisim-Val-pc-not-Invoke) }
  ultimately have [simp]: pc = length (compE2 e2) by(cases pc < length (compE2 e2)) auto
  with ins have False by(simp)
  thus ?thesis ..
qed
next
  case (bisim1CAS3 e3 e3' xs stk loc pc e1 D F e2 v v')
  note IH2 = ⟨[call1 e3' = [(a, M, vs)]; compE2 e3 ! pc = Invoke M n0] ⟹ ?concl e3 n e3' xs pc
stk loc⟩
  note bisim3 = ⟨P, e3, h ⊢ (e3', xs) ↔ (stk, loc, pc, None)⟩
  note call = ⟨call1 - = [(a, M, vs)]⟩
  note ins = ⟨compE2 - ! (length (compE2 e1) + length (compE2 e2) + pc) = Invoke M n0⟩

```

```

from call bisim3 have pc: pc < length (compE2 e3) by(auto intro: bisim1-call-pcD)
with ins have ins': compE2 e3 ! pc = Invoke M n0 by(simp)
from IH2 ins' pc call show ?case by(auto dest: bisim1-bisims1.bisim1CAS3)
next
  case (bisim1Call1 obj obj' xs stk loc pc M' ps)
  note IH1 =  $\langle \llbracket \text{call1 } \text{obj}' = \lfloor (a, M, \text{vs}) \rfloor; \text{compE2 } \text{obj} ! \text{pc} = \text{Invoke } M \text{ n0} \rrbracket \implies ?\text{concl } \text{obj } n \text{ obj}' \text{ xs}$ 
    pc stk loc  $\rangle$ 
  note bisim1 =  $\langle P, \text{obj}, h \vdash (\text{obj}', \text{xs}) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{None}) \rangle$ 
  note call =  $\langle \text{call1 } (\text{obj}' \cdot M'(\text{ps})) = \lfloor (a, M, \text{vs}) \rfloor \rangle$ 
  note ins =  $\langle \text{compE2 } (\text{obj} \cdot M'(\text{ps})) ! \text{pc} = \text{Invoke } M \text{ n0} \rangle$ 
  show ?case
  proof(cases is-val obj')
    case False
    with call bisim1 have pc < length (compE2 obj) by(auto intro: bisim1-call-pcD)
    with call False ins IH1 False show ?thesis
      by(auto intro: bisim1-bisims1.bisim1Call1)
    next
    case True
    then obtain v' where [simp]: obj' = Val v' by auto
    from bisim1 have pc ≤ length (compE2 obj) by(auto dest: bisim1-pc-length-compE2)
    moreover {
      assume pc: pc < length (compE2 obj)
      with bisim1 ins have False by(auto dest: bisim-Val-pc-not-Invoke) }
    ultimately have [simp]: pc = length (compE2 obj) by(cases pc < length (compE2 obj)) auto
    with ins have [simp]: ps = [] M' = M
      by(auto split: if-split-asm)(auto simp add: neq-Nil-conv)
    from ins call have [simp]: vs = [] by(auto split: if-split-asm)
    with bisim1 have [simp]: stk = [v] xs = loc by(auto dest: bisim1-pc-length-compE2D)
    from bisim1 Val2[of length (compE2 (obj · M([]))) obj · M([]) P h v loc] call ins
    show ?thesis by(auto simp add: is-val-iff)
  qed
next
  case (bisim1CallParams ps ps' xs stk loc pc obj M' v')
  note IH2 =  $\langle \llbracket \text{calls1 } \text{ps}' = \lfloor (a, M, \text{vs}) \rfloor; \text{compEs2 } \text{ps} ! \text{pc} = \text{Invoke } M \text{ n0} \rrbracket \implies ?\text{concls } \text{ps } n \text{ ps}' \text{ xs}$ 
    pc stk loc  $\rangle$ 
  note bisim =  $\langle P, \text{ps}, h \vdash (\text{ps}', \text{xs}) [\leftrightarrow] (\text{stk}, \text{loc}, \text{pc}, \text{None}) \rangle$ 
  note call =  $\langle \text{call1 } (\text{Val } v' \cdot M'(\text{ps}')) = \lfloor (a, M, \text{vs}) \rfloor \rangle$ 
  note ins =  $\langle \text{compE2 } (\text{obj} \cdot M'(\text{ps})) ! (\text{length } (\text{compE2 } \text{obj}) + \text{pc}) = \text{Invoke } M \text{ n0} \rangle$ 
  from call show ?case
  proof(cases rule: call1-callE)
    case CallObj thus ?thesis by simp
  next
    case (CallParams v'')
    hence [simp]: v'' = v' and call': calls1 ps' =  $\lfloor (a, M, \text{vs}) \rfloor$  by simp-all
    from bisim call' have pc: pc < length (compEs2 ps) by(rule bisims1-calls-pcD)
    with ins have ins': compEs2 ps ! pc = Invoke M n0 by(simp)
    with IH2 call' ins pc
    have P, ps, h ⊢ (inline-calls (Val v) ps', xs)
       $\leftrightarrow (v \# \text{drop } (\text{Suc } (\text{length } \text{vs})) \text{ stk}, \text{loc}, \text{Suc } \text{pc}, \text{None})$ 
    and len: Suc (length vs) ≤ length stk and n0: n0 = length vs by auto
    hence P, obj · M'(ps), h ⊢ (Val v' · M'(inline-calls (Val v) ps'), xs)
       $\leftrightarrow ((v \# \text{drop } (\text{Suc } (\text{length } \text{vs})) \text{ stk}) @ [v], \text{loc}, \text{length } (\text{compE2 } \text{obj}) + \text{Suc } \text{pc},$ 
    None)
    by—(rule bisim1-bisims1.bisim1CallParams)

```

```

  thus ?thesis using call' len n0 by(auto simp add: is-vals-conv)
next
  case Call
  hence [simp]:  $v' = \text{Addr } a \ M' = M \ ps' = \text{map } \text{Val } vs$  by auto
  from bisim have  $pc \leq \text{length } (\text{compEs2 } ps)$  by(auto dest: bisims1-pc-length-compEs2)
  moreover {
    assume  $pc < \text{length } (\text{compEs2 } ps)$ 
    with bisim ins have False by(auto dest: bisims-Val-pc-not-Invoke) }
  ultimately have [simp]:  $pc = \text{length } (\text{compEs2 } ps)$  by(cases  $pc < \text{length } (\text{compEs2 } ps)$ ) auto
  from bisim have [simp]:  $stk = \text{rev } vs \ xs = \text{loc}$  by(auto dest: bisims1-Val-length-compEs2D)
  hence  $P, \text{obj} \cdot M(ps), h \vdash (\text{Val } v, \text{loc}) \leftrightarrow ([v], \text{loc}, \text{length } (\text{compE2 } (\text{obj} \cdot M(ps))), \text{None})$  by-(rule
bisim1Val2, simp)
  moreover from bisim have  $\text{length } ps = \text{length } ps'$  by(rule bisims1-lengthD)
  ultimately show ?thesis using ins by(auto)
qed
next
  case bisim1BlockSome1 thus ?case by simp
next
  case bisim1BlockSome2 thus ?case by simp
next
  case bisim1BlockSome4 thus ?case
    by(auto intro: bisim1-bisims1.bisim1BlockSome4)
next
  case bisim1BlockNone thus ?case
    by(auto intro: bisim1-bisims1.bisim1BlockNone)
next
  case bisim1Sync1 thus ?case
    by(auto split: if-split-asm dest: bisim1-pc-length-compE2 intro: bisim1-bisims1.bisim1Sync1)
next
  case bisim1Sync2 thus ?case by simp
next
  case bisim1Sync3 thus ?case by simp
next
  case bisim1Sync4 thus ?case
    by(auto split: if-split-asm dest: bisim1-pc-length-compE2 bisim1-bisims1.bisim1Sync4)
next
  case bisim1Sync5 thus ?case by simp
next
  case bisim1Sync6 thus ?case by simp
next
  case bisim1Sync7 thus ?case by simp
next
  case bisim1Sync8 thus ?case by simp
next
  case bisim1Sync9 thus ?case by simp
next
  case bisim1InSync thus ?case by(simp)
next
  case bisim1Seq1 thus ?case
    by(auto split: if-split-asm dest: bisim1-pc-length-compE2 intro: bisim1-bisims1.bisim1Seq1)
next
  case bisim1Seq2 thus ?case
    by(auto split: if-split-asm dest: bisim1-pc-length-compE2)(fastforce dest: bisim1-bisims1.bisim1Seq2)
next

```

```

    case bisim1Cond1 thus ?case
      by(auto split: if-split-asm dest: bisim1-pc-length-compE2 intro: bisim1-bisims1.bisim1Cond1)
next
    case (bisim1CondThen e1 stk loc pc e e2 e' xs) thus ?case
      by(auto split: if-split-asm dest: bisim1-pc-length-compE2)
      (fastforce dest: bisim1-bisims1.bisim1CondThen[where e=e and ?e2.0=e2])
next
    case (bisim1CondElse e2 stk loc pc e e1 e' xs) thus ?case
      by(auto split: if-split-asm dest: bisim1-pc-length-compE2)
      (fastforce dest: bisim1-bisims1.bisim1CondElse[where e=e and ?e1.0=e1])
next
    case bisim1While1 thus ?case by simp
next
    case bisim1While3 thus ?case
      by(auto split: if-split-asm dest: bisim1-pc-length-compE2 intro: bisim1-bisims1.bisim1While3)
next
    case bisim1While4 thus ?case
      by(auto split: if-split-asm dest: bisim1-pc-length-compE2)(fastforce dest: bisim1-bisims1.bisim1While4)
next
    case bisim1While6 thus ?case by simp
next
    case bisim1While7 thus ?case by simp
next
    case bisim1Throw1 thus ?case
      by(auto split: if-split-asm dest: bisim1-pc-length-compE2 intro: bisim1-bisims1.bisim1Throw1)
next
    case bisim1Try thus ?case
      by(auto split: if-split-asm dest: bisim1-pc-length-compE2 intro: bisim1-bisims1.bisim1Try)
next
    case bisim1TryCatch1 thus ?case by simp
next
    case bisim1TryCatch2 thus ?case
      by(fastforce dest: bisim1-bisims1.bisim1TryCatch2)
next
    case bisims1Nil thus ?case by simp
next
    case (bisims1List1 e e' xs stk loc pc es)
      note IH1 =  $\langle \llbracket \text{call1 } e' = \lfloor (a, M, \text{vs}) \rfloor; \text{compE2 } e ! \text{ pc} = \text{Invoke } M \text{ } n0 \rrbracket \implies ?\text{concl } e \text{ } n \text{ } e' \text{ } xs \text{ } pc \text{ } stk \text{ } loc \rangle$ 
      note bisim1 =  $\langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, \text{None}) \rangle$ 
      note call =  $\langle \text{calls1 } (e' \# es) = \lfloor (a, M, \text{vs}) \rfloor \rangle$ 
      note ins =  $\langle \text{compEs2 } (e \# es) ! \text{ pc} = \text{Invoke } M \text{ } n0 \rangle$ 
      show ?case
      proof(cases is-val e')
        case False
          with bisim1 call have pc < length (compE2 e) by(auto intro: bisim1-call-pcD)
          with call ins False IH1 show ?thesis
            by(auto intro: bisim1-bisims1.bisims1List1)
        case True
          then obtain v where [simp]: e' = Val v by auto
          from bisim1 have pc ≤ length (compE2 e) by(auto dest: bisim1-pc-length-compE2)
          moreover {
            assume pc: pc < length (compE2 e)

```



```

    with bisim1 ins have False by(auto dest: bisim-Val-pc-not-Invoke) }
  ultimately have [simp]: pc = length (compE2 e) by(cases pc < length (compE2 e)) auto
  with ins call have False by(cases es)(auto)
  thus ?thesis ..
qed
next
  case (bisims1List2 es es' xs stk loc pc e v')
  note IH =  $\langle \llbracket \text{calls1 } es' = \lfloor (a, M, vs) \rfloor; \text{compEs2 } es ! pc = \text{Invoke } M \ n0 \rrbracket \implies ?\text{concls } es \ n \ es' \ xs \ pc \text{ } \text{stk } loc \rangle$ 
  note call =  $\langle \text{calls1 } (Val \ v' \ \# \ es') = \lfloor (a, M, vs) \rfloor \rangle$ 
  note bisim =  $\langle P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, None) \rangle$ 
  note ins =  $\langle \text{compEs2 } (e \ \# \ es) ! (\text{length } (\text{compE2 } e) + pc) = \text{Invoke } M \ n0 \rangle$ 
  from call have call': calls1 es' =  $\lfloor (a, M, vs) \rfloor$  by simp
  with bisim have pc: pc < length (compEs2 es) by(rule bisims1-calls-pcD)
  with ins have ins': compEs2 es ! pc = Invoke M n0 by(simp)
  from IH call ins pc show ?case
    by(auto split: if-split-asm dest: bisim1-bisims1.bisims1List2)
qed

lemma bisim1-fv:  $P, e, h \vdash (e', xs) \leftrightarrow s \implies fv \ e' \subseteq fv \ e$ 
  and bisims1-fvs:  $P, es, h \vdash (es', xs) [\leftrightarrow] s \implies fvs \ es' \subseteq fvs \ es$ 
apply(induct (e', xs) s and (es', xs) s arbitrary: e' xs and es' xs rule: bisim1-bisims1.inducts)
apply(auto)
done

lemma bisim1-syncvars:  $\llbracket P, e, h \vdash (e', xs) \leftrightarrow s; \text{syncvars } e \rrbracket \implies \text{syncvars } e'$ 
  and bisims1-syncvarss:  $\llbracket P, es, h \vdash (es', xs) [\leftrightarrow] s; \text{syncvarss } es \rrbracket \implies \text{syncvarss } es'$ 
apply(induct (e', xs) s and (es', xs) s arbitrary: e' xs and es' xs rule: bisim1-bisims1.inducts)
apply(auto dest: bisim1-fv)
done

declare pcs-stack-xlift [simp]

lemma bisim1-Val- $\tau$ red1r:
 $\llbracket P, E, h \vdash (e, xs) \leftrightarrow ([v], loc, \text{length } (\text{compE2 } E), None); n + \text{max-vars } e \leq \text{length } xs; \mathcal{B} \ E \ n \rrbracket$ 
 $\implies \tau\text{red1r } P \ t \ h \ (e, xs) \ (Val \ v, loc)$ 

  and bisims1-Val- $\tau$ Reds1r:
 $\llbracket P, Es, h \vdash (es, xs) [\leftrightarrow] (rev \ vs, loc, \text{length } (\text{compEs2 } Es), None); n + \text{max-varss } es \leq \text{length } xs; \mathcal{B} \ s \ Es \ n \rrbracket$ 
 $\implies \tau\text{reds1r } P \ t \ h \ (es, xs) \ (\text{map } Val \ vs, loc)$ 
proof(induct E n e xs stk  $\equiv [v]$  loc pc  $\equiv \text{length } (\text{compE2 } E)$  xcp  $\equiv None::'addr \ option$ 
  and Es n es xs stk  $\equiv rev \ vs$  loc pc  $\equiv \text{length } (\text{compEs2 } Es)$  xcp  $\equiv None::'addr \ option$ 
  arbitrary: v and vs rule: bisim1-bisims1-inducts-split)
  case bisim1BlockSome2 thus ?case by(simp (no-asm-use))
next
  case (bisim1BlockSome4 e n e' xs loc pc V T val)
  from  $\langle \mathcal{B} \ \{V:T=[val]; e\} \ n \rangle$  have [simp]: n = V and  $\mathcal{B} \ e \ (Suc \ n)$  by auto
  note len =  $\langle n + \text{max-vars } \{V:T=None; e'\} \leq \text{length } xs \rangle$ 
  hence V: V < length xs by simp
  from  $\langle P, e, h \vdash (e', xs) \leftrightarrow ([v], loc, pc, None) \rangle$ 
  have lenxs: length xs = length loc by(auto dest: bisim1-length-xs)
  note IH =  $\langle \llbracket pc = \text{length } (\text{compE2 } e); Suc \ n + \text{max-vars } e' \leq \text{length } xs; \mathcal{B} \ e \ (Suc \ n) \rrbracket$ 

```

$\implies \tau_{red1r} P t h (e', xs) (Val v, loc)\rangle$   
**with**  $len \langle Suc (Suc pc) = length (compE2 \{V:T=[val]; e\}) \rangle \langle \mathcal{B} e (Suc n) \rangle$   
**have**  $\tau_{red1r} P t h (e', xs) (Val v, loc) \text{ by } (simp)$   
**hence**  $\tau_{red1r} P t h (\{V:T=None; e'\}, xs) (\{V:T=None; Val v\}, loc)$   
**by**  $(rule \text{Block-None-}\tau_{red1r}\text{-xt})$   
**thus**  $?case \text{ using } V len xs \text{ by } (auto elim!: rtranclp.rtrancl\text{-}into\text{-}rtrancl \text{ intro: Red1Block } \tau move1BlockRed)$   
**next**  
**case**  $(bisim1BlockNone e n e' xs loc V T)$   
**from**  $\langle \mathcal{B} \{V:T=None; e\} n \rangle$  **have**  $[simp]: n = V \text{ and } \mathcal{B} e (Suc n) \text{ by } auto$   
**note**  $len = \langle n + max\text{-}vars \{V:T=None; e'\} \leq length xs \rangle$   
**hence**  $V: V < length xs \text{ by } simp$   
**from**  $\langle P, e, h \vdash (e', xs) \leftrightarrow ([v], loc, length (compE2 \{V:T=None; e\}), None) \rangle$   
**have**  $len xs: length xs = length loc \text{ by } (auto dest: bisim1\text{-}length\text{-}xs)$   
**note**  $IH = \langle \llbracket length (compE2 \{V:T=None; e\}) = length (compE2 e); Suc n + max\text{-}vars e' \leq length xs; \mathcal{B} e (Suc n) \rrbracket$   
 $\implies \tau_{red1r} P t h (e', xs) (Val v, loc)\rangle$   
**with**  $len \langle \mathcal{B} e (Suc n) \rangle$  **have**  $\tau_{red1r} P t h (e', xs) (Val v, loc) \text{ by } (simp)$   
**hence**  $\tau_{red1r} P t h (\{V:T=None; e'\}, xs) (\{V:T=None; Val v\}, loc)$   
**by**  $(rule \text{Block-None-}\tau_{red1r}\text{-xt})$   
**thus**  $?case \text{ using } V len xs \text{ by } (auto elim!: rtranclp.rtrancl\text{-}into\text{-}rtrancl \text{ intro: Red1Block } \tau move1BlockRed)$   
**next**  
**case**  $(bisim1TryCatch2 e2 n e' xs loc pc e C V)$   
**from**  $\langle \mathcal{B} (try e catch (C V) e2) n \rangle$  **have**  $[simp]: n = V \text{ and } \mathcal{B} e2 (Suc n) \text{ by } auto$   
**note**  $len = \langle n + max\text{-}vars \{V:Class C=None; e'\} \leq length xs \rangle$   
**hence**  $V: V < length xs \text{ by } simp$   
**from**  $\langle P, e2, h \vdash (e', xs) \leftrightarrow ([v], loc, pc, None) \rangle$   
**have**  $len xs: length xs = length loc \text{ by } (auto dest: bisim1\text{-}length\text{-}xs)$   
**note**  $IH = \langle \llbracket pc = length (compE2 e2); Suc n + max\text{-}vars e' \leq length xs; \mathcal{B} e2 (Suc n) \rrbracket$   
 $\implies \tau_{red1r} P t h (e', xs) (Val v, loc)\rangle$   
**with**  $len \langle Suc (Suc (length (compE2 e) + pc)) = length (compE2 (try e catch (C V) e2)) \rangle \langle \mathcal{B} e2 (Suc n) \rangle$   
**have**  $\tau_{red1r} P t h (e', xs) (Val v, loc) \text{ by } (simp)$   
**hence**  $\tau_{red1r} P t h (\{V:Class C=None; e'\}, xs) (\{V:Class C=None; Val v\}, loc)$   
**by**  $(rule \text{Block-None-}\tau_{red1r}\text{-xt})$   
**thus**  $?case \text{ using } V len xs \text{ by } (auto elim!: rtranclp.rtrancl\text{-}into\text{-}rtrancl \text{ intro: Red1Block } \tau move1BlockRed)$   
**next**  
**case**  $(bisims1List1 e n e' xs loc es)$   
**note**  $bisim = \langle P, e, h \vdash (e', xs) \leftrightarrow (rev vs, loc, length (compEs2 (e \# es)), None) \rangle$   
**then have**  $es: es = [] \text{ and } pc: length (compEs2 (e \# es)) = length (compE2 e)$   
**by**  $(auto dest: bisim1\text{-}pc\text{-}length\text{-}compE2)$   
**with**  $bisim \text{ obtain } val \text{ where } stk: rev vs = [val] \text{ and } e': is\text{-}val e' \implies e' = Val val$   
**by**  $(auto dest: bisim1\text{-}pc\text{-}length\text{-}compE2D)$   
**with**  $es pc bisims1List1 \text{ have } \tau_{red1r} P t h (e', xs) (Val val, loc) \text{ by } simp$   
**with**  $stk es \text{ show } ?case \text{ by } (auto intro: \tau_{red1r}\text{-inj-}\tau_{reds1r})$   
**next**  
**case**  $(bisims1List2 es n es' xs stk loc pc e v)$   
**from**  $\langle stk @ [v] = rev vs \rangle$  **obtain**  $vs' \text{ where } vs: vs = v \# vs' \text{ by } (cases vs) \text{ auto}$   
**with**  $bisims1List2 \text{ show } ?case \text{ by } (auto intro: \tau_{reds1r}\text{-cons-}\tau_{reds1r})$   
**qed**  $(fastforce dest: bisim1\text{-}pc\text{-}length\text{-}compE2 bisims1\text{-}pc\text{-}length\text{-}compEs2) +$

**lemma** *exec-meth-stk-split*:  
 $\llbracket P, E, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp);$   
 $exec\text{-}meth\text{-}d (compP2 P) (compE2 E) (stack\text{-}xlift (length STK) (compxE2 E 0 0)) t$   
 $h (stk @ STK, loc, pc, xcp) \text{ ta } h' (stk', loc', pc', xcp') \rrbracket$

$\implies \exists stk''. stk' = stk'' @ STK \wedge exec\text{-}meth\text{-}d (compP2 P) (compE2 E) (compxE2 E 0 0) t$   
 $h (stk, loc, pc, xcp) ta h' (stk'', loc', pc', xcp')$   
 $(is \llbracket -; ?exec E stk STK loc pc xcp stk' loc' pc' xcp' \rrbracket \implies ?concl E stk STK loc pc xcp stk' loc' pc' xcp')$

**and** *exec-meth-stk-splits*:

$\llbracket P, Es, h \vdash (es, xs) [\leftrightarrow] (stk, loc, pc, xcp);$   
 $exec\text{-}meth\text{-}d (compP2 P) (compEs2 Es) (stack\text{-}xlift (length STK) (compxEs2 Es 0 0)) t$   
 $h (stk @ STK, loc, pc, xcp) ta h' (stk', loc', pc', xcp') \rrbracket$   
 $\implies \exists stk''. stk' = stk'' @ STK \wedge exec\text{-}meth\text{-}d (compP2 P) (compEs2 Es) (compxEs2 Es 0 0) t$   
 $h (stk, loc, pc, xcp) ta h' (stk'', loc', pc', xcp')$   
 $(is \llbracket -; ?execs Es stk STK loc pc xcp stk' loc' pc' xcp' \rrbracket \implies ?concls Es stk STK loc pc xcp stk' loc' pc' xcp')$

**proof**(*induct*  $E n :: nat$   $e xs stk loc pc xcp$  **and**  $Es n :: nat$   $es xs stk loc pc xcp$

*arbitrary*:  $stk' loc' pc' xcp' STK$  **and**  $stk' loc' pc' xcp' STK$  *rule*: *bisim1-bisims1-inducts-split*)

**case** *bisim1InSync* **thus**  $?case$  **by**(*auto elim!*: *exec-meth.cases intro!*: *exec-meth.intros*)

**next**

**case** *bisim1Val2* **thus**  $?case$  **by**(*auto dest*: *exec-meth-length-compE2-stack-xliftD*)

**next**

**case** *bisim1New* **thus**  $?case$

**by** (*fastforce elim*: *exec-meth.cases intro*: *exec-meth.intros split*: *if-split-asm cong del*: *image-cong-simp*)

**next**

**case** *bisim1NewThrow* **thus**  $?case$  **by**(*fastforce elim*: *exec-meth.cases intro*: *exec-meth.intros*)

**next**

**case** (*bisim1NewArray*  $e n e' xs stk loc pc xcp T$ )

**note** *bisim* =  $\langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$

**note** *IH* =  $\langle \bigwedge stk' loc' pc' xcp' STK. ?exec e stk STK loc pc xcp stk' loc' pc' xcp' \implies ?concl e stk STK loc pc xcp stk' loc' pc' xcp' \rangle$

**note** *exec* =  $\langle ?exec (newA T[e]) stk STK loc pc xcp stk' loc' pc' xcp' \rangle$

**from** *bisim* **have**  $pc: pc \leq length (compE2 e)$  **by**(*rule bisim1-pc-length-compE2*)

**show**  $?case$

**proof**(*cases*  $pc < length (compE2 e)$ )

**case** *True*

**with** *exec* **have**  $?exec e stk STK loc pc xcp stk' loc' pc' xcp'$

**by**(*simp add*: *compxE2-size-convs*)(*erule exec-meth-take*)

**from** *IH*[*OF this*] **show**  $?thesis$  **by** *auto*

**next**

**case** *False*

**with**  $pc$  **have** [*simp*]:  $pc = length (compE2 e)$  **by** *simp*

**with** *bisim* **obtain**  $v$  **where** [*simp*]:  $stk = [v]$   $xcp = None$

**by**(*auto dest*: *dest*: *bisim1-pc-length-compE2D*)

**with** *exec* **show**  $?thesis$

**apply** *simp*

**apply** (*erule exec-meth.cases*)

**apply** (*auto 4 4 intro*: *exec-meth.intros split*: *if-split-asm cong del*: *image-cong-simp*)

**done**

**qed**

**next**

**case** (*bisim1NewArrayThrow*  $e n a xs stk loc pc T$ )

**note** *bisim* =  $\langle P, e, h \vdash (Throw a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$

**note** *IH* =  $\langle \bigwedge stk' loc' pc' xcp' STK. ?exec e stk STK loc pc [a] stk' loc' pc' xcp' \implies ?concl e stk STK loc pc [a] stk' loc' pc' xcp' \rangle$

**note** *exec* =  $\langle ?exec (newA T[e]) stk STK loc pc [a] stk' loc' pc' xcp' \rangle$

**from** *bisim* **have**  $pc: pc < length (compE2 e)$  **and** [*simp*]:  $xs = loc$

```

    by(auto dest: bisim1-ThrowD)
  from exec have ?exec e stk STK loc pc [a] stk' loc' pc' xcp'
    by(simp)(erule exec-meth-take[OF - pc])
  from IH[OF this] show ?case by(auto)
next
  case bisim1NewArrayFail thus ?case
    by(auto elim!: exec-meth.cases dest: match-ex-table-pcsD simp add: stack-xlift-compxEs2 stack-xlift-compxE2)
next
  case (bisim1Cast e n e' xs stk loc pc xcp T)
  note bisim = ⟨P,e,h ⊢ (e', xs) ↔ (stk, loc, pc, xcp)⟩
  note IH = ⟨∧stk' loc' pc' xcp' STK. ?exec e stk STK loc pc xcp stk' loc' pc' xcp'
    ⇒ ?concl e stk STK loc pc xcp stk' loc' pc' xcp'⟩
  note exec = ⟨?exec (Cast T e) stk STK loc pc xcp stk' loc' pc' xcp'⟩
  from bisim have pc: pc ≤ length (compE2 e) by(rule bisim1-pc-length-compE2)
  show ?case
  proof(cases pc < length (compE2 e))
    case True
      with exec have ?exec e stk STK loc pc xcp stk' loc' pc' xcp'
        by(simp add: compxE2-size-convs)(erule exec-meth-take)
      from IH[OF this] show ?thesis by auto
    next
      case False
      with pc have [simp]: pc = length (compE2 e) by simp
      with bisim obtain v where [simp]: stk = [v] xcp = None
        by(auto dest: dest: bisim1-pc-length-compE2D)
      with exec show ?thesis apply(simp)
        by(erule exec-meth.cases)(auto intro!: exec-meth.intros split: if-split-asm)
  qed
next
  case (bisim1CastThrow e n a xs stk loc pc T)
  note bisim = ⟨P,e,h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
  note IH = ⟨∧stk' loc' pc' xcp' STK. ?exec e stk STK loc pc [a] stk' loc' pc' xcp'
    ⇒ ?concl e stk STK loc pc [a] stk' loc' pc' xcp'⟩
  note exec = ⟨?exec (Cast T e) stk STK loc pc [a] stk' loc' pc' xcp'⟩
  from bisim have pc: pc < length (compE2 e) and [simp]: xs = loc
    by(auto dest: bisim1-ThrowD)
  from exec have ?exec e stk STK loc pc [a] stk' loc' pc' xcp'
    by(simp)(erule exec-meth-take[OF - pc])
  from IH[OF this] show ?case by(auto)
next
  case bisim1CastFail thus ?case
    by(auto elim!: exec-meth.cases dest: match-ex-table-pcsD simp add: stack-xlift-compxEs2 stack-xlift-compxE2)
next
  case (bisim1InstanceOf e n e' xs stk loc pc xcp T)
  note bisim = ⟨P,e,h ⊢ (e', xs) ↔ (stk, loc, pc, xcp)⟩
  note IH = ⟨∧stk' loc' pc' xcp' STK. ?exec e stk STK loc pc xcp stk' loc' pc' xcp'
    ⇒ ?concl e stk STK loc pc xcp stk' loc' pc' xcp'⟩
  note exec = ⟨?exec (e instanceof T) stk STK loc pc xcp stk' loc' pc' xcp'⟩
  from bisim have pc: pc ≤ length (compE2 e) by(rule bisim1-pc-length-compE2)
  show ?case
  proof(cases pc < length (compE2 e))
    case True
      with exec have ?exec e stk STK loc pc xcp stk' loc' pc' xcp'
        by(simp add: compxE2-size-convs)(erule exec-meth-take)

```

```

from IH[OF this] show ?thesis by auto
next
case False
with pc have [simp]: pc = length (compE2 e) by simp
with bisim obtain v where [simp]: stk = [v] xcp = None
  by(auto dest: dest: bisim1-pc-length-compE2D)
with exec show ?thesis apply(simp)
  by(erule exec-meth.cases)(auto intro!: exec-meth.intros split: if-split-asm)
qed
next
case (bisim1InstanceOfThrow e n a xs stk loc pc T)
note bisim = ⟨P,e,h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
note IH = ⟨∧stk' loc' pc' xcp' STK. ?exec e stk STK loc pc [a] stk' loc' pc' xcp'
  ⇒ ?concl e stk STK loc pc [a] stk' loc' pc' xcp'⟩
note exec = ⟨?exec (e instanceof T) stk STK loc pc [a] stk' loc' pc' xcp'⟩
from bisim have pc: pc < length (compE2 e) and [simp]: xs = loc
  by(auto dest: bisim1-ThrowD)
from exec have ?exec e stk STK loc pc [a] stk' loc' pc' xcp'
  by(simp)(erule exec-meth-take[OF - pc])
from IH[OF this] show ?case by(auto)
next
case bisim1Val thus ?case by(fastforce elim: exec-meth.cases intro: exec-meth.intros)
next
case bisim1Var thus ?case by(fastforce elim: exec-meth.cases intro: exec-meth.intros)
next
case (bisim1BinOp1 e1 n e1' xs stk loc pc xcp e2 bop)
note IH1 = ⟨∧stk' loc' pc' xcp' STK. ?exec e1 stk STK loc pc xcp stk' loc' pc' xcp'
  ⇒ ?concl e1 stk STK loc pc xcp stk' loc' pc' xcp'⟩
note IH2 = ⟨∧xs stk' loc' pc' xcp' STK. ?exec e2 [] STK xs 0 None stk' loc' pc' xcp'
  ⇒ ?concl e2 [] STK xs 0 None stk' loc' pc' xcp'⟩
note bisim1 = ⟨P,e1,h ⊢ (e1', xs) ↔ (stk, loc, pc, xcp)⟩
note bisim2 = ⟨P,e2,h ⊢ (e2, loc) ↔ ([], loc, 0, None)⟩
note exec = ⟨?exec (e1 «bop» e2) stk STK loc pc xcp stk' loc' pc' xcp'⟩
from bisim1 have pc: pc ≤ length (compE2 e1) by(rule bisim1-pc-length-compE2)
show ?case
proof(cases pc < length (compE2 e1))
case True
with exec have ?exec e1 stk STK loc pc xcp stk' loc' pc' xcp'
  by(simp add: compxE2-size-convs)(erule exec-meth-take-xt)
from IH1[OF this] show ?thesis by auto
next
case False
with pc have pc: pc = length (compE2 e1) by simp
with exec have pc' ≥ length (compE2 e1)
  by(simp add: compxE2-size-convs stack-xlift-compxE2)(auto split: bop.splits elim!: exec-meth-drop-xt-pc)
then obtain PC where PC: pc' = PC + length (compE2 e1)
  by -(rule-tac PC34=pc' - length (compE2 e1) in that, simp)
from pc bisim1 obtain v where stk = [v] xcp = None by(auto dest: bisim1-pc-length-compE2D)
with exec pc have exec-meth-d (compP2 P) (compE2 e1 @ compE2 e2)
  (stack-xlift (length STK) (compxE2 e1 0 0 @ compxE2 e2 (length (compE2 e1)) (Suc 0))) t h (stk
  @ STK, loc, length (compE2 e1) + 0, xcp) ta h' (stk', loc', pc', xcp')
  by-(rule exec-meth-take, auto)
hence ?exec e2 [] (v # STK) loc 0 None stk' loc' (pc' - length (compE2 e1)) xcp'
  using ⟨stk = [v]⟩ ⟨xcp = None⟩

```

by  $-(\text{rule exec-meth-drop-xt}, \text{auto simp add: stack-xlift-compxE2 shift-compxE2})$   
 from  $\text{IH2}[OF \text{ this}] \text{ PC}$  obtain  $\text{stk}''$  where  $\text{stk}': \text{stk}' = \text{stk}'' @ v \# \text{STK}$   
 and  $\text{exec-meth-d} (\text{compP2 } P) (\text{compE2 } e2) (\text{compxE2 } e2 \ 0 \ 0) \ t \ h \ ([], \text{loc}, 0, \text{None}) \ ta \ h' (\text{stk}'', \text{loc}', \text{PC}, \text{xcp})$  by  $\text{auto}$   
 hence  $\text{exec-meth-d} (\text{compP2 } P) ((\text{compE2 } e1 @ \text{compE2 } e2) @ [\text{BinOpInstr } \text{bop}])$   
 $(\text{compxE2 } e1 \ 0 \ 0 @ \text{shift} (\text{length } (\text{compE2 } e1)) (\text{stack-xlift} (\text{length } [v]) (\text{compxE2 } e2 \ 0 \ 0))) \ t \ h$   
 $([] @ [v], \text{loc}, \text{length } (\text{compE2 } e1) + 0, \text{None}) \ ta \ h' (\text{stk}'' @ [v], \text{loc}', \text{length } (\text{compE2 } e1) + \text{PC}, \text{xcp})$   
 apply  $-$   
 apply  $(\text{rule exec-meth-append})$   
 apply  $(\text{rule append-exec-meth-xt})$   
 apply  $(\text{erule exec-meth-stk-offer})$   
 by  $(\text{auto})$   
 thus  $?thesis$  using  $\langle \text{stk} = [v] \rangle \langle \text{xcp} = \text{None} \rangle \text{stk}' \text{pc } \text{PC}$   
 by  $(\text{clarsimp simp add: shift-compxE2 stack-xlift-compxE2 ac-simps})$   
 qed  
 next  
 case  $(\text{bisim1BinOp2 } e2 \ n \ e2' \ xs \ \text{stk} \ \text{loc} \ \text{pc} \ \text{xcp} \ e1 \ \text{bop} \ v1)$   
 note  $\text{IH2} = \langle \bigwedge \text{stk}' \ \text{loc}' \ \text{pc}' \ \text{xcp}' \ \text{STK}. ?\text{exec } e2 \ \text{stk} \ \text{STK} \ \text{loc} \ \text{pc} \ \text{xcp} \ \text{stk}' \ \text{loc}' \ \text{pc}' \ \text{xcp}'$   
 $\implies ?\text{concl } e2 \ \text{stk} \ \text{STK} \ \text{loc} \ \text{pc} \ \text{xcp} \ \text{stk}' \ \text{loc}' \ \text{pc}' \ \text{xcp}' \rangle$   
 note  $\text{bisim1} = \langle P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$   
 note  $\text{bisim2} = \langle P, e2, h \vdash (e2', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \rangle$   
 note  $\text{exec} = \langle ?\text{exec } (e1 \ll \text{bop} \gg e2) (\text{stk} @ [v1]) \ \text{STK} \ \text{loc} \ (\text{length } (\text{compE2 } e1) + \text{pc}) \ \text{xcp} \ \text{stk}' \ \text{loc}' \ \text{pc}' \ \text{xcp}' \rangle$   
 from  $\text{bisim2}$  have  $\text{pc}: \text{pc} \leq \text{length } (\text{compE2 } e2)$  by  $(\text{rule bisim1-pc-length-compE2})$   
 show  $?case$   
 proof  $(\text{cases } \text{pc} < \text{length } (\text{compE2 } e2))$   
 case  $\text{True}$   
 from  $\text{exec}$  have  $\text{exec-meth-d} (\text{compP2 } P) ((\text{compE2 } e1 @ \text{compE2 } e2) @ [\text{BinOpInstr } \text{bop}])$   
 $(\text{stack-xlift} (\text{length } \text{STK}) (\text{compxE2 } e1 \ 0 \ 0) @ \text{shift} (\text{length } (\text{compE2 } e1)) (\text{stack-xlift} (\text{length } \text{STK}) (\text{compxE2 } e2 \ 0 \ (\text{Suc } 0)))) \ t$   
 $h \ (\text{stk} @ v1 \# \text{STK}, \text{loc}, \text{length } (\text{compE2 } e1) + \text{pc}, \text{xcp}) \ ta \ h' (\text{stk}', \text{loc}', \text{pc}', \text{xcp}')$  by  $(\text{simp add: compxE2-size-convs})$   
 hence  $\text{exec}': \text{exec-meth-d} (\text{compP2 } P) (\text{compE2 } e1 @ \text{compE2 } e2) (\text{stack-xlift} (\text{length } \text{STK}) (\text{compxE2 } e1 \ 0 \ 0) @$   
 $\text{shift} (\text{length } (\text{compE2 } e1)) (\text{stack-xlift} (\text{length } \text{STK}) (\text{compxE2 } e2 \ 0 \ (\text{Suc } 0)))) \ t$   
 $h \ (\text{stk} @ v1 \# \text{STK}, \text{loc}, \text{length } (\text{compE2 } e1) + \text{pc}, \text{xcp}) \ ta \ h' (\text{stk}', \text{loc}', \text{pc}', \text{xcp}')$   
 by  $(\text{rule exec-meth-take})(\text{simp add: True})$   
 hence  $\text{exec-meth-d} (\text{compP2 } P) (\text{compE2 } e2) (\text{stack-xlift} (\text{length } \text{STK}) (\text{compxE2 } e2 \ 0 \ (\text{Suc } 0))) \ t$   
 $h \ (\text{stk} @ v1 \# \text{STK}, \text{loc}, \text{pc}, \text{xcp}) \ ta \ h' (\text{stk}', \text{loc}', \text{pc}' - \text{length } (\text{compE2 } e1), \text{xcp}')$   
 by  $(\text{rule exec-meth-drop-xt})(\text{auto simp add: stack-xlift-compxE2})$   
 hence  $?exec \ e2 \ \text{stk} \ (v1 \# \text{STK}) \ \text{loc} \ \text{pc} \ \text{xcp} \ \text{stk}' \ \text{loc}' \ (\text{pc}' - \text{length } (\text{compE2 } e1)) \ \text{xcp}'$   
 by  $(\text{simp add: compxE2-stack-xlift-convs})$   
 from  $\text{IH2}[OF \text{ this}]$  obtain  $\text{stk}''$  where  $\text{stk}': \text{stk}' = \text{stk}'' @ v1 \# \text{STK}$   
 and  $\text{exec}'': \text{exec-meth-d} (\text{compP2 } P) (\text{compE2 } e2) (\text{compxE2 } e2 \ 0 \ 0) \ t \ h \ (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \ ta \ h' (\text{stk}'', \text{loc}', \text{pc}' - \text{length } (\text{compE2 } e1), \text{xcp}')$  by  $\text{blast}$   
 from  $\text{exec}''$  have  $\text{exec-meth-d} (\text{compP2 } P) (\text{compE2 } e2) (\text{stack-xlift} (\text{length } [v1]) (\text{compxE2 } e2 \ 0 \ 0)) \ t \ h$   
 $(\text{stk} @ [v1], \text{loc}, \text{pc}, \text{xcp})$   
 $ta \ h' (\text{stk}'' @ [v1], \text{loc}', \text{pc}' - \text{length } (\text{compE2 } e1), \text{xcp}')$   
 by  $(\text{rule exec-meth-stk-offer})$   
 hence  $\text{exec-meth-d} (\text{compP2 } P) (\text{compE2 } e1 @ \text{compE2 } e2) (\text{compxE2 } e1 \ 0 \ 0 @ \text{shift} (\text{length } (\text{compE2 } e1)) (\text{stack-xlift} (\text{length } [v1]) (\text{compxE2 } e2 \ 0 \ 0))) \ t \ h$   
 $(\text{stk} @ [v1], \text{loc}, \text{length } (\text{compE2 } e1) + \text{pc}, \text{xcp})$   
 $ta \ h' (\text{stk}'' @ [v1], \text{loc}', \text{length } (\text{compE2 } e1) + (\text{pc}' - \text{length } (\text{compE2 } e1)), \text{xcp}')$

```

    by(rule append-exec-meth-xt) auto
  hence exec-meth-d (compP2 P) ((compE2 e1 @ compE2 e2) @ [BinOpInstr bop]) (compxE2 e1 0 0
@ shift (length (compE2 e1)) (stack-xlift (length [v1]) (compxE2 e2 0 0))) t h (stk @ [v1], loc, length
(compE2 e1) + pc, xcp)
    ta h' (stk'' @ [v1], loc', length (compE2 e1) + (pc' - length (compE2 e1)), xcp')
  by(rule exec-meth-append)
  moreover from exec' have pc' ≥ length (compE2 e1)
  by(rule exec-meth-drop-xt-pc)(auto simp add: stack-xlift-compxE2)
  ultimately show ?thesis using stk' by(simp add: stack-xlift-compxE2 shift-compxE2)
next
case False
with pc have pc: pc = length (compE2 e2) by simp
with bisim2 obtain v2 where [simp]: stk = [v2] xcp = None
  by(auto dest: dest: bisim1-pc-length-compE2D)
with exec pc show ?thesis
  by(fastforce elim: exec-meth.cases split: sum.split-asm intro!: exec-meth.intros)
qed
next
case (bisim1BinOpThrow1 e1 n a xs stk loc pc e2 bop)
note bisim1 = ⟨P, e1, h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
note IH1 = ⟨∧stk' loc' pc' xcp' STK. ?exec e1 stk STK loc pc [a] stk' loc' pc' xcp'
⇒ ?concl e1 stk STK loc pc [a] stk' loc' pc' xcp'⟩
note exec = ⟨?exec (e1 «bop» e2) stk STK loc pc [a] stk' loc' pc' xcp'⟩
from bisim1 have pc: pc < length (compE2 e1) and [simp]: xs = loc
  by(auto dest: bisim1-ThrowD)
from exec have exec-meth-d (compP2 P) (compE2 e1 @ (compE2 e2 @ [BinOpInstr bop]))
(stack-xlift (length STK) (compxE2 e1 0 0) @ shift (length (compE2 e1)) (stack-xlift (length STK)
(compxE2 e2 0 (Suc 0)))) t
  h (stk @ STK, loc, pc, [a]) ta h' (stk', loc', pc', xcp') by(simp add: compxE2-size-convs)
hence ?exec e1 stk STK loc pc [a] stk' loc' pc' xcp'
  by(rule exec-meth-take-xt)(rule pc)
from IH1[OF this] show ?case by(auto)
next
case (bisim1BinOpThrow2 e2 n a xs stk loc pc e1 bop v1)
note bisim2 = ⟨P, e2, h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
note IH2 = ⟨∧stk' loc' pc' xcp' STK. ?exec e2 stk STK loc pc [a] stk' loc' pc' xcp'
⇒ ?concl e2 stk STK loc pc [a] stk' loc' pc' xcp'⟩
note exec = ⟨?exec (e1 «bop» e2) (stk @ [v1]) STK loc (length (compE2 e1) + pc) [a] stk' loc' pc'
xcp'⟩
from bisim2 have pc: pc < length (compE2 e2) and [simp]: xs = loc
  by(auto dest: bisim1-ThrowD)
from exec have exec-meth-d (compP2 P) ((compE2 e1 @ compE2 e2) @ [BinOpInstr bop])
(stack-xlift (length STK) (compxE2 e1 0 0) @ shift (length (compE2 e1)) (stack-xlift (length STK)
(compxE2 e2 0 (Suc 0))))
  t h (stk @ v1 # STK, loc, length (compE2 e1) + pc, [a]) ta h' (stk', loc', pc', xcp')
  by(simp add: compxE2-size-convs)
hence exec': exec-meth-d (compP2 P) (compE2 e1 @ compE2 e2)
(stack-xlift (length STK) (compxE2 e1 0 0) @ shift (length (compE2 e1)) (stack-xlift (length STK)
(compxE2 e2 0 (Suc 0)))) t
  h (stk @ v1 # STK, loc, length (compE2 e1) + pc, [a]) ta h' (stk', loc', pc', xcp')
  by(rule exec-meth-take)(simp add: pc)
hence exec-meth-d (compP2 P) (compE2 e2) (stack-xlift (length STK) (compxE2 e2 0 (Suc 0))) t
  h (stk @ v1 # STK, loc, pc, [a]) ta h' (stk', loc', pc' - length (compE2 e1), xcp')
  by(rule exec-meth-drop-xt)(auto simp add: stack-xlift-compxE2)

```

hence  $?exec\ e2\ stk\ (v1\ \# \ STK)\ loc\ pc\ [a]\ stk'\ loc'\ (pc' - length\ (compE2\ e1))\ xcp'$   
 by(*simp add: compxE2-stack-xlift-convs*)  
 from *IH2[OF this]* obtain  $stk'$  where  $stk': stk' = stk'' @ v1\ \# \ STK$  and  
 $exec'': exec\ meth\ d\ (compP2\ P)\ (compE2\ e2)\ (compxE2\ e2\ 0\ 0)\ t\ h\ (stk,\ loc,\ pc,\ [a])\ ta\ h'\ (stk'',$   
 $loc',\ pc' - length\ (compE2\ e1),\ xcp')$  by *blast*  
 from  $exec''$  have  $exec\ meth\ d\ (compP2\ P)\ (compE2\ e2)\ (stack\ xlift\ (length\ [v1])\ (compxE2\ e2\ 0\ 0))$   
 $t\ h\ (stk\ @\ [v1],\ loc,\ pc,\ [a])$   
 $ta\ h'\ (stk''\ @\ [v1],\ loc',\ pc' - length\ (compE2\ e1),\ xcp')$   
 by(*rule exec-meth-stk-offer*)  
 hence  $exec\ meth\ d\ (compP2\ P)\ (compE2\ e1\ @\ compE2\ e2)\ (compxE2\ e1\ 0\ 0\ @\ shift\ (length\ (compE2$   
 $e1))\ (stack\ xlift\ (length\ [v1])\ (compxE2\ e2\ 0\ 0)))\ t\ h\ (stk\ @\ [v1],\ loc,\ length\ (compE2\ e1) + pc,\ [a])$   
 $ta\ h'\ (stk''\ @\ [v1],\ loc',\ length\ (compE2\ e1) + (pc' - length\ (compE2\ e1)),\ xcp')$   
 by(*rule append-exec-meth-xt*)(*auto*)  
 hence  $exec\ meth\ d\ (compP2\ P)\ ((compE2\ e1\ @\ compE2\ e2)\ @\ [BinOpInstr\ bop])\ (compxE2\ e1\ 0\ 0$   
 $@\ shift\ (length\ (compE2\ e1))\ (stack\ xlift\ (length\ [v1])\ (compxE2\ e2\ 0\ 0)))\ t\ h\ (stk\ @\ [v1],\ loc,\ length$   
 $(compE2\ e1) + pc,\ [a])$   
 $ta\ h'\ (stk''\ @\ [v1],\ loc',\ length\ (compE2\ e1) + (pc' - length\ (compE2\ e1)),\ xcp')$   
 by(*rule exec-meth-append*)  
 moreover from  $exec'$  have  $pc': pc' \geq length\ (compE2\ e1)$   
 by(*rule exec-meth-drop-xt-pc*)(*auto simp add: stack-xlift-compxE2*)  
 ultimately show  $?case$  using  $stk'$  by(*auto simp add: stack-xlift-compxE2 shift-compxE2*)  
 next  
 case *bisim1BinOpThrow* thus  $?case$   
 by(*auto elim!: exec-meth.cases dest: match-ex-table-pcsD simp add: stack-xlift-compxEs2 stack-xlift-compxE2*)  
 next  
 case (*bisim1LAss1 e n e' xs stk loc pc xcp V*)  
 note  $bisim = \langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$   
 note  $IH = \langle \bigwedge stk'\ loc'\ pc'\ xcp'\ STK. ?exec\ e\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \Rightarrow ?concl\ e\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$   
 note  $exec = \langle ?exec\ (V := e)\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$   
 from *bisim* have  $pc: pc \leq length\ (compE2\ e)$  by(*rule bisim1-pc-length-compE2*)  
 show  $?case$   
 proof(*cases pc < length (compE2 e)*)  
 case *True*  
 with  $exec$  have  $?exec\ e\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp'$   
 by(*simp add: compxE2-size-convs*)(*erule exec-meth-take*)  
 from *IH[OF this]* show  $?thesis$  by *auto*  
 next  
 case *False*  
 with  $pc$  have [*simp*]:  $pc = length\ (compE2\ e)$  by *simp*  
 with *bisim* obtain  $v$  where [*simp*]:  $stk = [v]\ xcp = None$   
 by(*auto dest: dest: bisim1-pc-length-compE2D*)  
 with  $exec$  show  $?thesis$  apply(*simp*)  
 by(*erule exec-meth.cases*)(*auto intro!: exec-meth.intros*)  
 qed  
 next  
 case (*bisim1LAss2 e n xs V*)  
 thus  $?case$  by(*fastforce elim: exec-meth.cases intro: exec-meth.intros*)  
 next  
 case (*bisim1LAssThrow e n a xs stk loc pc V*)  
 note  $bisim = \langle P, e, h \vdash (Throw\ a,\ xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$   
 note  $IH = \langle \bigwedge stk'\ loc'\ pc'\ xcp'\ STK. ?exec\ e\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \Rightarrow ?concl\ e\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$   
 note  $exec = \langle ?exec\ (V := e)\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$



```

from bisim have pc: pc < length (compE2 e) and [simp]: xs = loc
  by(auto dest: bisim1-ThrowD)
from exec have ?exec e stk STK loc pc [a] stk' loc' pc' xcp'
  by(simp)(erule exec-meth-take[OF - pc])
from IH[OF this] show ?case by(auto)
next
case (bisim1AAcc1 a n a' xs stk loc pc xcp i)
note IH1 = ⟨∧stk' loc' pc' xcp' STK. ?exec a stk STK loc pc xcp stk' loc' pc' xcp'⟩
   $\implies ?concl\ a\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp'$ 
note IH2 = ⟨∧xs stk' loc' pc' xcp' STK. ?exec i [] STK xs 0 None stk' loc' pc' xcp'⟩
   $\implies ?concl\ i\ []\ STK\ xs\ 0\ None\ stk'\ loc'\ pc'\ xcp'$ 
note bisim1 = ⟨P, a, h ⊢ (a', xs) ↔ (stk, loc, pc, xcp)⟩
note bisim2 = ⟨P, i, h ⊢ (i, loc) ↔ ([], loc, 0, None)⟩
note exec = ⟨?exec (a[i]) stk STK loc pc xcp stk' loc' pc' xcp'⟩
from bisim1 have pc: pc ≤ length (compE2 a) by(rule bisim1-pc-length-compE2)
show ?case
proof(cases pc < length (compE2 a))
  case True
    with exec have ?exec a stk STK loc pc xcp stk' loc' pc' xcp'
      by(simp add: compxE2-size-convs)(erule exec-meth-take-xt)
    from IH1[OF this] show ?thesis by auto
  next
    case False
      with pc have pc: pc = length (compE2 a) by simp
      with exec have pc' ≥ length (compE2 a)
        by(simp add: compxE2-size-convs stack-xlift-compxE2)(auto elim!: exec-meth-drop-xt-pc)
      then obtain PC where PC: pc' = PC + length (compE2 a)
        by  $-(rule-tac\ PC34=pc' - length\ (compE2\ a)\ in\ that,\ simp)$ 
      from pc bisim1 obtain v where stk = [v] xcp = None by(auto dest: bisim1-pc-length-compE2D)
      with exec pc have exec-meth-d (compP2 P) (compE2 a @ compE2 i)
        (stack-xlift (length STK) (compxE2 a 0 0 @ compxE2 i (length (compE2 a)) (Suc 0))) t h (stk @
STK, loc, length (compE2 a) + 0, xcp) ta h' (stk', loc', pc', xcp')
        by $-(rule\ exec-meth-take,\ auto)$ 
      hence ?exec i [] (v # STK) loc 0 None stk' loc' (pc' - length (compE2 a)) xcp'
        using  $\langle stk = [v] \rangle \langle xcp = None \rangle$ 
        by  $-(rule\ exec-meth-drop-xt,\ auto\ simp\ add:\ stack-xlift-compxE2\ shift-compxE2)$ 
      from IH2[OF this] PC obtain stk'' where stk': stk' = stk'' @ v # STK
        and exec-meth-d (compP2 P) (compE2 i) (compxE2 i 0 0) t h ([], loc, 0, None) ta h' (stk'', loc',
PC, xcp') by auto
      hence exec-meth-d (compP2 P) ((compE2 a @ compE2 i) @ [ALoad])
        (compxE2 a 0 0 @ shift (length (compE2 a)) (stack-xlift (length [v]) (compxE2 i 0 0))) t h
        ( $[] @ [v], loc, length (compE2 a) + 0, None$ ) ta h' (stk'' @ [v], loc', length (compE2 a) + PC,
xcp')
        apply  $-$ 
        apply(rule exec-meth-append)
        apply(rule append-exec-meth-xt)
        apply(erule exec-meth-stk-offer)
        by(auto)
      thus ?thesis using  $\langle stk = [v] \rangle \langle xcp = None \rangle\ stk'\ pc\ PC$ 
        by(clarsimp simp add: shift-compxE2 stack-xlift-compxE2 ac-simps)
    qed
  next
    case (bisim1AAcc2 i n i' xs stk loc pc xcp a v1)
    note IH2 = ⟨∧stk' loc' pc' xcp' STK. ?exec i stk STK loc pc xcp stk' loc' pc' xcp'⟩

```

$\implies ?concl\ i\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp'$   
**note**  $bisim2 = \langle P, i, h \vdash (i', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$   
**note**  $exec = \langle ?exec\ (a[i])\ (stk\ @\ [v1])\ STK\ loc\ (length\ (compE2\ a) + pc)\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$   
**from**  $bisim2$  **have**  $pc: pc \leq length\ (compE2\ i)$  **by**  $(rule\ bisim1-pc-length-compE2)$   
**show**  $?case$   
**proof**  $(cases\ pc < length\ (compE2\ i))$   
**case**  $True$   
**from**  $exec$  **have**  $exec-meth-d\ (compP2\ P)\ ((compE2\ a\ @\ compE2\ i)\ @\ [ALoad])$   
 $(stack-xlift\ (length\ STK)\ (compxE2\ a\ 0\ 0)\ @\ shift\ (length\ (compE2\ a))\ (stack-xlift\ (length\ STK)$   
 $(compxE2\ i\ 0\ (Suc\ 0))))\ t$   
 $h\ (stk\ @\ v1\ \# STK, loc, length\ (compE2\ a) + pc, xcp)\ ta\ h'\ (stk', loc', pc', xcp')$  **by**  $(simp\ add:$   
 $compxE2-size-conv)$   
**hence**  $exec': exec-meth-d\ (compP2\ P)\ (compE2\ a\ @\ compE2\ i)\ (stack-xlift\ (length\ STK)\ (compxE2$   
 $a\ 0\ 0)\ @$   
 $shift\ (length\ (compE2\ a))\ (stack-xlift\ (length\ STK)\ (compxE2\ i\ 0\ (Suc\ 0))))\ t$   
 $h\ (stk\ @\ v1\ \# STK, loc, length\ (compE2\ a) + pc, xcp)\ ta\ h'\ (stk', loc', pc', xcp')$   
**by**  $(rule\ exec-meth-take)(simp\ add: True)$   
**hence**  $exec-meth-d\ (compP2\ P)\ (compE2\ i)\ (stack-xlift\ (length\ STK)\ (compxE2\ i\ 0\ (Suc\ 0))))\ t$   
 $h\ (stk\ @\ v1\ \# STK, loc, pc, xcp)\ ta\ h'\ (stk', loc', pc' - length\ (compE2\ a), xcp')$   
**by**  $(rule\ exec-meth-drop-xt)(auto\ simp\ add: stack-xlift-compxE2)$   
**hence**  $?exec\ i\ stk\ (v1\ \# STK)\ loc\ pc\ xcp\ stk'\ loc'\ (pc' - length\ (compE2\ a))\ xcp'$   
**by**  $(simp\ add: compxE2-stack-xlift-conv)$   
**from**  $IH2[OF\ this]$  **obtain**  $stk''$  **where**  $stk': stk' = stk''\ @\ v1\ \# STK$   
**and**  $exec'': exec-meth-d\ (compP2\ P)\ (compE2\ i)\ (compxE2\ i\ 0\ 0)\ t\ h\ (stk, loc, pc, xcp)\ ta\ h'$   
 $(stk'', loc', pc' - length\ (compE2\ a), xcp')$  **by**  $blast$   
**from**  $exec''$  **have**  $exec-meth-d\ (compP2\ P)\ (compE2\ i)\ (stack-xlift\ (length\ [v1])\ (compxE2\ i\ 0\ 0))$   
 $t\ h\ (stk\ @\ [v1], loc, pc, xcp)$   
 $ta\ h'\ (stk''\ @\ [v1], loc', pc' - length\ (compE2\ a), xcp')$   
**by**  $(rule\ exec-meth-stk-offer)$   
**hence**  $exec-meth-d\ (compP2\ P)\ (compE2\ a\ @\ compE2\ i)\ (compxE2\ a\ 0\ 0\ @\ shift\ (length\ (compE2$   
 $a))\ (stack-xlift\ (length\ [v1])\ (compxE2\ i\ 0\ 0))))\ t\ h\ (stk\ @\ [v1], loc, length\ (compE2\ a) + pc, xcp)$   
 $ta\ h'\ (stk''\ @\ [v1], loc', length\ (compE2\ a) + (pc' - length\ (compE2\ a)), xcp')$   
**by**  $(rule\ append-exec-meth-xt)\ auto$   
**hence**  $exec-meth-d\ (compP2\ P)\ ((compE2\ a\ @\ compE2\ i)\ @\ [ALoad])\ (compxE2\ a\ 0\ 0\ @\ shift$   
 $(length\ (compE2\ a))\ (stack-xlift\ (length\ [v1])\ (compxE2\ i\ 0\ 0))))\ t\ h\ (stk\ @\ [v1], loc, length\ (compE2$   
 $a) + pc, xcp)$   
 $ta\ h'\ (stk''\ @\ [v1], loc', length\ (compE2\ a) + (pc' - length\ (compE2\ a)), xcp')$   
**by**  $(rule\ exec-meth-append)$   
**moreover from**  $exec'$  **have**  $pc' \geq length\ (compE2\ a)$   
**by**  $(rule\ exec-meth-drop-xt-pc)(auto\ simp\ add: stack-xlift-compxE2)$   
**ultimately show**  $?thesis$  **using**  $stk'$  **by**  $(simp\ add: stack-xlift-compxE2\ shift-compxE2)$   
**next**  
**case**  $False$   
**with**  $pc$  **have**  $pc: pc = length\ (compE2\ i)$  **by**  $simp$   
**with**  $bisim2$  **obtain**  $v2$  **where**  $[simp]: stk = [v2]\ xcp = None$   
**by**  $(auto\ dest: dest: bisim1-pc-length-compE2D)$   
**with**  $exec\ pc$  **show**  $?thesis$   
**by**  $(clarsimp)(erule\ exec-meth.cases, auto\ intro!: exec-meth.intros\ split: if-split-asm)$   
**qed**  
**next**  
**case**  $(bisim1AAccThrow1\ A\ n\ a\ xs\ stk\ loc\ pc\ i)$   
**note**  $bisim1 = \langle P, A, h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$   
**note**  $IH1 = \langle \bigwedge stk'\ loc'\ pc'\ xcp'\ STK. ?exec\ A\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \implies ?concl\ A\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$

**note**  $exec = \langle ?exec (A[i]) \text{ stk } STK \text{ loc } pc \text{ } [a] \text{ stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$   
**from**  $bisim1$  **have**  $pc: pc < length (compE2 A)$  **and**  $[simp]: xs = loc$   
**by**( $auto \text{ dest: } bisim1\text{-ThrowD}$ )  
**from**  $exec$  **have**  $exec\text{-meth-d } (compP2 P) (compE2 A @ (compE2 i @ [ALoad]))$   
 $(stack\text{-xlift } (length STK) (compxE2 A 0 0) @ shift (length (compE2 A)) (stack\text{-xlift } (length STK)$   
 $(compxE2 i 0 (Suc 0)))) t$   
 $h (stk @ STK, loc, pc, [a]) \text{ ta } h' (stk', loc', pc', xcp') \text{ by}(simp \text{ add: } compxE2\text{-size-convs})$   
**hence**  $?exec A \text{ stk } STK \text{ loc } pc \text{ } [a] \text{ stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \text{ by}(\text{rule } exec\text{-meth-take-xt})(\text{rule } pc)$   
**from**  $IH1[OF \text{ this}]$  **show**  $?case \text{ by}(auto)$   
**next**  
**case** ( $bisim1AAccThrow2 i n a xs \text{ stk } loc \text{ pc } A \text{ v1}$ )  
**note**  $bisim2 = \langle P, i, h \vdash (Throw a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$   
**note**  $IH2 = \langle \bigwedge stk' \text{ loc}' \text{ pc}' \text{ xcp}' STK. ?exec i \text{ stk } STK \text{ loc } pc \text{ } [a] \text{ stk}' \text{ loc}' \text{ pc}' \text{ xcp}'$   
 $\implies ?concl i \text{ stk } STK \text{ loc } pc \text{ } [a] \text{ stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$   
**note**  $exec = \langle ?exec (A[i]) (stk @ [v1]) STK \text{ loc } (length (compE2 A) + pc) [a] \text{ stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$   
**from**  $bisim2$  **have**  $pc: pc < length (compE2 i)$  **and**  $[simp]: xs = loc$   
**by**( $auto \text{ dest: } bisim1\text{-ThrowD}$ )  
**from**  $exec$  **have**  $exec\text{-meth-d } (compP2 P) ((compE2 A @ compE2 i) @ [ALoad])$   
 $(stack\text{-xlift } (length STK) (compxE2 A 0 0) @ shift (length (compE2 A)) (stack\text{-xlift } (length STK)$   
 $(compxE2 i 0 (Suc 0)))) t$   
 $h (stk @ v1 \# STK, loc, length (compE2 A) + pc, [a]) \text{ ta } h' (stk', loc', pc', xcp')$   
**by**( $simp \text{ add: } compxE2\text{-size-convs}$ )  
**hence**  $exec': exec\text{-meth-d } (compP2 P) (compE2 A @ compE2 i)$   
 $(stack\text{-xlift } (length STK) (compxE2 A 0 0) @ shift (length (compE2 A)) (stack\text{-xlift } (length STK)$   
 $(compxE2 i 0 (Suc 0)))) t$   
 $h (stk @ v1 \# STK, loc, length (compE2 A) + pc, [a]) \text{ ta } h' (stk', loc', pc', xcp')$   
**by**( $rule \text{ exec-meth-take}(simp \text{ add: } pc)$ )  
**hence**  $exec\text{-meth-d } (compP2 P) (compE2 i) (stack\text{-xlift } (length STK) (compxE2 i 0 (Suc 0))) t$   
 $h (stk @ v1 \# STK, loc, pc, [a]) \text{ ta } h' (stk', loc', pc' - length (compE2 A), xcp')$   
**by**( $rule \text{ exec-meth-drop-xt}(auto \text{ simp add: } stack\text{-xlift-compxE2})$ )  
**hence**  $?exec i \text{ stk } (v1 \# STK) \text{ loc } pc \text{ } [a] \text{ stk}' \text{ loc}' (pc' - length (compE2 A)) \text{ xcp}'$   
**by**( $simp \text{ add: } compxE2\text{-stack-xlift-convs}$ )  
**from**  $IH2[OF \text{ this}]$  **obtain**  $stk''$  **where**  $stk': stk' = stk'' @ v1 \# STK$  **and**  
 $exec'': exec\text{-meth-d } (compP2 P) (compE2 i) (compxE2 i 0 0) t h (stk, loc, pc, [a]) \text{ ta } h' (stk'', loc',$   
 $pc' - length (compE2 A), xcp') \text{ by } blast$   
**from**  $exec''$  **have**  $exec\text{-meth-d } (compP2 P) (compE2 i) (stack\text{-xlift } (length [v1]) (compxE2 i 0 0)) t$   
 $h (stk @ [v1], loc, pc, [a])$   
 $\text{ta } h' (stk'' @ [v1], loc', pc' - length (compE2 A), xcp')$   
**by**( $rule \text{ exec-meth-stk-offer}$ )  
**hence**  $exec\text{-meth-d } (compP2 P) (compE2 A @ compE2 i) (compxE2 A 0 0 @ shift (length (compE2$   
 $A)) (stack\text{-xlift } (length [v1]) (compxE2 i 0 0))) t h (stk @ [v1], loc, length (compE2 A) + pc, [a])$   
 $\text{ta } h' (stk'' @ [v1], loc', length (compE2 A) + (pc' - length (compE2 A)), xcp')$   
**by**( $rule \text{ append-exec-meth-xt}(auto)$ )  
**hence**  $exec\text{-meth-d } (compP2 P) ((compE2 A @ compE2 i) @ [ALoad]) (compxE2 A 0 0 @ shift$   
 $(length (compE2 A)) (stack\text{-xlift } (length [v1]) (compxE2 i 0 0))) t h (stk @ [v1], loc, length (compE2$   
 $A) + pc, [a])$   
 $\text{ta } h' (stk'' @ [v1], loc', length (compE2 A) + (pc' - length (compE2 A)), xcp')$   
**by**( $rule \text{ exec-meth-append}$ )  
**moreover from**  $exec'$  **have**  $pc': pc' \geq length (compE2 A)$   
**by**( $rule \text{ exec-meth-drop-xt-pc}(auto \text{ simp add: } stack\text{-xlift-compxE2})$ )  
**ultimately show**  $?case \text{ using } stk' \text{ by}(auto \text{ simp add: } stack\text{-xlift-compxE2 shift-compxE2})$   
**next**  
**case**  $bisim1AAccFail$  **thus**  $?case$   
**by**( $auto \text{ elim!: } exec\text{-meth.cases dest: match-ex-table-pcsD simp add: stack\text{-xlift-compxEs2 stack\text{-xlift-compxE2}$ )

next

**case** (*bisim1Ass1* *a n a' xs stk loc pc xcp i e*)  
**note** *IH1* =  $\langle \bigwedge \text{stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \text{ STK}. ?\text{exec } a \text{ stk STK loc pc xcp stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$   
 $\implies ?\text{concl } a \text{ stk STK loc pc xcp stk}' \text{ loc}' \text{ pc}' \text{ xcp}'$   
**note** *IH2* =  $\langle \bigwedge \text{xs stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \text{ STK}. ?\text{exec } i \ [] \text{ STK xs } 0 \text{ None stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$   
 $\implies ?\text{concl } i \ [] \text{ STK xs } 0 \text{ None stk}' \text{ loc}' \text{ pc}' \text{ xcp}'$   
**note** *bisim1* =  $\langle P, a, h \vdash (a', \text{xs}) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \rangle$   
**note** *bisim2* =  $\langle P, i, h \vdash (i, \text{loc}) \leftrightarrow ([], \text{loc}, 0, \text{None}) \rangle$   
**note** *exec* =  $\langle ?\text{exec } (a[i] := e) \text{ stk STK loc pc xcp stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$   
**from** *bisim1* **have** *pc*: *pc* ≤ *length* (*compE2 a*) **by** (*rule bisim1-pc-length-compE2*)  
**from** *exec* **have** *exec'*: *exec-meth-d* (*compP2 P*) (*compE2 a @ compE2 i @ compE2 e @ [AStore, Push Unit]*) (*stack-xlift* (*length STK*) (*compxE2 a 0 0 @ shift* (*length* (*compE2 a*)) (*stack-xlift* (*length STK*) (*compxE2 i 0* (*Suc 0*) @ *compxE2 e* (*length* (*compE2 i*)) (*Suc* (*Suc 0*)))))) *t*  
 $h (\text{stk} @ \text{STK}, \text{loc}, \text{pc}, \text{xcp}) \text{ ta } h' (\text{stk}', \text{loc}', \text{pc}', \text{xcp}')$   
**by** (*simp add: compxE2-size-convs*)  
**show** *?case*  
**proof** (*cases pc < length* (*compE2 a*))  
**case** *True*  
**with** *exec'* **have** *?exec a stk STK loc pc xcp stk' loc' pc' xcp'* **by** (*rule exec-meth-take-xt*)  
**from** *IH1* [*OF this*] **show** *?thesis* **by** *auto*  
next  
**case** *False*  
**with** *pc* **have** *pc*: *pc* = *length* (*compE2 a*) **by** *simp*  
**with** *exec'* **have** *pc' ≥ length* (*compE2 a*) **by**  $-(\text{erule exec-meth-drop-xt-pc}, \text{auto})$   
**then obtain** *PC* **where** *PC*: *pc' = PC + length* (*compE2 a*)  
**by**  $-(\text{rule-tac } PC34 = \text{pc}' - \text{length} (\text{compE2 } a) \text{ in that, simp})$   
**from** *pc bisim1* **obtain** *v* **where** *stk = [v] xcp = None* **by** (*auto dest: bisim1-pc-length-compE2D*)  
**with** *exec PC pc*  
**have** *exec-meth-d* (*compP2 P*) (*(compE2 a @ compE2 i) @ compE2 e @ [AStore, Push Unit]*) (*stack-xlift* (*length STK*) (*compxE2 a 0 0 @ shift* (*length* (*compE2 a*)) (*compxE2 i 0* (*Suc 0*))) @ *shift* (*length* (*compE2 a @ compE2 i*)) (*compxE2 e 0* (*length STK + Suc* (*Suc 0*)))))) *t*  
 $h (v \# \text{STK}, \text{loc}, \text{length} (\text{compE2 } a) + 0, \text{None}) \text{ ta } h' (\text{stk}', \text{loc}', \text{length} (\text{compE2 } a) + \text{PC}, \text{xcp}')$   
**by** (*simp add: shift-compxE2 stack-xlift-compxE2 ac-simps*)  
**hence** *exec-meth-d* (*compP2 P*) (*compE2 a @ compE2 i*)  
(*stack-xlift* (*length STK*) (*compxE2 a 0 0 @ shift* (*length* (*compE2 a*)) (*compxE2 i 0* (*Suc 0*)))))) *t*  $h (v \# \text{STK}, \text{loc}, \text{length} (\text{compE2 } a) + 0, \text{None}) \text{ ta } h' (\text{stk}', \text{loc}', \text{length} (\text{compE2 } a) + \text{PC}, \text{xcp}')$   
**by** (*rule exec-meth-take-xt*) *simp*  
**hence** *?exec i [] (v # STK) loc 0 None stk' loc' ((length* (*compE2 a*) *+ PC) - length* (*compE2 a*)) *xcp'*  
**by**  $-(\text{rule exec-meth-drop-xt}, \text{auto simp add: stack-xlift-compxE2 shift-compxE2})$   
**from** *IH2* [*OF this*] *PC* **obtain** *stk''* **where** *stk'*: *stk' = stk'' @ v # STK*  
**and** *exec-meth-d* (*compP2 P*) (*compE2 i*) (*compxE2 i 0 0*) *t*  $h ([], \text{loc}, 0, \text{None}) \text{ ta } h' (\text{stk}'', \text{loc}', \text{PC}, \text{xcp}')$  **by** *auto*  
**hence** *exec-meth-d* (*compP2 P*) (*(compE2 a @ compE2 i) @ (compE2 e @ [AStore, Push Unit])*)  
(*(compxE2 a 0 0 @ shift* (*length* (*compE2 a*)) (*stack-xlift* (*length [v]*) (*compxE2 i 0 0*))) @  
 $\text{shift} (\text{length} (\text{compE2 } a @ \text{compE2 } i)) (\text{compxE2 } e \ 0 \ (\text{Suc} \ (\text{Suc} \ 0)))) \text{ ta } h$   
( $[] @ [v], \text{loc}, \text{length} (\text{compE2 } a) + 0, \text{None}) \text{ ta } h' (\text{stk}'' @ [v], \text{loc}', \text{length} (\text{compE2 } a) + \text{PC}, \text{xcp}')$   
**apply**  $-$   
**apply** (*rule exec-meth-append-xt*)  
**apply** (*rule append-exec-meth-xt*)  
**apply** (*erule exec-meth-stk-offer*)  
**by** (*auto*)  
**thus** *?thesis* **using**  $\langle \text{stk} = [v] \rangle \langle \text{xcp} = \text{None} \rangle \text{ stk}' \text{ pc } \text{PC}$

by(*clarsimp simp add: shift-compxE2 stack-xlift-compxE2 ac-simps*)  
 qed  
 next  
 case (*bisim1AAss2 i n i' xs stk loc pc xcp a e v*)  
 note  $IH2 = \langle \bigwedge \text{stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \text{ STK}. ?\text{exec } i \text{ stk STK loc pc xcp stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$   
 $\implies ?\text{concl } i \text{ stk STK loc pc xcp stk}' \text{ loc}' \text{ pc}' \text{ xcp}'$   
 note  $IH3 = \langle \bigwedge \text{xs stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \text{ STK}. ?\text{exec } e \ [] \text{ STK xs } 0 \text{ None stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$   
 $\implies ?\text{concl } e \ [] \text{ STK xs } 0 \text{ None stk}' \text{ loc}' \text{ pc}' \text{ xcp}'$   
 note *bisim2* =  $\langle P, i, h \vdash (i', \text{xs}) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \rangle$   
 note *bisim3* =  $\langle P, e, h \vdash (e, \text{loc}) \leftrightarrow ([], \text{loc}, 0, \text{None}) \rangle$   
 note *exec* =  $\langle ?\text{exec } (a[i] := e) (\text{stk} @ [v]) \text{ STK loc } (\text{length } (\text{compE2 } a) + \text{pc}) \text{ xcp stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$   
 from *bisim2* have *pc*:  $\text{pc} \leq \text{length } (\text{compE2 } i)$  by(*rule bisim1-pc-length-compE2*)  
 from *exec* have *exec'*:  $\bigwedge v'. \text{exec-meth-d } (\text{compP2 } P) ((\text{compE2 } a @ \text{compE2 } i) @ \text{compE2 } e @ [\text{AStore}, \text{Push Unit}]) ((\text{compxE2 } a \ 0 \ (\text{length } \text{STK}) @ \text{shift } (\text{length } (\text{compE2 } a)) (\text{stack-xlift } (\text{length } (v \# \text{STK})) (\text{compxE2 } i \ 0 \ 0))) @ \text{shift } (\text{length } (\text{compE2 } a @ \text{compE2 } i)) (\text{stack-xlift } (\text{length } (v' \# v \# \text{STK})) (\text{compxE2 } e \ 0 \ 0))) t$   
 $h (\text{stk} @ v \# \text{STK}, \text{loc}, \text{length } (\text{compE2 } a) + \text{pc}, \text{xcp}) \text{ ta } h' (\text{stk}', \text{loc}', \text{pc}', \text{xcp}')$   
 by(*simp add: shift-compxE2 stack-xlift-compxE2*)  
 show ?*case*  
 proof(*cases pc < length (compE2 i)*)  
 case *True* with *exec'*[*of arbitrary*]  
 have *exec''*:  $\text{exec-meth-d } (\text{compP2 } P) (\text{compE2 } a @ \text{compE2 } i) (\text{compxE2 } a \ 0 \ (\text{length } \text{STK}) @ \text{shift } (\text{length } (\text{compE2 } a)) (\text{stack-xlift } (\text{length } (v \# \text{STK})) (\text{compxE2 } i \ 0 \ 0))) t h (\text{stk} @ v \# \text{STK}, \text{loc}, \text{length } (\text{compE2 } a) + \text{pc}, \text{xcp}) \text{ ta } h' (\text{stk}', \text{loc}', \text{pc}', \text{xcp}')$   
 by-(*erule exec-meth-take-xt, simp*)  
 hence ?*exec* *i* *stk* ( $v \# \text{STK}$ ) *loc* *pc* *xcp* *stk'* *loc'* ( $\text{pc}' - \text{length } (\text{compE2 } a)$ ) *xcp'*  
 by(*rule exec-meth-drop-xt*) *auto*  
 from *IH2*[*OF this*] obtain *stk''* where *stk'*:  $\text{stk}' = \text{stk}'' @ v \# \text{STK}$   
 and *exec'''*:  $\text{exec-meth-d } (\text{compP2 } P) (\text{compE2 } i) (\text{compxE2 } i \ 0 \ 0) t h (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \text{ ta } h' (\text{stk}'', \text{loc}', \text{pc}' - \text{length } (\text{compE2 } a), \text{xcp}')$  by *blast*  
 from *exec'''* have  $\text{exec-meth-d } (\text{compP2 } P) ((\text{compE2 } a @ \text{compE2 } i) @ \text{compE2 } e @ [\text{AStore}, \text{Push Unit}]) ((\text{compxE2 } a \ 0 \ 0 @ \text{shift } (\text{length } (\text{compE2 } a)) (\text{stack-xlift } (\text{length } [v]) (\text{compxE2 } i \ 0 \ 0))) @ \text{shift } (\text{length } (\text{compE2 } a @ \text{compE2 } i)) (\text{compxE2 } e \ 0 \ (\text{Suc } (\text{Suc } 0)))) t h (\text{stk} @ [v], \text{loc}, \text{length } (\text{compE2 } a) + \text{pc}, \text{xcp}) \text{ ta } h' (\text{stk}'' @ [v], \text{loc}', \text{length } (\text{compE2 } a) + (\text{pc}' - \text{length } (\text{compE2 } a)), \text{xcp}')$   
 apply -  
 apply(*rule exec-meth-append-xt*)  
 apply(*rule append-exec-meth-xt*)  
 apply(*erule exec-meth-stk-offer*)  
 by *auto*  
 moreover from *exec''* have  $\text{pc}' \geq \text{length } (\text{compE2 } a)$   
 by(*rule exec-meth-drop-xt-pc*) *auto*  
 ultimately show ?*thesis* using *stk'* by(*auto simp add: shift-compxE2 stack-xlift-compxE2*)  
 next  
 case *False*  
 with *pc* have *pc*:  $\text{pc} = \text{length } (\text{compE2 } i)$  by *simp*  
 with *exec'*[*of arbitrary*] have  $\text{pc}' \geq \text{length } (\text{compE2 } a @ \text{compE2 } i)$   
 by-(*erule exec-meth-drop-xt-pc, auto simp add: shift-compxE2 stack-xlift-compxE2*)  
 then obtain *PC* where *PC*:  $\text{pc}' = \text{PC} + \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i)$   
 by -(*rule-tac PC34=pc' - length (compE2 a @ compE2 i) in that, simp*)  
 from *pc bisim2* obtain *v'* where *stk*:  $\text{stk} = [v]$  and *xcp*:  $\text{xcp} = \text{None}$  by(*auto dest: bisim1-pc-length-compE2D*)  
 from *exec'*[*of v'*]  
 have  $\text{exec-meth-d } (\text{compP2 } P) (\text{compE2 } e @ [\text{AStore}, \text{Push Unit}]) (\text{stack-xlift } (\text{length } (v' \# v \# \text{STK})) (\text{compxE2 } e \ 0 \ 0)) t$   
 $h (v' \# v \# \text{STK}, \text{loc}, 0, \text{xcp}) \text{ ta } h' (\text{stk}', \text{loc}', \text{pc}' - \text{length } (\text{compE2 } a @ \text{compE2 } i),$

$xcp'$   
**unfolding**  $stk\ pc\ append\ Cons\ append\ Nil$   
**by**  $-(rule\ exec\ meth\ drop\ xt,\ simp\ only:\ add\ 0\ right\ length\ append,\ auto\ simp\ add:\ shift\ compxE2\ stack\ xlift\ compxE2)$   
**with**  $PC\ xcp\ have\ ?exec\ e\ []\ (v' \# v \# STK)\ loc\ 0\ None\ stk'\ loc'\ PC\ xcp'$   
**by**  $-(rule\ exec\ meth\ take,\ auto)$   
**from**  $IH3[OF\ this]\ obtain\ stk''\ where\ stk':\ stk' = stk'' @ v' \# v \# STK$   
**and**  $exec\ meth\ d\ (compP2\ P)\ (compE2\ e)\ (compxE2\ e\ 0\ 0)\ t\ h\ ([],\ loc,\ 0,\ None)\ ta\ h'\ (stk'',\ loc',\ PC,\ xcp')\ by\ auto$   
**hence**  $exec\ meth\ d\ (compP2\ P)\ (((compE2\ a @ compE2\ i) @ compE2\ e) @ [AStore,\ Push\ Unit])\ ((compxE2\ a\ 0\ 0 @ compxE2\ i\ (length\ (compE2\ a))\ (Suc\ 0)) @ shift\ (length\ (compE2\ a @ compE2\ i))\ (stack\ xlift\ (length\ [v',\ v])\ (compxE2\ e\ 0\ 0)))\ t\ h\ ([ @ [v',\ v],\ loc,\ length\ (compE2\ a @ compE2\ i) + 0,\ None)\ ta\ h'\ (stk'' @ [v',\ v],\ loc',\ length\ (compE2\ a @ compE2\ i) + PC,\ xcp')$   
**apply**  $-$   
**apply**  $(rule\ exec\ meth\ append)$   
**apply**  $(rule\ append\ exec\ meth\ xt)$   
**apply**  $(erule\ exec\ meth\ stk\ offer)$   
**by**  $auto$   
**thus**  $?thesis\ using\ stk\ xcp\ stk'\ pc\ PC$   
**by**  $(clarsimp\ simp\ add:\ shift\ compxE2\ stack\ xlift\ compxE2\ ac\_simps)$   
**qed**  
**next**  
**case**  $(bisim1AAss3\ e\ n\ e'\ xs\ stk\ loc\ pc\ xcp\ a\ i\ v1\ v2)$   
**note**  $IH3 = \langle \bigwedge stk'\ loc'\ pc'\ xcp'\ STK.\ ?exec\ e\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \Rightarrow ?concl\ e\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$   
**note**  $bisim3 = \langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$   
**note**  $exec = \langle ?exec\ (a[i] := e)\ (stk @ [v2,\ v1])\ STK\ loc\ (length\ (compE2\ a) + length\ (compE2\ i) + pc)\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$   
**from**  $bisim3\ have\ pc:\ pc \leq length\ (compE2\ e)\ by\ (rule\ bisim1\ pc\ length\ compE2)$   
**show**  $?case$   
**proof**  $(cases\ pc < length\ (compE2\ e))$   
**case**  $True$   
**from**  $exec\ have\ exec\ meth\ d\ (compP2\ P)\ (((compE2\ a @ compE2\ i) @ compE2\ e) @ [AStore,\ Push\ Unit])\ ((compxE2\ a\ 0\ (length\ STK) @ compxE2\ i\ (length\ (compE2\ a))\ (Suc\ (length\ STK))) @ shift\ (length\ (compE2\ a @ compE2\ i))\ (stack\ xlift\ (length\ (v2 \# v1 \# STK))\ (compxE2\ e\ 0\ 0)))\ t\ h\ (stk @ v2 \# v1 \# STK,\ loc,\ length\ (compE2\ a @ compE2\ i) + pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$   
**by**  $(simp\ add:\ shift\ compxE2\ stack\ xlift\ compxE2)$   
**hence**  $exec':\ exec\ meth\ d\ (compP2\ P)\ ((compE2\ a @ compE2\ i) @ compE2\ e)\ ((compxE2\ a\ 0\ (length\ STK) @ compxE2\ i\ (length\ (compE2\ a))\ (Suc\ (length\ STK))) @ shift\ (length\ (compE2\ a @ compE2\ i))\ (stack\ xlift\ (length\ (v2 \# v1 \# STK))\ (compxE2\ e\ 0\ 0)))\ t\ h\ (stk @ v2 \# v1 \# STK,\ loc,\ length\ (compE2\ a @ compE2\ i) + pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$   
**by**  $(rule\ exec\ meth\ take)(simp\ add:\ True)$   
**hence**  $?exec\ e\ stk\ (v2 \# v1 \# STK)\ loc\ pc\ xcp\ stk'\ loc'\ (pc' - length\ (compE2\ a @ compE2\ i))\ xcp'$   
**by**  $(rule\ exec\ meth\ drop\ xt)\ auto$   
**from**  $IH3[OF\ this]\ obtain\ stk''\ where\ stk':\ stk' = stk'' @ v2 \# v1 \# STK$   
**and**  $exec'':\ exec\ meth\ d\ (compP2\ P)\ (compE2\ e)\ (compxE2\ e\ 0\ 0)\ t\ h\ (stk,\ loc,\ pc,\ xcp)\ ta\ h'\ (stk'',\ loc',\ pc' - length\ (compE2\ a @ compE2\ i),\ xcp')\ by\ blast$   
**from**  $exec''\ have\ exec\ meth\ d\ (compP2\ P)\ (compE2\ e)\ (stack\ xlift\ (length\ [v2,\ v1])\ (compxE2\ e\ 0\ 0))\ t\ h\ (stk @ [v2,\ v1],\ loc,\ pc,\ xcp)\ ta\ h'\ (stk'' @ [v2,\ v1],\ loc',\ pc' - length\ (compE2\ a @ compE2\ i),\ xcp')$

by(rule exec-meth-stk-offer)  
 hence exec-meth-d (compP2 P) ((compE2 a @ compE2 i) @ compE2 e) ((compxE2 a 0 0 @ compxE2 i (length (compE2 a)) (Suc 0)) @ shift (length (compE2 a @ compE2 i)) (stack-xlift (length [v2, v1]) (compxE2 e 0 0))) t  
 h (stk @ [v2, v1], loc, length (compE2 a @ compE2 i) + pc, xcp)  
 ta h' (stk'' @ [v2, v1], loc', length (compE2 a @ compE2 i) + (pc' - length (compE2 a @ compE2 i)), xcp')  
 by(rule append-exec-meth-xt) auto  
 hence exec-meth-d (compP2 P) (((compE2 a @ compE2 i) @ compE2 e) @ [AStore, Push Unit]) ((compxE2 a 0 0 @ compxE2 i (length (compE2 a)) (Suc 0)) @ shift (length (compE2 a @ compE2 i)) (stack-xlift (length [v2, v1]) (compxE2 e 0 0))) t  
 h (stk @ [v2, v1], loc, length (compE2 a @ compE2 i) + pc, xcp)  
 ta h' (stk'' @ [v2, v1], loc', length (compE2 a @ compE2 i) + (pc' - length (compE2 a @ compE2 i)), xcp')  
 by(rule exec-meth-append)  
 moreover from exec' have pc' ≥ length (compE2 a @ compE2 i)  
 by(rule exec-meth-drop-xt-pc)(auto simp add: stack-xlift-compxE2)  
 ultimately show ?thesis using stk' by(simp add: stack-xlift-compxE2 shift-compxE2)  
 next  
 case False  
 with pc have pc: pc = length (compE2 e) by simp  
 with bisim3 obtain v3 where [simp]: stk = [v3] xcp = None  
 by(auto dest: dest: bisim1-pc-length-compE2D)  
 with exec pc show ?thesis apply(simp)  
 by(erule exec-meth.cases)(auto intro!: exec-meth.intros split: if-split-asm)  
 qed  
 next  
 case (bisim1AAssThrow1 A n a xs stk loc pc i e)  
 note bisim1 = ⟨P, A, h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩  
 note IH1 = ⟨∧stk' loc' pc' xcp' STK. ?exec A stk STK loc pc [a] stk' loc' pc' xcp' ⇒ ?concl A stk STK loc pc [a] stk' loc' pc' xcp'⟩  
 note exec = ⟨?exec (A[i] := e) stk STK loc pc [a] stk' loc' pc' xcp'⟩  
 from bisim1 have pc: pc < length (compE2 A) and [simp]: xs = loc  
 by(auto dest: bisim1-ThrowD)  
 from exec have exec-meth-d (compP2 P) (compE2 A @ (compE2 i @ compE2 e @ [AStore, Push Unit]))  
 (stack-xlift (length STK) (compxE2 A 0 0) @ shift (length (compE2 A)) (stack-xlift (length STK) (compxE2 i 0 (Suc 0) @ compxE2 e (length (compE2 i)) (Suc (Suc 0)))) t  
 h (stk @ STK, loc, pc, [a]) ta h' (stk', loc', pc', xcp') by(simp add: compxE2-size-convs)  
 hence ?exec A stk STK loc pc [a] stk' loc' pc' xcp' by(rule exec-meth-take-xt)(rule pc)  
 from IH1[OF this] show ?case by(auto)  
 next  
 case (bisim1AAssThrow2 i n a xs stk loc pc A e v1)  
 note bisim2 = ⟨P, i, h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩  
 note IH2 = ⟨∧stk' loc' pc' xcp' STK. ?exec i stk STK loc pc [a] stk' loc' pc' xcp' ⇒ ?concl i stk STK loc pc [a] stk' loc' pc' xcp'⟩  
 note exec = ⟨?exec (A[i] := e) (stk @ [v1]) STK loc (length (compE2 A) + pc) [a] stk' loc' pc' xcp'⟩  
 from bisim2 have pc: pc < length (compE2 i) and [simp]: xs = loc  
 by(auto dest: bisim1-ThrowD)  
 from exec have exec-meth-d (compP2 P) ((compE2 A @ compE2 i) @ compE2 e @ [AStore, Push Unit])  
 ((stack-xlift (length STK) (compxE2 A 0 0) @ shift (length (compE2 A)) (stack-xlift (length STK) (compxE2 i 0 (Suc 0)))) @ (shift (length (compE2 A @ compE2 i)) (compxE2 e 0 (Suc (Suc (length

$STK)))))) t$   
 $h (stk @ v1 \# STK, loc, length (compE2 A) + pc, [a]) ta h' (stk', loc', pc', xcp')$   
**by**(simp add: shift-compxE2 stack-xlift-compxE2 ac-simps)  
**hence**  $exec'$ :  $exec\text{-meth-d} (compP2 P) (compE2 A @ compE2 i)$   
 $(stack\text{-xlift} (length STK) (compxE2 A 0 0) @ shift (length (compE2 A)) (stack\text{-xlift} (length STK)$   
 $(compxE2 i 0 (Suc 0)))) t$   
 $h (stk @ v1 \# STK, loc, length (compE2 A) + pc, [a]) ta h' (stk', loc', pc', xcp')$   
**by**(rule  $exec\text{-meth-take-xt}$ )(simp add: pc)  
**hence**  $exec\text{-meth-d} (compP2 P) (compE2 i) (stack\text{-xlift} (length STK) (compxE2 i 0 (Suc 0))) t$   
 $h (stk @ v1 \# STK, loc, pc, [a]) ta h' (stk', loc', pc' - length (compE2 A), xcp')$   
**by**(rule  $exec\text{-meth-drop-xt}$ )(auto simp add: stack-xlift-compxE2)  
**hence**  $?exec i stk (v1 \# STK) loc pc [a] stk' loc' (pc' - length (compE2 A)) xcp'$   
**by**(simp add: compxE2-stack-xlift-conv)  
**from**  $IH2[OF this]$  **obtain**  $stk''$  **where**  $stk' : stk' = stk'' @ v1 \# STK$  **and**  
 $exec'' : exec\text{-meth-d} (compP2 P) (compE2 i) (compxE2 i 0 0) t h (stk, loc, pc, [a]) ta h' (stk'', loc',$   
 $pc' - length (compE2 A), xcp')$  **by** blast  
**from**  $exec''$  **have**  $exec\text{-meth-d} (compP2 P) (compE2 i) (stack\text{-xlift} (length [v1]) (compxE2 i 0 0)) t$   
 $h (stk @ [v1], loc, pc, [a])$   
 $ta h' (stk'' @ [v1], loc', pc' - length (compE2 A), xcp')$   
**by**(rule  $exec\text{-meth-stk-offer}$ )  
**hence**  $exec\text{-meth-d} (compP2 P) (compE2 A @ compE2 i) (compxE2 A 0 0 @ shift (length (compE2$   
 $A)) (stack\text{-xlift} (length [v1]) (compxE2 i 0 0))) t$   
 $h (stk @ [v1], loc, length (compE2 A) + pc, [a])$   
 $ta h' (stk'' @ [v1], loc', length (compE2 A) + (pc' - length (compE2 A)), xcp')$   
**by**(rule  $append\text{-exec-meth-xt}$ )(auto)  
**hence**  $exec\text{-meth-d} (compP2 P) ((compE2 A @ compE2 i) @ compE2 e @ [AStore, Push Unit])$   
 $((compxE2 A 0 0 @ shift (length (compE2 A)) (stack\text{-xlift} (length [v1]) (compxE2 i 0 0))) @ (shift$   
 $(length (compE2 A @ compE2 i)) (compxE2 e 0 (Suc (Suc 0))))) t$   
 $h (stk @ [v1], loc, length (compE2 A) + pc, [a])$   
 $ta h' (stk'' @ [v1], loc', length (compE2 A) + (pc' - length (compE2 A)), xcp')$   
**by**(rule  $exec\text{-meth-append-xt}$ )  
**moreover from**  $exec'$  **have**  $pc' : pc' \geq length (compE2 A)$   
**by**(rule  $exec\text{-meth-drop-xt-pc}$ )(auto simp add: stack-xlift-compxE2)  
**ultimately show**  $?case$  **using**  $stk'$  **by**(auto simp add: stack-xlift-compxE2 shift-compxE2)  
**next**  
**case** ( $bisim1AAssThrow3 e n a xs stk loc pc A i v2 v1$ )  
**note**  $bisim3 = \langle P, e, h \vdash (Throw a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$   
**note**  $IH3 = \langle \bigwedge stk' loc' pc' xcp' STK. ?exec e stk STK loc pc [a] stk' loc' pc' xcp' \Rightarrow ?concl e stk STK loc pc [a] stk' loc' pc' xcp' \rangle$   
**note**  $exec = \langle ?exec (A[i] := e) (stk @ [v2, v1]) STK loc (length (compE2 A) + length (compE2 i)$   
 $+ pc) [a] stk' loc' pc' xcp' \rangle$   
**from**  $bisim3$  **have**  $pc : pc < length (compE2 e)$  **and**  $[simp] : xs = loc$   
**by**(auto dest:  $bisim1\text{-ThrowD}$ )  
**from**  $exec$  **have**  $exec\text{-meth-d} (compP2 P) (((compE2 A @ compE2 i) @ compE2 e) @ [AStore, Push$   
 $Unit])$   
 $((stack\text{-xlift} (length STK) (compxE2 A 0 0 @ compxE2 i (length (compE2 A)) (Suc 0))) @ shift$   
 $(length (compE2 A @ compE2 i)) (stack\text{-xlift} (length (v2 \# v1 \# STK)) (compxE2 e 0 0))) t$   
 $h (stk @ v2 \# v1 \# STK, loc, length (compE2 A @ compE2 i) + pc, [a]) ta h' (stk', loc', pc',$   
 $xcp')$   
**by**(simp add: shift-compxE2 stack-xlift-compxE2 ac-simps)  
**hence**  $exec'$ :  $exec\text{-meth-d} (compP2 P) ((compE2 A @ compE2 i) @ compE2 e)$   
 $((stack\text{-xlift} (length STK) (compxE2 A 0 0 @ compxE2 i (length (compE2 A)) (Suc 0))) @ shift$   
 $(length (compE2 A @ compE2 i)) (stack\text{-xlift} (length (v2 \# v1 \# STK)) (compxE2 e 0 0))) t$   
 $h (stk @ v2 \# v1 \# STK, loc, length (compE2 A @ compE2 i) + pc, [a]) ta h' (stk', loc', pc',$



$xcp'$   
**by**(rule *exec-meth-take*)(simp add: pc)  
**hence**  $?exec\ e\ stk\ (v2\ \# \ v1\ \# \ STK)\ loc\ pc\ [a]\ stk'\ loc'\ (pc' - length\ (compE2\ A\ @\ compE2\ i))$   
 $xcp'$   
**by**(rule *exec-meth-drop-xt*) auto  
**from** *IH3[OF this]* **obtain**  $stk''$  **where**  $stk':\ stk' = stk''\ @\ v2\ \# \ v1\ \# \ STK$  **and**  
 $exec'':\ exec\ meth\ d\ (compP2\ P)\ (compE2\ e)\ (compxE2\ e\ 0\ 0)\ t\ h\ (stk,\ loc,\ pc,\ [a])\ ta\ h'\ (stk'',$   
 $loc',\ pc' - length\ (compE2\ A\ @\ compE2\ i),\ xcp')$  **by** blast  
**from**  $exec''$  **have**  $exec\ meth\ d\ (compP2\ P)\ (compE2\ e)\ (stack\ xlift\ (length\ [v2,\ v1])\ (compxE2\ e\ 0\ 0))\ t\ h\ (stk\ @\ [v2,\ v1],\ loc,\ pc,\ [a])$   
 $ta\ h'\ (stk''\ @\ [v2,\ v1],\ loc',\ pc' - length\ (compE2\ A\ @\ compE2\ i),\ xcp')$   
**by**(rule *exec-meth-stk-offer*)  
**hence**  $exec\ meth\ d\ (compP2\ P)\ (((compE2\ A\ @\ compE2\ i)\ @\ compE2\ e)\ ((compxE2\ A\ 0\ 0\ @\ compxE2\ i\ (length\ (compE2\ A))\ (Suc\ 0))\ @\ shift\ (length\ (compE2\ A\ @\ compE2\ i))\ (stack\ xlift\ (length\ [v2,\ v1])\ (compxE2\ e\ 0\ 0))))\ t\ h\ (stk\ @\ [v2,\ v1],\ loc,\ length\ (compE2\ A\ @\ compE2\ i) + pc,\ [a])$   
 $ta\ h'\ (stk''\ @\ [v2,\ v1],\ loc',\ length\ (compE2\ A\ @\ compE2\ i) + (pc' - length\ (compE2\ A\ @\ compE2\ i)),\ xcp')$   
**by**(rule *append-exec-meth-xt*)(auto)  
**hence**  $exec\ meth\ d\ (compP2\ P)\ (((compE2\ A\ @\ compE2\ i)\ @\ compE2\ e)\ @\ [AStore,\ Push\ Unit])\ ((compxE2\ A\ 0\ 0\ @\ compxE2\ i\ (length\ (compE2\ A))\ (Suc\ 0))\ @\ shift\ (length\ (compE2\ A\ @\ compE2\ i))\ (stack\ xlift\ (length\ [v2,\ v1])\ (compxE2\ e\ 0\ 0))))\ t\ h\ (stk\ @\ [v2,\ v1],\ loc,\ length\ (compE2\ A\ @\ compE2\ i) + pc,\ [a])$   
 $ta\ h'\ (stk''\ @\ [v2,\ v1],\ loc',\ length\ (compE2\ A\ @\ compE2\ i) + (pc' - length\ (compE2\ A\ @\ compE2\ i)),\ xcp')$   
**by**(rule *exec-meth-append*)  
**moreover from**  $exec'$  **have**  $pc':\ pc' \geq length\ (compE2\ A\ @\ compE2\ i)$   
**by**(rule *exec-meth-drop-xt-pc*)(auto simp add: *stack-xlift-compxE2*)  
**ultimately show**  $?case$  **using**  $stk'$  **by**(auto simp add: *stack-xlift-compxE2 shift-compxE2*)  
**next**  
**case** *bisim1AAssFail* **thus**  $?case$   
**by**(auto elim!: *exec-meth.cases* dest: *match-ex-table-pcsD* simp add: *stack-xlift-compxEs2 stack-xlift-compxE2*)  
**next**  
**case** *bisim1AAss4* **thus**  $?case$   
**by**  $-(erule\ exec\ meth.cases,\ auto\ intro!:\ exec\ meth.exec\ instr)$   
**next**  
**case** (*bisim1ALength*  $a\ n\ a'\ xs\ stk\ loc\ pc\ xcp$ )  
**note**  $bisim = \langle P, a, h \vdash (a', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$   
**note**  $IH = \langle \bigwedge stk'\ loc'\ pc'\ xcp'\ STK.\ ?exec\ a\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \Rightarrow ?concl\ a\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$   
**note**  $exec = \langle ?exec\ (a.length)\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$   
**from** *bisim* **have**  $pc:\ pc \leq length\ (compE2\ a)$  **by**(rule *bisim1-pc-length-compE2*)  
**show**  $?case$   
**proof**(cases  $pc < length\ (compE2\ a)$ )  
**case** *True*  
**with**  $exec$  **have**  $?exec\ a\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp'$   
**by**(simp add: *compxE2-size-convs*)(erule *exec-meth-take*)  
**from** *IH[OF this]* **show**  $?thesis$  **by** auto  
**next**  
**case** *False*  
**with**  $pc$  **have**  $[simp]:\ pc = length\ (compE2\ a)$  **by** simp  
**with** *bisim* **obtain**  $v$  **where**  $[simp]:\ stk = [v]\ xcp = None$   
**by**(auto dest: *dest: bisim1-pc-length-compE2D*)  
**with**  $exec$  **show**  $?thesis$  **apply**(simp)  
**by**(erule *exec-meth.cases*)(auto intro!: *exec-meth.intros split: if-split-asm*)

```

qed
next
case (bisim1ALengthThrow e n a xs stk loc pc)
note bisim =  $\langle P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$ 
note IH =  $\langle \bigwedge stk' loc' pc' xcp' STK. ?exec\ e\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \Rightarrow ?concl\ e\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$ 
note exec =  $\langle ?exec\ (e.length)\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$ 
from bisim have pc:  $pc < length\ (compE2\ e)$  and [simp]:  $xs = loc$ 
by(auto dest: bisim1-ThrowD)
from exec have ?exec e stk STK loc pc [a] stk' loc' pc' xcp'
by(simp)(erule exec-meth-take[OF - pc])
from IH[OF this] show ?case by(auto)
next
case bisim1ALengthNull thus ?case
by(auto elim!: exec-meth.cases dest: match-ex-table-pcsD simp add: stack-xlift-compxEs2 stack-xlift-compxE2)
next
case (bisim1FAcc e n e' xs stk loc pc xcp F D)
note bisim =  $\langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
note IH =  $\langle \bigwedge stk' loc' pc' xcp' STK. ?exec\ e\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \Rightarrow ?concl\ e\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$ 
note exec =  $\langle ?exec\ (e.F\{D\})\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$ 
from bisim have pc:  $pc \leq length\ (compE2\ e)$  by(rule bisim1-pc-length-compE2)
show ?case
proof(cases pc < length (compE2 e))
case True
with exec have ?exec e stk STK loc pc xcp stk' loc' pc' xcp'
by(simp add: compxE2-size-convs)(erule exec-meth-take)
from IH[OF this] show ?thesis by auto
next
case False
with pc have [simp]:  $pc = length\ (compE2\ e)$  by simp
with bisim obtain v where [simp]:  $stk = [v]\ xcp = None$ 
by(auto dest: dest: bisim1-pc-length-compE2D)
with exec show ?thesis apply(simp)
by(erule exec-meth.cases)(fastforce intro!: exec-meth.intros simp add: is-Ref-def split: if-split-asm)+
qed
next
case (bisim1FAccThrow e n a xs stk loc pc F D)
note bisim =  $\langle P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$ 
note IH =  $\langle \bigwedge stk' loc' pc' xcp' STK. ?exec\ e\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \Rightarrow ?concl\ e\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$ 
note exec =  $\langle ?exec\ (e.F\{D\})\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$ 
from bisim have pc:  $pc < length\ (compE2\ e)$  and [simp]:  $xs = loc$ 
by(auto dest: bisim1-ThrowD)
from exec have ?exec e stk STK loc pc [a] stk' loc' pc' xcp'
by(simp)(erule exec-meth-take[OF - pc])
from IH[OF this] show ?case by(auto)
next
case bisim1FAccNull thus ?case
by(auto elim!: exec-meth.cases dest: match-ex-table-pcsD simp add: stack-xlift-compxEs2 stack-xlift-compxE2)
next
case (bisim1FAss1 e n e' xs stk loc pc xcp e2 F D)
note IH1 =  $\langle \bigwedge stk' loc' pc' xcp' STK. ?exec\ e\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \Rightarrow ?concl\ e\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$ 

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note IH2 = ⟨ $\bigwedge xs\ stk'\ loc'\ pc'\ xcp'\ STK. ?exec\ e2 \sqcap STK\ xs\ 0\ None\ stk'\ loc'\ pc'\ xcp'$ 
 $\implies ?concl\ e2 \sqcap STK\ xs\ 0\ None\ stk'\ loc'\ pc'\ xcp'$ ⟩
note bisim1 = ⟨ $P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp)$ ⟩
note bisim2 = ⟨ $P, e2, h \vdash (e2, loc) \leftrightarrow (\sqcap, loc, 0, None)$ ⟩
note exec = ⟨ $?exec\ (e \cdot F\{D\} := e2)\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp'$ ⟩
from bisim1 have pc: pc ≤ length (compE2 e) by(rule bisim1-pc-length-compE2)
show ?case
proof(cases pc < length (compE2 e))
  case True
    with exec have ?exec e stk STK loc pc xcp stk' loc' pc' xcp'
      by(simp add: compxE2-size-convs)(erule exec-meth-take-xt)
    from IH1[OF this] show ?thesis by auto
  next
    case False
      with pc have pc: pc = length (compE2 e) by simp
      with exec have pc' ≥ length (compE2 e)
        by(simp add: compxE2-size-convs stack-xlift-compxE2)(auto elim!: exec-meth-drop-xt-pc)
      then obtain PC where PC: pc' = PC + length (compE2 e)
        by -(rule-tac PC34=pc' - length (compE2 e) in that, simp)
      from pc bisim1 obtain v where stk = [v] xcp = None by(auto dest: bisim1-pc-length-compE2D)
      with exec pc have exec-meth-d (compP2 P) (compE2 e @ compE2 e2)
        (stack-xlift (length STK) (compxE2 e 0 0 @ compxE2 e2 (length (compE2 e)) (Suc 0))) t
        h (stk @ STK, loc, length (compE2 e) + 0, xcp) ta h' (stk', loc', pc', xcp')
        by-(rule exec-meth-take, auto)
      hence ?exec e2  $\sqcap (v \# STK)\ loc\ 0\ None\ stk'\ loc'\ (pc' - length (compE2 e))\ xcp'$ 
        using ⟨stk = [v]⟩ ⟨xcp = None⟩
        by -(rule exec-meth-drop-xt, auto simp add: stack-xlift-compxE2 shift-compxE2)
      from IH2[OF this] PC obtain stk'' where stk': stk' = stk'' @ v # STK
        and exec-meth-d (compP2 P) (compE2 e2) (compxE2 e2 0 0) t h (loc, loc, 0, None) ta h' (stk'',
        loc', PC, xcp') by auto
      hence exec-meth-d (compP2 P) ((compE2 e @ compE2 e2) @ [Putfield F D, Push Unit])
        (compxE2 e 0 0 @ shift (length (compE2 e)) (stack-xlift (length [v]) (compxE2 e2 0 0))) t h
        (loc @ [v], loc, length (compE2 e) + 0, None) ta h' (stk'' @ [v], loc', length (compE2 e) + PC,
        xcp')
      apply -
      apply(rule exec-meth-append)
      apply(rule append-exec-meth-xt)
      apply(erule exec-meth-stk-offer)
      by(auto)
      thus ?thesis using ⟨stk = [v]⟩ ⟨xcp = None⟩ stk' pc PC
        by(clarsimp simp add: shift-compxE2 stack-xlift-compxE2 ac-simps)
    qed
  next
    case (bisim1FAss2 e2 n e' xs stk loc pc xcp e F D v1)
    note IH2 = ⟨ $\bigwedge stk'\ loc'\ pc'\ xcp'\ STK. ?exec\ e2\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp'$ 
 $\implies ?concl\ e2\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp'$ ⟩
    note bisim2 = ⟨ $P, e2, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp)$ ⟩
    note exec = ⟨ $?exec\ (e \cdot F\{D\} := e2)\ (stk @ [v1])\ STK\ loc\ (length (compE2 e) + pc)\ xcp\ stk'\ loc'\ pc'$ 
 $xcp'$ ⟩
    from bisim2 have pc: pc ≤ length (compE2 e2) by(rule bisim1-pc-length-compE2)
    show ?case
    proof(cases pc < length (compE2 e2))
      case True
        from exec have exec-meth-d (compP2 P) ((compE2 e @ compE2 e2) @ [Putfield F D, Push Unit])

```

$(\text{stack-xlift } (\text{length } STK) (\text{compxE2 } e \ 0 \ 0) @ \text{shift } (\text{length } (\text{compE2 } e)) (\text{stack-xlift } (\text{length } STK) (\text{compxE2 } e2 \ 0 \ (\text{Suc } 0)))) t$   
 $h (stk @ v1 \# STK, loc, \text{length } (\text{compE2 } e) + pc, xcp) \text{ ta } h' (stk', loc', pc', xcp') \text{ by } (\text{simp add: compxE2-size-conv})$   
**hence**  $\text{exec}'$ :  $\text{exec-meth-d } (\text{compP2 } P) (\text{compE2 } e @ \text{compE2 } e2) (\text{stack-xlift } (\text{length } STK) (\text{compxE2 } e \ 0 \ 0) @$   
 $\text{shift } (\text{length } (\text{compE2 } e)) (\text{stack-xlift } (\text{length } STK) (\text{compxE2 } e2 \ 0 \ (\text{Suc } 0)))) t$   
 $h (stk @ v1 \# STK, loc, \text{length } (\text{compE2 } e) + pc, xcp) \text{ ta } h' (stk', loc', pc', xcp')$   
**by**  $(\text{rule exec-meth-take})(\text{simp add: True})$   
**hence**  $\text{exec-meth-d } (\text{compP2 } P) (\text{compE2 } e2) (\text{stack-xlift } (\text{length } STK) (\text{compxE2 } e2 \ 0 \ (\text{Suc } 0)))) t$   
 $h (stk @ v1 \# STK, loc, pc, xcp) \text{ ta } h' (stk', loc', pc' - \text{length } (\text{compE2 } e), xcp')$   
**by**  $(\text{rule exec-meth-drop-xt})(\text{auto simp add: stack-xlift-compxE2})$   
**hence**  $?exec \ e2 \ stk \ (v1 \# STK) \ loc \ pc \ xcp \ stk' \ loc' \ (pc' - \text{length } (\text{compE2 } e)) \ xcp'$   
**by**  $(\text{simp add: compxE2-stack-xlift-conv})$   
**from**  $IH2[OF \ this] \text{ obtain } stk'' \text{ where } stk': stk' = stk'' @ v1 \# STK$   
**and**  $\text{exec}''$ :  $\text{exec-meth-d } (\text{compP2 } P) (\text{compE2 } e2) (\text{compxE2 } e2 \ 0 \ 0) \ t \ h (stk, loc, pc, xcp) \text{ ta } h' (stk'', loc', pc' - \text{length } (\text{compE2 } e), xcp') \text{ by } \text{blast}$   
**from**  $\text{exec}'' \text{ have } \text{exec-meth-d } (\text{compP2 } P) (\text{compE2 } e2) (\text{stack-xlift } (\text{length } [v1]) (\text{compxE2 } e2 \ 0 \ 0)) \ t \ h (stk @ [v1], loc, pc, xcp)$   
 $\text{ta } h' (stk'' @ [v1], loc', pc' - \text{length } (\text{compE2 } e), xcp')$   
**by**  $(\text{rule exec-meth-stk-offer})$   
**hence**  $\text{exec-meth-d } (\text{compP2 } P) (\text{compE2 } e @ \text{compE2 } e2) (\text{compxE2 } e \ 0 \ 0 @ \text{shift } (\text{length } (\text{compE2 } e)) (\text{stack-xlift } (\text{length } [v1]) (\text{compxE2 } e2 \ 0 \ 0)))) t \ h (stk @ [v1], loc, \text{length } (\text{compE2 } e) + pc, xcp)$   
 $\text{ta } h' (stk'' @ [v1], loc', \text{length } (\text{compE2 } e) + (pc' - \text{length } (\text{compE2 } e)), xcp')$   
**by**  $(\text{rule append-exec-meth-xt}) \text{ auto}$   
**hence**  $\text{exec-meth-d } (\text{compP2 } P) ((\text{compE2 } e @ \text{compE2 } e2) @ [\text{Putfield } F \ D, \text{Push Unit}]) (\text{compxE2 } e \ 0 \ 0 @ \text{shift } (\text{length } (\text{compE2 } e)) (\text{stack-xlift } (\text{length } [v1]) (\text{compxE2 } e2 \ 0 \ 0)))) t \ h (stk @ [v1], loc, \text{length } (\text{compE2 } e) + pc, xcp)$   
 $\text{ta } h' (stk'' @ [v1], loc', \text{length } (\text{compE2 } e) + (pc' - \text{length } (\text{compE2 } e)), xcp')$   
**by**  $(\text{rule exec-meth-append})$   
**moreover from**  $\text{exec}' \text{ have } pc' \geq \text{length } (\text{compE2 } e)$   
**by**  $(\text{rule exec-meth-drop-xt-pc})(\text{auto simp add: stack-xlift-compxE2})$   
**ultimately show**  $?thesis \text{ using } stk' \text{ by } (\text{simp add: stack-xlift-compxE2 shift-compxE2})$   
**next**  
**case**  $False$   
**with**  $pc \text{ have } pc: pc = \text{length } (\text{compE2 } e2) \text{ by } \text{simp}$   
**with**  $\text{bisim2} \text{ obtain } v2 \text{ where } [simp]: stk = [v2] \ xcp = None$   
**by**  $(\text{auto dest: dest: bisim1-pc-length-compE2D})$   
**with**  $\text{exec } pc \text{ show } ?thesis \text{ apply } (\text{simp})$   
**by**  $(\text{erule exec-meth.cases})(\text{fastforce intro!: exec-meth.intros split: if-split-asm}) +$   
**qed**  
**next**  
**case**  $(\text{bisim1FAssThrow1 } e \ n \ a \ xs \ stk \ loc \ pc \ e2 \ F \ D)$   
**note**  $\text{bisim1} = \langle P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$   
**note**  $IH1 = \langle \bigwedge stk' \ loc' \ pc' \ xcp' \ STK. ?exec \ e \ stk \ STK \ loc \ pc \ [a] \ stk' \ loc' \ pc' \ xcp' \Rightarrow ?concl \ e \ stk \ STK \ loc \ pc \ [a] \ stk' \ loc' \ pc' \ xcp' \rangle$   
**note**  $\text{exec} = \langle ?exec \ (e \cdot F \{D\} := e2) \ stk \ STK \ loc \ pc \ [a] \ stk' \ loc' \ pc' \ xcp' \rangle$   
**from**  $\text{bisim1} \text{ have } pc: pc < \text{length } (\text{compE2 } e) \text{ and } [simp]: xs = loc$   
**by**  $(\text{auto dest: bisim1-ThrowD})$   
**from**  $\text{exec} \text{ have } \text{exec-meth-d } (\text{compP2 } P) (\text{compE2 } e @ (\text{compE2 } e2 @ [\text{Putfield } F \ D, \text{Push Unit}])) (\text{stack-xlift } (\text{length } STK) (\text{compxE2 } e \ 0 \ 0) @ \text{shift } (\text{length } (\text{compE2 } e)) (\text{stack-xlift } (\text{length } STK) (\text{compxE2 } e2 \ 0 \ (\text{Suc } 0)))) t$   
 $h (stk @ STK, loc, pc, [a]) \text{ ta } h' (stk', loc', pc', xcp') \text{ by } (\text{simp add: compxE2-size-conv})$   
**hence**  $?exec \ e \ stk \ STK \ loc \ pc \ [a] \ stk' \ loc' \ pc' \ xcp' \text{ by } (\text{rule exec-meth-take-xt})(\text{rule } pc)$

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from IH1[OF this] show ?case by(auto)
next
  case (bisim1FAssThrow2 e2 n a xs stk loc pc e F D v1)
  note bisim2 =  $\langle P, e2, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$ 
  note IH2 =  $\langle \bigwedge stk' loc' pc' xcp' STK. ?exec\ e2\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \Rightarrow ?concl\ e2\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$ 
  note exec =  $\langle ?exec\ (e \cdot F\{D\} := e2)\ (stk\ @\ [v1])\ STK\ loc\ (length\ (compE2\ e) + pc)\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$ 
  from bisim2 have pc: pc < length (compE2 e2) and [simp]: xs = loc
  by(auto dest: bisim1-ThrowD)
  from exec have exec-meth-d (compP2 P) ((compE2 e @ compE2 e2) @ [Putfield F D, Push Unit])
    (stack-xlift (length STK) (compxE2 e 0 0) @ shift (length (compE2 e)) (stack-xlift (length STK)
    (compxE2 e2 0 (Suc 0)))) t
    h (stk @ v1 # STK, loc, length (compE2 e) + pc, [a]) ta h' (stk', loc', pc', xcp')
  by(simp add: compxE2-size-convs)
  hence exec': exec-meth-d (compP2 P) (compE2 e @ compE2 e2)
    (stack-xlift (length STK) (compxE2 e 0 0) @ shift (length (compE2 e)) (stack-xlift (length STK)
    (compxE2 e2 0 (Suc 0)))) t
    h (stk @ v1 # STK, loc, length (compE2 e) + pc, [a]) ta h' (stk', loc', pc', xcp')
  by(rule exec-meth-take)(simp add: pc)
  hence exec-meth-d (compP2 P) (compE2 e2) (stack-xlift (length STK) (compxE2 e2 0 (Suc 0))) t
    h (stk @ v1 # STK, loc, pc, [a]) ta h' (stk', loc', pc' - length (compE2 e), xcp')
  by(rule exec-meth-drop-xt)(auto simp add: stack-xlift-compxE2)
  hence ?exec e2 stk (v1 # STK) loc pc [a] stk' loc' (pc' - length (compE2 e)) xcp'
  by(simp add: compxE2-stack-xlift-convs)
  from IH2[OF this] obtain stk'' where stk': stk' = stk'' @ v1 # STK and
    exec'': exec-meth-d (compP2 P) (compE2 e2) (compxE2 e2 0 0) t h (stk, loc, pc, [a]) ta h' (stk',
    loc', pc' - length (compE2 e), xcp') by blast
  from exec'' have exec-meth-d (compP2 P) (compE2 e2) (stack-xlift (length [v1]) (compxE2 e2 0 0))
    t h (stk @ [v1], loc, pc, [a])
    ta h' (stk'' @ [v1], loc', pc' - length (compE2 e), xcp')
  by(rule exec-meth-stk-offer)
  hence exec-meth-d (compP2 P) (compE2 e @ compE2 e2) (compxE2 e 0 0 @ shift (length (compE2
    e)) (stack-xlift (length [v1]) (compxE2 e2 0 0))) t h (stk @ [v1], loc, length (compE2 e) + pc, [a])
    ta h' (stk'' @ [v1], loc', length (compE2 e) + (pc' - length (compE2 e)), xcp')
  by(rule append-exec-meth-xt)(auto)
  hence exec-meth-d (compP2 P) ((compE2 e @ compE2 e2) @ [Putfield F D, Push Unit]) (compxE2
    e 0 0 @ shift (length (compE2 e)) (stack-xlift (length [v1]) (compxE2 e2 0 0))) t h (stk @ [v1], loc,
    length (compE2 e) + pc, [a])
    ta h' (stk'' @ [v1], loc', length (compE2 e) + (pc' - length (compE2 e)), xcp')
  by(rule exec-meth-append)
  moreover from exec' have pc': pc'  $\geq$  length (compE2 e)
  by(rule exec-meth-drop-xt-pc)(auto simp add: stack-xlift-compxE2)
  ultimately show ?case using stk' by(auto simp add: stack-xlift-compxE2 shift-compxE2)
next
  case bisim1FAssNull thus ?case
  by(auto elim!: exec-meth.cases dest: match-ex-table-pcsD simp add: stack-xlift-compxEs2 stack-xlift-compxE2)
next
  case bisim1FAss3 thus ?case
  by -(erule exec-meth.cases, auto intro!: exec-meth.exec-instr)
next
  case (bisim1CAS1 e1 n e1' xs stk loc pc xcp e2 e3 D F)
  note IH1 =  $\langle \bigwedge stk' loc' pc' xcp' STK. ?exec\ e1\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \Rightarrow ?concl\ e1\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$ 

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note IH2 = ⟨ $\bigwedge xs \ stk' \ loc' \ pc' \ xcp' \ STK. \ ?exec \ e2 \ [] \ STK \ xs \ 0 \ None \ stk' \ loc' \ pc' \ xcp'$ 
 $\implies \ ?concl \ e2 \ [] \ STK \ xs \ 0 \ None \ stk' \ loc' \ pc' \ xcp'$ ⟩
note bisim1 = ⟨ $P, e1, h \vdash (e1', xs) \leftrightarrow (stk, loc, pc, xcp)$ ⟩
note bisim2 = ⟨ $P, e2, h \vdash (e2, loc) \leftrightarrow ([], loc, 0, None)$ ⟩
note exec = ⟨ $?exec - stk \ STK \ loc \ pc \ xcp \ stk' \ loc' \ pc' \ xcp'$ ⟩
from bisim1 have pc:  $pc \leq length \ (compE2 \ e1)$  by (rule bisim1-pc-length-compE2)
from exec have exec':  $exec\text{-meth-d} \ (compP2 \ P) \ (compE2 \ e1 \ @ \ compE2 \ e2 \ @ \ compE2 \ e3 \ @ \ [CAS \ F \ D]) \ (stack\text{-xlift} \ (length \ STK) \ (compxE2 \ e1 \ 0 \ 0) \ @ \ shift \ (length \ (compE2 \ e1)) \ (stack\text{-xlift} \ (length \ STK) \ (compxE2 \ e2 \ 0 \ (Suc \ 0)) \ @ \ compxE2 \ e3 \ (length \ (compE2 \ e2)) \ (Suc \ (Suc \ 0)))) \ t$ 
 $h \ (stk \ @ \ STK, \ loc, \ pc, \ xcp) \ ta \ h' \ (stk', \ loc', \ pc', \ xcp')$ 
by (simp add: compxE2-size-convs)
show ?case
proof (cases  $pc < length \ (compE2 \ e1)$ )
  case True
    with exec' have ?exec e1  $stk \ STK \ loc \ pc \ xcp \ stk' \ loc' \ pc' \ xcp'$  by (rule exec-meth-take-xt)
    from IH1[OF this] show ?thesis by auto
  next
    case False
      with pc have pc:  $pc = length \ (compE2 \ e1)$  by simp
      with exec' have pc':  $pc' \geq length \ (compE2 \ e1)$  by  $-(erule \ exec\text{-meth-drop-xt-pc}, \ auto)$ 
      then obtain PC where PC:  $pc' = PC + length \ (compE2 \ e1)$ 
        by  $-(rule \ tac \ PC34 = pc' - length \ (compE2 \ e1) \ in \ that, \ simp)$ 
      from pc bisim1 obtain v where  $stk = [v] \ xcp = None$  by (auto dest: bisim1-pc-length-compE2D)
      with exec PC pc
        have  $exec\text{-meth-d} \ (compP2 \ P) \ ((compE2 \ e1 \ @ \ compE2 \ e2) \ @ \ compE2 \ e3 \ @ \ [CAS \ F \ D]) \ (stack\text{-xlift} \ (length \ STK) \ (compxE2 \ e1 \ 0 \ 0 \ @ \ shift \ (length \ (compE2 \ e1)) \ (compxE2 \ e2 \ 0 \ (Suc \ 0))) \ @ \ shift \ (length \ (compE2 \ e1 \ @ \ compE2 \ e2)) \ (compxE2 \ e3 \ 0 \ (length \ STK + Suc \ (Suc \ 0)))) \ t$ 
 $h \ (v \ # \ STK, \ loc, \ length \ (compE2 \ e1) + 0, \ None) \ ta \ h' \ (stk', \ loc', \ length \ (compE2 \ e1) + PC, \ xcp')$ 
        by (simp add: shift-compxE2 stack-xlift-compxE2 ac-simps)
      hence  $exec\text{-meth-d} \ (compP2 \ P) \ (compE2 \ e1 \ @ \ compE2 \ e2) \ (stack\text{-xlift} \ (length \ STK) \ (compxE2 \ e1 \ 0 \ 0 \ @ \ shift \ (length \ (compE2 \ e1)) \ (compxE2 \ e2 \ 0 \ (Suc \ 0)))) \ t$ 
 $h \ (v \ # \ STK, \ loc, \ length \ (compE2 \ e1) + 0, \ None) \ ta \ h' \ (stk', \ loc', \ length \ (compE2 \ e1) + PC, \ xcp')$ 
        by (rule exec-meth-take-xt) simp
      hence  $?exec \ e2 \ [] \ (v \ # \ STK) \ loc \ 0 \ None \ stk' \ loc' \ ((length \ (compE2 \ e1) + PC) - length \ (compE2 \ e1)) \ xcp'$ 
        by  $-(rule \ exec\text{-meth-drop-xt}, \ auto \ simp \ add: \ stack\text{-xlift-compxE2} \ shift\text{-compxE2})$ 
      from IH2[OF this] PC obtain stk'' where  $stk': stk' = stk'' \ @ \ v \ # \ STK$ 
        and  $exec\text{-meth-d} \ (compP2 \ P) \ (compE2 \ e2) \ (compxE2 \ e2 \ 0 \ 0) \ t \ h \ ([], \ loc, \ 0, \ None) \ ta \ h' \ (stk'', \ loc', \ PC, \ xcp')$ 
by auto
      hence  $exec\text{-meth-d} \ (compP2 \ P) \ ((compE2 \ e1 \ @ \ compE2 \ e2) \ @ \ (compE2 \ e3 \ @ \ [CAS \ F \ D])) \ ((compxE2 \ e1 \ 0 \ 0 \ @ \ shift \ (length \ (compE2 \ e1)) \ (stack\text{-xlift} \ (length \ [v]) \ (compxE2 \ e2 \ 0 \ 0))) \ @ \ shift \ (length \ (compE2 \ e1 \ @ \ compE2 \ e2)) \ (compxE2 \ e3 \ 0 \ (Suc \ (Suc \ 0)))) \ t \ h$ 
 $([] \ @ \ [v], \ loc, \ length \ (compE2 \ e1) + 0, \ None) \ ta \ h' \ (stk'' \ @ \ [v], \ loc', \ length \ (compE2 \ e1) + PC, \ xcp')$ 
apply  $-(rule \ exec\text{-meth-append-xt})$ 
apply (rule append-exec-meth-xt)
apply (erule exec-meth-stk-offer)
by (auto)
      thus ?thesis using  $\langle stk = [v] \rangle \langle xcp = None \rangle \ stk' \ pc \ PC$ 
by (clarsimp simp add: shift-compxE2 stack-xlift-compxE2 ac-simps)
qed
next

```

**case** (*bisim1CAS2* *e2 n e2' xs stk loc pc xcp e1 e3 D F v*)  
**note** *IH2* =  $\langle \bigwedge \text{stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \text{ STK}. ?\text{exec } e2 \text{ stk STK loc pc xcp stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$   
 $\implies ?\text{concl } e2 \text{ stk STK loc pc xcp stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$   
**note** *IH3* =  $\langle \bigwedge \text{xs stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \text{ STK}. ?\text{exec } e3 \text{ [] STK xs 0 None stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$   
 $\implies ?\text{concl } e3 \text{ [] STK xs 0 None stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$   
**note** *bisim2* =  $\langle P, e2, h \vdash (e2', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$   
**note** *bisim3* =  $\langle P, e3, h \vdash (e3, loc) \leftrightarrow ([], loc, 0, \text{None}) \rangle$   
**note** *exec* =  $\langle ?\text{exec} - (stk @ [v]) \text{ STK loc (length (compE2 } e1) + pc) \text{ xcp stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$   
**from** *bisim2* **have** *pc*: *pc* ≤ *length (compE2 e2)* **by** (*rule bisim1-pc-length-compE2*)  
**from** *exec* **have** *exec'*:  $\bigwedge v'. \text{exec-meth-d (compP2 } P) ((\text{compE2 } e1 @ \text{compE2 } e2) @ \text{compE2 } e3 @ [\text{CAS } F \text{ D}]) ((\text{compxE2 } e1 \text{ 0 (length STK)} @ \text{shift (length (compE2 } e1)) (\text{stack-xlift (length (v \# STK)) (compxE2 } e2 \text{ 0 0)))}) @ \text{shift (length (compE2 } e1 @ \text{compE2 } e2)) (\text{stack-xlift (length (v' \# v \# STK)) (compxE2 } e3 \text{ 0 0)))}) t$   
 $h (stk @ v \# STK, loc, \text{length (compE2 } e1) + pc, xcp) \text{ ta } h' (stk', loc', pc', xcp')$   
**by** (*simp add: shift-compxE2 stack-xlift-compxE2*)  
**show** *?case*  
**proof** (*cases pc < length (compE2 e2)*)  
**case** *True* **with** *exec'* [*of undefined*]  
**have** *exec''*: *exec-meth-d (compP2 P) (compE2 e1 @ compE2 e2) (compxE2 e1 0 (length STK) @ shift (length (compE2 e1)) (stack-xlift (length (v # STK)) (compxE2 e2 0 0))) t h (stk @ v # STK, loc, length (compE2 e1) + pc, xcp) ta h' (stk', loc', pc', xcp')*  
**by** −(*erule exec-meth-take-xt, simp*)  
**hence** *?exec e2 stk (v # STK) loc pc xcp stk' loc' (pc' − length (compE2 e1)) xcp'*  
**by** (*rule exec-meth-drop-xt*) *auto*  
**from** *IH2* [*OF this*] **obtain** *stk''* **where** *stk'*: *stk' = stk'' @ v # STK*  
**and** *exec'''*: *exec-meth-d (compP2 P) (compE2 e2) (compxE2 e2 0 0) t h (stk, loc, pc, xcp) ta h' (stk'', loc', pc' − length (compE2 e1), xcp')* **by** *blast*  
**from** *exec'''* **have** *exec-meth-d (compP2 P) ((compE2 e1 @ compE2 e2) @ compE2 e3 @ [CAS F D]) ((compxE2 e1 0 0 @ shift (length (compE2 e1)) (stack-xlift (length [v]) (compxE2 e2 0 0))) @ shift (length (compE2 e1 @ compE2 e2)) (compxE2 e3 0 (Suc (Suc 0)))) t h (stk @ [v], loc, length (compE2 e1) + pc, xcp) ta h' (stk'' @ [v], loc', length (compE2 e1) + (pc' − length (compE2 e1)), xcp')*  
**apply** −  
**apply** (*rule exec-meth-append-xt*)  
**apply** (*rule append-exec-meth-xt*)  
**apply** (*erule exec-meth-stk-offer*)  
**by** *auto*  
**moreover from** *exec''* **have** *pc' ≥ length (compE2 e1)*  
**by** (*rule exec-meth-drop-xt-pc*) *auto*  
**ultimately show** *?thesis* **using** *stk'* **by** (*auto simp add: shift-compxE2 stack-xlift-compxE2*)  
**next**  
**case** *False*  
**with** *pc* **have** *pc*: *pc = length (compE2 e2)* **by** *simp*  
**with** *exec'* [*of undefined*] **have** *pc' ≥ length (compE2 e1 @ compE2 e2)*  
**by** −(*erule exec-meth-drop-xt-pc, auto simp add: shift-compxE2 stack-xlift-compxE2*)  
**then obtain** *PC* **where** *PC*: *pc' = PC + length (compE2 e1) + length (compE2 e2)*  
**by** −(*rule-tac PC34=pc' − length (compE2 e1 @ compE2 e2) in that, simp*)  
**from** *pc bisim2* **obtain** *v'* **where** *stk*: *stk = [v']* **and** *xcp*: *xcp = None* **by** (*auto dest: bisim1-pc-length-compE2D*)  
**from** *exec'* [*of v'*]  
**have** *exec-meth-d (compP2 P) (compE2 e3 @ [CAS F D]) (stack-xlift (length (v' # v # STK)) (compxE2 e3 0 0)) t*  
 $h (v' \# v \# STK, loc, 0, xcp) \text{ ta } h' (stk', loc', pc' − \text{length (compE2 } e1 @ \text{compE2 } e2), xcp')$   
**unfolding** *stk pc append-Cons append-Nil*

**by**  $-(\text{rule exec-meth-drop-xt, simp only: add-0-right length-append, auto simp add: shift-compxE2 stack-xlift-compxE2})$   
**with**  $PC\ xcp\ \text{have } ?exec\ e3\ []\ (v' \# v \# STK)\ loc\ 0\ None\ stk'\ loc'\ PC\ xcp'$   
**by**  $-(\text{rule exec-meth-take, auto})$   
**from**  $IH3[OF\ this]\ \text{obtain } stk''\ \text{where } stk':\ stk' = stk'' @ v' \# v \# STK$   
**and**  $exec\text{-meth-d}\ (compP2\ P)\ (compE2\ e3)\ (compxE2\ e3\ 0\ 0)\ t\ h\ ([],\ loc,\ 0,\ None)\ ta\ h'\ (stk'',\ loc',\ PC,\ xcp')$  **by**  $auto$   
**hence**  $exec\text{-meth-d}\ (compP2\ P)\ (((compE2\ e1\ @\ compE2\ e2)\ @\ compE2\ e3)\ @\ [CAS\ F\ D])$   
 $((compxE2\ e1\ 0\ 0\ @\ compxE2\ e2\ (length\ (compE2\ e1))\ (Suc\ 0))\ @\ shift\ (length\ (compE2\ e1\ @\ compE2\ e2))\ (stack\text{-xlift}\ (length\ [v',\ v])\ (compxE2\ e3\ 0\ 0)))\ t\ h\ ([]\ @\ [v',\ v],\ loc,\ length\ (compE2\ e1\ @\ compE2\ e2) + 0,\ None)\ ta\ h'\ (stk''\ @\ [v',\ v],\ loc',\ length\ (compE2\ e1\ @\ compE2\ e2) + PC,\ xcp')$   
**apply**  $-$   
**apply**  $(\text{rule exec-meth-append})$   
**apply**  $(\text{rule append-exec-meth-xt})$   
**apply**  $(\text{erule exec-meth-stk-offer})$   
**by**  $auto$   
**thus**  $?thesis\ \text{using } stk\ xcp\ stk'\ pc\ PC$   
**by**  $(\text{clarsimp simp add: shift-compxE2 stack-xlift-compxE2 ac-simps})$   
**qed**  
**next**  
**case**  $(bisim1CAS3\ e3\ n\ e3'\ xs\ stk\ loc\ pc\ xcp\ e1\ e2\ D\ F\ v1\ v2)$   
**note**  $IH3 = \langle \bigwedge stk'\ loc'\ pc'\ xcp'\ STK.\ ?exec\ e3\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$   
 $\implies ?concl\ e3\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp'$   
**note**  $bisim3 = \langle P, e3, h \vdash (e3', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$   
**note**  $exec = \langle ?exec - (stk\ @\ [v2,\ v1])\ STK\ loc\ (length\ (compE2\ e1) + length\ (compE2\ e2) + pc) \rangle$   
 $xcp\ stk'\ loc'\ pc'\ xcp'$   
**from**  $bisim3\ \text{have } pc:\ pc \leq length\ (compE2\ e3)\ \text{by}(\text{rule bisim1-pc-length-compE2})$   
**show**  $?case$   
**proof**  $(\text{cases } pc < length\ (compE2\ e3))$   
**case**  $True$   
**from**  $exec\ \text{have } exec\text{-meth-d}\ (compP2\ P)\ (((compE2\ e1\ @\ compE2\ e2)\ @\ compE2\ e3)\ @\ [CAS\ F\ D])$   
 $((compxE2\ e1\ 0\ (length\ STK)\ @\ compxE2\ e2\ (length\ (compE2\ e1))\ (Suc\ (length\ STK)))\ @\ shift\ (length\ (compE2\ e1\ @\ compE2\ e2))\ (stack\text{-xlift}\ (length\ (v2\ \# v1\ \# STK))\ (compxE2\ e3\ 0\ 0)))\ t$   
 $h\ (stk\ @\ v2\ \# v1\ \# STK,\ loc,\ length\ (compE2\ e1\ @\ compE2\ e2) + pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$   
**by**  $(\text{simp add: shift-compxE2 stack-xlift-compxE2})$   
**hence**  $exec':\ exec\text{-meth-d}\ (compP2\ P)\ ((compE2\ e1\ @\ compE2\ e2)\ @\ compE2\ e3)$   
 $((compxE2\ e1\ 0\ (length\ STK)\ @\ compxE2\ e2\ (length\ (compE2\ e1))\ (Suc\ (length\ STK)))\ @\ shift\ (length\ (compE2\ e1\ @\ compE2\ e2))\ (stack\text{-xlift}\ (length\ (v2\ \# v1\ \# STK))\ (compxE2\ e3\ 0\ 0)))\ t$   
 $h\ (stk\ @\ v2\ \# v1\ \# STK,\ loc,\ length\ (compE2\ e1\ @\ compE2\ e2) + pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$   
**by**  $(\text{rule exec-meth-take})(\text{simp add: True})$   
**hence**  $?exec\ e3\ stk\ (v2\ \# v1\ \# STK)\ loc\ pc\ xcp\ stk'\ loc'\ (pc' - length\ (compE2\ e1\ @\ compE2\ e2))\ xcp'$   
**by**  $(\text{rule exec-meth-drop-xt})\ auto$   
**from**  $IH3[OF\ this]\ \text{obtain } stk''\ \text{where } stk':\ stk' = stk'' @ v2\ \# v1\ \# STK$   
**and**  $exec'':\ exec\text{-meth-d}\ (compP2\ P)\ (compE2\ e3)\ (compxE2\ e3\ 0\ 0)\ t\ h\ (stk,\ loc,\ pc,\ xcp)\ ta\ h'\ (stk'',\ loc',\ pc' - length\ (compE2\ e1\ @\ compE2\ e2),\ xcp')$  **by**  $blast$   
**from**  $exec''\ \text{have } exec\text{-meth-d}\ (compP2\ P)\ (compE2\ e3)\ (stack\text{-xlift}\ (length\ [v2,\ v1])\ (compxE2\ e3\ 0\ 0))\ t\ h\ (stk\ @\ [v2,\ v1],\ loc,\ pc,\ xcp)$   
 $ta\ h'\ (stk''\ @\ [v2,\ v1],\ loc',\ pc' - length\ (compE2\ e1\ @\ compE2\ e2),\ xcp')$   
**by**  $(\text{rule exec-meth-stk-offer})$   
**hence**  $exec\text{-meth-d}\ (compP2\ P)\ ((compE2\ e1\ @\ compE2\ e2)\ @\ compE2\ e3)\ ((compxE2\ e1\ 0\ 0\ @\$



$\text{compxE2 } e2 \text{ (length (compE2 } e1)) \text{ (Suc 0)) @ shift (length (compE2 } e1 \text{ @ compE2 } e2)) \text{ (stack-xlift (length [v2, v1]) (compxE2 } e3 \text{ 0 0))) } t$   
 $h \text{ (stk @ [v2, v1], loc, length (compE2 } e1 \text{ @ compE2 } e2) + pc, xcp)$   
 $ta \text{ } h' \text{ (stk'' @ [v2, v1], loc', length (compE2 } e1 \text{ @ compE2 } e2) + (pc' - \text{length (compE2 } e1 \text{ @ compE2 } e2)), xcp')$   
 $\text{by(rule append-exec-meth-xt) auto}$   
 $\text{hence exec-meth-d (compP2 } P) (((\text{compE2 } e1 \text{ @ compE2 } e2) \text{ @ compE2 } e3) \text{ @ [CAS F D])}$   
 $((\text{compxE2 } e1 \text{ 0 0 @ compxE2 } e2 \text{ (length (compE2 } e1)) \text{ (Suc 0)) @ shift (length (compE2 } e1 \text{ @ compE2 } e2)) \text{ (stack-xlift (length [v2, v1]) (compxE2 } e3 \text{ 0 0))) } t$   
 $h \text{ (stk @ [v2, v1], loc, length (compE2 } e1 \text{ @ compE2 } e2) + pc, xcp)$   
 $ta \text{ } h' \text{ (stk'' @ [v2, v1], loc', length (compE2 } e1 \text{ @ compE2 } e2) + (pc' - \text{length (compE2 } e1 \text{ @ compE2 } e2)), xcp')$   
 $\text{by(rule exec-meth-append)}$   
**moreover from**  $\text{exec' have } pc' \geq \text{length (compE2 } e1 \text{ @ compE2 } e2)$   
 $\text{by(rule exec-meth-drop-xt-pc)(auto simp add: stack-xlift-compxE2)}$   
**ultimately show**  $?thesis \text{ using } \text{stk' by(simp add: stack-xlift-compxE2 shift-compxE2)}$   
**next**  
**case**  $\text{False}$   
**with**  $pc \text{ have } pc: pc = \text{length (compE2 } e3) \text{ by simp}$   
**with**  $\text{bisim3 obtain } v3 \text{ where [simp]: } \text{stk} = [v3] \text{ } xcp = \text{None}$   
 $\text{by(auto dest: dest: bisim1-pc-length-compE2D)}$   
**with**  $\text{exec } pc \text{ show } ?thesis \text{ apply(simp)}$   
 $\text{by(erule exec-meth.cases)(fastforce intro!: exec-meth.intros split: if-split-asm)+}$   
**qed**  
**next**  
**case**  $(\text{bisim1CASThrow1 } e1 \text{ } n \text{ } a \text{ } xs \text{ } stk \text{ } loc \text{ } pc \text{ } e2 \text{ } e3 \text{ } D \text{ } F)$   
**note**  $\text{bisim1} = \langle P, e1, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$   
**note**  $\text{IH1} = \langle \bigwedge \text{stk' } loc' \text{ } pc' \text{ } xcp' \text{ STK. } ?exec \text{ } e1 \text{ } stk \text{ STK } loc \text{ } pc \text{ } [a] \text{ } stk' \text{ } loc' \text{ } pc' \text{ } xcp' \rangle$   
 $\implies ?concl \text{ } e1 \text{ } stk \text{ STK } loc \text{ } pc \text{ } [a] \text{ } stk' \text{ } loc' \text{ } pc' \text{ } xcp'$   
**note**  $\text{exec} = \langle ?exec - \text{stk STK } loc \text{ } pc \text{ } [a] \text{ } stk' \text{ } loc' \text{ } pc' \text{ } xcp' \rangle$   
**from**  $\text{bisim1 have } pc: pc < \text{length (compE2 } e1) \text{ and [simp]: } xs = loc$   
 $\text{by(auto dest: bisim1-ThrowD)}$   
**from**  $\text{exec have exec-meth-d (compP2 } P) (\text{compE2 } e1 \text{ @ (compE2 } e2 \text{ @ compE2 } e3 \text{ @ [CAS F D])})$   
 $(\text{stack-xlift (length STK) (compxE2 } e1 \text{ 0 0) @ shift (length (compE2 } e1)) \text{ (stack-xlift (length STK) (compxE2 } e2 \text{ 0 (Suc 0) @ compxE2 } e3 \text{ (length (compE2 } e2)) (Suc (Suc 0)))) } t$   
 $h \text{ (stk @ STK, loc, pc, [a]) } ta \text{ } h' \text{ (stk', loc', pc', xcp') by(simp add: compxE2-size-convs)}$   
**hence**  $?exec \text{ } e1 \text{ } stk \text{ STK } loc \text{ } pc \text{ } [a] \text{ } stk' \text{ } loc' \text{ } pc' \text{ } xcp' \text{ by(rule exec-meth-take-xt)(rule pc)}$   
**from**  $\text{IH1[OF this] show } ?case \text{ by(auto)}$   
**next**  
**case**  $(\text{bisim1CASThrow2 } e2 \text{ } n \text{ } a \text{ } xs \text{ } stk \text{ } loc \text{ } pc \text{ } e1 \text{ } e3 \text{ } D \text{ } F \text{ } v1)$   
**note**  $\text{bisim2} = \langle P, e2, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$   
**note**  $\text{IH2} = \langle \bigwedge \text{stk' } loc' \text{ } pc' \text{ } xcp' \text{ STK. } ?exec \text{ } e2 \text{ } stk \text{ STK } loc \text{ } pc \text{ } [a] \text{ } stk' \text{ } loc' \text{ } pc' \text{ } xcp' \rangle$   
 $\implies ?concl \text{ } e2 \text{ } stk \text{ STK } loc \text{ } pc \text{ } [a] \text{ } stk' \text{ } loc' \text{ } pc' \text{ } xcp'$   
**note**  $\text{exec} = \langle ?exec - (\text{stk @ [v1]}) \text{ STK } loc \text{ (length (compE2 } e1) + pc) \text{ } [a] \text{ } stk' \text{ } loc' \text{ } pc' \text{ } xcp' \rangle$   
**from**  $\text{bisim2 have } pc: pc < \text{length (compE2 } e2) \text{ and [simp]: } xs = loc$   
 $\text{by(auto dest: bisim1-ThrowD)}$   
**from**  $\text{exec have exec-meth-d (compP2 } P) ((\text{compE2 } e1 \text{ @ compE2 } e2) \text{ @ compE2 } e3 \text{ @ [CAS F D])}$   
 $((\text{stack-xlift (length STK) (compxE2 } e1 \text{ 0 0) @ shift (length (compE2 } e1)) \text{ (stack-xlift (length STK) (compxE2 } e2 \text{ 0 (Suc 0))) @ (shift (length (compE2 } e1 \text{ @ compE2 } e2)) (compxE2 } e3 \text{ 0 (Suc (length STK)))) } t$   
 $h \text{ (stk @ v1 \# STK, loc, length (compE2 } e1) + pc, [a]) } ta \text{ } h' \text{ (stk', loc', pc', xcp')}$   
 $\text{by(simp add: shift-compxE2 stack-xlift-compxE2 ac-simps)}$   
**hence**  $\text{exec': exec-meth-d (compP2 } P) (\text{compE2 } e1 \text{ @ compE2 } e2)$   
 $(\text{stack-xlift (length STK) (compxE2 } e1 \text{ 0 0) @ shift (length (compE2 } e1)) \text{ (stack-xlift (length STK)$

$(\text{compxE2 } e2 \ 0 \ (\text{Suc } 0))) \ t$   
 $h \ (stk \ @ \ v1 \ \# \ STK, \ loc, \ length \ (\text{compE2 } e1) + pc, \ [a]) \ ta \ h' \ (stk', \ loc', \ pc', \ xcp')$   
 $\text{by}(\text{rule exec-meth-take-xt})(\text{simp add: } pc)$   
 $\text{hence exec-meth-d } (\text{compP2 } P) \ (\text{compE2 } e2) \ (\text{stack-xlift } (length \ STK) \ (\text{compxE2 } e2 \ 0 \ (\text{Suc } 0))) \ t$   
 $h \ (stk \ @ \ v1 \ \# \ STK, \ loc, \ pc, \ [a]) \ ta \ h' \ (stk', \ loc', \ pc' - length \ (\text{compE2 } e1), \ xcp')$   
 $\text{by}(\text{rule exec-meth-drop-xt})(\text{auto simp add: stack-xlift-compxE2})$   
 $\text{hence } ?exec \ e2 \ stk \ (v1 \ \# \ STK) \ loc \ pc \ [a] \ stk' \ loc' \ (pc' - length \ (\text{compE2 } e1)) \ xcp'$   
 $\text{by}(\text{simp add: compxE2-stack-xlift-conv})$   
 $\text{from IH2[OF this] obtain } stk'' \ \text{where } stk': \ stk' = stk'' \ @ \ v1 \ \# \ STK \ \text{and}$   
 $exec'': \text{exec-meth-d } (\text{compP2 } P) \ (\text{compE2 } e2) \ (\text{compxE2 } e2 \ 0 \ 0) \ t \ h \ (stk, \ loc, \ pc, \ [a]) \ ta \ h' \ (stk'',$   
 $loc', \ pc' - length \ (\text{compE2 } e1), \ xcp') \ \text{by blast}$   
 $\text{from } exec'' \ \text{have exec-meth-d } (\text{compP2 } P) \ (\text{compE2 } e2) \ (\text{stack-xlift } (length \ [v1]) \ (\text{compxE2 } e2 \ 0 \ 0))$   
 $t$   
 $h \ (stk \ @ \ [v1], \ loc, \ pc, \ [a])$   
 $ta \ h' \ (stk'' \ @ \ [v1], \ loc', \ pc' - length \ (\text{compE2 } e1), \ xcp')$   
 $\text{by}(\text{rule exec-meth-stk-offer})$   
 $\text{hence exec-meth-d } (\text{compP2 } P) \ (\text{compE2 } e1 \ @ \ \text{compE2 } e2) \ (\text{compxE2 } e1 \ 0 \ 0 \ @ \ \text{shift } (length \ (\text{compE2}$   
 $e1)) \ (\text{stack-xlift } (length \ [v1]) \ (\text{compxE2 } e2 \ 0 \ 0))) \ t$   
 $h \ (stk \ @ \ [v1], \ loc, \ length \ (\text{compE2 } e1) + pc, \ [a])$   
 $ta \ h' \ (stk'' \ @ \ [v1], \ loc', \ length \ (\text{compE2 } e1) + (pc' - length \ (\text{compE2 } e1)), \ xcp')$   
 $\text{by}(\text{rule append-exec-meth-xt})(\text{auto})$   
 $\text{hence exec-meth-d } (\text{compP2 } P) \ (((\text{compE2 } e1 \ @ \ \text{compE2 } e2) \ @ \ \text{compE2 } e3 \ @ \ [\text{CAS } F \ D]) \ (((\text{compxE2}$   
 $e1 \ 0 \ 0 \ @ \ \text{shift } (length \ (\text{compE2 } e1)) \ (\text{stack-xlift } (length \ [v1]) \ (\text{compxE2 } e2 \ 0 \ 0))) \ @ \ (\text{shift } (length$   
 $(\text{compE2 } e1 \ @ \ \text{compE2 } e2)) \ (\text{compxE2 } e3 \ 0 \ (\text{Suc } (\text{Suc } 0)))))) \ t$   
 $h \ (stk \ @ \ [v1], \ loc, \ length \ (\text{compE2 } e1) + pc, \ [a])$   
 $ta \ h' \ (stk'' \ @ \ [v1], \ loc', \ length \ (\text{compE2 } e1) + (pc' - length \ (\text{compE2 } e1)), \ xcp')$   
 $\text{by}(\text{rule exec-meth-append-xt})$   
 $\text{moreover from } exec' \ \text{have } pc': \ pc' \geq length \ (\text{compE2 } e1)$   
 $\text{by}(\text{rule exec-meth-drop-xt-pc})(\text{auto simp add: stack-xlift-compxE2})$   
 $\text{ultimately show } ?case \ \text{using } stk' \ \text{by}(\text{auto simp add: stack-xlift-compxE2 shift-compxE2})$   
 $\text{next}$   
 $\text{case } (\text{bisim1CASThrow3 } e3 \ n \ a \ xs \ stk \ loc \ pc \ e1 \ e2 \ D \ F \ v2 \ v1)$   
 $\text{note bisim3} = \langle P, e3, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$   
 $\text{note IH3} = \langle \bigwedge stk' \ loc' \ pc' \ xcp' \ STK. \ ?exec \ e3 \ stk \ STK \ loc \ pc \ [a] \ stk' \ loc' \ pc' \ xcp' \rangle$   
 $\implies ?concl \ e3 \ stk \ STK \ loc \ pc \ [a] \ stk' \ loc' \ pc' \ xcp'$   
 $\text{note exec} = \langle ?exec - (stk \ @ \ [v2, v1]) \ STK \ loc \ (length \ (\text{compE2 } e1) + length \ (\text{compE2 } e2) + pc)$   
 $[a] \ stk' \ loc' \ pc' \ xcp' \rangle$   
 $\text{from bisim3 have } pc: \ pc < length \ (\text{compE2 } e3) \ \text{and } [simp]: \ xs = loc$   
 $\text{by}(\text{auto dest: bisim1-ThrowD})$   
 $\text{from } exec \ \text{have exec-meth-d } (\text{compP2 } P) \ (((\text{compE2 } e1 \ @ \ \text{compE2 } e2) \ @ \ \text{compE2 } e3) \ @ \ [\text{CAS } F$   
 $D])$   
 $((\text{stack-xlift } (length \ STK) \ (\text{compxE2 } e1 \ 0 \ 0 \ @ \ \text{compxE2 } e2 \ (length \ (\text{compE2 } e1)) \ (\text{Suc } 0))) \ @ \ \text{shift}$   
 $(length \ (\text{compE2 } e1 \ @ \ \text{compE2 } e2)) \ (\text{stack-xlift } (length \ (v2 \ \# \ v1 \ \# \ STK)) \ (\text{compxE2 } e3 \ 0 \ 0))) \ t$   
 $h \ (stk \ @ \ v2 \ \# \ v1 \ \# \ STK, \ loc, \ length \ (\text{compE2 } e1 \ @ \ \text{compE2 } e2) + pc, \ [a]) \ ta \ h' \ (stk', \ loc', \ pc',$   
 $xcp')$   
 $\text{by}(\text{simp add: shift-compxE2 stack-xlift-compxE2 ac-simps})$   
 $\text{hence } exec': \text{exec-meth-d } (\text{compP2 } P) \ (((\text{compE2 } e1 \ @ \ \text{compE2 } e2) \ @ \ \text{compE2 } e3)$   
 $((\text{stack-xlift } (length \ STK) \ (\text{compxE2 } e1 \ 0 \ 0 \ @ \ \text{compxE2 } e2 \ (length \ (\text{compE2 } e1)) \ (\text{Suc } 0))) \ @ \ \text{shift}$   
 $(length \ (\text{compE2 } e1 \ @ \ \text{compE2 } e2)) \ (\text{stack-xlift } (length \ (v2 \ \# \ v1 \ \# \ STK)) \ (\text{compxE2 } e3 \ 0 \ 0))) \ t$   
 $h \ (stk \ @ \ v2 \ \# \ v1 \ \# \ STK, \ loc, \ length \ (\text{compE2 } e1 \ @ \ \text{compE2 } e2) + pc, \ [a]) \ ta \ h' \ (stk', \ loc', \ pc',$   
 $xcp')$   
 $\text{by}(\text{rule exec-meth-take})(\text{simp add: } pc)$   
 $\text{hence } ?exec \ e3 \ stk \ (v2 \ \# \ v1 \ \# \ STK) \ loc \ pc \ [a] \ stk' \ loc' \ (pc' - length \ (\text{compE2 } e1 \ @ \ \text{compE2 } e2))$   
 $xcp'$

```

by(rule exec-meth-drop-xt) auto
from IH3[OF this] obtain stk'' where stk': stk' = stk'' @ v2 # v1 # STK and
  exec'': exec-meth-d (compP2 P) (compE2 e3) (compxE2 e3 0 0) t h (stk, loc, pc, [a]) ta h' (stk'',
loc', pc' - length (compE2 e1 @ compE2 e2), xcp') by blast
  from exec'' have exec-meth-d (compP2 P) (compE2 e3) (stack-xlift (length [v2, v1]) (compxE2 e3
0 0)) t h (stk @ [v2, v1], loc, pc, [a])
    ta h' (stk'' @ [v2, v1], loc', pc' - length (compE2 e1 @ compE2 e2), xcp')
  by(rule exec-meth-stk-offer)
  hence exec-meth-d (compP2 P) ((compE2 e1 @ compE2 e2) @ compE2 e3) ((compxE2 e1 0 0 @
compxE2 e2 (length (compE2 e1)) (Suc 0)) @ shift (length (compE2 e1 @ compE2 e2)) (stack-xlift
(length [v2, v1]) (compxE2 e3 0 0))) t h (stk @ [v2, v1], loc, length (compE2 e1 @ compE2 e2) + pc,
[a])
    ta h' (stk'' @ [v2, v1], loc', length (compE2 e1 @ compE2 e2) + (pc' - length (compE2 e1 @
compE2 e2)), xcp')
  by(rule append-exec-meth-xt)(auto)
  hence exec-meth-d (compP2 P) (((compE2 e1 @ compE2 e2) @ compE2 e3) @ [CAS F D])
((compxE2 e1 0 0 @ compxE2 e2 (length (compE2 e1)) (Suc 0)) @ shift (length (compE2 e1 @
compE2 e2)) (stack-xlift (length [v2, v1]) (compxE2 e3 0 0))) t h (stk @ [v2, v1], loc, length (compE2
e1 @ compE2 e2) + pc, [a])
    ta h' (stk'' @ [v2, v1], loc', length (compE2 e1 @ compE2 e2) + (pc' - length (compE2 e1 @
compE2 e2)), xcp')
  by(rule exec-meth-append)
  moreover from exec' have pc': pc' ≥ length (compE2 e1 @ compE2 e2)
  by(rule exec-meth-drop-xt-pc)(auto simp add: stack-xlift-compxE2)
  ultimately show ?case using stk' by(auto simp add: stack-xlift-compxE2 shift-compxE2)
next
case bisim1CASFail thus ?case
  by(auto elim!: exec-meth.cases dest: match-ex-table-pcsD simp add: stack-xlift-compxEs2 stack-xlift-compxE2)
next
case (bisim1Call1 obj n obj' xs stk loc pc xcp ps M')
note bisimObj = ⟨P, obj, h ⊢ (obj', xs) ↔ (stk, loc, pc, xcp)⟩
note IHobj = ⟨∧stk' loc' pc' xcp' STK. ?exec obj stk STK loc pc xcp stk' loc' pc' xcp'
⇒ ?concl obj stk STK loc pc xcp stk' loc' pc' xcp'⟩
note IHparams = ⟨∧xs stk' loc' pc' xcp' STK. ?execs ps [] STK xs 0 None stk' loc' pc' xcp'
⇒ ?concls ps [] STK xs 0 None stk' loc' pc' xcp'⟩
note exec = ⟨?exec (obj.M'(ps)) stk STK loc pc xcp stk' loc' pc' xcp'⟩
from bisimObj have pc: pc ≤ length (compE2 obj) by(rule bisim1-pc-length-compE2)
show ?case
proof(cases pc < length (compE2 obj))
  case True
  from exec have ?exec obj stk STK loc pc xcp stk' loc' pc' xcp'
  by(simp add: compxEs2-size-convs)(erule exec-meth-take-xt[OF - True])
  from IHobj[OF this] show ?thesis by auto
next
  case False
  with pc have [simp]: pc = length (compE2 obj) by simp
  with exec have pc' ≥ length (compE2 obj)
  by(simp add: compxEs2-size-convs stack-xlift-compxE2)(auto elim!: exec-meth-drop-xt-pc)
  then obtain PC where PC: pc' = PC + length (compE2 obj)
  by -(rule-tac PC34=pc' - length (compE2 obj) in that, simp)
  from pc bisimObj obtain v where [simp]: stk = [v] xcp = None by(auto dest: bisim1-pc-length-compE2D)
  show ?thesis
  proof(cases ps)
    case Cons

```

```

with exec pc have exec-meth-d (compP2 P) (compE2 obj @ compEs2 ps)
  (stack-xlift (length STK) (compxE2 obj 0 0 @ compxEs2 ps (length (compE2 obj)) (Suc 0))) t
  h (stk @ STK, loc, length (compE2 obj) + 0, xcp) ta h' (stk', loc', pc', xcp')
  by  $-(\text{rule } \text{exec-meth-take}, \text{auto})$ 
hence  $?execs\ ps \ []\ (v \# STK)\ loc\ 0\ None\ stk'\ loc'\ (pc' - \text{length } (compE2\ obj))\ xcp'$ 
  apply  $-$ 
  apply (rule exec-meth-drop-xt)
  apply (simp add: compxEs2-size-convs compxEs2-stack-xlift-convs)
  apply (auto simp add: stack-xlift-compxE2)
  done
from IHparams[OF this] PC obtain stk'' where stk' = stk'' @ v # STK
  and exec': exec-meth-d (compP2 P) (compEs2 ps) (compxEs2 ps 0 0) t h ( $[], loc, 0, None$ ) ta
h' (stk'', loc', PC, xcp')
  by auto
  from exec' have exec-meth-d (compP2 P) ((compE2 obj @ compEs2 ps) @ [Invoke M' (length
ps)]) (compxE2 obj 0 0 @ shift (length (compE2 obj)) (stack-xlift (length [v]) (compxEs2 ps 0 0))) t
h ( $[] @ [v], loc, \text{length } (compE2\ obj) + 0, None$ ) ta h' (stk'' @ [v], loc', \text{length } (compE2\ obj) + PC,
xcp')
  apply  $-$ 
  apply (rule exec-meth-append)
  apply (rule append-exec-meth-xt)
  apply (erule exec-meth-stk-offer)
  by (auto)
thus  $?thesis$  using stk' PC by (clarsimp simp add: shift-compxEs2 stack-xlift-compxEs2 ac-simps)
next
case Nil
with exec pc show  $?thesis$ 
  apply (auto elim!: exec-meth.cases intro!: exec-meth.intros simp add: split-beta split: if-split-asm)
  apply (auto split: extCallRet.split-asm intro!: exec-meth.intros)
  apply (force intro!: exI)
  apply (force intro!: exI)
  apply (force intro!: exI)
  done
qed
qed
next
case (bisim1CallParams ps n ps' xs stk loc pc xcp obj M' v)
note bisimParam =  $\langle P, ps, h \vdash (ps', xs) [\leftrightarrow] (stk, loc, pc, xcp) \rangle$ 
note IHparam =  $\langle \bigwedge stk' loc' pc' xcp' STK. ?execs\ ps\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \Rightarrow ?concls\ ps\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$ 
note exec =  $\langle ?exec\ (obj \cdot M'(ps))\ (stk @ [v])\ STK\ loc\ (\text{length } (compE2\ obj) + pc)\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$ 
show  $?case$ 
proof (cases ps)
  case Nil
  with bisimParam have pc = 0 xcp = None by (auto elim: bisims1.cases)
  with exec Nil show  $?thesis$ 
  apply (auto elim!: exec-meth.cases intro!: exec-meth.intros simp add: split-beta extRet2JVM-def
split: if-split-asm)
  apply (auto split: extCallRet.split-asm simp add: neq-Nil-conv)
  apply (force intro!: exec-meth.intros) +
  done
next
case Cons

```

```

from bisimParam have pc: pc ≤ length (compEs2 ps) by(rule bisims1-pc-length-compEs2)
show ?thesis
proof(cases pc < length (compEs2 ps))
  case True
    from exec have exec-meth-d (compP2 P) ((compE2 obj @ compEs2 ps) @ [Invoke M' (length
ps)])
      (stack-xlift (length STK) (compxE2 obj 0 0) @ shift (length (compE2 obj)) (stack-xlift (length
(v # STK)) (compxEs2 ps 0 0))) t
      h (stk @ v # STK, loc, length (compE2 obj) + pc, xcp) ta h' (stk', loc', pc', xcp')
      by(simp add: compxEs2-size-convs compxEs2-stack-xlift-convs)
      hence exec': exec-meth-d (compP2 P) (compE2 obj @ compEs2 ps) (stack-xlift (length STK)
(compxE2 obj 0 0) @
      shift (length (compE2 obj)) (stack-xlift (length (v # STK)) (compxEs2 ps 0 0))) t
      h (stk @ v # STK, loc, length (compE2 obj) + pc, xcp) ta h' (stk', loc', pc', xcp')
      by(rule exec-meth-take)(simp add: True)
      hence ?execs ps stk (v # STK) loc pc xcp stk' loc' (pc' - length (compE2 obj)) xcp'
      by(rule exec-meth-drop-xt)(auto simp add: stack-xlift-compxE2)
      from IHparam[OF this] obtain stk'' where stk': stk' = stk'' @ v # STK
      and exec'': exec-meth-d (compP2 P) (compEs2 ps) (compxEs2 ps 0 0) t h (stk, loc, pc, xcp) ta
h' (stk'', loc', pc' - length (compE2 obj), xcp') by blast
      from exec'' have exec-meth-d (compP2 P) (compEs2 ps) (stack-xlift (length [v]) (compxEs2 ps 0
0)) t h (stk @ [v], loc, pc, xcp) ta h' (stk'' @ [v], loc', pc' - length (compE2 obj), xcp')
      by(rule exec-meth-stk-offer)
      hence exec-meth-d (compP2 P) (compE2 obj @ compEs2 ps) (compxE2 obj 0 0 @ shift (length
(compE2 obj)) (stack-xlift (length [v]) (compxEs2 ps 0 0))) t
      h (stk @ [v], loc, length (compE2 obj) + pc, xcp) ta h' (stk'' @ [v], loc', length (compE2 obj) + (pc'
- length (compE2 obj)), xcp')
      by(rule append-exec-meth-xt) auto
      hence exec-meth-d (compP2 P) ((compE2 obj @ compEs2 ps) @ [Invoke M' (length ps)])
      (compxE2 obj 0 0 @ shift (length (compE2 obj)) (stack-xlift (length [v]) (compxEs2 ps 0 0))) t
      h (stk @ [v], loc, length (compE2 obj) + pc, xcp) ta h' (stk'' @ [v], loc', length (compE2 obj) + (pc'
- length (compE2 obj)), xcp')
      by(rule exec-meth-append)
      moreover from exec' have pc' ≥ length (compE2 obj)
      by(rule exec-meth-drop-xt-pc)(auto simp add: stack-xlift-compxE2)
      ultimately show ?thesis using stk'
      by(auto simp add: shift-compxEs2 stack-xlift-compxEs2)
    next
      case False
      with pc have pc: pc = length (compEs2 ps) by simp
      with bisimParam obtain vs where stk = vs length vs = length ps xcp = None
      by(auto dest: bisims1-pc-length-compEs2D)
      with exec pc Cons show ?thesis
      apply(auto elim!: exec-meth.cases intro!: exec-meth.intros simp add: split-beta extRet2JVM-def
split: if-split-asm)
      apply(auto simp add: neq-Nil-conv split: extCallRet.split-asm)
      apply(force intro!: exec-meth.intros)+
      done
    qed
  qed
next
  case (bisim1CallThrowObj obj n a xs stk loc pc ps M')
  note bisim = ⟨P, obj, h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
  note IH = ⟨∧stk' loc' pc' xcp' STK. ?exec obj stk STK loc pc [a] stk' loc' pc' xcp'

```

$\implies ?concl\ obj\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp'$   
**note**  $exec = \langle ?exec\ (obj.M'(ps))\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$   
**from**  $bisim$  **have**  $pc < length\ (compE2\ obj)$  **and**  $[simp]: xs = loc$  **by**  $(auto\ dest: bisim1-ThrowD)$   
**with**  $exec$  **have**  $?exec\ obj\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp'$   
**by**  $(simp\ add: compxEs2-size-convs\ compxEs2-stack-xlift-convs)(erule\ exec-meth-take-xt)$   
**from**  $IH[OF\ this]$  **show**  $?case$  **by**  $auto$   
**next**  
**case**  $(bisim1CallThrowParams\ ps\ n\ vs\ a\ ps'\ xs\ stk\ loc\ pc\ obj\ M'\ v)$   
**note**  $bisim = \langle P, ps, h \vdash (map\ Val\ vs\ @\ Throw\ a\ \# \ ps', xs) [\leftrightarrow] (stk, loc, pc, [a]) \rangle$   
**note**  $IH = \langle \bigwedge stk'\ loc'\ pc'\ xcp'\ STK. ?execs\ ps\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \implies ?concls\ ps\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$   
**note**  $exec = \langle ?exec\ (obj.M'(ps))\ (stk\ @\ [v])\ STK\ loc\ (length\ (compE2\ obj) + pc)\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$   
**from**  $bisim$  **have**  $pc: pc < length\ (compEs2\ ps)\ loc = xs$  **by**  $(auto\ dest: bisims1-ThrowD)$   
**from**  $exec$  **have**  $exec-meth-d\ (compP2\ P)\ ((compE2\ obj\ @\ compEs2\ ps)\ @\ [Invoke\ M'\ (length\ ps)])$   
 $(stack-xlift\ (length\ STK)\ (compxE2\ obj\ 0\ 0)\ @\ shift\ (length\ (compE2\ obj))\ (stack-xlift\ (length\ (v\ \# \ STK))\ (compxEs2\ ps\ 0\ 0)))\ t$   
 $h\ (stk\ @\ v\ \# \ STK, loc, length\ (compE2\ obj) + pc, [a])\ ta\ h'\ (stk', loc', pc', xcp')$   
**by**  $(simp\ add: compxEs2-size-convs\ compxEs2-stack-xlift-convs)$   
**hence**  $exec': exec-meth-d\ (compP2\ P)\ (compE2\ obj\ @\ compEs2\ ps)\ (stack-xlift\ (length\ STK)\ (compxE2\ obj\ 0\ 0)\ @\ shift\ (length\ (compE2\ obj))\ (stack-xlift\ (length\ (v\ \# \ STK))\ (compxEs2\ ps\ 0\ 0)))\ t$   
 $h\ (stk\ @\ v\ \# \ STK, loc, length\ (compE2\ obj) + pc, [a])\ ta\ h'\ (stk', loc', pc', xcp')$   
**by**  $(rule\ exec-meth-take)(simp\ add: pc)$   
**hence**  $?execs\ ps\ stk\ (v\ \# \ STK)\ loc\ pc\ [a]\ stk'\ loc'\ (pc' - length\ (compE2\ obj))\ xcp'$   
**by**  $(rule\ exec-meth-drop-xt)(auto\ simp\ add: stack-xlift-compxE2)$   
**from**  $IH[OF\ this]$  **obtain**  $stk''$  **where**  $stk': stk' = stk''\ @\ v\ \# \ STK$   
**and**  $exec'': exec-meth-d\ (compP2\ P)\ (compEs2\ ps)\ (compxEs2\ ps\ 0\ 0)\ t\ h\ (stk, loc, pc, [a])\ ta\ h'\ (stk'', loc', pc' - length\ (compE2\ obj), xcp')$  **by**  $auto$   
**from**  $exec''$  **have**  $exec-meth-d\ (compP2\ P)\ ((compE2\ obj\ @\ compEs2\ ps)\ @\ [Invoke\ M'\ (length\ ps)])$   
 $(compxE2\ obj\ 0\ 0\ @\ shift\ (length\ (compE2\ obj))\ (stack-xlift\ (length\ [v])\ (compxEs2\ ps\ 0\ 0)))\ t$   
 $h\ (stk\ @\ [v], loc, length\ (compE2\ obj) + pc, [a])\ ta\ h'\ (stk''\ @\ [v], loc', length\ (compE2\ obj) + (pc' - length\ (compE2\ obj)), xcp')$   
**apply**  $-$   
**apply**  $(rule\ exec-meth-append)$   
**apply**  $(rule\ append-exec-meth-xt)$   
**apply**  $(erule\ exec-meth-stk-offer)$   
**apply**  $auto$   
**done**  
**moreover from**  $exec'$  **have**  $pc' \geq length\ (compE2\ obj)$   
**by**  $(rule\ exec-meth-drop-xt-pc)(auto\ simp\ add: stack-xlift-compxE2)$   
**ultimately show**  $?case$  **using**  $stk'$  **by**  $(auto\ simp\ add: compxEs2-size-convs\ compxEs2-stack-xlift-convs)$   
**next**  
**case**  $bisim1CallThrow$  **thus**  $?case$   
**by**  $(auto\ elim!: exec-meth.cases\ dest: match-ex-table-pcsD\ simp\ add: stack-xlift-compxEs2\ stack-xlift-compxE2)$   
**next**  
**case**  $bisim1BlockSome1$  **thus**  $?case$   
**by**  $(fastforce\ elim: exec-meth.cases\ intro: exec-meth.intros)$   
**next**  
**case**  $bisim1BlockSome2$  **thus**  $?case$   
**by**  $(fastforce\ elim: exec-meth.cases\ intro: exec-meth.intros)$   
**next**  
**case**  $(bisim1BlockSome4\ e\ n\ e'\ xs\ stk\ loc\ pc\ xcp\ V\ T\ v)$   
**note**  $IH = \langle \bigwedge stk'\ loc'\ pc'\ xcp'\ STK. ?exec\ e\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$

$\implies ?concl\ e\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp'$   
**note**  $bisim = \langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$   
**note**  $exec = \langle ?exec\ \{V:T=[v];\ e\}\ stk\ STK\ loc\ (Suc\ (Suc\ pc))\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$   
**hence**  $exec': exec\text{-meth-d}\ (compP2\ P)\ ([Push\ v,\ Store\ V] @\ compE2\ e)$   
 $(shift\ (length\ [Push\ v,\ Store\ V])\ (stack\text{-xlift}\ (length\ STK)\ (compxE2\ e\ 0\ 0)))\ t\ h\ (stk @\ STK,\ loc,$   
 $length\ [Push\ v,\ Store\ V] + pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$   
**by**(simp add: compxE2-size-convs)  
**hence**  $?exec\ e\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ (pc' - length\ [Push\ v,\ Store\ V])\ xcp'$   
**by**(rule exec-meth-drop) auto  
**from**  $IH[OF\ this]$  **obtain**  $stk''$  **where**  $stk': stk' = stk'' @\ STK$   
**and**  $exec'': exec\text{-meth-d}\ (compP2\ P)\ (compE2\ e)\ (compxE2\ e\ 0\ 0)\ t\ h\ (stk,\ loc,\ pc,\ xcp)\ ta$   
 $h'\ (stk'',\ loc',\ pc' - length\ [Push\ v,\ Store\ V],\ xcp')$  **by** auto  
**from**  $exec''$  **have**  $exec\text{-meth-d}\ (compP2\ P)\ ([Push\ v,\ Store\ V] @\ compE2\ e)$   
 $(shift\ (length\ [Push\ v,\ Store\ V])\ (compxE2\ e\ 0\ 0))\ t\ h\ (stk,\ loc,\ length\ [Push\ v,\ Store\ V] + pc,$   
 $xcp)\ ta\ h'\ (stk'',\ loc',\ length\ [Push\ v,\ Store\ V] + (pc' - length\ [Push\ v,\ Store\ V]),\ xcp')$   
**by**(rule append-exec-meth) auto  
**moreover from**  $exec'$  **have**  $pc' \geq length\ [Push\ v,\ Store\ V]$   
**by**(rule exec-meth-drop-pc)(auto simp add: stack-xlift-compxE2)  
**hence**  $Suc\ (Suc\ (pc' - Suc\ (Suc\ 0))) = pc'$  **by**(simp)  
**ultimately show**  $?case\ using\ stk'$  **by**(auto simp add: compxE2-size-convs)  
**next**  
**case** (bisim1BlockThrowSome  $e\ n\ a\ xs\ stk\ loc\ pc\ V\ T\ v$ )  
**note**  $IH = \langle \bigwedge stk'\ loc'\ pc'\ xcp'\ STK.\ ?exec\ e\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$   
 $\implies ?concl\ e\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp'$   
**note**  $exec = \langle ?exec\ \{V:T=[v];\ e\}\ stk\ STK\ loc\ (Suc\ (Suc\ pc))\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$   
**hence**  $exec': exec\text{-meth-d}\ (compP2\ P)\ ([Push\ v,\ Store\ V] @\ compE2\ e)$   
 $(shift\ (length\ [Push\ v,\ Store\ V])\ (stack\text{-xlift}\ (length\ STK)\ (compxE2\ e\ 0\ 0)))\ t\ h\ (stk @\ STK,\ loc,$   
 $length\ [Push\ v,\ Store\ V] + pc,\ [a])\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$   
**by**(simp add: compxE2-size-convs)  
**hence**  $?exec\ e\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ (pc' - length\ [Push\ v,\ Store\ V])\ xcp'$   
**by**(rule exec-meth-drop) auto  
**from**  $IH[OF\ this]$  **obtain**  $stk''$  **where**  $stk': stk' = stk'' @\ STK$   
**and**  $exec'': exec\text{-meth-d}\ (compP2\ P)\ (compE2\ e)\ (compxE2\ e\ 0\ 0)\ t\ h\ (stk,\ loc,\ pc,\ [a])\ ta$   
 $h'\ (stk'',\ loc',\ pc' - length\ [Push\ v,\ Store\ V],\ xcp')$  **by** auto  
**from**  $exec''$  **have**  $exec\text{-meth-d}\ (compP2\ P)\ ([Push\ v,\ Store\ V] @\ compE2\ e)$   
 $(shift\ (length\ [Push\ v,\ Store\ V])\ (compxE2\ e\ 0\ 0))\ t\ h\ (stk,\ loc,\ length\ [Push\ v,\ Store\ V] + pc,$   
 $[a])\ ta\ h'\ (stk'',\ loc',\ length\ [Push\ v,\ Store\ V] + (pc' - length\ [Push\ v,\ Store\ V]),\ xcp')$   
**by**(rule append-exec-meth) auto  
**moreover from**  $exec'$  **have**  $pc' \geq length\ [Push\ v,\ Store\ V]$   
**by**(rule exec-meth-drop-pc)(auto simp add: stack-xlift-compxE2)  
**hence**  $Suc\ (Suc\ (pc' - Suc\ (Suc\ 0))) = pc'$  **by**(simp)  
**ultimately show**  $?case\ using\ stk'$  **by**(auto simp add: compxE2-size-convs)  
**next**  
**case** bisim1BlockNone **thus**  $?case$   
**by**(fastforce elim: exec-meth.cases intro: exec-meth.intros)  
**next**  
**case** bisim1BlockThrowNone **thus**  $?case$   
**by**(fastforce elim: exec-meth.cases intro: exec-meth.intros)  
**next**  
**case** (bisim1Sync1  $e1\ n\ e1'\ xs\ stk\ loc\ pc\ xcp\ e2\ V$ )  
**note**  $bisim = \langle P, e1, h \vdash (e1', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$   
**note**  $IH = \langle \bigwedge stk'\ loc'\ pc'\ xcp'\ STK.\ ?exec\ e1\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$   
 $\implies ?concl\ e1\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp'$   
**note**  $exec = \langle ?exec\ (sync_V\ (e1)\ e2)\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$

```

from bisim have pc:  $pc \leq \text{length} (\text{compE2 } e1)$  by(rule bisim1-pc-length-compE2)
show ?case
proof(cases pc < length (compE2 e1))
  case True
    from exec have exec-meth-d (compP2 P)
      (compE2 e1 @ (Dup # Store V # MEnter # compE2 e2 @ [Load V, MExit, Goto 4, Load V,
MExit, ThrowExc]))
      (stack-xlift (length STK) (compxE2 e1 0 0) @ shift (length (compE2 e1)) (stack-xlift (length
STK) (compxE2 e2 3 0) @
        stack-xlift (length STK) [(3, 3 + length (compE2 e2), None, 6 + length (compE2 e2), 0)])) t
      h (stk @ STK, loc, pc, xcp) ta h' (stk', loc', pc', xcp')
      by(simp add: shift-compxE2 stack-xlift-compxE2 ac-simps)
    hence ?exec e1 stk STK loc pc xcp stk' loc' pc' xcp'
      by(rule exec-meth-take-xt)(rule True)
    from IH[OF this] obtain stk'' where stk': stk' = stk'' @ STK
      and exec': exec-meth-d (compP2 P) (compE2 e1) (compxE2 e1 0 0) t h (stk, loc, pc, xcp) ta h'
      (stk'', loc', pc', xcp')
      by blast
    from exec' have exec-meth-d (compP2 P) (compE2 e1 @ (Dup # Store V # MEnter # compE2
e2 @ [Load V, MExit, Goto 4, Load V, MExit, ThrowExc]))
      (compxE2 e1 0 0 @ shift (length (compE2 e1)) (compxE2 e2 3 0 @ [(3, 3 + length (compE2 e2),
None, 6 + length (compE2 e2), 0)])) t
      h (stk, loc, pc, xcp) ta h' (stk'', loc', pc', xcp')
      by(rule exec-meth-append-xt)
    thus ?thesis using stk' by(simp add: shift-compxE2 stack-xlift-compxE2 ac-simps)
  next
    case False
      with pc have [simp]:  $pc = \text{length} (\text{compE2 } e1)$  by simp
      with bisim obtain v where stk = [v] xcp = None by(auto dest: bisim1-pc-length-compE2D)
      thus ?thesis using exec by(auto elim!: exec-meth.cases intro!: exec-meth.intros)
    qed
  next
    case bisim1Sync2 thus ?case
      by(fastforce elim!: exec-meth.cases intro!: exec-meth.intros)
  next
    case bisim1Sync3 thus ?case
      by(fastforce elim!: exec-meth.cases intro!: exec-meth.intros split: if-split-asm)
  next
    case (bisim1Sync4 e2 n e2' xs stk loc pc xcp e1 V a)
      note IH =  $\langle \bigwedge \text{stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \text{ STK}. ?\text{exec } e2 \text{ stk STK loc pc xcp stk}' \text{ loc}' \text{ pc}' \text{ xcp}'$ 
         $\implies ?\text{concl } e2 \text{ stk STK loc pc xcp stk}' \text{ loc}' \text{ pc}' \text{ xcp}'$ 
 $\rangle$ 
      note bisim =  $\langle P, e2, h \vdash (e2', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
      note exec =  $\langle ?\text{exec} (\text{sync}_V (e1) e2) \text{ stk STK loc (Suc (Suc (Suc (length (compE2 } e1) + pc)))) \text{ xcp}$ 
 $\text{stk}' \text{ loc}' \text{ pc}' \text{ xcp}'$ 
 $\rangle$ 
      from bisim have pc:  $pc \leq \text{length} (\text{compE2 } e2)$  by(rule bisim1-pc-length-compE2)
      show ?case
      proof(cases pc < length (compE2 e2))
        case True
          let ?pre = compE2 e1 @ [Dup, Store V, MEnter]
          from exec have exec': exec-meth-d (compP2 P) (?pre @ compE2 e2 @ [Load V, MExit, Goto 4,
Load V, MExit, ThrowExc])
            (stack-xlift (length STK) (compxE2 e1 0 0) @ shift (length ?pre) (stack-xlift (length STK) (compxE2
e2 0 0) @
               $[(0, \text{length} (\text{compE2 } e2), \text{None}, 3 + \text{length} (\text{compE2 } e2), \text{length STK})]$ 
)) t

```



```

  h (stk @ STK, loc, length ?pre + pc, xcp) ta h' (stk', loc', pc', xcp')
  by(simp add: stack-xlift-compxE2 shift-compxE2 eval-nat-numeral ac-simps)
  hence exec'': exec-meth-d (compP2 P) (compE2 e2 @ [Load V, MExit, Goto 4, Load V, MExit,
ThrowExc])
  (stack-xlift (length STK) (compxE2 e2 0 0) @ [(0, length (compE2 e2), None, 3 + length (compE2
e2), length STK)]) t
  h (stk @ STK, loc, pc, xcp) ta h' (stk', loc', pc' - length ?pre, xcp')
  by(rule exec-meth-drop-xt[where n=1])(auto simp add: stack-xlift-compxE2)
  from exec' have pc': pc' ≥ length ?pre
  by(rule exec-meth-drop-xt-pc[where n'=1])(auto simp add: stack-xlift-compxE2)
  hence pc'': (Suc (Suc (Suc (pc' - Suc (Suc (Suc 0))))) = pc' by simp
  show ?thesis
  proof(cases xcp)
    case None
    from exec'' None True
    have ?exec e2 stk STK loc pc xcp stk' loc' (pc' - length ?pre) xcp'
    apply -
    apply (erule exec-meth.cases)
    apply (cases compE2 e2 ! pc)
    apply (fastforce simp add: is-Ref-def intro: exec-meth.intros split: if-split-asm
cong del: image-cong-simp)+
    done
    from IH[OF this] obtain stk'' where stk: stk' = stk'' @ STK
    and exec''': exec-meth-d (compP2 P) (compE2 e2) (compxE2 e2 0 0) t h (stk, loc, pc, xcp)
    ta h' (stk'', loc', pc' - length ?pre, xcp') by blast
    from exec''' have exec-meth-d (compP2 P) (compE2 e2 @ [Load V, MExit, Goto 4, Load V,
MExit, ThrowExc])
    (compxE2 e2 0 0 @ [(0, length (compE2 e2), None, 3 + length (compE2 e2), 0)]) t
    h (stk, loc, pc, xcp) ta h' (stk'', loc', pc' - length ?pre, xcp')
    by(rule exec-meth-append-xt)
    hence exec-meth-d (compP2 P) (?pre @ compE2 e2 @ [Load V, MExit, Goto 4, Load V, MExit,
ThrowExc])
    (compxE2 e1 0 0 @ shift (length ?pre) (compxE2 e2 0 0 @ [(0, length (compE2 e2), None, 3 +
length (compE2 e2), 0)])) t
    h (stk, loc, length ?pre + pc, xcp) ta h' (stk'', loc', length ?pre + (pc' - length ?pre), xcp')
    by(rule append-exec-meth-xt[where n=1]) auto
    thus ?thesis using stk pc' pc'' by(simp add: eval-nat-numeral shift-compxE2 ac-simps)
  next
  case (Some a)
  with exec'' have [simp]: h' = h xcp' = None loc' = loc ta = ε
  by(auto elim!: exec-meth.cases simp add: match-ex-table-append
split: if-split-asm dest!: match-ex-table-stack-xliftD)
  show ?thesis
  proof(cases match-ex-table (compP2 P) (cname-of h a) pc (compxE2 e2 0 0))
    case None
    with Some exec'' True have [simp]: stk' = Addr a # STK
    and pc': pc' = length (compE2 e1) + length (compE2 e2) + 6
    by(auto elim!: exec-meth.cases simp add: match-ex-table-append
split: if-split-asm dest!: match-ex-table-stack-xliftD)
    with exec'' Some None
    have exec-meth-d (compP2 P) (compE2 e2 @ [Load V, MExit, Goto 4, Load V, MExit,
ThrowExc])
    (compxE2 e2 0 0 @ [(0, length (compE2 e2), None, 3 + length (compE2 e2), 0)]) t
    h (stk, loc, pc, [a]) ε h (Addr a # drop (length stk - 0) stk, loc, pc' - length ?pre, None)

```

```

    by  $-(rule\ exec\_catch, auto\ elim!: exec\_meth.cases\ simp\ add: match\_ex\_table\_append\ matches\_ex\_entry\_def$ 
       $split: if\_split\_asm\ dest!: match\_ex\_table\_stack\_xliftD)$ 
  hence  $exec\_meth\_d\ (compP2\ P)\ (?pre\ @\ compE2\ e2\ @\ [Load\ V,\ MExit,\ Goto\ 4,\ Load\ V,\ MExit,$ 
     $ThrowExc])$ 
     $(compxE2\ e1\ 0\ 0\ @\ shift\ (length\ ?pre)\ (compxE2\ e2\ 0\ 0\ @\ [(0,\ length\ (compE2\ e2),\ None,\ 3 +$ 
     $length\ (compE2\ e2),\ 0)]))\ t$ 
     $h\ (stk,\ loc,\ length\ ?pre + pc,\ [a]) \varepsilon h\ (Addr\ a\ \# \ drop\ (length\ stk - 0)\ stk,\ loc,$ 
     $length\ ?pre + (pc' - length\ ?pre),\ None)$ 
    by  $(rule\ append\_exec\_meth\_xt[where\ n=1])\ auto$ 
  with  $pc'\ Some\ show\ ?thesis\ by(simp\ add: eval\_nat\_numeral\ shift\_compxE2\ ac\_simps)$ 
next
  case  $(Some\ pcd)$ 
  with  $\langle xcp = [a] \rangle\ exec''\ True$ 
  have  $exec\_meth\_d\ (compP2\ P)\ (compE2\ e2)\ (compxE2\ e2\ 0\ 0)\ t$ 
     $h\ (stk,\ loc,\ pc,\ [a]) \varepsilon h\ (Addr\ a\ \# \ drop\ (length\ stk - snd\ pcd)\ stk,\ loc,\ pc' - length\ ?pre,$ 
     $None)$ 
    apply  $-$ 
    apply  $(rule\ exec\_catch)$ 
    apply  $(auto\ elim!: exec\_meth.cases\ simp\ add: match\_ex\_table\_append\ split: if\_split\_asm$ 
       $dest!: match\_ex\_table\_stack\_xliftD)$ 
    done
  hence  $exec\_meth\_d\ (compP2\ P)\ (compE2\ e2\ @\ [Load\ V,\ MExit,\ Goto\ 4,\ Load\ V,\ MExit,$ 
     $ThrowExc])\ (compxE2\ e2\ 0\ 0\ @\ [(0,\ length\ (compE2\ e2),\ None,\ 3 + length\ (compE2\ e2),\ 0)])\ t$ 
     $h\ (stk,\ loc,\ pc,\ [a]) \varepsilon h\ (Addr\ a\ \# \ drop\ (length\ stk - snd\ pcd)\ stk,\ loc,\ pc' - length\ ?pre,\ None)$ 
    by  $(rule\ exec\_meth\_append\_xt)$ 
  hence  $exec\_meth\_d\ (compP2\ P)\ (?pre\ @\ compE2\ e2\ @\ [Load\ V,\ MExit,\ Goto\ 4,\ Load\ V,\ MExit,$ 
     $ThrowExc])$ 
     $(compxE2\ e1\ 0\ 0\ @\ shift\ (length\ ?pre)\ (compxE2\ e2\ 0\ 0\ @\ [(0,\ length\ (compE2\ e2),\ None,$ 
     $3 + length\ (compE2\ e2),\ 0)]))\ t$ 
     $h\ (stk,\ loc,\ length\ ?pre + pc,\ [a]) \varepsilon h\ (Addr\ a\ \# \ drop\ (length\ stk - snd\ pcd)\ stk,\ loc,\ length\ ?pre$ 
     $+ (pc' - length\ ?pre),\ None)$ 
    by  $(rule\ append\_exec\_meth\_xt[where\ n=1])(auto)$ 
    moreover from  $Some\ \langle xcp = [a] \rangle\ exec''\ True\ pc'$ 
    have  $pc' = length\ (compE2\ e1) + 3 + fst\ pcd\ stk' = Addr\ a\ \# \ drop\ (length\ stk - snd\ pcd)\ stk$ 
    @  $STK$ 
    by  $(auto\ elim!: exec\_meth.cases\ dest!: match\_ex\_table\_stack\_xliftD\ simp: match\_ex\_table\_append$ 
     $split: if\_split\_asm)$ 
    ultimately show  $?thesis\ using\ \langle xcp = [a] \rangle\ by(auto\ simp\ add: eval\_nat\_numeral\ shift\_compxE2$ 
     $ac\_simps)$ 
    qed
  qed
next
  case  $False$ 
  with  $pc\ have\ [simp]:\ pc = length\ (compE2\ e2)\ by\ simp$ 
  with  $exec\ show\ ?thesis$ 
  by  $(auto\ elim!: exec\_meth.cases\ intro!: exec\_meth.intros\ split: if\_split\_asm\ simp\ add: match\_ex\_table\_append\_not-p$ 
     $eval\_nat\_numeral)(simp\_all\ add: matches\_ex\_entry\_def)$ 
  qed
next
  case  $bisim1Sync5\ thus\ ?case$ 
  by  $(fastforce\ elim: exec\_meth.cases\ intro: exec\_meth.intros\ split: if\_split\_asm)$ 
next
  case  $bisim1Sync6\ thus\ ?case$ 
  by  $(fastforce\ elim: exec\_meth.cases\ intro: exec\_meth.intros\ split: if\_split\_asm)$ 

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next
  case bisim1Sync7 thus ?case
    by(fastforce elim: exec-meth.cases intro: exec-meth.intros split: if-split-asm)
next
  case bisim1Sync8 thus ?case
    by(fastforce elim: exec-meth.cases intro: exec-meth.intros split: if-split-asm)
next
  case (bisim1Sync9 e1 n e2 V a xs)
    note exec = ⟨?exec (syncV (e1) e2) [Addr a] STK xs (8 + length (compE2 e1) + length (compE2 e2)) None stk' loc' pc' xcp'⟩
    let ?pre = compE2 e1 @ Dup # Store V # MEnter # compE2 e2 @ [Load V, MExit, Goto 4, Load V, MExit]
    from exec have exec': exec-meth-d (compP2 P) (?pre @ [ThrowExc]) (stack-xlift (length STK) (compE2 (syncV (e1) e2) 0 0) @ shift (length ?pre) []) t h (Addr a # STK, xs, length ?pre + 0, None) ta h' (stk', loc', pc', xcp')
    by(simp add: eval-nat-numeral)
    hence exec-meth-d (compP2 P) [ThrowExc] [] t h (Addr a # STK, xs, 0, None) ta h' (stk', loc', pc' - length ?pre, xcp')
    by(rule exec-meth-drop-xt)(auto simp add: stack-xlift-compE2)
    moreover from exec' have pc' = 8 + length (compE2 e1) + length (compE2 e2) stk' = Addr a # STK
    STK
    by(auto elim!: exec-meth.cases)
    ultimately show ?case by(fastforce elim!: exec-meth.cases intro: exec-meth.intros)
next
  case (bisim1Sync10 e1 n e2 V a xs)
    note exec = ⟨?exec (syncV (e1) e2) [Addr a] STK xs (8 + length (compE2 e1) + length (compE2 e2)) [a] stk' loc' pc' xcp'⟩
    hence match-ex-table (compP2 P) (cname-of h a) (8 + length (compE2 e1) + length (compE2 e2)) (stack-xlift (length STK) (compE2 (syncV (e1) e2) 0 0)) ≠ None
    by(rule exec-meth.cases) auto
    hence False by(auto split: if-split-asm simp add: match-ex-table-append-not-pcs)(simp add: matches-ex-entry-def)
    thus ?case ..
next
  case (bisim1Sync11 e1 n e2 V xs)
    note exec = ⟨?exec (syncV (e1) e2) [Null] STK xs (Suc (Suc (length (compE2 e1)))) [addr-of-sys-xcpt NullPointer] stk' loc' pc' xcp'⟩
    hence match-ex-table (compP2 P) (cname-of h (addr-of-sys-xcpt NullPointer)) (2 + length (compE2 e1)) (stack-xlift (length STK) (compE2 (syncV (e1) e2) 0 0)) ≠ None
    by(rule exec-meth.cases)(auto split: if-split-asm)
    hence False by(auto split: if-split-asm simp add: match-ex-table-append-not-pcs)(simp add: matches-ex-entry-def)
    thus ?case ..
next
  case (bisim1SyncThrow e1 n a xs stk loc pc e2 V)
    note exec = ⟨?exec (syncV (e1) e2) stk STK loc pc [a] stk' loc' pc' xcp'⟩
    note IH = ⟨∧stk' loc' pc' xcp' STK. ?exec e1 stk STK loc pc [a] stk' loc' pc' xcp'
      ⇒ ?concl e1 stk STK loc pc [a] stk' loc' pc' xcp'⟩
    note bisim = ⟨P, e1, h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
    from bisim have pc: pc < length (compE2 e1)
    and [simp]: loc = xs by(auto dest: bisim1-ThrowD)
    from exec have exec-meth-d (compP2 P) (compE2 e1 @ Dup # Store V # MEnter # compE2 e2 @ [Load V, MExit, Goto 4, Load V, MExit, ThrowExc])
      (stack-xlift (length STK) (compE2 e1 0 0) @ shift (length (compE2 e1)) (stack-xlift (length STK) (compE2 e2 3 0) @
        [(3, 3 + length (compE2 e2), None, 6 + length (compE2 e2), length STK)])) t

```

```

    h (stk @ STK, loc, pc, [a]) ta h' (stk', loc', pc', xcp')
    by(simp add: shift-compxE2 stack-xlift-compxE2 ac-simps)
  hence ?exec e1 stk STK loc pc [a] stk' loc' pc' xcp'
    by(rule exec-meth-take-xt)(rule pc)
  from IH[OF this] show ?case by auto
next
case (bisim1Seq1 e1 n e1' xs stk loc pc xcp e2)
note bisim1 = ⟨P, e1, h ⊢ (e1', xs) ↔ (stk, loc, pc, xcp)⟩
note IH = ⟨∧stk' loc' pc' xcp' STK. ?exec e1 stk STK loc pc xcp stk' loc' pc' xcp'
  ⇒ ?concl e1 stk STK loc pc xcp stk' loc' pc' xcp'⟩
note exec = ⟨?exec (e1;;e2) stk STK loc pc xcp stk' loc' pc' xcp'⟩
from bisim1 have pc: pc ≤ length (compE2 e1) by(rule bisim1-pc-length-compE2)
show ?case
proof(cases pc < length (compE2 e1))
  case True
  from exec have exec-meth-d (compP2 P) (compE2 e1 @ Pop # compE2 e2) (stack-xlift (length
    STK) (compxE2 e1 0 0) @
    shift (length (compE2 e1)) (stack-xlift (length STK) (compxE2 e2 (Suc 0) 0))) t
    h (stk @ STK, loc, pc, xcp) ta h' (stk', loc', pc', xcp')
    by(simp add: shift-compxE2 stack-xlift-compxE2)
  hence ?exec e1 stk STK loc pc xcp stk' loc' pc' xcp'
    by(rule exec-meth-take-xt)(rule True)
  from IH[OF this] show ?thesis by auto
next
case False
  with pc have [simp]: pc = length (compE2 e1) by simp
  with bisim1 obtain v where xcp = None stk = [v] by(auto dest: bisim1-pc-length-compE2D)
  with exec show ?thesis by(fastforce elim: exec-meth.cases intro: exec-meth.intros)
qed
next
case (bisim1SeqThrow1 e1 n a xs stk loc pc e2)
note bisim1 = ⟨P, e1, h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
note IH = ⟨∧stk' loc' pc' xcp' STK. ?exec e1 stk STK loc pc [a] stk' loc' pc' xcp'
  ⇒ ?concl e1 stk STK loc pc [a] stk' loc' pc' xcp'⟩
note exec = ⟨?exec (e1;;e2) stk STK loc pc [a] stk' loc' pc' xcp'⟩
from bisim1 have pc: pc < length (compE2 e1) by(auto dest: bisim1-ThrowD)
from exec have exec-meth-d (compP2 P) (compE2 e1 @ Pop # compE2 e2) (stack-xlift (length
    STK) (compxE2 e1 0 0) @
    shift (length (compE2 e1)) (stack-xlift (length STK) (compxE2 e2 (Suc 0) 0))) t
    h (stk @ STK, loc, pc, [a]) ta h' (stk', loc', pc', xcp')
    by(simp add: shift-compxE2 stack-xlift-compxE2)
  hence ?exec e1 stk STK loc pc [a] stk' loc' pc' xcp'
    by(rule exec-meth-take-xt)(rule pc)
  from IH[OF this] show ?case by(fastforce elim: exec-meth.cases intro: exec-meth.intros)
next
case (bisim1Seq2 e2 n e2' xs stk loc pc xcp e1)
note bisim2 = ⟨P, e2, h ⊢ (e2', xs) ↔ (stk, loc, pc, xcp)⟩
note IH = ⟨∧stk' loc' pc' xcp' STK. ?exec e2 stk STK loc pc xcp stk' loc' pc' xcp'
  ⇒ ?concl e2 stk STK loc pc xcp stk' loc' pc' xcp'⟩
note exec = ⟨?exec (e1;;e2) stk STK loc (Suc (length (compE2 e1) + pc)) xcp stk' loc' pc' xcp'⟩
from bisim2 have pc: pc ≤ length (compE2 e2) by(rule bisim1-pc-length-compE2)
show ?case
proof(cases pc < length (compE2 e2))
  case False

```

```

with pc have [simp]: pc = length (compE2 e2) by simp
from bisim2 have xcp = None by(auto dest: bisim1-pc-length-compE2D)
with exec have False by(auto elim: exec-meth.cases)
thus ?thesis ..
next
case True
from exec have exec':
  exec-meth-d (compP2 P) ((compE2 e1 @ [Pop]) @ compE2 e2) (stack-xlift (length STK) (compxE2
e1 0 0) @
  shift (length (compE2 e1 @ [Pop])) (stack-xlift (length STK) (compxE2 e2 0 0))) t
  h (stk @ STK, loc, length ((compE2 e1) @ [Pop]) + pc, xcp) ta h' (stk', loc', pc', xcp')
  by(simp add: compxE2-size-convs)
hence ?exec e2 stk STK loc pc xcp stk' loc' (pc' - length ((compE2 e1) @ [Pop])) xcp'
  by(rule exec-meth-drop-xt)(auto simp add: stack-xlift-compxE2)
from IH[OF this] obtain stk'' where stk': stk' = stk'' @ STK
  and exec'': exec-meth-d (compP2 P) (compE2 e2) (compxE2 e2 0 0) t h (stk, loc, pc, xcp)
  ta h' (stk'', loc', pc' - length (compE2 e1 @ [Pop]), xcp') by auto
from exec'' have exec-meth-d (compP2 P) ((compE2 e1 @ [Pop]) @ compE2 e2) (compxE2 e1 0
0 @ shift (length (compE2 e1 @ [Pop])) (compxE2 e2 0 0)) t
  h (stk, loc, length ((compE2 e1) @ [Pop]) + pc, xcp) ta h' (stk'', loc', length ((compE2 e1) @
[Pop]) + (pc' - length ((compE2 e1) @ [Pop])), xcp')
  by(rule append-exec-meth-xt) auto
moreover from exec' have pc' ≥ length ((compE2 e1) @ [Pop])
  by(rule exec-meth-drop-xt-pc)(auto simp add: stack-xlift-compxE2)
ultimately show ?thesis using stk' by(auto simp add: shift-compxE2 stack-xlift-compxE2)
qed
next
case (bisim1Cond1 e n e' xs stk loc pc xcp e1 e2)
note bisim = ⟨P, e, h ⊢ (e', xs) ↔ (stk, loc, pc, xcp)⟩
note IH = ⟨∧stk' loc' pc' xcp' STK. ?exec e stk STK loc pc xcp stk' loc' pc' xcp'
  ⇒ ?concl e stk STK loc pc xcp stk' loc' pc' xcp'⟩
note exec = ⟨?exec (if (e) e1 else e2) stk STK loc pc xcp stk' loc' pc' xcp'⟩
from bisim have pc: pc ≤ length (compE2 e) by(rule bisim1-pc-length-compE2)
show ?case
proof(cases pc < length (compE2 e))
case True
  from exec have exec-meth-d (compP2 P) (compE2 e @ IfFalse (2 + int (length (compE2 e1))) #
compE2 e1 @ Goto (1 + int (length (compE2 e2))) # compE2 e2)
    (stack-xlift (length STK) (compxE2 e 0 0) @ shift (length (compE2 e)) (stack-xlift (length STK)
(compxE2 e1 (Suc 0) 0) @
    stack-xlift (length STK) (compxE2 e2 (Suc (Suc (length (compE2 e1)))) 0))) t
    h (stk @ STK, loc, pc, xcp) ta h' (stk', loc', pc', xcp')
    by(simp add: stack-xlift-compxE2 shift-compxE2 ac-simps)
  hence ?exec e stk STK loc pc xcp stk' loc' pc' xcp'
    by(rule exec-meth-take-xt)(rule True)
  from IH[OF this] show ?thesis by auto
next
case False
  with pc have [simp]: pc = length (compE2 e) by simp
  from bisim obtain v where stk = [v] xcp = None
    by(auto dest: bisim1-pc-length-compE2D)
  with exec show ?thesis by(auto elim!: exec-meth.cases intro!: exec-meth.intros)
qed
next

```

```

case (bisim1CondThen e1 n e1' xs stk loc pc xcp e e2)
note bisim =  $\langle P, e1, h \vdash (e1', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
note IH =  $\langle \bigwedge stk' loc' pc' xcp' STK. ?exec\ e1\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \Rightarrow ?concl\ e1\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$ 
note exec =  $\langle ?exec\ (if\ (e)\ e1\ else\ e2)\ stk\ STK\ loc\ (Suc\ (length\ (compE2\ e) + pc))\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$ 
from bisim have pc:  $pc \leq length\ (compE2\ e1)$  by (rule bisim1-pc-length-compE2)
show ?case
proof (cases pc < length (compE2 e1))
  case True
    let ?pre = compE2 e @ [IfFalse (2 + int (length (compE2 e1)))]
    from exec have exec': exec-meth-d (compP2 P) (?pre @ compE2 e1 @ Goto (1 + int (length (compE2 e2)))) # compE2 e2
    (stack-xlift (length STK) (compxE2 e 0 0) @ shift (length ?pre) (stack-xlift (length STK) (compxE2 e1 0 0) @
      shift (length (compE2 e1)) (stack-xlift (length STK) (compxE2 e2 (Suc 0) 0)))) t
    h (stk @ STK, loc, length ?pre + pc, xcp) ta h' (stk', loc', pc', xcp')
    by (simp add: stack-xlift-compxE2 shift-compxE2 ac-simps)
    hence exec-meth-d (compP2 P) (compE2 e1 @ Goto (1 + int (length (compE2 e2)))) # compE2 e2
    (stack-xlift (length STK) (compxE2 e1 0 0) @ shift (length (compE2 e1)) (stack-xlift (length STK) (compxE2 e2 (Suc 0) 0))) t
    h (stk @ STK, loc, pc, xcp) ta h' (stk', loc', pc' - length ?pre, xcp')
    by (rule exec-meth-drop-xt) (auto simp add: stack-xlift-compxE2)
    hence ?exec e1 stk STK loc pc xcp stk' loc' (pc' - length ?pre) xcp'
    by (rule exec-meth-take-xt) (rule True)
    from IH[OF this] obtain stk'' where stk':  $stk' = stk'' @ STK$ 
    and exec'': exec-meth-d (compP2 P) (compE2 e1) (compxE2 e1 0 0) t h (stk, loc, pc, xcp)
    ta h' (stk'', loc', pc' - length ?pre, xcp') by blast
    from exec'' have exec-meth-d (compP2 P) (compE2 e1 @ Goto (1 + int (length (compE2 e2)))) # compE2 e2
    (compxE2 e1 0 0 @ shift (length (compE2 e1)) (compxE2 e2 (Suc 0) 0)) t
    h (stk, loc, pc, xcp) ta h' (stk'', loc', pc' - length ?pre, xcp')
    by (rule exec-meth-append-xt)
    hence exec-meth-d (compP2 P) (?pre @ compE2 e1 @ Goto (1 + int (length (compE2 e2)))) # compE2 e2
    (compxE2 e 0 0 @ shift (length ?pre) (compxE2 e1 0 0 @ shift (length (compE2 e1)) (compxE2 e2 (Suc 0) 0))) t
    h (stk, loc, length ?pre + pc, xcp) ta h' (stk'', loc', length ?pre + (pc' - length ?pre), xcp')
    by (rule append-exec-meth-xt) (auto)
    moreover from exec' have pc'  $\geq length\ ?pre$ 
    by (rule exec-meth-drop-xt-pc) (auto simp add: stack-xlift-compxE2)
    ultimately show ?thesis using stk'
    by (auto simp add: shift-compxE2 stack-xlift-compxE2 ac-simps)
  next
    case False
    with pc have [simp]:  $pc = length\ (compE2\ e1)$  by simp
    from bisim obtain v where stk = [v] xcp = None
    by (auto dest: bisim1-pc-length-compE2D)
    with exec show ?thesis by (auto elim!: exec-meth.cases intro!: exec-meth.intros)
  qed
next
  case (bisim1CondElse e2 n e2' xs stk loc pc xcp e e1)
  note IH =  $\langle \bigwedge stk' loc' pc' xcp' STK. ?exec\ e2\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$ 

```

$\implies ?concl\ e2\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp'$   
**note**  $exec = \langle ?exec\ (if\ (e)\ e1\ else\ e2)\ stk\ STK\ loc\ (Suc\ (Suc\ (length\ (compE2\ e) + length\ (compE2\ e1) + pc)))\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$   
**note**  $bisim = \langle P, e2, h \vdash (e2', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$   
**from**  $bisim$  **have**  $pc: pc \leq length\ (compE2\ e2)$  **by**  $(rule\ bisim1-pc-length-compE2)$

**let**  $?pre = compE2\ e\ @\ IfFalse\ (2 + int\ (length\ (compE2\ e1)))\ \# \ compE2\ e1\ @\ [Goto\ (1 + int\ (length\ (compE2\ e2)))]$   
**from**  $exec$  **have**  $exec': exec-meth-d\ (compP2\ P)\ (?pre\ @\ compE2\ e2)$   
 $(stack-xlift\ (length\ STK)\ (compxE2\ e\ 0\ 0\ @\ compxE2\ e1\ (Suc\ (length\ (compE2\ e)))\ 0)\ @\ shift\ (length\ ?pre)\ (stack-xlift\ (length\ STK)\ (compxE2\ e2\ 0\ 0)))\ t$   
 $h\ (stk\ @\ STK, loc, length\ ?pre + pc, xcp)\ ta\ h'\ (stk', loc', pc', xcp')$   
**by**  $(simp\ add: stack-xlift-compxE2\ shift-compxE2\ ac-simps)$   
**hence**  $?exec\ e2\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ (pc' - length\ ?pre)\ xcp'$   
**by**  $(rule\ exec-meth-drop-xt)(auto\ simp\ add: stack-xlift-compxE2\ shift-compxE2\ ac-simps)$   
**from**  $IH[OF\ this]$  **obtain**  $stk''$  **where**  $stk': stk' = stk''\ @\ STK$   
**and**  $exec'': exec-meth-d\ (compP2\ P)\ (compE2\ e2)\ (compxE2\ e2\ 0\ 0)\ t\ h\ (stk, loc, pc, xcp)$   
 $ta\ h'\ (stk'', loc', pc' - length\ ?pre, xcp')$  **by**  $blast$   
**from**  $exec''$  **have**  $exec-meth-d\ (compP2\ P)\ (?pre\ @\ compE2\ e2)$   
 $((compxE2\ e\ 0\ 0\ @\ compxE2\ e1\ (Suc\ (length\ (compE2\ e)))\ 0)\ @\ shift\ (length\ ?pre)\ (compxE2\ e2\ 0\ 0))\ t$   
 $h\ (stk, loc, length\ ?pre + pc, xcp)\ ta\ h'\ (stk'', loc', length\ ?pre + (pc' - length\ ?pre), xcp')$   
**by**  $(rule\ append-exec-meth-xt)(auto)$   
**moreover from**  $exec'$  **have**  $pc' \geq length\ ?pre$   
**by**  $(rule\ exec-meth-drop-xt-pc)(auto\ simp\ add: stack-xlift-compxE2)$   
**moreover hence**  $(Suc\ (Suc\ (pc' - Suc\ (Suc\ 0)))) = pc'$  **by**  $simp$   
**ultimately show**  $?case$  **using**  $stk'$   
**by**  $(auto\ simp\ add: shift-compxE2\ stack-xlift-compxE2\ ac-simps\ eval-nat-numeral)$

**next**  
**case**  $(bisim1CondThrow\ e\ n\ a\ xs\ stk\ loc\ pc\ e1\ e2)$   
**note**  $bisim = \langle P, e, h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$   
**note**  $IH = \langle \bigwedge stk'\ loc'\ pc'\ xcp'\ STK. ?exec\ e\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \implies ?concl\ e\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$   
**note**  $exec = \langle ?exec\ (if\ (e)\ e1\ else\ e2)\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$   
**from**  $bisim$  **have**  $pc: pc < length\ (compE2\ e)$  **by**  $(auto\ dest: bisim1-ThrowD)$   
**from**  $exec$  **have**  $exec-meth-d\ (compP2\ P)\ (compE2\ e\ @\ IfFalse\ (2 + int\ (length\ (compE2\ e1)))\ \# \ compE2\ e1\ @\ Goto\ (1 + int\ (length\ (compE2\ e2)))\ \# \ compE2\ e2)$   
 $(stack-xlift\ (length\ STK)\ (compxE2\ e\ 0\ 0)\ @\ shift\ (length\ (compE2\ e))\ (stack-xlift\ (length\ STK)\ (compxE2\ e1\ (Suc\ 0)\ 0)\ @\ stack-xlift\ (length\ STK)\ (compxE2\ e2\ (Suc\ (Suc\ (length\ (compE2\ e1))))\ 0)))\ t$   
 $h\ (stk\ @\ STK, loc, pc, [a])\ ta\ h'\ (stk', loc', pc', xcp')$   
**by**  $(simp\ add: stack-xlift-compxE2\ shift-compxE2\ ac-simps)$   
**hence**  $?exec\ e\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp'$   
**by**  $(rule\ exec-meth-take-xt)(rule\ pc)$   
**from**  $IH[OF\ this]$  **show**  $?case$  **by**  $auto$

**next**  
**case**  $(bisim1While1\ c\ n\ e\ xs)$   
**note**  $IH = \langle \bigwedge stk'\ loc'\ pc'\ xcp'\ STK. ?exec\ c\ []\ STK\ xs\ 0\ None\ stk'\ loc'\ pc'\ xcp' \implies ?concl\ c\ []\ STK\ xs\ 0\ None\ stk'\ loc'\ pc'\ xcp' \rangle$   
**note**  $exec = \langle ?exec\ (while\ (c)\ e)\ []\ STK\ xs\ 0\ None\ stk'\ loc'\ pc'\ xcp' \rangle$   
**hence**  $exec-meth-d\ (compP2\ P)\ (compE2\ c\ @\ IfFalse\ (int\ (length\ (compE2\ e)) + 3)\ \# \ compE2\ e\ @\ [Pop, Goto\ (-2 + (-int\ (length\ (compE2\ e)) - int\ (length\ (compE2\ c))))], Push\ Unit)$   
 $(stack-xlift\ (length\ STK)\ (compxE2\ c\ 0\ 0)\ @\ shift\ (length\ (compE2\ c))\ (stack-xlift\ (length\ STK)\ (compxE2\ e\ (Suc\ 0)\ 0)))\ t$

```

    h ([] @ STK, xs, 0, None) ta h' (stk', loc', pc', xcp')
  by(simp add: compxE2-size-convs)
hence ?exec c [] STK xs 0 None stk' loc' pc' xcp'
  by(rule exec-meth-take-xt) simp
from IH[OF this] show ?case by auto
next
case (bisim1While3 c n c' xs stk loc pc xcp e)
note bisim = ⟨P, c, h ⊢ (c', xs) ↔ (stk, loc, pc, xcp)⟩
note IH = ⟨∧stk' loc' pc' xcp' STK. ?exec c stk STK loc pc xcp stk' loc' pc' xcp'
  ⇒ ?concl c stk STK loc pc xcp stk' loc' pc' xcp'⟩
note exec = ⟨?exec (while (c) e) stk STK loc pc xcp stk' loc' pc' xcp'⟩
from bisim have pc: pc ≤ length (compE2 c) by(rule bisim1-pc-length-compE2)
show ?case
proof(cases pc < length (compE2 c))
  case True
  from exec have exec-meth-d (compP2 P) (compE2 c @ IfFalse (int (length (compE2 e)) + 3) #
    compE2 e @ [Pop, Goto (-2 + (- int (length (compE2 e)) - int (length (compE2 c)))), Push Unit])
    (stack-xlift (length STK) (compxE2 c 0 0) @ shift (length (compE2 c)) (stack-xlift (length STK)
    (compxE2 e (Suc 0) 0))) t
    h (stk @ STK, loc, pc, xcp) ta h' (stk', loc', pc', xcp')
  by(simp add: compxE2-size-convs)
  hence ?exec c stk STK loc pc xcp stk' loc' pc' xcp'
    by(rule exec-meth-take-xt)(rule True)
  from IH[OF this] show ?thesis by auto
  next
  case False
  with pc have [simp]: pc = length (compE2 c) by simp
  from bisim obtain v where stk = [v] xcp = None by(auto dest: bisim1-pc-length-compE2D)
  with exec show ?thesis by(auto elim!: exec-meth.cases intro!: exec-meth.intros)
qed
next
case (bisim1While4 e n e' xs stk loc pc xcp c)
note bisim = ⟨P, e, h ⊢ (e', xs) ↔ (stk, loc, pc, xcp)⟩
note IH = ⟨∧stk' loc' pc' xcp' STK. ?exec e stk STK loc pc xcp stk' loc' pc' xcp'
  ⇒ ?concl e stk STK loc pc xcp stk' loc' pc' xcp'⟩
note exec = ⟨?exec (while (c) e) stk STK loc (Suc (length (compE2 c) + pc)) xcp stk' loc' pc' xcp'⟩
from bisim have pc: pc ≤ length (compE2 e) by(rule bisim1-pc-length-compE2)
show ?case
proof(cases pc < length (compE2 e))
  case True
  let ?pre = compE2 c @ [IfFalse (int (length (compE2 e)) + 3)]
  from exec have exec-meth-d (compP2 P) ((?pre @ compE2 e) @ [Pop, Goto (-2 + (- int (length
    (compE2 e)) - int (length (compE2 c)))), Push Unit])
    (stack-xlift (length STK) (compxE2 c 0 0) @ shift (length ?pre) (stack-xlift (length
    STK) (compxE2 e 0 0))) t
    h (stk @ STK, loc, length ?pre + pc, xcp) ta h' (stk', loc', pc', xcp')
  by(simp add: compxE2-size-convs)
  hence exec': exec-meth-d (compP2 P) (?pre @ compE2 e) (stack-xlift (length STK) (compxE2 c 0
    0) @ shift (length ?pre) (stack-xlift (length STK) (compxE2 e 0 0))) t
    h (stk @ STK, loc, length ?pre + pc, xcp) ta h' (stk', loc', pc', xcp')
  by(rule exec-meth-take)(auto intro: True)
  hence ?exec e stk STK loc pc xcp stk' loc' (pc' - length ?pre) xcp'
    by(rule exec-meth-drop-xt)(auto simp add: stack-xlift-compxE2)
  from IH[OF this] obtain stk'' where stk': stk' = stk'' @ STK

```



**and**  $exec''$ :  $exec\text{-}meth\text{-}d\ (compP2\ P)\ (compE2\ e)\ (compxE2\ e\ 0\ 0)\ t\ h\ (stk,\ loc,\ pc,\ xcp)\ ta\ h'\ (stk'',\ loc',\ pc' - length\ ?pre,\ xcp')$  **by** *auto*  
**from**  $exec''$  **have**  $exec\text{-}meth\text{-}d\ (compP2\ P)\ (?pre\ @\ compE2\ e)\ (compxE2\ c\ 0\ 0\ @\ shift\ (length\ ?pre)\ (compxE2\ e\ 0\ 0))\ t$   
 $h\ (stk,\ loc,\ length\ ?pre + pc,\ xcp)\ ta\ h'\ (stk'',\ loc',\ length\ ?pre + (pc' - length\ ?pre),\ xcp')$   
**by**(*rule append-exec-meth-xt*) *auto*  
**hence**  $exec\text{-}meth\text{-}d\ (compP2\ P)\ ((?pre\ @\ compE2\ e)\ @\ [Pop,\ Goto\ (-2 + (-\ int\ (length\ (compE2\ e)) - \ int\ (length\ (compE2\ c))))],\ Push\ Unit])$   
 $(compxE2\ c\ 0\ 0\ @\ shift\ (length\ ?pre)\ (compxE2\ e\ 0\ 0))\ t$   
 $h\ (stk,\ loc,\ length\ ?pre + pc,\ xcp)\ ta\ h'\ (stk'',\ loc',\ length\ ?pre + (pc' - length\ ?pre),\ xcp')$   
**by**(*rule exec-meth-append*)  
**moreover from**  $exec'$  **have**  $pc' \geq length\ ?pre$   
**by**(*rule exec-meth-drop-xt-pc*)(*auto simp add: stack-xlift-compxE2*)  
**moreover have**  $-2 + (-\ int\ (length\ (compE2\ e)) - \ int\ (length\ (compE2\ c))) = -\ int\ (length\ (compE2\ c)) + (-2 - \ int\ (length\ (compE2\ e)))$  **by** *simp*  
**ultimately show**  $?thesis$  **using**  $stk'$   
**by**(*auto simp add: shift-compxE2 stack-xlift-compxE2 algebra-simps uminus-minus-left-commute*)  
**next**  
**case** *False*  
**with**  $pc$  **have**  $[simp]: pc = length\ (compE2\ e)$  **by** *simp*  
**from** *bisim* **obtain**  $v$  **where**  $stk = [v]\ xcp = None$  **by**(*auto dest: bisim1-pc-length-compE2D*)  
**with**  $exec$  **show**  $?thesis$  **by**(*auto elim!: exec-meth.cases intro!: exec-meth.intros*)  
**qed**  
**next**  
**case** (*bisim1While6*  $c\ n\ e\ xs$ )  
**note**  $exec = \langle ?exec\ (while\ (c)\ e)\ []\ STK\ xs\ (Suc\ (Suc\ (length\ (compE2\ c) + length\ (compE2\ e))))\ None\ stk'\ loc'\ pc'\ xcp' \rangle$   
**thus**  $?case$  **by**(*rule exec-meth.cases*)(*simp-all, auto intro!: exec-meth.intros*)  
**next**  
**case** (*bisim1While7*  $c\ n\ e\ xs$ )  
**note**  $exec = \langle ?exec\ (while\ (c)\ e)\ []\ STK\ xs\ (Suc\ (Suc\ (Suc\ (length\ (compE2\ c) + length\ (compE2\ e))))\ None\ stk'\ loc'\ pc'\ xcp' \rangle$   
**thus**  $?case$  **by**(*rule exec-meth.cases*)(*simp-all, auto intro!: exec-meth.intros*)  
**next**  
**case** (*bisim1WhileThrow1*  $c\ n\ a\ xs\ stk\ loc\ pc\ e$ )  
**note**  $bisim = \langle P, c, h \vdash (Throw\ a,\ xs) \leftrightarrow (stk,\ loc,\ pc,\ [a]) \rangle$   
**note**  $IH = \langle \bigwedge stk'\ loc'\ pc'\ xcp'\ STK. ?exec\ c\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \Rightarrow ?concl\ c\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$   
**note**  $exec = \langle ?exec\ (while\ (c)\ e)\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$   
**from** *bisim* **have**  $pc: pc < length\ (compE2\ c)$  **by**(*auto dest: bisim1-ThrowD*)  
**from**  $exec$  **have**  $exec\text{-}meth\text{-}d\ (compP2\ P)\ (compE2\ c\ @\ IfFalse\ (int\ (length\ (compE2\ e)) + 3)\ \#compE2\ e\ @\ [Pop,\ Goto\ (-2 + (-\ int\ (length\ (compE2\ e)) - \ int\ (length\ (compE2\ c))))],\ Push\ Unit])$   
 $(stack\text{-}xlift\ (length\ STK)\ (compxE2\ c\ 0\ 0)\ @\ shift\ (length\ (compE2\ c))\ (stack\text{-}xlift\ (length\ STK)\ (compxE2\ e\ (Suc\ 0)\ 0)))\ t$   
 $h\ (stk\ @\ STK,\ loc,\ pc,\ [a])\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$   
**by**(*simp add: compxE2-size-convs*)  
**hence**  $?exec\ c\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp'$   
**by**(*rule exec-meth-take-xt*)(*rule pc*)  
**from**  $IH[OF\ this]$  **show**  $?case$  **by** *auto*  
**next**  
**case** (*bisim1WhileThrow2*  $e\ n\ a\ xs\ stk\ loc\ pc\ c$ )  
**note**  $bisim = \langle P, e, h \vdash (Throw\ a,\ xs) \leftrightarrow (stk,\ loc,\ pc,\ [a]) \rangle$   
**note**  $IH = \langle \bigwedge stk'\ loc'\ pc'\ xcp'\ STK. ?exec\ e\ stk\ STK\ loc\ pc\ [a]\ stk'\ loc'\ pc'\ xcp' \rangle$

```

    ⇒ ?concl e stk STK loc pc [a] stk' loc' pc' xcp'
  note exec = ⟨?exec (while (c) e) stk STK loc (Suc (length (compE2 c) + pc)) [a] stk' loc' pc' xcp'⟩
  from bisim have pc: pc < length (compE2 e) by(auto dest: bisim1-ThrowD)
  let ?pre = compE2 c @ [IfFalse (int (length (compE2 e)) + 3)]
  from exec have exec-meth-d (compP2 P) ((?pre @ compE2 e) @ [Pop, Goto (-2 + (- int (length (compE2 e)) - int (length (compE2 c))))], Push Unit])
    (stack-xlift (length STK) (compxE2 c 0 0) @ shift (length ?pre) (stack-xlift (length STK) (compxE2 e 0 0))) t
    h (stk @ STK, loc, length ?pre + pc, [a]) ta h' (stk', loc', pc', xcp')
    by(simp add: compxE2-size-convs)
  hence exec': exec-meth-d (compP2 P) (?pre @ compE2 e)
    (stack-xlift (length STK) (compxE2 c 0 0) @ shift (length ?pre) (stack-xlift (length STK) (compxE2 e 0 0))) t
    h (stk @ STK, loc, (length ?pre + pc), [a]) ta h' (stk', loc', pc', xcp')
    by(rule exec-meth-take)(auto intro: pc)
  hence ?exec e stk STK loc pc [a] stk' loc' (pc' - length ?pre) xcp'
    by(rule exec-meth-drop-xt)(auto simp add: stack-xlift-compxE2)
  from IH[OF this] obtain stk'' where stk': stk' = stk'' @ STK
    and exec'': exec-meth-d (compP2 P) (compE2 e) (compxE2 e 0 0) t h (stk, loc, pc, [a]) ta h' (stk'', loc', pc' - length ?pre, xcp') by auto
  from exec'' have exec-meth-d (compP2 P) (?pre @ compE2 e) (compxE2 c 0 0 @ shift (length ?pre) (compxE2 e 0 0)) t
    h (stk, loc, length ?pre + pc, [a]) ta h' (stk'', loc', length ?pre + (pc' - length ?pre), xcp')
    by(rule append-exec-meth-xt) auto
  hence exec-meth-d (compP2 P) ((?pre @ compE2 e) @ [Pop, Goto (-2 + (- int (length (compE2 e)) - int (length (compE2 c))))], Push Unit])
    (compxE2 c 0 0 @ shift (length ?pre) (compxE2 e 0 0)) t
    h (stk, loc, length ?pre + pc, [a]) ta h' (stk'', loc', length ?pre + (pc' - length ?pre), xcp')
    by(rule exec-meth-append)
  moreover from exec' have pc' ≥ length ?pre
    by(rule exec-meth-drop-xt-pc)(auto simp add: stack-xlift-compxE2)
  moreover have -2 + (- int (length (compE2 e)) - int (length (compE2 c))) = - int (length (compE2 c)) + (-2 - int (length (compE2 e))) by simp
  ultimately show ?case using stk'
    by(auto simp add: shift-compxE2 stack-xlift-compxE2 algebra-simps uminus-minus-left-commute)
next
  case (bisim1Throw1 e n e' xs stk loc pc xcp)
  note bisim = ⟨P, e, h ⊢ (e', xs) ↔ (stk, loc, pc, xcp)⟩
  note IH = ⟨∧stk' loc' pc' xcp' STK. ?exec e stk STK loc pc xcp stk' loc' pc' xcp'⟩
    ⇒ ?concl e stk STK loc pc xcp stk' loc' pc' xcp'
  note exec = ⟨?exec (throw e) stk STK loc pc xcp stk' loc' pc' xcp'⟩
  from bisim have pc: pc ≤ length (compE2 e) by(rule bisim1-pc-length-compE2)
  show ?case
  proof(cases pc < length (compE2 e))
    case True
    with exec have ?exec e stk STK loc pc xcp stk' loc' pc' xcp' by(auto elim: exec-meth-take)
    from IH[OF this] show ?thesis by auto
  next
    case False
    with pc have [simp]: pc = length (compE2 e) by simp
    from bisim obtain v where stk = [v] xcp = None by(auto dest: bisim1-pc-length-compE2D)
    with exec show ?thesis by(auto elim!: exec-meth.cases intro!: exec-meth.intros split: if-split-asm)
  qed
next

```

```

case bisim1Throw2 thus ?case
  apply(auto elim!: exec-meth.cases intro: exec-meth.intros dest!: match-ex-table-stack-xliftD)
  apply(auto intro: exec-meth.intros dest!: match-ex-table-stack-xliftD intro!: exI)
  apply(auto simp add: le-Suc-eq)
  done
next
case bisim1ThrowNull thus ?case
  apply(auto elim!: exec-meth.cases intro: exec-meth.intros dest!: match-ex-table-stack-xliftD)
  apply(auto intro: exec-meth.intros dest!: match-ex-table-stack-xliftD intro!: exI)
  apply(auto simp add: le-Suc-eq)
  done
next
case (bisim1ThrowThrow e n a xs stk loc pc)
note bisim =  $\langle P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor) \rangle$ 
note IH =  $\langle \bigwedge stk' loc' pc' xcp' STK. ?exec\ e\ stk\ STK\ loc\ pc\ \lfloor a \rfloor\ stk'\ loc'\ pc'\ xcp' \Rightarrow ?concl\ e\ stk\ STK\ loc\ pc\ \lfloor a \rfloor\ stk'\ loc'\ pc'\ xcp' \rangle$ 
note exec =  $\langle ?exec\ (throw\ e)\ stk\ STK\ loc\ pc\ \lfloor a \rfloor\ stk'\ loc'\ pc'\ xcp' \rangle$ 
from bisim have pc: pc < length (compE2 e) by(auto dest: bisim1-ThrowD)
with exec have ?exec e stk STK loc pc  $\lfloor a \rfloor\ stk'\ loc'\ pc' xcp'$ 
  by(auto elim: exec-meth-take simp add: compxE2-size-convs)
from IH[OF this] show ?case by auto
next
case (bisim1Try e n e' xs stk loc pc xcp e2 C' V)
note bisim =  $\langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
note IH =  $\langle \bigwedge stk' loc' pc' xcp' STK. ?exec\ e\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \Rightarrow ?concl\ e\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$ 
note exec =  $\langle ?exec\ (try\ e\ catch\ (C'\ V)\ e2)\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$ 
from bisim have pc: pc ≤ length (compE2 e) by(rule bisim1-pc-length-compE2)
show ?case
proof(cases pc < length (compE2 e))
  case True
    from exec have exec': exec-meth-d (compP2 P) (compE2 e @ Goto (int (length (compE2 e2)) + 2) # Store V # compE2 e2)
      (stack-xlift (length STK) (compxE2 e 0 0) @ shift (length (compE2 e)) (stack-xlift (length STK) (compxE2 e2 (Suc (Suc 0)) 0)) @ [(0, length (compE2 e), ⌊C'⌋, Suc (length (compE2 e)), length STK)]) t
      h (stk @ STK, loc, pc, xcp) ta h' (stk', loc', pc', xcp')
      by(simp add: compxE2-size-convs)
    show ?thesis
    proof(cases xcp)
      case None
        with exec' True have ?exec e stk STK loc pc xcp stk' loc' pc' xcp'
          apply –
          apply (erule exec-meth.cases)
          apply (cases compE2 e ! pc)
          apply (fastforce simp add: is-Ref-def intro: exec-meth.intros split: if-split-asm cong del: image-cong-simp)
          done
        from IH[OF this] show ?thesis by auto
      next
      case (Some a)
      with exec' have [simp]: h' = h loc' = loc xcp' = None ta = ε
        by(auto elim: exec-meth.cases)
      show ?thesis

```

```

proof(cases match-ex-table (compP2 P) (cname-of h a) pc (compxE2 e 0 0))
  case (Some pcd)
    from exec ⟨xcp = [a]⟩ Some pc
    have stk': stk' = Addr a # (drop (length stk - snd pcd) stk) @ STK
      by(auto elim!: exec-meth.cases simp add: match-ex-table-append split: if-split-asm dest!:
match-ex-table-stack-xliftD)
    from exec' ⟨xcp = [a]⟩ Some pc have exec-meth-d (compP2 P)
      (compE2 e) (stack-xlift (length STK) (compxE2 e 0 0)) t h (stk @ STK, loc, pc, [a]) ∈ h
      (Addr a # (drop (length (stk @ STK) - (snd pcd + length STK)) (stk @ STK)), loc, pc', None)
    apply -
    apply(rule exec-meth.intros)
    apply(auto elim!: exec-meth.cases simp add: match-ex-table-append split: if-split-asm dest!:
match-ex-table-shift-pcD match-ex-table-stack-xliftD)
    done
    from IH[unfolded ⟨ta = ε⟩ ⟨xcp = [a]⟩ ⟨h' = h⟩, OF this]
    have stk: Addr a # drop (length stk - snd pcd) (stk @ STK) = Addr a # drop (length stk -
snd pcd) stk @ STK
      and exec'': exec-meth-d (compP2 P) (compE2 e) (compxE2 e 0 0) t h (stk, loc, pc, [a]) ∈ h
      (Addr a # drop (length stk - snd pcd) stk, loc, pc', None) by auto
    thus ?thesis using Some stk' ⟨xcp = [a]⟩ by (auto)
  next
  case None
  with Some exec pc have stk': stk' = Addr a # STK
    and pc': pc' = Suc (length (compE2 e))
    and subcls: compP2 P ⊢ cname-of h a ≼* C'
    by(auto elim!: exec-meth.cases split: if-split-asm simp add: match-ex-table-append-not-pcs)(simp
add: matches-ex-entry-def)
    moreover from Some True None pc' subcls
    have exec-meth-d (compP2 P) (compE2 (try e catch(C' V) e2)) (compxE2 (try e catch(C' V)
e2) 0 0) t h
      (stk, loc, pc, [a]) ∈ h (Addr a # drop (length stk - 0) stk, loc, pc', None)
      by -(rule exec-catch,auto simp add: match-ex-table-append-not-pcs matches-ex-entry-def)
    ultimately show ?thesis using Some by auto
  qed
qed
next
  case False
  with pc have [simp]: pc = length (compE2 e) by simp
  from bisim obtain v where stk = [v] xcp = None by(auto dest: bisim1-pc-length-compE2D)
  with exec show ?thesis by(auto elim!: exec-meth.cases intro!: exec-meth.intros split: if-split-asm)
qed
next
  case bisim1TryCatch1 thus ?case
    by(auto elim!: exec-meth.cases intro!: exec-meth.intros split: if-split-asm)
next
  case (bisim1TryCatch2 e2 n e' xs stk loc pc xcp e C' V)
  note bisim = ⟨P,e2,h ⊢ (e', xs) ↔ (stk, loc, pc, xcp)⟩
  note IH = ⟨∧stk' loc' pc' xcp' STK. ?exec e2 stk STK loc pc xcp stk' loc' pc' xcp'
    ⇒ ?concl e2 stk STK loc pc xcp stk' loc' pc' xcp'⟩
  note exec = ⟨?exec (try e catch(C' V) e2) stk STK loc (Suc (Suc (length (compE2 e) + pc))) xcp
stk' loc' pc' xcp'⟩
  let ?pre = compE2 e @ [Goto (int (length (compE2 e2)) + 2), Store V]
  from exec have exec': exec-meth-d (compP2 P) (?pre @ compE2 e2)
    (stack-xlift (length STK) (compxE2 e 0 0) @ shift (length ?pre) (stack-xlift (length STK) (compxE2

```

$e2\ 0\ 0)))\ t$   
 $h\ (stk\ @\ STK,\ loc,\ length\ ?pre + pc,\ xcp)\ ta\ h'\ (stk',\ loc',\ pc',\ xcp')$   
**proof**(cases)  
**case** (exec-catch xcp'' d)  
**let**  $?stk = stk\ @\ STK$  **and**  $?PC = Suc\ (Suc\ (length\ (compE2\ e) + pc))$   
**note**  $s = \langle stk' = Addr\ xcp''\ \# \ drop\ (length\ ?stk - d)\ ?stk \rangle$   
 $\langle ta = \varepsilon \rangle \langle h' = h \rangle \langle xcp' = None \rangle \langle xcp = \lfloor xcp'' \rfloor \rangle \langle loc' = loc \rangle$   
**from**  $\langle match\text{-}ex\text{-}table\ (compP2\ P)\ (cname\text{-}of\ h\ xcp'')\ ?PC\ (stack\text{-}xlift\ (length\ STK)\ (compxE2\ (try\ e\ catch\ (C'\ V)\ e2)\ 0\ 0)) = \lfloor (pc',\ d) \rfloor \rangle \langle d \leq length\ ?stk \rangle$   
**show** ?thesis **unfolding** s  
**by**  $-(rule\ exec\text{-}meth.\text{exec-catch},\ simp\text{-}all\ add:\ shift\text{-}compxE2\ stack\text{-}xlift\text{-}compxE2,\ simp\ add:\ match\text{-}ex\text{-}table\text{-}append\ add:\ matches\text{-}ex\text{-}entry\text{-}def)$   
**qed**(auto intro: exec-meth.intros simp add: shift-compxE2 stack-xlift-compxE2)  
**hence**  $?exec\ e2\ stk\ STK\ loc\ pc\ xcp\ stk'\ loc'\ (pc' - length\ ?pre)\ xcp'$   
**by**(rule exec-meth-drop-xt)(auto simp add: stack-xlift-compxE2)  
**from** IH[OF this] **obtain**  $stk''$  **where**  $stk':\ stk' = stk''\ @\ STK$   
**and**  $exec'':\ exec\text{-}meth\text{-}d\ (compP2\ P)\ (compE2\ e2)\ (compxE2\ e2\ 0\ 0)\ t\ h\ (stk,\ loc,\ pc,\ xcp)\ ta\ h'\ (stk'',\ loc',\ pc' - length\ ?pre,\ xcp')$  **by** auto  
**from**  $exec''$  **have**  $exec\text{-}meth\text{-}d\ (compP2\ P)\ (?pre\ @\ compE2\ e2)\ (compxE2\ e\ 0\ 0\ @\ shift\ (length\ ?pre)\ (compxE2\ e2\ 0\ 0))\ t\ h\ (stk,\ loc,\ length\ ?pre + pc,\ xcp)\ ta\ h'\ (stk'',\ loc',\ length\ ?pre + (pc' - length\ ?pre),\ xcp')$   
**by**(rule append-exec-meth-xt) auto  
**hence**  $exec\text{-}meth\text{-}d\ (compP2\ P)\ (?pre\ @\ compE2\ e2)\ (compxE2\ e\ 0\ 0\ @\ shift\ (length\ ?pre)\ (compxE2\ e2\ 0\ 0)\ @\ [(0,\ length\ (compE2\ e),\ \lfloor C' \rfloor,\ Suc\ (length\ (compE2\ e)),\ 0)])\ t\ h\ (stk,\ loc,\ length\ ?pre + pc,\ xcp)\ ta\ h'\ (stk'',\ loc',\ length\ ?pre + (pc' - length\ ?pre),\ xcp')$   
**by**(rule exec-meth.cases)(auto intro: exec-meth.intros simp add: match-ex-table-append-not-pcs)  
**moreover from**  $exec'$  **have**  $pc' \geq length\ ?pre$   
**by**(rule exec-meth-drop-xt-pc)(auto simp add: stack-xlift-compxE2)  
**moreover hence**  $(Suc\ (Suc\ (pc' - Suc\ (Suc\ 0)))) = pc'$  **by** simp  
**ultimately show** ?case **using**  $stk'$  **by**(auto simp add: shift-compxE2 eval-nat-numeral)

**next**  
**case** (bisim1TryFail e n a xs stk loc pc C' C'' e2 V)  
**note**  $bisim = \langle P, e, h \vdash (Throw\ a,\ xs) \leftrightarrow (stk,\ loc,\ pc,\ \lfloor a \rfloor) \rangle$   
**note**  $exec = \langle ?exec\ (try\ e\ catch\ (C''\ V)\ e2)\ stk\ STK\ loc\ pc\ \lfloor a \rfloor\ stk'\ loc'\ pc'\ xcp' \rangle$   
**note**  $a = \langle type\text{-}of\text{-}addr\ h\ a = \lfloor Class\text{-}type\ C' \rfloor \rangle \langle \neg P \vdash C' \preceq^* C'' \rangle$   
**from** bisim **have**  $match\text{-}ex\text{-}table\ (compP2\ P)\ (cname\text{-}of\ h\ a)\ (0 + pc)\ (compxE2\ e\ 0\ 0) = None$   
**unfolding** compP2-def **by**(rule bisim1-xcp-Some-not-caught)  
**moreover from** bisim **have**  $pc < length\ (compE2\ e)$  **by**(auto dest: bisim1-ThrowD)  
**ultimately have** False **using**  $exec\ a$   
**apply**(auto elim!: exec-meth.cases simp add: outside-pcs-compxE2-not-matches-entry outside-pcs-not-matches-entry split: if-split-asm)  
**apply**(auto simp add: compP2-def match-ex-entry match-ex-table-append-not-pcs cname-of-def split: if-split-asm)  
**done**  
**thus** ?case ..

**next**  
**case** (bisim1TryCatchThrow e2 n a xs stk loc pc e C' V)  
**note**  $bisim = \langle P, e2, h \vdash (Throw\ a,\ xs) \leftrightarrow (stk,\ loc,\ pc,\ \lfloor a \rfloor) \rangle$   
**note**  $IH = \langle \bigwedge stk'\ loc'\ pc'\ xcp'\ STK.\ ?exec\ e2\ stk\ STK\ loc\ pc\ \lfloor a \rfloor\ stk'\ loc'\ pc'\ xcp' \implies ?concl\ e2\ stk\ STK\ loc\ pc\ \lfloor a \rfloor\ stk'\ loc'\ pc'\ xcp' \rangle$   
**note**  $exec = \langle ?exec\ (try\ e\ catch\ (C'\ V)\ e2)\ stk\ STK\ loc\ (Suc\ (Suc\ (length\ (compE2\ e) + pc)))\ \lfloor a \rfloor\ stk'\ loc'\ pc'\ xcp' \rangle$   
**from** bisim **have**  $pc:\ pc < length\ (compE2\ e2)$  **by**(auto dest: bisim1-ThrowD)  
**let**  $?pre = compE2\ e\ @\ [Goto\ (int\ (length\ (compE2\ e2)) + 2),\ Store\ V]$

**from** *exec* **have** *exec'*: *exec-meth-d* (*compP2 P*) (*?pre @ compE2 e2*) (*stack-xlift* (*length STK*) (*compxE2 e 0 0*) @  
*shift* (*length ?pre*) (*stack-xlift* (*length STK*) (*compxE2 e2 0 0*))) *t*  
*h* (*stk @ STK*, *loc*, *length ?pre + pc*, [*a*]) *ta* *h'* (*stk'*, *loc'*, *pc'*, *xcp'*)  
**proof**(*cases*)  
**case** (*exec-catch d*)  
**let** *?stk* = *stk @ STK* **and** *?PC* = *Suc* (*Suc* (*length* (*compE2 e*) + *pc*))  
**note** *s* =  $\langle \text{stk}' = \text{Addr } a \# \text{drop}(\text{length } ?\text{stk} - d) \text{ } ?\text{stk} \rangle \langle \text{loc}' = \text{loc} \rangle$   
 $\langle \text{ta} = \varepsilon \rangle \langle \text{h}' = \text{h} \rangle \langle \text{xcp}' = \text{None} \rangle$   
**from**  $\langle \text{match-ex-table}(\text{compP2 } P) (\text{cname-of } h \ a) \ ?PC (\text{stack-xlift}(\text{length } STK) (\text{compxE2}(\text{try } e \text{ catch}(C' \ V) \ e2) \ 0 \ 0)) = \lfloor (pc', d) \rfloor \rangle \langle d \leq \text{length } ?stk \rangle$   
**show** *?thesis unfolding s*  
**by**  $-(\text{rule } \text{exec-meth.exec-catch}, \text{simp-all add: shift-compxE2 stack-xlift-compxE2}, \text{simp add: match-ex-table-append add: matches-ex-entry-def})$   
**qed**  
**hence** *?exec e2 stk STK loc pc [a] stk' loc' (pc' - length ?pre) xcp'*  
**by**(*rule exec-meth-drop-xt*)(*auto simp add: stack-xlift-compxE2*)  
**from** *IH[OF this]* **obtain** *stk''* **where** *stk'*: *stk' = stk'' @ STK*  
**and** *exec''*: *exec-meth-d* (*compP2 P*) (*compE2 e2*) (*compxE2 e2 0 0*) *t* *h* (*stk*, *loc*, *pc*, [*a*]) *ta* *h'* (*stk''*, *loc'*, *pc' - length ?pre*, *xcp'*) **by** *auto*  
**from** *exec''* **have** *exec-meth-d* (*compP2 P*) (*?pre @ compE2 e2*) (*compxE2 e 0 0 @ shift* (*length ?pre*) (*compxE2 e2 0 0*)) *t* *h* (*stk*, *loc*, *length ?pre + pc*, [*a*]) *ta* *h'* (*stk''*, *loc'*, *length ?pre + (pc' - length ?pre)*, *xcp'*)  
**by**(*rule append-exec-meth-xt*) *auto*  
**hence** *exec-meth-d* (*compP2 P*) (*?pre @ compE2 e2*) (*compxE2 e 0 0 @ shift* (*length ?pre*) (*compxE2 e2 0 0*) @  $[(0, \text{length}(\text{compE2 } e), \lfloor C' \rfloor, \text{Suc}(\text{length}(\text{compE2 } e)), 0)]$ ) *t* *h* (*stk*, *loc*, *length ?pre + pc*, [*a*]) *ta* *h'* (*stk''*, *loc'*, *length ?pre + (pc' - length ?pre)*, *xcp'*)  
**by**(*rule exec-meth.cases*)(*auto intro!: exec-meth.intros simp add: match-ex-table-append-not-pcs*)  
**moreover from** *exec'* **have** *pc' ≥ length ?pre*  
**by**(*rule exec-meth-drop-xt-pc*)(*auto simp add: stack-xlift-compxE2*)  
**moreover hence** (*Suc* (*Suc* (*pc' - Suc* (*Suc* (*Suc* (*0*)))))) = *pc'* **by** *simp*  
**ultimately show** *?case using stk' by*(*auto simp add: shift-compxE2 eval-nat-numeral*)  
**next**  
**case** *bisims1Nil* **thus** *?case by*(*auto elim: exec-meth.cases*)  
**next**  
**case** (*bisims1List1 e n e' xs stk loc pc xcp es*)  
**note** *bisim1* =  $\langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$   
**note** *IH1* =  $\langle \bigwedge \text{stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \text{ STK}. ?\text{exec } e \text{ stk STK loc pc xcp stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \Rightarrow ?\text{concl } e \text{ stk STK loc pc xcp stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$   
**note** *IH2* =  $\langle \bigwedge \text{xs} \text{ stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \text{ STK}. ?\text{execs } es \ [] \text{ STK xs } 0 \text{ None stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \Rightarrow ?\text{concls } es \ [] \text{ STK xs } 0 \text{ None stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$   
**note** *exec* =  $\langle ?\text{execs } (e \# es) \text{ stk STK loc pc xcp stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$   
**from** *bisim1* **have** *pc*: *pc ≤ length* (*compE2 e*) **by**(*rule bisim1-pc-length-compE2*)  
**show** *?case*  
**proof**(*cases pc < length* (*compE2 e*))  
**case** *True*  
**with** *exec* **have** *?exec e stk STK loc pc xcp stk' loc' pc' xcp'*  
**by**(*simp add: compxEs2-size-convs*)(*erule exec-meth-take-xt*)  
**from** *IH1[OF this]* **show** *?thesis by* *auto*  
**next**  
**case** *False*  
**with** *pc* **have** *pc*: *pc = length* (*compE2 e*) **by** *simp*  
**with** *bisim1* **obtain** *v* **where** *s*: *stk = [v]* *xcp = None* **by**(*auto dest: bisim1-pc-length-compE2D*)  
**with** *exec pc* **have** *exec'*: *exec-meth-d* (*compP2 P*) (*compE2 e @ compEs2 es*)

$(\text{stack-xlift} (\text{length } STK) (\text{compxE2 } e \ 0 \ 0) @ \text{shift} (\text{length} (\text{compE2 } e)) (\text{stack-xlift} (\text{length} (v \# STK)) (\text{compxEs2 } es \ 0 \ 0))) \ t$   
 $h (\ [] @ v \# STK, loc, \text{length} (\text{compE2 } e) + 0, \text{None}) \ ta \ h' (stk', loc', pc', xcp')$   
 $\text{by}(\text{simp add: compxEs2-size-convs compxEs2-stack-xlift-convs})$   
 $\text{hence } ?execs \ es \ [] \ (v \# STK) \ loc \ 0 \ \text{None} \ stk' \ loc' \ (pc' - \text{length} (\text{compE2 } e)) \ xcp'$   
 $\text{by}(\text{rule exec-meth-drop-xt})(\text{auto simp add: stack-xlift-compxE2})$   
 $\text{from } IH2[OF \ this] \ \text{obtain } stk'' \ \text{where } stk': \ stk' = stk'' @ v \# STK$   
 $\text{and } exec'': \text{exec-meth-d} (\text{compP2 } P) (\text{compEs2 } es) (\text{compxEs2 } es \ 0 \ 0) \ t \ h (\ [], loc, 0, \text{None}) \ ta \ h' (stk'', loc', pc' - \text{length} (\text{compE2 } e), xcp') \ \text{by auto}$   
 $\text{from } exec'' \ \text{have } \text{exec-meth-d} (\text{compP2 } P) (\text{compEs2 } es) (\text{stack-xlift} (\text{length } [v]) (\text{compxEs2 } es \ 0 \ 0)) \ t \ h (\ [] @ [v], loc, 0, \text{None}) \ ta \ h' (stk'' @ [v], loc', pc' - \text{length} (\text{compE2 } e), xcp')$   
 $\text{by}(\text{rule exec-meth-stk-offer})$   
 $\text{hence } \text{exec-meth-d} (\text{compP2 } P) (\text{compE2 } e @ \text{compEs2 } es) (\text{compxE2 } e \ 0 \ 0 @ \text{shift} (\text{length} (\text{compE2 } e)) (\text{stack-xlift} (\text{length } [v]) (\text{compxEs2 } es \ 0 \ 0))) \ t \ h (\ [] @ [v], loc, \text{length} (\text{compE2 } e) + 0, \text{None}) \ ta \ h' (stk'' @ [v], loc', \text{length} (\text{compE2 } e) + (pc' - \text{length} (\text{compE2 } e)), xcp')$   
 $\text{by}(\text{rule append-exec-meth-xt}) \ \text{auto}$   
 $\text{moreover from } exec' \ \text{have } pc' \geq \text{length} (\text{compE2 } e)$   
 $\text{by}(\text{rule exec-meth-drop-xt-pc})(\text{auto simp add: stack-xlift-compxE2})$   
 $\text{ultimately show } ?thesis \ \text{using } s \ pc \ stk' \ \text{by}(\text{auto simp add: shift-compxEs2 stack-xlift-compxEs2})$   
 $\text{qed}$   
 $\text{next}$   
 $\text{case } (bisims1List2 \ es \ n \ es' \ xs \ stk \ loc \ pc \ xcp \ e \ v)$   
 $\text{note } bisim = \langle P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, xcp) \rangle$   
 $\text{note } IH = \langle \bigwedge stk' \ loc' \ pc' \ xcp' \ STK. \ ?execs \ es \ stk \ STK \ loc \ pc \ xcp \ stk' \ loc' \ pc' \ xcp' \Rightarrow ?concls \ es \ stk \ STK \ loc \ pc \ xcp \ stk' \ loc' \ pc' \ xcp' \rangle$   
 $\text{note } exec = \langle ?execs \ (e \# es) \ (stk @ [v]) \ STK \ loc \ (\text{length} (\text{compE2 } e) + pc) \ xcp \ stk' \ loc' \ pc' \ xcp' \rangle$   
 $\text{from } exec \ \text{have } exec': \text{exec-meth-d} (\text{compP2 } P) (\text{compE2 } e @ \text{compEs2 } es)$   
 $(\text{stack-xlift} (\text{length } STK) (\text{compxE2 } e \ 0 \ 0) @ \text{shift} (\text{length} (\text{compE2 } e)) (\text{stack-xlift} (\text{length} (v \# STK)) (\text{compxEs2 } es \ 0 \ 0))) \ t$   
 $h (stk @ v \# STK, loc, \text{length} (\text{compE2 } e) + pc, xcp) \ ta \ h' (stk', loc', pc', xcp')$   
 $\text{by}(\text{simp add: compxEs2-size-convs compxEs2-stack-xlift-convs})$   
 $\text{hence } ?execs \ es \ stk \ (v \# STK) \ loc \ pc \ xcp \ stk' \ loc' \ (pc' - \text{length} (\text{compE2 } e)) \ xcp'$   
 $\text{by}(\text{rule exec-meth-drop-xt})(\text{auto simp add: stack-xlift-compxE2})$   
 $\text{from } IH[OF \ this] \ \text{obtain } stk'' \ \text{where } stk': \ stk' = stk'' @ v \# STK$   
 $\text{and } exec'': \text{exec-meth-d} (\text{compP2 } P) (\text{compEs2 } es) (\text{compxEs2 } es \ 0 \ 0) \ t \ h (stk, loc, pc, xcp) \ ta \ h' (stk'', loc', pc' - \text{length} (\text{compE2 } e), xcp') \ \text{by auto}$   
 $\text{from } exec'' \ \text{have } \text{exec-meth-d} (\text{compP2 } P) (\text{compEs2 } es) (\text{stack-xlift} (\text{length } [v]) (\text{compxEs2 } es \ 0 \ 0)) \ t \ h (stk @ [v], loc, pc, xcp) \ ta \ h' (stk'' @ [v], loc', pc' - \text{length} (\text{compE2 } e), xcp')$   
 $\text{by}(\text{rule exec-meth-stk-offer})$   
 $\text{hence } \text{exec-meth-d} (\text{compP2 } P) (\text{compE2 } e @ \text{compEs2 } es) (\text{compxE2 } e \ 0 \ 0 @ \text{shift} (\text{length} (\text{compE2 } e)) (\text{stack-xlift} (\text{length } [v]) (\text{compxEs2 } es \ 0 \ 0))) \ t \ h (stk @ [v], loc, \text{length} (\text{compE2 } e) + pc, xcp) \ ta \ h' (stk'' @ [v], loc', \text{length} (\text{compE2 } e) + (pc' - \text{length} (\text{compE2 } e)), xcp')$   
 $\text{by}(\text{rule append-exec-meth-xt}) \ \text{auto}$   
 $\text{moreover from } exec' \ \text{have } pc' \geq \text{length} (\text{compE2 } e)$   
 $\text{by}(\text{rule exec-meth-drop-xt-pc})(\text{auto simp add: stack-xlift-compxE2})$   
 $\text{ultimately show } ?case \ \text{using } stk' \ \text{by}(\text{auto simp add: shift-compxEs2 stack-xlift-compxEs2})$   
 $\text{next}$   
 $\text{case } (bisim1Sync12 \ e1 \ n \ e2 \ V \ a \ xs \ v \ v')$   
 $\text{note } exec = \langle ?exec \ (\text{sync}_V (e1) \ e2) \ [v, v'] \ STK \ xs \ (4 + \text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2)) \ [a] \ stk' \ loc' \ pc' \ xcp' \rangle$   
 $\text{thus } ?case \ \text{by}(\text{auto elim!: exec-meth.cases split: if-split-asm simp add: match-ex-table-append-not-pcs})(\text{simp add: matches-ex-entry-def})$   
 $\text{next}$   
 $\text{case } (bisim1Sync14 \ e1 \ n \ e2 \ V \ a \ xs \ v \ a')$

**note**  $exec = \langle ?exec (sync_V (e1) e2) [v, Addr a] STK xs (7 + length (compE2 e1) + length (compE2 e2)) [a] stk' loc' pc' xcp' \rangle$   
**thus**  $?case \text{by} (auto elim!: exec-meth.cases split: if-split-asm simp add: match-ex-table-append-not-pcs) (simp add: matches-ex-entry-def)$   
**qed**

**lemma shows** *bisim1-callD*:

$\llbracket P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp); call1 e' = \lfloor (a, M, vs) \rfloor; compE2 e ! pc = Invoke M' n0 \rrbracket$   
 $\implies M = M'$

**and** *bisims1-callD*:

$\llbracket P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, xcp); calls1 es' = \lfloor (a, M, vs) \rfloor; compEs2 es ! pc = Invoke M' n0 \rrbracket$   
 $\implies M = M'$

**proof** (*induct e n :: nat e' xs stk loc pc xcp and es n :: nat es' xs stk loc pc xcp*  
*rule: bisim1-bisims1-inducts-split*)

**case** *bisim1AAss1* **thus**  $?case$

**apply** (*simp (no-asm-use) split: if-split-asm add: is-val-iff*)  
**apply** (*fastforce dest: bisim-Val-pc-not-Invoke*)  
**apply** (*fastforce dest: bisim-Val-pc-not-Invoke*)  
**apply** (*fastforce dest: bisim-Val-pc-not-Invoke bisim1-pc-length-compE2*) +  
**done**

**next**

**case** *bisim1Call1* **thus**  $?case$

**apply** (*clarsimp split: if-split-asm simp add: is-vals-conv*)  
**apply** (*drule bisim-Val-pc-not-Invoke, simp, fastforce*)  
**apply** (*drule bisim-Val-pc-not-Invoke, simp, fastforce*)  
**apply** (*drule bisim1-pc-length-compE2, clarsimp simp add: neq-Nil-conv*)  
**apply** (*drule bisim1-pc-length-compE2, simp*)  
**apply** (*drule bisim1-pc-length-compE2, simp*)  
**apply** (*drule bisim1-pc-length-compE2, simp*)  
**apply** (*drule bisim1-call-pcD, simp, simp*)  
**apply** (*drule bisim1-call-pcD, simp, simp*)  
**done**

**next**

**case** *bisim1CallParams* **thus**  $?case$

**apply** (*clarsimp split: if-split-asm simp add: is-vals-conv*)  
**apply** (*drule bisims-Val-pc-not-Invoke, simp, fastforce*)  
**apply** (*drule bisims1-pc-length-compEs2, simp*)  
**apply** (*drule bisims1-calls-pcD, simp, simp*)  
**done**

**qed** (*fastforce split: if-split-asm dest: bisim1-pc-length-compE2 bisims1-pc-length-compEs2 bisim1-call-pcD bisims1-calls-pcD bisim1-call-xcpNone bisims1-calls-xcpNone bisim-Val-pc-not-Invoke bisims-Val-pc-not-Invoke*) +

**lemma** *bisim1-xcpD*:  $P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor) \implies pc < length (compE2 e)$   
**and** *bisims1-xcpD*:  $P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, \lfloor a \rfloor) \implies pc < length (compEs2 es)$   
**by** (*induct (e', xs) (stk, loc, pc, \lfloor a :: 'addr \rfloor) and (es', xs) (stk, loc, pc, \lfloor a :: 'addr \rfloor)*  
*arbitrary: e' xs stk loc pc and es' xs stk loc pc rule: bisim1-bisims1.inducts*)  
*simp-all*

**lemma** *bisim1-match-Some-stk-length*:

$\llbracket P, E, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor); match-ex-table (compP2 P) (cname-of h a) pc (compE2 E 0 0) = \lfloor (pc', d) \rfloor \rrbracket$



```

 $\Rightarrow d \leq \text{length } stk$ 

and bisims1-match-Some-stk-length:
 $\llbracket P, Es, h \vdash (es, xs) [\leftrightarrow] (stk, loc, pc, \lfloor a \rfloor);$ 
 $\text{match-ex-table } (compP2 P) \text{ (cname-of } h \ a) \ pc \ (compEs2 \ Es \ 0 \ 0) = \lfloor (pc', d) \rfloor \rrbracket$ 
 $\Rightarrow d \leq \text{length } stk$ 
proof(induct (e, xs) (stk, loc, pc,  $\lfloor a :: 'addr \rfloor$ ) and (es, xs) (stk, loc, pc,  $\lfloor a :: 'addr \rfloor$ )
arbitrary:  $pc' \ d \ e \ xs \ stk \ loc \ pc$  and  $pc' \ d \ es \ xs \ stk \ loc \ pc$  rule: bisim1-bisims1.inducts)
case bisim1Call1 thus ?case
  by(fastforce dest: bisim1-xcpD simp add: match-ex-table-append match-ex-table-not-pcs-None)
next
case bisim1CallThrowObj thus ?case
  by(fastforce dest: bisim1-xcpD simp add: match-ex-table-append match-ex-table-not-pcs-None)
next
case bisim1Sync4 thus ?case
  apply(clarsimp simp add: match-ex-table-not-pcs-None match-ex-table-append matches-ex-entry-def
split: if-split-asm)
  apply(fastforce simp add: match-ex-table-compxE2-shift-conv dest: bisim1-xcpD)
  done
next
case bisim1Try thus ?case
  by(fastforce simp add: match-ex-table-append matches-ex-entry-def match-ex-table-not-pcs-None
dest: bisim1-xcpD split: if-split-asm)
next
case bisim1TryCatch2 thus ?case
  apply(clarsimp simp add: match-ex-table-not-pcs-None match-ex-table-append matches-ex-entry-def
split: if-split-asm)
  apply(fastforce simp add: match-ex-table-compxE2-shift-conv dest: bisim1-xcpD)
  done
next
case bisim1TryFail thus ?case
  by(fastforce simp add: match-ex-table-append matches-ex-entry-def match-ex-table-not-pcs-None
dest: bisim1-xcpD split: if-split-asm)
next
case bisim1TryCatchThrow thus ?case
  apply(clarsimp simp add: match-ex-table-not-pcs-None match-ex-table-append matches-ex-entry-def
split: if-split-asm)
  apply(fastforce simp add: match-ex-table-compxE2-shift-conv dest: bisim1-xcpD)
  done
next
case bisims1List1 thus ?case
  by(fastforce simp add: match-ex-table-append split: if-split-asm dest: bisim1-xcpD match-ex-table-pcsD)
qed(fastforce simp add: match-ex-table-not-pcs-None match-ex-table-append match-ex-table-compxE2-shift-conv
match-ex-table-compxEs2-shift-conv match-ex-table-compxE2-stack-conv match-ex-table-compxEs2-stack-conv
matches-ex-entry-def dest: bisim1-xcpD) +

end

locale J1-JVM-heap-conf-base =
  J1-JVM-heap-base
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  +

```

```

J1-heap-conf-base
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  hconf P
+
JVM-heap-conf-base
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  hconf compP2 P
for addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool
and P :: 'addr J1-prog
begin

inductive bisim1-list1 ::
  'thread-id ⇒ 'heap ⇒ 'addr expr1 × 'addr locals1 ⇒ ('addr expr1 × 'addr locals1) list
  ⇒ 'addr option ⇒ 'addr frame list ⇒ bool
for t :: 'thread-id and h :: 'heap
where
  bl1-Normal:
  [ compTP P ⊢ t:(xcp, h, (stk, loc, C, M, pc) # frs) √;
    P ⊢ C sees M : Ts→T = [ body ] in D;
    P, blocks1 0 (Class D#Ts) body, h ⊢ (e, xs) ↔ (stk, loc, pc, xcp); max-vars e ≤ length xs;
    list-all2 (bisim1-fr P h) exs frs ]
  ⇒ bisim1-list1 t h (e, xs) exs xcp ((stk, loc, C, M, pc) # frs)

| bl1-finalVal:
  [ hconf h; preallocated h ] ⇒ bisim1-list1 t h (Val v, xs) [] None []

| bl1-finalThrow:
  [ hconf h; preallocated h ] ⇒ bisim1-list1 t h (Throw a, xs) [] [a] []

fun wbisim1 ::
  'thread-id
  ⇒ ((('addr expr1 × 'addr locals1) × ('addr expr1 × 'addr locals1) list) × 'heap,
    ('addr option × 'addr frame list) × 'heap) bisim
where wbisim1 t ((ex, exs), h) ((xcp, frs), h') ⇔ h = h' ∧ bisim1-list1 t h ex exs xcp frs

lemma new-thread-conf-compTP:
  assumes hconf: hconf h preallocated h
  and ha: typeof-addr h a = [ Class-type C ]
  and sub: typeof-addr h (thread-id2addr t) = [ Class-type C' ] P ⊢ C' ≤* Thread
  and sees: P ⊢ C sees M: []→T = [ meth ] in D
  shows compTP P ⊢ t:(None, h, [([], Addr a # replicate (max-vars meth) undefined-value, D, M, 0)])) √

```

**proof** –

**from**  $ha$  sees-method-decl-above[ $OF$  sees]  
**have**  $P, h \vdash \text{Addr } a : \leq \text{Class } D$  **by** ( $\text{simp add: conf-def}$ )  
**moreover**  
**hence**  $\text{compP2 } P, h \vdash \text{Addr } a : \leq \text{Class } D$  **by** ( $\text{simp add: compP2-def}$ )  
**hence**  $\text{compP2 } P, h \vdash \text{Addr } a \# \text{replicate } (\text{max-vars meth}) \text{ undefined-value } [:\leq_{\top}] \text{ map } (\lambda i. \text{if } i = 0$   
 $\text{then OK } ([\text{Class } D] ! i) \text{ else Err}) [0..<\text{max-vars meth}] @ [\text{Err}]$   
**by** –( $\text{rule list-all2-all-nthI, simp-all}$ )  
**hence**  $\text{conf-f } (\text{compP2 } P) h ([], \text{map } (\lambda i. \text{if } i = 0 \text{ then OK } ([\text{Class } D] ! i) \text{ else Err}) [0..<\text{max-vars}$   
 $\text{meth}] @ [\text{Err}])$   
 $(\text{compE2 meth } @ [\text{Return}]) ([], \text{Addr } a \# \text{replicate } (\text{max-vars meth}) \text{ undefined-value, } D,$   
 $M, 0)$   
**unfolding**  $\text{conf-f-def2}$  **by** ( $\text{simp add: compP2-def}$ )  
**ultimately have**  $\text{conf-f } (\text{compP2 } P) h ([], \text{TC0.ty}_l (\text{Suc } (\text{max-vars meth})) [\text{Class } D] \{0\}) (\text{compE2}$   
 $\text{meth } @ [\text{Return}])$   
 $([], \text{Addr } a \# \text{replicate } (\text{max-vars meth}) \text{ undefined-value, } D, M, 0)$   
**by** ( $\text{simp add: TC0.ty}_l\text{-def conf-f-def2 compP2-def}$ )  
**with**  $h\text{conf } ha \text{ sub sees-method-compP}[OF \text{ sees, where } f = \lambda C M Ts T. \text{compMb2}] \text{ sees-method-idemp}[OF$   
 $\text{sees}]$   
**show** ?thesis  
**by** ( $\text{auto simp add: TC0.ty}_i'\text{-def correct-state-def compTP-def tconf-def}$ )( $\text{fastforce simp add: compP2-def}$   
 $\text{compMb2-def tconf-def intro: sees-method-idemp}$ )+  
**qed**

**lemma**  $ta\text{-bisim12-extTA2J1-extTA2JVM}$ :

**assumes**  $nt: \bigwedge n T C M a h. \llbracket n < \text{length } \{ta\}_t; \{ta\}_t ! n = \text{NewThread } T (C, M, a) h \rrbracket$   
 $\implies \text{typeof-addr } h a = \lfloor \text{Class-type } C \rfloor \wedge (\exists C'. \text{typeof-addr } h (\text{thread-id2addr } T) = \lfloor \text{Class-type}$   
 $C' \rfloor \wedge P \vdash C' \preceq^* \text{Thread}) \wedge$   
 $(\exists T \text{ meth } D. P \vdash C \text{ sees } M: [] \rightarrow T = \lfloor \text{meth} \rfloor \text{ in } D) \wedge h\text{conf } h \wedge \text{preallocated } h$   
**shows**  $ta\text{-bisim } wbisim1 (\text{extTA2J1 } P \text{ ta}) (\text{extTA2JVM } (\text{compP2 } P) \text{ ta})$

**proof** –

**{ fix**  $n t C M a m$   
**assume**  $n < \text{length } \{ta\}_t$  **and**  $\{ta\}_t ! n = \text{NewThread } t (C, M, a) m$   
**from**  $nt[OF \text{ this}]$  **obtain**  $T \text{ meth } D C'$   
**where**  $ma: \text{typeof-addr } m a = \lfloor \text{Class-type } C \rfloor$   
**and**  $\text{sees: } P \vdash C \text{ sees } M: [] \rightarrow T = \lfloor \text{meth} \rfloor \text{ in } D$   
**and**  $\text{sub: typeof-addr } m (\text{thread-id2addr } t) = \lfloor \text{Class-type } C' \rfloor$   $P \vdash C' \preceq^* \text{Thread}$   
**and**  $m\text{conf: } h\text{conf } m \text{ preallocated } m$  **by**  $\text{fastforce}$   
**from**  $\text{sees-method-compP}[OF \text{ sees, where } f = \lambda C M Ts T. \text{compMb2}]$   
**have**  $\text{sees': compP2 } P \vdash C \text{ sees } M: [] \rightarrow T = \lfloor (\text{max-stack meth, max-vars meth, compE2 meth } @$   
 $[\text{Return}], \text{compxE2 meth } 0 \ 0) \rfloor \text{ in } D$   
**by** ( $\text{simp add: compMb2-def compP2-def}$ )  
**have**  $\text{bisim1-list1 } t m (\{0:\text{Class } D = \text{None}; \text{meth}\}, \text{Addr } a \# \text{replicate } (\text{max-vars meth}) \text{ unde-}$   
 $\text{fined-value}) ([], \text{None } [([], \text{Addr } a \# \text{replicate } (\text{max-vars meth}) \text{ undefined-value, } D, M, 0)])$   
**proof**  
**from**  $m\text{conf } ma \text{ sub sees}$   
**show**  $\text{compTP } P \vdash t: (\text{None}, m, [([], \text{Addr } a \# \text{replicate } (\text{max-vars meth}) \text{ undefined-value, } D, M,$   
 $0)]) \checkmark$   
**by** ( $\text{rule new-thread-conf-compTP}$ )  
  
**from**  $\text{sees}$  **show**  $P \vdash D \text{ sees } M: [] \rightarrow T = \lfloor \text{meth} \rfloor \text{ in } D$  **by** ( $\text{rule sees-method-idemp}$ )  
**show**  $\text{list-all2 } (\text{bisim1-fr } P m) [] []$  **by**  $\text{simp}$   
**show**  $P, \text{blocks1 } 0 [\text{Class } D] \text{ meth, } m \vdash (\{0:\text{Class } D = \text{None}; \text{meth}\}, \text{Addr } a \# \text{replicate } (\text{max-vars}$   
 $\text{meth}) \text{ undefined-value}) \leftrightarrow$

```

                                ([], Addr a # replicate (max-vars meth) undefined-value, 0, None)
      by simp(rule bisim1-refl)
    qed simp
    with sees sees' have bisim1-list1 t m ({0:Class (fst (method P C M))=None; the (snd (snd
(snd (method P C M))))}, Addr a # replicate (max-vars (the (snd (snd (snd (method P C M))))))
undefined-value) [] None [([], Addr a # replicate (fst (snd (the (snd (snd (snd (method (compP2 P)
C M)))))) undefined-value, fst (method (compP2 P) C M), M, 0)] by simp }
    thus ?thesis
      apply(auto simp add: ta-bisim-def intro!: list-all2-all-nthI)
      apply(case-tac ⌊ta⌋t ! n, auto simp add: extNTA2JVM-def)
    done
  qed
end

```

**end**

**definition** *no-call2* :: 'addr expr1  $\Rightarrow$  pc  $\Rightarrow$  bool  
**where** *no-call2* e pc  $\longleftrightarrow$  (pc  $\leq$  length (compE2 e))  $\wedge$  (pc < length (compE2 e)  $\longrightarrow$  ( $\forall$  M n. compE2 e ! pc  $\neq$  Invoke M n))

**definition** *no-calls2* :: 'addr expr1 list  $\Rightarrow$  pc  $\Rightarrow$  bool  
**where** *no-calls2* es pc  $\longleftrightarrow$  (pc  $\leq$  length (compEs2 es))  $\wedge$  (pc < length (compEs2 es)  $\longrightarrow$  ( $\forall$  M n. compEs2 es ! pc  $\neq$  Invoke M n))

**locale** *J1-JVM-conf-read* =  
*J1-JVM-heap-conf-base*  
 addr2thread-id thread-id2addr  
 spurious-wakeups  
 empty-heap allocate typeof-addr heap-read heap-write  
 hconf P  
 +  
*JVM-conf-read*  
 addr2thread-id thread-id2addr  
 spurious-wakeups  
 empty-heap allocate typeof-addr heap-read heap-write  
 hconf compP2 P  
**for** addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id  
**and** thread-id2addr :: 'thread-id  $\Rightarrow$  'addr  
**and** spurious-wakeups :: bool  
**and** empty-heap :: 'heap  
**and** allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set  
**and** typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype  
**and** heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool  
**and** heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool  
**and** hconf :: 'heap  $\Rightarrow$  bool  
**and** P :: 'addr J1-prog

**locale** *J1-JVM-heap-conf* =  
*J1-JVM-heap-conf-base*  
 addr2thread-id thread-id2addr  
 spurious-wakeups  
 empty-heap allocate typeof-addr heap-read heap-write  
 hconf P  
 +  
*JVM-heap-conf*

```

  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  hconf compP2 P
for addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype
and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
and hconf :: 'heap  $\Rightarrow$  bool
and P :: 'addr J1-prog
begin

lemma red-external-ta-bisim21:
   $\llbracket$  wf-prog wf-md P; P, t  $\vdash$   $\langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle$ ; hconf h'; preallocated h'  $\rrbracket$ 
   $\Rightarrow$  ta-bisim wbisim1 (extTA2J1 P ta) (extTA2JVM (compP2 P) ta)
apply(rule ta-bisim12-extTA2J1-extTA2JVM)
apply(frule (1) red-external-new-thread-sees)
apply(fastforce simp add: in-set-conv-nth)
apply(frule red-ext-new-thread-heap)
apply(fastforce simp add: in-set-conv-nth)
apply(frule red-external-new-thread-exists-thread-object[unfolded compP2-def, simplified])
apply(fastforce simp add: in-set-conv-nth)
apply simp
done

lemma ta-bisim-red-extTA2J1-extTA2JVM:
  assumes wf: wf-prog wf-md P
  and red: wf, P, t'  $\vdash$  1  $\langle e, s \rangle -ta \rightarrow \langle e', s' \rangle$ 
  and hconf: hconf (hp s') preallocated (hp s')
  shows ta-bisim wbisim1 (extTA2J1 P ta) (extTA2JVM (compP2 P) ta)
proof -
  { fix n t C M a H
    assume len: n < length  $\{ta\}_t$  and tan:  $\{ta\}_t ! n = NewThread\ t\ (C, M, a)\ H$ 
    hence nt: NewThread t (C, M, a) H  $\in$  set  $\{ta\}_t$  unfolding set-conv-nth by(auto intro!: exI)
    from red1-new-threadD[OF red nt] obtain ad M' vs va T C' Ts' Tr' D'
      where rede: P, t'  $\vdash$   $\langle ad \cdot M'(vs), hp\ s \rangle -ta \rightarrow ext \langle va, hp\ s' \rangle$ 
      and ad: typeof-addr (hp s) ad =  $\lfloor T \rfloor$  by blast
    from red-ext-new-thread-heap[OF rede nt] have [simp]: hp s' = H by simp
    from red-external-new-thread-sees[OF wf rede nt]
    obtain T body D where Ha: typeof-addr H a =  $\lfloor Class\text{-}type\ C \rfloor$ 
      and sees: P  $\vdash$  C sees M:[]  $\rightarrow$  T =  $\lfloor body \rfloor$  in D by auto
    have sees': compP2 P  $\vdash$  C sees M:[]  $\rightarrow$  T =  $\lfloor (max\text{-}stack\ body, max\text{-}vars\ body, compE2\ body\ @\ [Return],$ 
      compxE2 body 0 0)  $\rfloor$  in D
      using sees unfolding compP2-def compMb2-def Let-def by(auto dest: sees-method-compP)
      from red-external-new-thread-exists-thread-object[unfolded compP2-def, simplified, OF rede nt]
      hconf Ha sees
    have compTP P  $\vdash$  t:(None, H,  $\lfloor ([], Addr\ a\ \# replicate\ (max\text{-}vars\ body)\ undefined\text{-}value, D, M,$ 
      0)  $\rfloor$ )  $\checkmark$ 
      by(auto intro: new-thread-conf-compTP)
    hence bisim1-list1 t H ( $\{0:Class\ D=None; body\}$ , Addr a  $\# replicate\ (max\text{-}vars\ body)\ unde-$ 

```

```

fin-value) [] None [( [], Addr a # replicate (max-vars body) undefined-value, D, M, 0)]
proof
  from sees show  $P \vdash D \text{ sees } M:[] \rightarrow T = [body] \text{ in } D$  by (rule sees-method-idemp)

  show  $P, blocks1\ 0 \ [Class\ D] \ body, H \vdash (\{0:Class\ D=None; body\}, Addr\ a \# replicate\ (max-vars\ body)\ undefined-value) \leftrightarrow$ 
     $([], Addr\ a \# replicate\ (max-vars\ body)\ undefined-value, 0, None)$ 
    by (auto intro: bisim1-refl)
  qed simp-all
  hence bisim1-list1  $t\ H\ (\{0:Class\ (fst\ (method\ P\ C\ M))=None; the\ (snd\ (snd\ (snd\ (method\ P\ C\ M))))\},$ 
     $Addr\ a \# replicate\ (max-vars\ (the\ (snd\ (snd\ (snd\ (method\ P\ C\ M))))))$ 
     $undefined-value)$ 
     $\square$ 
     $None\ [( [], Addr\ a \# replicate\ (fst\ (snd\ (the\ (snd\ (snd\ (snd\ (method\ (compP2\ P)\ C\ M))))))\ undefined-value,$ 
       $fst\ (method\ (compP2\ P)\ C\ M), M, 0)]$ 
    using sees sees' by simp }
  thus ?thesis
    apply (auto simp add: ta-bisim-def intro!: list-all2-all-nthI)
    apply (case-tac  $\{ta\}_t!n$ )
    apply (auto simp add: extNTA2JVM-def extNTA2J1-def)
  done
qed

end

sublocale J1-JVM-conf-read < heap-conf?: J1-JVM-heap-conf
by (unfold-locales)

sublocale J1-JVM-conf-read < heap?: J1-heap
apply (rule J1-heap.intro)
apply (subst compP-heap[symmetric, where  $f=\lambda- - -. compMb2, folded\ compP2-def$ ])
apply (unfold-locales)
done

end

```

## 7.17 Correctness of Stage: From intermediate language to JVM

**theory** J1JVM **imports** J1JVMBisim **begin**

**context** J1-JVM-heap-base **begin**

**declare**  $\tau move1.simps$  [simp del]  $\tau moves1.simps$  [simp del]

**lemma** bisim1-insync-Throw-exec:

```

  assumes bisim2:  $P, e2, h \vdash (Throw\ ad, xs) \leftrightarrow (stk, loc, pc, xcp)$ 
  shows  $\tau Exec\ move1\ a\ P\ t\ (sync_V\ (e1)\ e2)\ h\ (stk, loc, Suc\ (Suc\ (Suc\ (length\ (compE2\ e1) + pc))))$ ,
     $xcp\ ([Addr\ ad], loc, 6 + length\ (compE2\ e1) + length\ (compE2\ e2), None)$ 
  proof -
    from bisim2 have  $pc: pc < length\ (compE2\ e2)$  and [simp]:  $xs = loc$  by (auto dest: bisim1-ThrowD)
    let ?pc =  $6 + length\ (compE2\ e1) + length\ (compE2\ e2)$ 

```

```

let ?stk = Addr ad # drop (size stk - 0) stk
from bisim2 have xcp = [ad] ∨ xcp = None by(auto dest: bisim1-ThrowD)
thus ?thesis
proof
  assume [simp]: xcp = [ad]
  have  $\tau\text{Exec-movet-a } P \ t \ (\text{sync}_V \ (e1) \ e2) \ h \ (stk, \text{loc}, \text{Suc} \ (\text{Suc} \ (\text{length} \ (\text{compE2} \ e1) + pc)))$ ,
  [ad] ( $\text{?stk}, \text{loc}, \text{?pc}, \text{None}$ )
  proof(rule  $\tau\text{Exec1step}$ [unfolded exec-move-def, OF exec-catch])
    from bisim1-xcp-Some-not-caught[OF bisim2[simplified], of  $\lambda C \ M \ Ts \ T. \text{compMb2} \ \text{Suc} \ (\text{Suc} \ (\text{length} \ (\text{compE2} \ e1)))$ ] 0]
    have match-ex-table (compP2 P) (cname-of h ad) (Suc (Suc (Suc (length (compE2 e1) + pc))))
    (compE2 e2 (Suc (Suc (Suc (length (compE2 e1))))) 0) = None
    by(simp add: compP2-def)
    thus match-ex-table (compP2 P) (cname-of h ad) (Suc (Suc (Suc (length (compE2 e1) + pc))))
    (compE2 (syncV (e1) e2) 0 0) = [(6 + length (compE2 e1) + length (compE2 e2), 0)]
    using pc
    by(auto simp add: compP2-def match-ex-table-append matches-ex-entry-def eval-nat-numeral
    dest: match-ex-table-pc-length-compE2)
    qed(insert pc, auto intro:  $\tau\text{move2xcp}$ )
    thus ?thesis by simp
next
  assume [simp]: xcp = None
  with bisim2 obtain pc'
    where  $\tau\text{Exec-movet-a } P \ t \ e2 \ h \ (stk, \text{loc}, pc, \text{None}) \ ([\text{Addr} \ ad], \text{loc}, pc', [ad])$ 
    and bisim':  $P, e2, h \vdash (\text{Throw} \ ad, xs) \leftrightarrow ([\text{Addr} \ ad], \text{loc}, pc', [ad])$  and [simp]:  $xs = \text{loc}$ 
    by(auto dest: bisim1-Throw- $\tau\text{Exec-movet}$ )
    hence  $\tau\text{Exec-movet-a } P \ t \ (\text{sync}_V \ (e1) \ e2) \ h \ (stk, \text{loc}, \text{Suc} \ (\text{Suc} \ (\text{Suc} \ (\text{length} \ (\text{compE2} \ e1) + pc)))$ ,
    None) ([Addr ad], loc, Suc (Suc (Suc (length (compE2 e1) + pc'))), [ad])
    by-(rule Insync- $\tau\text{ExecI}$ )
    also let ?stk = Addr ad # drop (size [Addr ad] - 0) [Addr ad]
    from bisim' have pc':  $pc' < \text{length} \ (\text{compE2} \ e2)$  by(auto dest: bisim1-ThrowD)
    have  $\tau\text{Exec-movet-a } P \ t \ (\text{sync}_V \ (e1) \ e2) \ h \ ([\text{Addr} \ ad], \text{loc}, \text{Suc} \ (\text{Suc} \ (\text{Suc} \ (\text{length} \ (\text{compE2} \ e1) + pc'))$ ,
    [ad]) ( $\text{?stk}, \text{loc}, \text{?pc}, \text{None}$ )
    proof(rule  $\tau\text{Exec1step}$ [unfolded exec-move-def, OF exec-catch])
      from bisim1-xcp-Some-not-caught[OF bisim', of  $\lambda C \ M \ Ts \ T. \text{compMb2} \ \text{Suc} \ (\text{Suc} \ (\text{length} \ (\text{compE2} \ e1)))$ ] 0]
      have match-ex-table (compP2 P) (cname-of h ad) (Suc (Suc (Suc (length (compE2 e1) + pc'))))
      (compE2 e2 (Suc (Suc (Suc (length (compE2 e1))))) 0) = None
      by(simp add: compP2-def)
      thus match-ex-table (compP2 P) (cname-of h ad) (Suc (Suc (Suc (length (compE2 e1) + pc'))))
      (compE2 (syncV (e1) e2) 0 0) = [(6 + length (compE2 e1) + length (compE2 e2), 0)]
      using pc'
      by(auto simp add: compP2-def match-ex-table-append matches-ex-entry-def eval-nat-numeral
      dest: match-ex-table-pc-length-compE2)
      qed(insert pc', auto intro:  $\tau\text{move2xcp}$ )
      finally (tranclp-trans) show ?thesis by simp
    qed
  qed
end

primrec sim12-size :: ('a, 'b, 'addr) exp  $\Rightarrow$  nat
  and sim12-sizes :: ('a, 'b, 'addr) exp list  $\Rightarrow$  nat
where

```

```

sim12-size (new C) = 0
| sim12-size (newA T[e]) = Suc (sim12-size e)
| sim12-size (Cast T e) = Suc (sim12-size e)
| sim12-size (e instanceof T) = Suc (sim12-size e)
| sim12-size (e «bop» e') = Suc (sim12-size e + sim12-size e')
| sim12-size (Val v) = 0
| sim12-size (Var V) = 0
| sim12-size (V := e) = Suc (sim12-size e)
| sim12-size (a[i]) = Suc (sim12-size a + sim12-size i)
| sim12-size (a[i] := e) = Suc (sim12-size a + sim12-size i + sim12-size e)
| sim12-size (a.length) = Suc (sim12-size a)
| sim12-size (e.F{D}) = Suc (sim12-size e)
| sim12-size (e.F{D} := e') = Suc (sim12-size e + sim12-size e')
| sim12-size (e.compareAndSwap(D.F, e', e')) = Suc (sim12-size e + sim12-size e' + sim12-size e'')
| sim12-size (e.M(es)) = Suc (sim12-size e + sim12-sizes es)
| sim12-size ({V:T=vo; e}) = Suc (sim12-size e)
| sim12-size (syncV(e) e') = Suc (sim12-size e + sim12-size e')
| sim12-size (insyncV(a) e) = Suc (sim12-size e)
| sim12-size (e;; e') = Suc (sim12-size e + sim12-size e')
| sim12-size (if (e) e1 else e2) = Suc (sim12-size e)
| sim12-size (while(b) c) = Suc (Suc (sim12-size b))
| sim12-size (throw e) = Suc (sim12-size e)
| sim12-size (try e catch(C V) e') = Suc (sim12-size e)

| sim12-sizes [] = 0
| sim12-sizes (e # es) = sim12-size e + sim12-sizes es

```

**lemma** *sim12-sizes-map-Val [simp]*:

*sim12-sizes (map Val vs) = 0*

**by**(*induct vs*) *simp-all*

**lemma** *sim12-sizes-append [simp]*:

*sim12-sizes (es @ es') = sim12-sizes es + sim12-sizes es'*

**by**(*induct es*) *simp-all*

**context** *JVM-heap-base* **begin**

**lemma** *τExec-mover-length-compE2-conv [simp]*:

**assumes** *pc: pc ≥ length (compE2 e)*

**shows** *τExec-mover ci P t e h (stk, loc, pc, xcp) s ⟷ s = (stk, loc, pc, xcp)*

**proof**

**assume** *τExec-mover ci P t e h (stk, loc, pc, xcp) s*

**thus** *s = (stk, loc, pc, xcp)* **using** *pc*

**by** *induct(auto simp add: τexec-move-def)*

**qed** *auto*

**lemma** *τExec-movesr-length-compE2-conv [simp]*:

**assumes** *pc: pc ≥ length (compEs2 es)*

**shows** *τExec-movesr ci P t es h (stk, loc, pc, xcp) s ⟷ s = (stk, loc, pc, xcp)*

**proof**

**assume** *τExec-movesr ci P t es h (stk, loc, pc, xcp) s*

**thus** *s = (stk, loc, pc, xcp)* **using** *pc*

**by** *induct(auto simp add: τexec-moves-def)*

**qed** *auto*



end

context *J1-JVM-heap-base* begin

lemma assumes *wf*: *wf-J1-prog P*

defines [*simp*]: *sim-move*  $\equiv \lambda e e'. \text{if } \text{sim12-size } e' < \text{sim12-size } e \text{ then } \tau\text{Exec-mover-a} \text{ else } \tau\text{Exec-movet-a}$   
 and [*simp*]: *sim-moves*  $\equiv \lambda es es'. \text{if } \text{sim12-sizes } es' < \text{sim12-sizes } es \text{ then } \tau\text{Exec-movesr-a} \text{ else}$

$\tau\text{Exec-movest-a}$

shows *exec-instr-simulates-red1*:

$\llbracket P, E, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp); \text{True}, P, t \vdash 1 \langle e, (h, xs) \rangle \xrightarrow{-ta} \langle e', (h', xs') \rangle; \text{bsok } E \ n \rrbracket$   
 $\implies \exists pc'' stk'' loc'' xcp''. P, E, h' \vdash (e', xs') \leftrightarrow (stk'', loc'', pc'', xcp'') \wedge$   
 (if  $\tau\text{move1 } P \ h \ e$   
 then  $h = h' \wedge \text{sim-move } e \ e' \ P \ t \ E \ h \ (stk, loc, pc, xcp) \ (stk'', loc'', pc'', xcp'')$   
 else  $\exists pc' stk' loc' xcp'. \tau\text{Exec-mover-a } P \ t \ E \ h \ (stk, loc, pc, xcp) \ (stk', loc', pc', xcp') \wedge$   
 $\text{exec-move-a } P \ t \ E \ h \ (stk', loc', pc', xcp') \ (\text{extTA2JVM } (\text{compP2 } P) \ ta) \ h'$   
 $(stk'', loc'', pc'', xcp'') \wedge$   
 $\neg \tau\text{move2 } (\text{compP2 } P) \ h \ stk' \ E \ pc' \ xcp' \wedge$   
 $(\text{call1 } e = \text{None} \vee \text{no-call2 } E \ pc \vee pc' = pc \wedge stk' = stk \wedge loc' = loc \wedge$   
 $xcp' = xcp))$   
 (is  $\llbracket -; -; - \rrbracket$   
 $\implies \exists pc'' stk'' loc'' xcp''. - \wedge ?\text{exec } ta \ E \ e \ e' \ h \ stk \ loc \ pc \ xcp \ h' \ pc'' \ stk'' \ loc'' \ xcp''$ )

and *exec-instr-simulates-reds1*:

$\llbracket P, Es, h \vdash (es, xs) [\leftrightarrow] (stk, loc, pc, xcp); \text{True}, P, t \vdash 1 \langle es, (h, xs) \rangle \xrightarrow{[-ta]} \langle es', (h', xs') \rangle; \text{bsoks}$   
 $Es \ n \rrbracket$   
 $\implies \exists pc'' stk'' loc'' xcp''. P, Es, h' \vdash (es', xs') [\leftrightarrow] (stk'', loc'', pc'', xcp'') \wedge$   
 (if  $\tau\text{moves1 } P \ h \ es$   
 then  $h = h' \wedge \text{sim-moves } es \ es' \ P \ t \ Es \ h \ (stk, loc, pc, xcp) \ (stk'', loc'', pc'', xcp'')$   
 else  $\exists pc' stk' loc' xcp'. \tau\text{Exec-movesr-a } P \ t \ Es \ h \ (stk, loc, pc, xcp) \ (stk', loc', pc', xcp') \wedge$   
 $\text{exec-moves-a } P \ t \ Es \ h \ (stk', loc', pc', xcp') \ (\text{extTA2JVM } (\text{compP2 } P) \ ta)$   
 $h' \ (stk'', loc'', pc'', xcp'') \wedge$   
 $\neg \tau\text{moves2 } (\text{compP2 } P) \ h \ stk' \ Es \ pc' \ xcp' \wedge$   
 $(\text{calls1 } es = \text{None} \vee \text{no-calls2 } Es \ pc \vee pc' = pc \wedge stk' = stk \wedge loc' = loc \wedge$   
 $xcp' = xcp))$   
 (is  $\llbracket -; -; - \rrbracket$   
 $\implies \exists pc'' stk'' loc'' xcp''. - \wedge ?\text{execs } ta \ Es \ es \ es' \ h \ stk \ loc \ pc \ xcp \ h' \ pc'' \ stk'' \ loc'' \ xcp''$ )

proof(induction *E n e xs stk loc pc xcp* and *Es n es xs stk loc pc xcp*

arbitrary:  $e' h' xs' \text{Env } T \text{Env}' T'$  and  $es' h' xs' \text{Env } Ts \text{Env}' Ts'$  rule: *bisim1-bisims1-inducts-split*)

case (*bisim1Call1 obj n obj' xs stk loc pc xcp ps M'*)

note *IHobj* = *bisim1Call1.IH*(2)

note *IHparam* = *bisim1Call1.IH*(4)

note *bisim1* =  $\langle P, obj, h \vdash (obj', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$

note *bisim2* =  $\langle \bigwedge xs. P, ps, h \vdash (ps, xs) [\leftrightarrow] ([], xs, 0, \text{None}) \rangle$

note *bsok* =  $\langle \text{bsok } (obj \cdot M'(ps)) \ n \rangle$

note *red* =  $\langle \text{True}, P, t \vdash 1 \langle obj \cdot M'(ps), (h, xs) \rangle \xrightarrow{-ta} \langle e', (h', xs') \rangle \rangle$

from *red* show *?case*

proof(cases)

case (*Call1Obj E'*)

note [*simp*] =  $\langle e' = E' \cdot M'(ps) \rangle$

and *red* =  $\langle \text{True}, P, t \vdash 1 \langle obj', (h, xs) \rangle \xrightarrow{-ta} \langle E', (h', xs') \rangle \rangle$

from *red* have  $\tau: \tau\text{move1 } P \ h \ obj' = \tau\text{move1 } P \ h \ (obj' \cdot M'(ps))$  by(*auto simp add:  $\tau\text{move1.simps}$*

*$\tau\text{moves1.simps}$* )

moreover from *red* have *call1* ( $obj' \cdot M'(ps)$ ) = *call1 obj'* by *auto*

**moreover from**  $IHobj[OF\ red]$   $bsok$   
**obtain**  $pc''\ stk''\ loc''\ xcp''$  **where**  $bisim: P, obj, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$   
**and**  $redo: ?exec\ ta\ obj\ obj'\ E'\ h\ stk\ loc\ pc\ xcp\ h'\ pc''\ stk''\ loc''\ xcp''$  **by**  $auto$   
**from**  $bisim$   
**have**  $P, obj \cdot M'(ps), h' \vdash (E' \cdot M'(ps), xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$   
**by**  $(rule\ bisim1-bisims1.bisim1Call1)$   
**moreover** {  
**assume**  $no-call2\ obj\ pc$   
**hence**  $no-call2\ (obj \cdot M'(ps))\ pc \vee pc = length\ (compE2\ obj)$  **by**  $(auto\ simp\ add: no-call2-def)$  }  
**ultimately show**  $?thesis$  **using**  $redo$   
**by**  $(auto\ simp\ del: call1.simps\ calls1.simps\ split: if-split-asm\ split\ del: if-split)(blast\ intro:$   
 $Call\text{-}\tau\ ExecrI1\ Call\text{-}\tau\ ExecI1\ exec-move-CallI1)+$   
**next**  
**case**  $(Call1Params\ es\ v)$   
**note**  $[simp] = \langle obj' = Val\ v \rangle \langle e' = Val\ v \cdot M'(es) \rangle$   
**and**  $red = \langle True, P, t \vdash 1\ \langle ps, (h, xs) \rangle \ [-ta\rightarrow] \ \langle es, (h', xs') \rangle \rangle$   
**from**  $red$  **have**  $\tau: \tau move1\ P\ h\ (obj' \cdot M'(ps)) = \tau moves1\ P\ h\ ps$  **by**  $(auto\ simp\ add: \tau move1.simps\ \tau moves1.simps)$   
**from**  $bisim1$  **have**  $s: xcp = None\ xs = loc$   
**and**  $execo: \tau Exec-mover-a\ P\ t\ obj\ h\ (stk, loc, pc, xcp)\ ([v], loc, length\ (compE2\ obj), None)$   
**by**  $(auto\ dest: bisim1Val2D1)$   
**hence**  $\tau Exec-mover-a\ P\ t\ (obj \cdot M'(ps))\ h\ (stk, loc, pc, xcp)\ ([v], loc, length\ (compE2\ obj), None)$   
**by**  $-(rule\ Call\text{-}\tau\ ExecrI1)$   
**moreover from**  $IHparam[OF\ red]$   $bsok$  **obtain**  $pc''\ stk''\ loc''\ xcp''$   
**where**  $bisim': P, ps, h' \vdash (es, xs') [\leftrightarrow] (stk'', loc'', pc'', xcp'')$   
**and**  $exec': ?execs\ ta\ ps\ ps\ es\ h\ []\ xs\ 0\ None\ h'\ pc''\ stk''\ loc''\ xcp''$  **by**  $auto$   
**have**  $?exec\ ta\ (obj \cdot M'(ps))\ (obj' \cdot M'(ps))\ (obj' \cdot M(es))\ h\ [v]\ loc\ (length\ (compE2\ obj))\ None\ h'$   
 $(length\ (compE2\ obj) + pc'')\ (stk''\ @\ [v])\ loc''\ xcp''$   
**proof**  $(cases\ \tau move1\ P\ h\ (obj' \cdot M'(ps)))$   
**case**  $True$   
**with**  $exec'\ \tau$  **have**  $[simp]: h = h'$   
**and**  $e: sim-moves\ ps\ es\ P\ t\ ps\ h\ ([], xs, 0, None)\ (stk'', loc'', pc'', xcp'')$  **by**  $auto$   
**from**  $e$  **have**  $sim-move\ (obj' \cdot M'(ps))\ (obj' \cdot M'(es))\ P\ t\ (obj \cdot M'(ps))\ h\ ([[]\ @\ [v], xs, length\ (compE2\ obj) + 0, None)\ (stk''\ @\ [v], loc'', length\ (compE2\ obj) + pc'', xcp'')$   
**by**  $(fastforce\ dest: Call\text{-}\tau\ ExecrI2\ Call\text{-}\tau\ ExecI2)$   
**with**  $s\ True$  **show**  $?thesis$  **by**  $auto$   
**next**  
**case**  $False$   
**with**  $exec'\ \tau$  **obtain**  $pc'\ stk'\ loc'\ xcp'$   
**where**  $e: \tau Exec-movesr-a\ P\ t\ ps\ h\ ([], xs, 0, None)\ (stk', loc', pc', xcp')$   
**and**  $e': exec-moves-a\ P\ t\ ps\ h\ (stk', loc', pc', xcp')\ (extTA2JVM\ (compP2\ P)\ ta)\ h'\ (stk'', loc'', pc'', xcp'')$   
**and**  $\tau': \neg\ \tau moves2\ (compP2\ P)\ h\ stk'\ ps\ pc'\ xcp'$   
**and**  $call: calls1\ ps = None \vee no-calls2\ ps\ 0 \vee pc' = 0 \wedge stk' = [] \wedge loc' = xs \wedge xcp' = None$   
**by**  $auto$   
**from**  $e$  **have**  $\tau Exec-mover-a\ P\ t\ (obj \cdot M'(ps))\ h\ ([[]\ @\ [v], xs, length\ (compE2\ obj) + 0, None)\ (stk'\ @\ [v], loc', length\ (compE2\ obj) + pc', xcp')$  **by**  $(rule\ Call\text{-}\tau\ ExecrI2)$   
**moreover from**  $e'$  **have**  $exec-move-a\ P\ t\ (obj \cdot M'(ps))\ h\ (stk'\ @\ [v], loc', length\ (compE2\ obj) + pc', xcp')\ (extTA2JVM\ (compP2\ P)\ ta)\ h'\ (stk''\ @\ [v], loc'', length\ (compE2\ obj) + pc'', xcp'')$   
**by**  $(rule\ exec-move-CallI2)$   
**moreover from**  $\tau'\ e'$  **have**  $\tau move2\ (compP2\ P)\ h\ (stk'\ @\ [v])\ (obj \cdot M'(ps))\ (length\ (compE2\ obj) + pc')\ xcp' \implies False$   
**by**  $(fastforce\ simp\ add: \tau move2-iff\ \tau moves2-iff\ \tau instr-stk-drop-exec-moves\ split: if-split-asm)$   
**moreover from**  $red$  **have**  $call1\ (obj \cdot M'(ps)) = calls1\ ps$  **by**  $(auto\ simp\ add: is-vals-conv)$

```

moreover have no-calls2 ps 0  $\implies$  no-call2 (obj.M'(ps)) (length (compE2 obj))  $\vee$  ps = [] calls1
[] = None
  by(auto simp add: no-calls2-def no-call2-def)
ultimately show ?thesis using False s call
  by(auto simp del: split-paired-Ex call1.simps calls1.simps) blast
qed
moreover from bisim'
have P,obj.M'(ps),h'  $\vdash$  (Val v.M'(es), xs')  $\leftrightarrow$  ((stk'' @ [v]), loc'', length (compE2 obj) + pc'',
xcp'')
  by(rule bisim1-bisims1.bisim1CallParams)
moreover from bisim1 have pc  $\neq$  length (compE2 obj)  $\longrightarrow$  no-call2 (obj.M'(ps)) pc
  by(auto simp add: no-call2-def dest: bisim-Val-pc-not-Invoke bisim1-pc-length-compE2)
ultimately show ?thesis using  $\tau$  execo
  apply(auto simp del: split-paired-Ex call1.simps calls1.simps split: if-split-asm split del: if-split)
  apply(blast intro:  $\tau$ Exec-mover-trans|fastforce elim!:  $\tau$ Exec-mover-trans simp del: split-paired-Ex
call1.simps calls1.simps)+
  done
next
case (Call1ThrowObj a)
note [simp] =  $\langle \text{obj}' = \text{Throw } a \rangle \langle \text{ta} = \varepsilon \rangle \langle e' = \text{Throw } a \rangle \langle h' = h \rangle \langle \text{xs}' = \text{xs} \rangle$ 
have  $\tau$ :  $\tau\text{move1 } P \ h \ (\text{Throw } a.M'(ps))$  by(rule  $\tau\text{move1CallThrowObj}$ )
from bisim1 have xcp = [a]  $\vee$  xcp = None by(auto dest: bisim1-ThrowD)
thus ?thesis
proof
  assume [simp]: xcp = [a]
  with bisim1 have P, obj.M'(ps), h  $\vdash$  (Throw a, xs)  $\leftrightarrow$  (stk, loc, pc, [a])
    by(auto intro: bisim1-bisims1.bisim1CallThrowObj)
  thus ?thesis using  $\tau$  by(fastforce)
next
  assume [simp]: xcp = None
  with bisim1 obtain pc'
    where  $\tau\text{Exec-mover-a } P \ t \ \text{obj } h \ (stk, loc, pc, \text{None}) \ ([\text{Addr } a], loc, pc', [a])$ 
    and bisim': P, obj, h  $\vdash$  (Throw a, xs)  $\leftrightarrow$  ([Addr a], loc, pc', [a]) and [simp]: xs = loc
    by(auto dest: bisim1-Throw- $\tau\text{Exec-mover}$ )
  hence  $\tau\text{Exec-mover-a } P \ t \ (\text{obj.M}'(ps)) \ h \ (stk, loc, pc, \text{None}) \ ([\text{Addr } a], loc, pc', [a])$ 
    by-(rule Call- $\tau\text{ExecrI1}$ )
  moreover from bisim' have P, obj.M'(ps), h  $\vdash$  (Throw a, xs)  $\leftrightarrow$  ([Addr a], loc, pc', [a])
    by(rule bisim1CallThrowObj)
  ultimately show ?thesis using  $\tau$  by auto
qed
next
case (Call1ThrowParams vs a es' v)
note [simp] =  $\langle \text{obj}' = \text{Val } v \rangle \langle \text{ta} = \varepsilon \rangle \langle e' = \text{Throw } a \rangle \langle h' = h \rangle \langle \text{xs}' = \text{xs} \rangle$ 
  and ps =  $\langle \text{ps} = \text{map Val vs } @ \ \text{Throw } a \ \# \ \text{es}' \rangle$ 
from bisim1 have [simp]: xcp = None xs = loc
  and  $\tau\text{Exec-mover-a } P \ t \ \text{obj } h \ (stk, loc, pc, xcp) \ ([v], loc, \text{length } (\text{compE2 } \text{obj}), \text{None})$ 
    by(auto dest: bisim1Val2D1)
  hence  $\tau\text{Exec-mover-a } P \ t \ (\text{obj.M}'(ps)) \ h \ (stk, loc, pc, xcp) \ ([v], loc, \text{length } (\text{compE2 } \text{obj}), \text{None})$ 
    by-(rule Call- $\tau\text{ExecrI1}$ )
  also from bisims1-Throw- $\tau\text{Exec-movest}$ [OF bisim2[of xs, unfolded ps]]
  obtain pc' where exec':  $\tau\text{Exec-movest-a } P \ t \ (\text{map Val vs } @ \ \text{Throw } a \ \# \ \text{es}') \ h \ ([], xs, 0, \text{None})$ 
    (Addr a  $\#$  rev vs, xs, pc', [a])
  and bisim': P, map Val vs @ Throw a  $\#$  es', h  $\vdash$  (map Val vs @ Throw a  $\#$  es', xs)  $\leftrightarrow$  (Addr a
 $\#$  rev vs, xs, pc', [a])

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    by auto
  from Call- $\tau$ ExecI2[OF exec', of obj M' v] ps
  have  $\tau$ Exec-mover-a P t (obj.M'(ps)) h ([v], loc, length (compE2 obj), None) (Addr a # rev vs @
[v], xs, length (compE2 obj) + pc', [a]) by simp
  also from bisim1-bisims1.bisim1CallThrowParams[OF bisim', of obj M' v] ps
  have bisim'': P, obj.M'(ps), h  $\vdash$  (Throw a, xs)  $\leftrightarrow$  (Addr a # rev vs @ [v], xs, length (compE2 obj)
+ pc', [a]) by simp
  moreover have  $\tau$ move1 P h (obj'.M'(ps)) using ps by (auto intro:  $\tau$ move1CallThrowParams)
  ultimately show ?thesis by fastforce
next
case (Red1CallExternal a Ta Ts Tr D vs va H')
hence [simp]: obj' = addr a ps = map Val vs
  e' = extRet2J (addr a.M'(map Val vs)) va H' = h' xs' = xs
  and Ta: typeof-addr h a = [Ta]
  and iec: P  $\vdash$  class-type-of Ta sees M': Ts  $\rightarrow$  Tr = Native in D
  and redex: P, t  $\vdash$   $\langle$  a.M'(vs), h  $\rangle$   $\rightarrow$  ext  $\langle$  va, h  $\rangle$  by auto
  from bisim1 have [simp]: xs = loc by (auto dest: bisim-Val-loc-eq-xcp-None)
  have  $\tau$ :  $\tau$ move1 P h (addr a.M'(map Val vs))  $\longleftrightarrow$   $\tau$ move2 (compP2 P) h (rev vs @ [Addr a])
(obj.M'(ps)) (length (compE2 obj) + length (compEs2 ps)) None using Ta iec
  by (auto simp add: map-eq-append-conv  $\tau$ move1.simps  $\tau$ moves1.simps  $\tau$ move2-iff compP2-def)
  from bisim1 have s: xcp = None lcl (h, xs) = loc
  and  $\tau$ Exec-mover-a P t obj h (stk, loc, pc, xcp) ([Addr a], loc, length (compE2 obj), None)
  by (auto dest: bisim1Val2D1)
  hence  $\tau$ Exec-mover-a P t (obj.M'(ps)) h (stk, loc, pc, xcp) ([Addr a], loc, length (compE2 obj),
None)
  by (rule Call- $\tau$ ExecrI1)
  also have  $\tau$ Exec-movesr-a P t ps h ([], loc, 0, None) (rev vs, loc, length (compEs2 ps), None)
  unfolding  $\langle$  ps = map Val vs  $\rangle$  by (rule  $\tau$ Exec-movesr-map-Val)
  from Call- $\tau$ ExecrI2[OF this, of obj M' Addr a]
  have  $\tau$ Exec-mover-a P t (obj.M'(ps)) h ([Addr a], loc, length (compE2 obj), None) (rev vs @ [Addr
a], loc, length (compE2 obj) + length (compEs2 ps), None) by simp
  also (rtranclp-trans) from bisim1 have pc  $\leq$  length (compE2 obj) by (rule bisim1-pc-length-compE2)
  hence no-call2 (obj.M'(ps)) pc  $\vee$  pc = length (compE2 obj) + length (compEs2 ps)
  using bisim1 by (fastforce simp add: no-call2-def neq-Nil-conv dest: bisim-Val-pc-not-Invoke)
  moreover {
    assume pc = length (compE2 obj) + length (compEs2 ps)
    with  $\langle$   $\tau$ Exec-mover-a P t obj h (stk, loc, pc, xcp) ([Addr a], loc, length (compE2 obj), None)  $\rangle$ 
    have stk = rev vs @ [Addr a] xcp = None by auto }
  moreover
  let ?ret = extRet2JVM (length ps) h' (rev vs @ [Addr a]) loc undefined undefined (length (compE2
obj) + length (compEs2 ps)) [] va
  let ?stk' = fst (hd (snd (snd ?ret)))
  let ?xcp' = fst ?ret
  let ?pc' = snd (snd (snd (snd (hd (snd (snd ?ret)))))
  from redex have redex': (ta, va, h')  $\in$  red-external-aggr (compP2 P) t a M' vs h
  by (rule red-external-imp-red-external-aggr, simp add: compP2-def)
  with Ta iec redex'
  have exec-move-a P t (obj.M'(ps)) h (rev vs @ [Addr a], loc, length (compE2 obj) + length (compEs2
ps), None) (extTA2JVM (compP2 P) ta) h' (?stk', loc, ?pc', ?xcp')
  unfolding exec-move-def
  by (rule exec-instr, cases va, force simp add: compP2-def simp del: split-paired-Ex+)
  moreover have P, obj.M'(ps), h'  $\vdash$  (extRet2J1 (addr a.M'(map Val vs)) va, loc)  $\leftrightarrow$  (?stk', loc,
?pc', ?xcp')
  proof (cases va)

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    case (RetVal v)
    have  $P, \text{obj} \cdot M'(ps), h' \vdash (\text{Val } v, \text{loc}) \leftrightarrow ([v], \text{loc}, \text{length } (\text{compE2 } (\text{obj} \cdot M'(ps))), \text{None})$ 
      by (rule bisim1Val2) simp
    thus ?thesis unfolding RetVal by simp
  next
    case (RetExc ad) thus ?thesis by (auto intro: bisim1CallThrow)
  next
    case RetStaySame
    from bisims1-map-Val-append[OF bisims1Nil, of map Val vs vs P h' loc]
    have  $P, \text{map Val vs}, h' \vdash (\text{map Val vs}, \text{loc}) [\leftrightarrow] (\text{rev vs}, \text{loc}, \text{length } (\text{compEs2 } (\text{map Val vs})), \text{None})$ 
  by simp
    hence  $P, \text{obj} \cdot M'(\text{map Val vs}), h' \vdash (\text{addr } a \cdot M'(\text{map Val vs}), \text{loc}) \leftrightarrow (\text{rev vs } @ [\text{Addr } a], \text{loc}, \text{length } (\text{compE2 } \text{obj}) + \text{length } (\text{compEs2 } (\text{map Val vs})), \text{None})$ 
      by (rule bisim1CallParams)
    thus ?thesis using RetStaySame by simp
  qed
  moreover from redex Ta iec
  have  $\tau \text{move1 } P h (\text{addr } a \cdot M'(\text{map Val vs})) \implies ta = \varepsilon \wedge h' = h$ 
  by (fastforce simp add:  $\tau \text{move1} \cdot \text{simps } \tau \text{moves1} \cdot \text{simps map-eq-append-conv } \tau \text{external}'\text{-def } \tau \text{external-def}$ 
    dest:  $\tau \text{external}'\text{-red-external-heap-unchanged } \tau \text{external}'\text{-red-external-TA-empty sees-method-fun}$ )
  ultimately show ?thesis using  $\tau$ 
    apply (cases  $\tau \text{move1 } P h (\text{addr } a \cdot M'(\text{map Val vs})) :: \text{'addr expr1}$ )
    apply (auto simp del: split-paired-Ex simp add: compP2-def)
    apply (blast intro: rtranclp.rtrancl-into-rtrancl rtranclp-into-tranclp1  $\tau \text{exec-moveI}$ ) +
    done
  next
    case (Red1CallNull vs)
    note [simp] =  $\langle h' = h \rangle \langle xs' = xs \rangle \langle ta = \varepsilon \rangle \langle \text{obj}' = \text{null} \rangle \langle ps = \text{map Val vs} \rangle \langle e' = \text{THROW NullPointer} \rangle$ 
    from bisim1 have  $s: xcp = \text{None } xs = \text{loc}$ 
      and  $\tau \text{Exec-mover-a } P t \text{ obj } h (stk, \text{loc}, pc, xcp) ([\text{Null}], \text{loc}, \text{length } (\text{compE2 } \text{obj}), \text{None})$ 
      by (auto dest: bisim1Val2D1)
    hence  $\tau \text{Exec-mover-a } P t (\text{obj} \cdot M'(\text{map Val vs})) h (stk, \text{loc}, pc, xcp) ([\text{Null}], \text{loc}, \text{length } (\text{compE2 } \text{obj}), \text{None})$ 
      by  $-(\text{rule Call-}\tau \text{ExecrI1})$ 
    also have  $\tau \text{Exec-movesr-a } P t (\text{map Val vs}) h ([], \text{loc}, 0, \text{None}) (\text{rev vs}, \text{loc}, \text{length } (\text{compEs2 } (\text{map Val vs})), \text{None})$ 
      proof (cases vs)
      case Nil thus ?thesis by (auto)
    next
      case Cons
      with bisims1-refl[of P h map Val vs loc, simplified] show ?thesis
        by  $-(\text{drule bisims1-Val-}\tau \text{Exec-moves}, \text{auto})$ 
    qed
    from Call- $\tau \text{ExecrI2}$ [OF this, of obj M' Null]
    have  $\tau \text{Exec-mover-a } P t (\text{obj} \cdot M'(\text{map Val vs})) h ([\text{Null}], \text{loc}, \text{length } (\text{compE2 } \text{obj}), \text{None}) (\text{rev vs } @ [\text{Null}], \text{loc}, \text{length } (\text{compE2 } \text{obj}) + \text{length } (\text{compEs2 } (\text{map Val vs})), \text{None})$  by simp
    also (rtranclp-trans) {
      have  $\tau \text{move2 } (\text{compP2 } P) h (\text{rev vs } @ [\text{Null}]) (\text{obj} \cdot M'(\text{map Val vs})) (\text{length } (\text{compE2 } \text{obj}) + \text{length } (\text{compEs2 } (\text{map Val vs}))) \text{None}$ 
        by (simp add:  $\tau \text{move2-iff}$ )
      moreover have  $\text{exec-move-a } P t (\text{obj} \cdot M'(\text{map Val vs})) h (\text{rev vs } @ [\text{Null}], \text{loc}, \text{length } (\text{compE2 } \text{obj}) + \text{length } (\text{compEs2 } (\text{map Val vs})), \text{None}) \varepsilon h (\text{rev vs } @ [\text{Null}], \text{loc}, \text{length } (\text{compE2 } \text{obj}) + \text{length } (\text{compEs2 } (\text{map Val vs})), [\text{addr-of-sys-xcpt NullPointer}])$ 

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    unfolding exec-move-def by(cases vs)(auto intro: exec-instr)
    ultimately have  $\tau_{\text{Exec-movet-a}} P t (obj \cdot M'(\text{map Val vs})) h$  ( $\text{rev vs} @ [\text{Null}]$ ,  $\text{loc}$ ,  $\text{length (compE2 obj) + length (compEs2 (map Val vs))$ ,  $\text{None}$ ) ( $\text{rev vs} @ [\text{Null}]$ ,  $\text{loc}$ ,  $\text{length (compE2 obj) + length (compEs2 (map Val vs))$ ,  $[\text{addr-of-sys-xcpt NullPointer}]$ )
    by(auto intro:  $\tau_{\text{exec-moveI}} \text{simp add: compP2-def}$ ) }
    also have  $\tau_{\text{move1}} P h$  ( $\text{null} \cdot M'(\text{map Val vs}))$  by(auto simp add:  $\tau_{\text{move1.simps}}$   $\tau_{\text{moves1.simps}}$   $\text{map-eq-append-conv}$ )
    moreover have  $P, obj \cdot M'(\text{map Val vs}), h \vdash (\text{THROW NullPointer}, \text{loc}) \leftrightarrow ((\text{rev vs} @ [\text{Null}]), \text{loc}, \text{length (compE2 obj) + length (compEs2 (map Val vs))}, [\text{addr-of-sys-xcpt NullPointer}])$ 
    by(rule bisim1CallThrow) simp
    ultimately show ?thesis using s by(auto simp del: split-paired-Ex)
  qed
next
  case bisim1Val2 thus ?case by fastforce
next
  case (bisim1New C' n xs)
  have  $\tau: \neg \tau_{\text{move1}} P h$  ( $\text{new C'}$ ) by(auto simp add:  $\tau_{\text{move1.simps}}$   $\tau_{\text{moves1.simps}}$ )
  from  $\langle \text{True}, P, t \vdash 1 \langle \text{new C'}, (h, xs) \rangle -ta\rightarrow \langle e', (h', xs') \rangle \rangle$  show ?case
  proof cases
    case (Red1New a)
    hence  $\text{exec-meth-a (compP2 P) [New C'] [] t h ([], xs, 0, \text{None})} \Downarrow \text{NewHeapElem a (Class-type C')}$ 
     $h' ([\text{Addr a}], xs, \text{Suc } 0, \text{None})$ 
    and  $[\text{simp}]: e' = \text{addr a } xs' = xs \text{ ta} = \Downarrow \text{NewHeapElem a (Class-type C')}$ 
    by (auto intro!:  $\text{exec-instr simp add: compP2-def simp del: fun-upd-apply cong cong del: image-cong-simp}$ )
    moreover have  $P, \text{new C'}, h' \vdash (\text{addr a}, xs) \leftrightarrow ([\text{Addr a}], xs, \text{length (compE2 (new C'))}, \text{None})$ 
    by(rule bisim1Val2)(simp)
    moreover have  $\neg \tau_{\text{move2}} (\text{compP2 P}) h [] (\text{new C'}) 0 \text{None}$  by(simp add:  $\tau_{\text{move2-iff}}$ )
    ultimately show ?thesis using  $\tau$ 
    by(fastforce simp add:  $\text{exec-move-def ta-upd-simps}$ )
  next
    case Red1NewFail
    hence  $\text{exec-meth-a (compP2 P) [New C'] [] t h ([], xs, 0, \text{None})} \varepsilon h' ([], xs, 0, [\text{addr-of-sys-xcpt OutOfMemory}])$ 
    and  $[\text{simp}]: \text{ta} = \varepsilon xs' = xs \text{ e' = THROW OutOfMemory}$ 
    by(auto intro!:  $\text{exec-instr simp add: compP2-def simp del: fun-upd-apply}$ )
    moreover have  $P, \text{new C'}, h' \vdash (\text{THROW OutOfMemory}, xs) \leftrightarrow ([], xs, 0, [\text{addr-of-sys-xcpt OutOfMemory}])$ 
    by(rule bisim1NewThrow)
    moreover have  $\neg \tau_{\text{move2}} (\text{compP2 P}) h [] (\text{new C'}) 0 \text{None}$  by(simp add:  $\tau_{\text{move2-iff}}$ )
    ultimately show ?thesis using  $\tau$  by(fastforce simp add:  $\text{exec-move-def}$ )
  qed
next
  case bisim1NewThrow thus ?case by fastforce
next
  case (bisim1NewArray E n e xs stk loc pc xcp U)
  note  $IH = \text{bisim1NewArray.IH}(2)$ 
  note  $\text{bisim} = \langle P, E, h \vdash (e, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \rangle$ 
  note  $\text{red} = \langle \text{True}, P, t \vdash 1 \langle \text{newA U}[e], (h, xs) \rangle -ta\rightarrow \langle e', (h', xs') \rangle \rangle$ 
  note  $\text{bsok} = \langle \text{bsok (newA U}[E]) \text{ n} \rangle$ 
  from red show ?case
  proof cases
    case (New1ArrayRed ee')
    note  $[\text{simp}] = \langle e' = \text{newA U}[ee'] \rangle$ 

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**and**  $red = \langle True, P, t \vdash 1 \langle e, (h, xs) \rangle - ta \rightarrow \langle ee', (h', xs') \rangle \rangle$   
**from**  $red$  **have**  $\tau move1 P h (newA U[e]) = \tau move1 P h e$  **by**  $(auto simp add: \tau move1.simps \tau moves1.simps)$   
**moreover from**  $red$  **have**  $call1 (newA U[e]) = call1 e$  **by**  $auto$   
**moreover from**  $IH[OF red]$   $bsok$   
**obtain**  $pc'' stk'' loc'' xcp''$  **where**  $bisim: P, E, h' \vdash (ee', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$   
**and**  $redo: ?exec ta E e ee' h stk loc pc xcp h' pc'' stk'' loc'' xcp''$  **by**  $auto$   
**from**  $bisim$   
**have**  $P, newA U[E], h' \vdash (newA U[ee'], xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$   
**by**  $(rule bisim1-bisims1.bisim1NewArray)$   
**moreover** {  
**assume**  $no-call2 E pc$   
**hence**  $no-call2 (newA U[E]) pc$  **by**  $(auto simp add: no-call2-def)$  }  
**ultimately show**  $?thesis$  **using**  $redo$   
**by**  $(auto simp del: call1.simps calls1.simps split: if-split-asm split del: if-split)(blast intro: NewArray-\tau ExecrI NewArray-\tau ExecI exec-move-newArrayI)+$   
**next**  
**case**  $(Red1NewArray i a)$   
**note**  $[simp] = \langle e = Val (Intg i) \rangle \langle ta = \llbracket NewHeapElem a (Array-type U (nat (sint i))) \rrbracket \rangle \langle e' = addr a \rangle \langle xs' = xs \rangle$   
**and**  $new = \langle (h', a) \in allocate h (Array-type U (nat (sint i))) \rangle$   
**from**  $bisim$  **have**  $s: xcp = None xs = loc$  **by**  $(auto dest: bisim-Val-loc-eq-xcp-None)$   
**from**  $bisim$  **have**  $\tau Exec-mover-a P t E h (stk, loc, pc, xcp) ([Intg i], loc, length (compE2 E), None)$   
**by**  $(auto dest: bisim1Val2D1)$   
**hence**  $\tau Exec-mover-a P t (newA U[E]) h (stk, loc, pc, xcp) ([Intg i], loc, length (compE2 E), None)$   
**by**  $(rule NewArray-\tau ExecrI)$   
**moreover from**  $new \langle 0 \leq s i \rangle$   
**have**  $exec-move-a P t (newA U[E]) h ([Intg i], loc, length (compE2 E), None) \llbracket NewHeapElem a (Array-type U (nat (sint i))) \rrbracket h' ([Addr a], loc, Suc (length (compE2 E)), None)$   
**by**  $(auto intro!: exec-instr simp add: compP2-def exec-move-def cong del: image-cong-simp)$   
**moreover have**  $\tau move2 (compP2 P) h [Intg i] (newA U[E]) (length (compE2 E)) None \implies False$  **by**  $(simp add: \tau move2-iff)$   
**moreover have**  $\neg \tau move1 P h (newA U[Val (Intg i)])$  **by**  $(auto simp add: \tau move1.simps \tau moves1.simps)$   
**moreover have**  $P, newA U[E], h' \vdash (addr a, loc) \leftrightarrow ([Addr a], loc, length (compE2 (newA U[E])), None)$   
**by**  $(rule bisim1Val2) simp$   
**ultimately show**  $?thesis$  **using**  $s$  **by**  $(auto simp del: fun-upd-apply simp add: ta-upd-simps) blast$   
**next**  
**case**  $(Red1NewArrayNegative i)$   
**note**  $[simp] = \langle e = Val (Intg i) \rangle \langle e' = THROW NegativeArraySize \rangle \langle h' = h \rangle \langle xs' = xs \rangle \langle ta = \varepsilon \rangle$   
**have**  $\neg \tau move1 P h (newA U[Val (Intg i)])$  **by**  $(auto simp add: \tau move1.simps \tau moves1.simps)$   
**moreover from**  $bisim$  **have**  $s: xcp = None xs = loc$   
**and**  $\tau Exec-mover-a P t E h (stk, loc, pc, xcp) ([Intg i], loc, length (compE2 E), None)$   
**by**  $(auto dest: bisim1Val2D1)$   
**moreover from**  $\langle i < s 0 \rangle$   
**have**  $exec-meth-a (compP2 P) (compE2 (newA U[E])) (compE2 (newA U[E]) 0 0) t h ([Intg i], loc, length (compE2 E), None) \varepsilon h ([Intg i], loc, length (compE2 E), [addr-of-sys-xcpt NegativeArraySize])$   
**by**  $-(rule exec-instr, auto simp add: compP2-def)$   
**moreover have**  $\tau move2 (compP2 P) h [Intg i] (newA U[E]) (length (compE2 E)) None \implies False$  **by**  $(simp add: \tau move2-iff)$   
**moreover**  
**have**  $P, newA U[E], h \vdash (THROW NegativeArraySize, loc) \leftrightarrow ([Intg i], loc, length (compE2 E),$

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[addr-of-sys-xcpt NegativeArraySize]
  by(auto intro!: bisim1-bisims1.bisim1NewArrayFail)
  ultimately show ?thesis using s
  by(auto simp add: exec-move-def)(blast intro: NewArray-τExecrI)
next
case (Red1NewArrayFail i)
note [simp] = ⟨e = Val (Intg i)⟩ ⟨e' = THROW OutOfMemory⟩ ⟨xs' = xs⟩ ⟨ta = ε⟩ ⟨h' = h⟩
  and new = ⟨allocate h (Array-type U (nat (sint i))) = {}⟩
have ¬ τmove1 P h (newA U [Val (Intg i)]) by(auto simp add: τmove1.simps τmoves1.simps)
moreover from bisim have s: xcp = None xs = loc
  and τExec-mover-a P t E h (stk, loc, pc, xcp) ([Intg i], loc, length (compE2 E), None)
  by(auto dest: bisim1Val2D1)
moreover from ⟨0 ≤ s i⟩ new
have exec-meth-a (compP2 P) (compE2 (newA U [E])) (compxE2 (newA U [E]) 0 0) t h ([Intg i],
loc, length (compE2 E), None) ε h' ([Intg i], loc, length (compE2 E), [addr-of-sys-xcpt OutOfMemory])
  by -(rule exec-instr, auto simp add: compP2-def)
moreover have τmove2 (compP2 P) h [Intg i] (newA U [E]) (length (compE2 E)) None ⇒
False by(simp add: τmove2-iff)
moreover
  have P, newA U [E], h' ⊢ (THROW OutOfMemory, loc) ↔ ([Intg i], loc, length (compE2 E),
[addr-of-sys-xcpt OutOfMemory])
  by(auto intro!: bisim1-bisims1.bisim1NewArrayFail)
  ultimately show ?thesis using s by (auto simp add: exec-move-def)(blast intro: NewArray-τExecrI)
next
case (New1ArrayThrow a)
note [simp] = ⟨e = Throw a⟩ ⟨h' = h⟩ ⟨xs' = xs⟩ ⟨ta = ε⟩ ⟨e' = Throw a⟩
have τ: τmove1 P h (newA U [e]) by(auto intro: τmove1NewArrayThrow)
from bisim have xcp = [a] ∨ xcp = None by(auto dest: bisim1-ThrowD)
thus ?thesis
proof
  assume [simp]: xcp = [a]
  with bisim have P, newA U [E], h ⊢ (Throw a, xs) ↔ (stk, loc, pc, xcp)
  by(auto intro: bisim1-bisims1.bisim1NewArrayThrow)
  thus ?thesis using τ by(fastforce)
next
  assume [simp]: xcp = None
  with bisim obtain pc'
  where τExec-mover-a P t E h (stk, loc, pc, None) ([Addr a], loc, pc', [a])
  and bisim': P, E, h ⊢ (Throw a, xs) ↔ ([Addr a], loc, pc', [a]) and [simp]: xs = loc
  by(auto dest: bisim1-Throw-τExec-mover)
  hence τExec-mover-a P t (newA U [E]) h (stk, loc, pc, None) ([Addr a], loc, pc', [a])
  by-(rule NewArray-τExecrI)
  moreover from bisim' have P, newA U [E], h ⊢ (Throw a, xs) ↔ ([Addr a], loc, pc', [a])
  by(rule bisim1-bisims1.bisim1NewArrayThrow)
  ultimately show ?thesis using τ by auto
qed
qed
next
case bisim1NewArrayThrow thus ?case by auto
next
case bisim1NewArrayFail thus ?case by auto
next
case (bisim1Cast E n e xs stk loc pc xcp U)
note IH = bisim1Cast.IH(2)

```



**note**  $\text{bisim} = \langle P, E, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$   
**note**  $\text{red} = \langle \text{True}, P, t \vdash 1 \langle \text{Cast } U \ e, (h, xs) \rangle -ta\rightarrow \langle e', (h', xs') \rangle \rangle$   
**note**  $\text{bsok} = \langle \text{bsok } (\text{Cast } U \ E) \ n \rangle$   
**from**  $\text{red}$  **show**  $?case$   
**proof**  $cases$   
**case**  $(\text{Cast1Red } ee')$   
**note**  $[simp] = \langle e' = \text{Cast } U \ ee' \rangle$   
**and**  $\text{red} = \langle \text{True}, P, t \vdash 1 \langle e, (h, xs) \rangle -ta\rightarrow \langle ee', (h', xs') \rangle \rangle$   
**from**  $\text{red}$  **have**  $\tau\text{move1 } P \ h \ (\text{Cast } U \ e) = \tau\text{move1 } P \ h \ e$  **by**  $(\text{auto simp add: } \tau\text{move1.simps } \tau\text{moves1.simps})$   
**moreover from**  $\text{red}$  **have**  $\text{call1 } (\text{Cast } U \ e) = \text{call1 } e$  **by**  $\text{auto}$   
**moreover from**  $IH[OF \ \text{red}] \ \text{bsok}$   
**obtain**  $pc'' \ stk'' \ loc'' \ xcp''$  **where**  $\text{bisim}: P, E, h' \vdash (ee', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$   
**and**  $\text{redo}: ?exec \ ta \ E \ e \ ee' \ h \ stk \ loc \ pc \ xcp \ h' \ pc'' \ stk'' \ loc'' \ xcp''$  **by**  $\text{auto}$   
**from**  $\text{bisim}$   
**have**  $P, \text{Cast } U \ E, h' \vdash (\text{Cast } U \ ee', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$   
**by**  $(\text{rule } \text{bisim1-bisims1.bisim1Cast})$   
**moreover** {  
**assume**  $\text{no-call2 } E \ pc$   
**hence**  $\text{no-call2 } (\text{Cast } U \ E) \ pc$  **by**  $(\text{auto simp add: no-call2-def})$  }  
**ultimately show**  $?thesis$  **using**  $\text{redo}$   
**by**  $(\text{auto simp del: call1.simps calls1.simps split: if-split-asm split del: if-split})(\text{blast intro: Cast-}\tau\text{ExecrI Cast-}\tau\text{ExectI exec-move-CastI})+$   
**next**  
**case**  $(\text{Red1Cast } c \ U')$   
**hence**  $[simp]: e = \text{Val } c \ ta = \varepsilon \ e' = \text{Val } c \ h' = h \ xs' = xs$   
**and**  $\text{type: typeof}_h \ c = \lfloor U' \rfloor \ P \vdash U' \leq U$  **by**  $\text{auto}$   
**from**  $\text{bisim}$  **have**  $s: xcp = \text{None} \ xs = loc$  **by**  $(\text{auto dest: bisim-Val-loc-eq-xcp-None})$   
**from**  $\text{bisim}$  **have**  $\tau\text{Exec-mover-a } P \ t \ E \ h \ (stk, loc, pc, xcp) ([c], loc, \text{length } (\text{compE2 } E), \text{None})$   
**by**  $(\text{auto dest: bisim1Val2D1})$   
**hence**  $\tau\text{Exec-mover-a } P \ t \ (\text{Cast } U \ E) \ h \ (stk, loc, pc, xcp) ([c], loc, \text{length } (\text{compE2 } E), \text{None})$   
**by**  $(\text{rule Cast-}\tau\text{ExecrI})$   
**moreover from**  $\text{type}$   
**have**  $\text{exec-meth-a } (\text{compP2 } P) \ (\text{compE2 } (\text{Cast } U \ E)) \ (\text{compxE2 } (\text{Cast } U \ E) \ 0 \ 0) \ t \ h \ ([c], loc, \text{length } (\text{compE2 } E), \text{None}) \varepsilon \ h' \ ([c], loc, \text{Suc } (\text{length } (\text{compE2 } E)), \text{None})$   
**by**  $(\text{auto intro!: exec-instr simp add: compP2-def})$   
**moreover have**  $\tau\text{move2 } (\text{compP2 } P) \ h \ [c] \ (\text{Cast } U \ E) \ (\text{length } (\text{compE2 } E)) \ \text{None}$  **by**  $(\text{simp add: } \tau\text{move2-iff})$   
**ultimately have**  $\tau\text{Exec-mover-a } P \ t \ (\text{Cast } U \ E) \ h \ (stk, loc, pc, xcp) ([c], loc, \text{Suc } (\text{length } (\text{compE2 } E)), \text{None})$   
**by**  $(\text{fastforce elim: rtranclp.rtrancl-into-rtrancl intro: } \tau\text{exec-moveI simp add: exec-move-def compP2-def})$   
**moreover have**  $\tau\text{move1 } P \ h \ (\text{Cast } U \ (\text{Val } c))$  **by**  $(\text{rule } \tau\text{move1CastRed})$   
**moreover**  
**have**  $P, \text{Cast } U \ E, h' \vdash (\text{Val } c, loc) \leftrightarrow ([c], loc, \text{length } (\text{compE2 } (\text{Cast } U \ E)), \text{None})$   
**by**  $(\text{rule bisim1Val2}) \ \text{simp}$   
**ultimately show**  $?thesis$  **using**  $s$  **by**  $(\text{auto simp add: exec-move-def})$   
**next**  
**case**  $(\text{Red1CastFail } v \ U')$   
**note**  $[simp] = \langle e = \text{Val } v \rangle \langle e' = \text{THROW ClassCast} \rangle \langle h' = h \rangle \langle xs' = xs \rangle \langle ta = \varepsilon \rangle$   
**moreover from**  $\text{bisim}$  **have**  $s: xcp = \text{None} \ xs = loc$   
**and**  $\tau\text{Exec-mover-a } P \ t \ E \ h \ (stk, loc, pc, xcp) ([v], loc, \text{length } (\text{compE2 } E), \text{None})$   
**by**  $(\text{auto dest: bisim1Val2D1})$   
**hence**  $\tau\text{Exec-mover-a } P \ t \ (\text{Cast } U \ E) \ h \ (stk, loc, pc, xcp) ([v], loc, \text{length } (\text{compE2 } E), \text{None})$   
**by**  $(\text{auto elim: Cast-}\tau\text{ExecrI})$

```

moreover from  $\langle \text{typeof}_{hp} (h, xs) \ v = \lfloor U' \rfloor \rangle \langle \neg P \vdash U' \leq U \rangle$ 
have  $\text{exec-meth-a} \ (compP2 \ P) \ (compE2 \ (Cast \ U \ E)) \ (compxE2 \ (Cast \ U \ E) \ 0 \ 0) \ t \ h \ ([v], \text{loc},$ 
 $\text{length} \ (compE2 \ E), \text{None}) \varepsilon \ h \ ([v], \text{loc}, \text{length} \ (compE2 \ E), \lfloor \text{addr-of-sys-xcpt} \ \text{ClassCast} \rfloor)$ 
by  $\neg(\text{rule} \ \text{exec-instr}, \text{auto} \ \text{simp} \ \text{add:} \ compP2\text{-def})$ 
moreover have  $\tau\text{move2} \ (compP2 \ P) \ h \ [v] \ (Cast \ U \ E) \ (\text{length} \ (compE2 \ E)) \ \text{None} \ \text{by}(\text{simp} \ \text{add:}$ 
 $\tau\text{move2-iff})$ 
ultimately have  $\tau\text{Exec-movet-a} \ P \ t \ (Cast \ U \ E) \ h \ (stk, \text{loc}, pc, xcp) \ ([v], \text{loc}, \text{length} \ (compE2 \ E),$ 
 $\lfloor \text{addr-of-sys-xcpt} \ \text{ClassCast} \rfloor)$ 
by( $\text{fastforce} \ \text{simp} \ \text{add:} \ \text{exec-move-def} \ compP2\text{-def} \ \text{intro:} \ r\text{tranc1p-into-tranc1p1} \ \tau\text{exec-moveI}$ )
moreover have  $\tau\text{move1} \ P \ h \ (Cast \ U \ (\text{Val} \ v)) \ \text{by}(\text{rule} \ \tau\text{move1CastRed})$ 
moreover
have  $P, Cast \ U \ E, h \vdash (THROW \ \text{ClassCast}, \text{loc}) \leftrightarrow ([v], \text{loc}, \text{length} \ (compE2 \ E), \lfloor \text{addr-of-sys-xcpt}$ 
 $\text{ClassCast} \rfloor)$ 
by( $\text{auto} \ \text{intro!} \text{:} \ bisim1\text{-bisims1}.\text{bisim1CastFail}$ )
ultimately show  $?thesis \ \text{using} \ s \ \text{by}(\text{auto} \ \text{simp} \ \text{add:} \ \text{exec-move-def})$ 
next
case  $[simp] \text{:} \ (Cast1Throw \ a)$ 
have  $\tau \text{:} \ \tau\text{move1} \ P \ h \ (Cast \ U \ e) \ \text{by}(\text{auto} \ \text{intro:} \ \tau\text{move1CastThrow})$ 
from  $bisim \ \text{have} \ xcp = \lfloor a \rfloor \vee xcp = \text{None} \ \text{by}(\text{auto} \ \text{dest:} \ bisim1\text{-ThrowD})$ 
thus  $?thesis$ 
proof
assume  $[simp] \text{:} \ xcp = \lfloor a \rfloor$ 
with  $bisim \ \text{have} \ P, Cast \ U \ E, h \vdash (Throw \ a, xs) \leftrightarrow (stk, \text{loc}, pc, xcp)$ 
by( $\text{auto} \ \text{intro:} \ bisim1\text{-bisims1}.\text{bisim1CastThrow}$ )
thus  $?thesis \ \text{using} \ \tau \ \text{by}(\text{fastforce})$ 
next
assume  $[simp] \text{:} \ xcp = \text{None}$ 
with  $bisim \ \text{obtain} \ pc'$ 
where  $\tau\text{Exec-mover-a} \ P \ t \ E \ h \ (stk, \text{loc}, pc, \text{None}) \ ([Addr \ a], \text{loc}, pc', \lfloor a \rfloor)$ 
and  $bisim' \text{:} \ P, E, h \vdash (Throw \ a, xs) \leftrightarrow ([Addr \ a], \text{loc}, pc', \lfloor a \rfloor) \ \text{and} \ [simp] \text{:} \ xs = \text{loc}$ 
by( $\text{auto} \ \text{dest:} \ bisim1\text{-Throw-}\tau\text{Exec-mover}$ )
hence  $\tau\text{Exec-mover-a} \ P \ t \ (Cast \ U \ E) \ h \ (stk, \text{loc}, pc, \text{None}) \ ([Addr \ a], \text{loc}, pc', \lfloor a \rfloor)$ 
by-( $\text{rule} \ Cast\text{-}\tau\text{ExecrI}$ )
moreover from  $bisim' \ \text{have} \ P, Cast \ U \ E, h \vdash (Throw \ a, xs) \leftrightarrow ([Addr \ a], \text{loc}, pc', \lfloor a \rfloor)$ 
by-( $\text{rule} \ bisim1\text{-bisims1}.\text{bisim1CastThrow}, \text{auto}$ )
ultimately show  $?thesis \ \text{using} \ \tau \ \text{by} \ \text{auto}$ 
qed
qed
next
case  $bisim1CastThrow \ \text{thus} \ ?case \ \text{by} \ \text{auto}$ 
next
case  $bisim1CastFail \ \text{thus} \ ?case \ \text{by} \ \text{auto}$ 
next
case  $(bisim1InstanceOf \ E \ n \ e \ xs \ stk \ \text{loc} \ pc \ xcp \ U)$ 
note  $IH = bisim1InstanceOf(2)$ 
note  $bisim = \langle P, E, h \vdash (e, xs) \leftrightarrow (stk, \text{loc}, pc, xcp) \rangle$ 
note  $red = \langle \text{True}, P, t \vdash 1 \ \langle e \ \text{instanceof} \ U, (h, xs) \rangle \rightarrow \langle e', (h', xs') \rangle \rangle$ 
note  $bsok = \langle bsok \ (E \ \text{instanceof} \ U) \ n \rangle$ 
from  $red \ \text{show} \ ?case$ 
proof  $cases$ 
case  $(InstanceOf1Red \ ee')$ 
note  $[simp] = \langle e' = ee' \ \text{instanceof} \ U \rangle$ 
and  $red = \langle \text{True}, P, t \vdash 1 \ \langle e, (h, xs) \rangle \rightarrow \langle ee', (h', xs') \rangle \rangle$ 
from  $red \ \text{have} \ \tau\text{move1} \ P \ h \ (e \ \text{instanceof} \ U) = \tau\text{move1} \ P \ h \ e \ \text{by}(\text{auto} \ \text{simp} \ \text{add:} \ \tau\text{move1}.\text{simps})$ 

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τmoves1.simps)
moreover from red have call1 (e instanceof U) = call1 e by auto
moreover from IH[OF red] bsok
obtain pc'' stk'' loc'' xcp'' where bisim: P,E,h' ⊢ (ee', xs') ↔ (stk'', loc'', pc'', xcp'')
  and redo: ?exec ta E e ee' h stk loc pc xcp h' pc'' stk'' loc'' xcp'' by auto
from bisim
have P,E instanceof U,h' ⊢ (ee' instanceof U, xs') ↔ (stk'', loc'', pc'', xcp'')
  by(rule bisim1-bisims1.bisim1InstanceOf)
moreover {
  assume no-call2 E pc
  hence no-call2 (E instanceof U) pc by(auto simp add: no-call2-def) }
ultimately show ?thesis using redo
  by(auto simp del: call1.simps calls1.simps split: if-split-asm split del: if-split)(blast intro: InstanceOf-τExecrI InstanceOf-τExecI exec-move-InstanceOfI)+
next
case (Red1InstanceOf c U' b)
hence [simp]: e = Val c ta = ε e' = Val (Bool (c ≠ Null ∧ P ⊢ U' ≤ U)) h' = h xs' = xs
  b = (c ≠ Null ∧ P ⊢ U' ≤ U)
  and type: typeofh c = [U'] by auto
from bisim have s: xcp = None xs = loc by(auto dest: bisim-Val-loc-eq-xcp-None)
from bisim have τExec-mover-a P t E h (stk, loc, pc, xcp) ([c], loc, length (compE2 E), None)
  by(auto dest: bisim1Val2D1)
hence τExec-mover-a P t (E instanceof U) h (stk, loc, pc, xcp) ([c], loc, length (compE2 E), None)
  by(rule InstanceOf-τExecrI)
moreover from type
  have exec-meth-a (compP2 P) (compE2 (E instanceof U)) (compxE2 (E instanceof U) 0 0) t h
    ([c], loc, length (compE2 E), None) ε h' ([Bool b], loc, Suc (length (compE2 E)), None)
  by(auto intro!: exec-instr simp add: compP2-def)
  moreover have τmove2 (compP2 P) h [c] (E instanceof U) (length (compE2 E)) None by(simp
    add: τmove2-iff)
  ultimately have τExec-mover-a P t (E instanceof U) h (stk, loc, pc, xcp) ([Bool b], loc, Suc (length
    (compE2 E)), None)
  by(fastforce elim: rtranclp.rtrancl-into-rtrancl intro: τexec-moveI simp add: exec-move-def compP2-def)
  moreover have τmove1 P h ((Val c) instanceof U) by(rule τmove1InstanceOfRed)
  moreover
  have P, E instanceof U, h' ⊢ (Val (Bool b), loc) ↔ ([Bool b], loc, length (compE2 (E instanceof
    U)), None)
  by(rule bisim1Val2) simp
  ultimately show ?thesis using s by(auto simp add: exec-move-def)
next
case (InstanceOf1Throw a)
note [simp] = ⟨e = Throw a⟩ ⟨h' = h⟩ ⟨xs' = xs⟩ ⟨ta = ε⟩ ⟨e' = Throw a⟩
have τ: τmove1 P h (e instanceof U) by(auto intro: τmove1InstanceOfThrow)
from bisim have xcp = [a] ∨ xcp = None by(auto dest: bisim1-ThrowD)
thus ?thesis
proof
  assume [simp]: xcp = [a]
  with bisim have P,E instanceof U, h ⊢ (Throw a, xs) ↔ (stk, loc, pc, xcp)
  by(auto intro: bisim1-bisims1.bisim1InstanceOfThrow)
  thus ?thesis using τ by(fastforce)
next
  assume [simp]: xcp = None
  with bisim obtain pc'
  where τExec-mover-a P t E h (stk, loc, pc, None) ([Addr a], loc, pc', [a])

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    and  $\text{bisim}'$ :  $P, E, h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], \text{loc}, \text{pc}', [a])$  and  $[\text{simp}]$ :  $xs = \text{loc}$ 
    by(auto dest:  $\text{bisim1-Throw-}\tau\text{Exec-mover}$ )
  hence  $\tau\text{Exec-mover-a } P \ t \ (E \text{ instanceof } U) \ h \ (stk, \text{loc}, \text{pc}, \text{None}) \ ([\text{Addr } a], \text{loc}, \text{pc}', [a])$ 
  by-(rule  $\text{InstanceOf-}\tau\text{ExecrI}$ )
  moreover from  $\text{bisim}'$  have  $P, E \text{ instanceof } U, h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], \text{loc}, \text{pc}', [a])$ 
  by-(rule  $\text{bisim1-bisims1.bisim1InstanceOfThrow, auto}$ )
  ultimately show ?thesis using  $\tau$  by auto
qed
qed
next
  case  $\text{bisim1InstanceOfThrow}$  thus ?case by auto
next
  case  $\text{bisim1Val}$  thus ?case by fastforce
next
  case ( $\text{bisim1Var } V \ n \ xs$ )
  from  $\langle \text{True}, P, t \vdash 1 \ \langle \text{Var } V, (h, xs) \rangle -ta\rightarrow \langle e', (h', xs') \rangle \rangle$  show ?case
  proof cases
    case ( $\text{Red1Var } v$ )
    hence  $\text{exec-meth-a } (\text{compP2 } P) \ [\text{Load } V] \ [] \ t \ h \ ([], xs, 0, \text{None}) \in h \ ([v], xs, 1, \text{None})$ 
    and  $[\text{simp}]$ :  $ta = \varepsilon \ h' = h \ xs' = xs \ e' = \text{Val } v$ 
    by(auto intro:  $\text{exec-instr}$ )
    moreover have  $\tau\text{move2 } (\text{compP2 } P) \ h \ [] \ (\text{Var } V) \ 0 \ \text{None}$  by( $\text{simp add: } \tau\text{move2-iff}$ )
    ultimately have  $\tau\text{Exec-movet-a } P \ t \ (\text{Var } V) \ h \ ([], xs, 0, \text{None}) \ ([v], xs, 1, \text{None})$ 
    by(auto intro:  $\tau\text{Exec1step simp add: exec-move-def compP2-def}$ )
    moreover have  $P, \text{Var } V, h \vdash (\text{Val } v, xs) \leftrightarrow ([v], xs, \text{length } (\text{compE2 } (\text{Var } V)), \text{None})$ 
    by(rule  $\text{bisim1Val2}$ ) simp
    moreover have  $\tau\text{move1 } P \ h \ (\text{Var } V)$  by(rule  $\tau\text{move1Var}$ )
    ultimately show ?thesis by(fastforce)
  qed
next
  case ( $\text{bisim1BinOp1 } e1 \ n \ e1' \ xs \ stk \ \text{loc} \ \text{pc} \ \text{xcp} \ e2 \ \text{bop}$ )
  note  $\text{IH1} = \text{bisim1BinOp1.IH}(2)$ 
  note  $\text{IH2} = \text{bisim1BinOp1.IH}(4)$ 
  note  $\text{bisim1} = \langle P, e1, h \vdash (e1', xs) \leftrightarrow (stk, \text{loc}, \text{pc}, \text{xcp}) \rangle$ 
  note  $\text{bisim2} = \langle \bigwedge xs. P, e2, h \vdash (e2, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$ 
  note  $\text{bsok} = \langle \text{bsok } (e1 \ll \text{bop} \gg e2) \ n \rangle$ 
  from  $\langle \text{True}, P, t \vdash 1 \ \langle e1' \ll \text{bop} \gg e2, (h, xs) \rangle -ta\rightarrow \langle e', (h', xs') \rangle \rangle$  show ?case
  proof cases
    case ( $\text{Bin1OpRed1 } E'$ )
    note  $[\text{simp}] = \langle e' = E' \ll \text{bop} \gg e2 \rangle$ 
    and  $\text{red} = \langle \text{True}, P, t \vdash 1 \ \langle e1', (h, xs) \rangle -ta\rightarrow \langle E', (h', xs') \rangle \rangle$ 
    from  $\text{red}$  have  $\tau\text{move1 } P \ h \ (e1' \ll \text{bop} \gg e2) = \tau\text{move1 } P \ h \ e1'$  by(auto simp add:  $\tau\text{move1.simps}$ 
 $\tau\text{moves1.simps}$ )
    moreover from  $\text{red}$  have  $\text{call1 } (e1' \ll \text{bop} \gg e2) = \text{call1 } e1'$  by auto
    moreover from  $\text{IH1}[\text{OF red}] \ \text{bsok}$ 
    obtain  $\text{pc}'' \ \text{stk}'' \ \text{loc}'' \ \text{xcp}''$  where  $\text{bisim}$ :  $P, e1, h' \vdash (E', xs') \leftrightarrow (\text{stk}'', \text{loc}'', \text{pc}'', \text{xcp}'')$ 
    and  $\text{redo}$ :  $?exec \ ta \ e1 \ e1' \ E' \ h \ stk \ \text{loc} \ \text{pc} \ \text{xcp} \ h' \ \text{pc}'' \ \text{stk}'' \ \text{loc}'' \ \text{xcp}''$  by auto
    from  $\text{bisim}$ 
    have  $P, e1 \ll \text{bop} \gg e2, h' \vdash (E' \ll \text{bop} \gg e2, xs') \leftrightarrow (\text{stk}'', \text{loc}'', \text{pc}'', \text{xcp}'')$ 
    by(rule  $\text{bisim1-bisims1.bisim1BinOp1}$ )
    moreover {
      assume  $\text{no-call2 } e1 \ \text{pc}$ 
      hence  $\text{no-call2 } (e1 \ll \text{bop} \gg e2) \ \text{pc} \vee \text{pc} = \text{length } (\text{compE2 } e1)$  by(auto simp add:  $\text{no-call2-def}$ ) }
    ultimately show ?thesis using  $\text{redo}$ 
  
```

$\text{by}(\text{auto simp del: call1.simps calls1.simps split: if-split-asm split del: if-split})(\text{blast intro: BinOp-}\tau\text{ExecrI1 BinOp-}\tau\text{ExectI1 exec-move-BinOpI1})+$   
**next**  
**case** ( $\text{Bin1OpRed2 } E' v$ )  
**note**  $[simp] = \langle e1' = \text{Val } v \rangle \langle e' = \text{Val } v \llbracket \text{bop} \rrbracket E' \rangle$   
**and**  $\text{red} = \langle \text{True}, P, t \vdash 1 \langle e2, (h, xs) \rangle -ta \rightarrow \langle E', (h', xs') \rangle \rangle$   
**from**  $\text{red have } \tau: \tau \text{move1 } P h (\text{Val } v \llbracket \text{bop} \rrbracket e2) = \tau \text{move1 } P h e2$  **by** ( $\text{auto simp add: } \tau \text{move1.simps } \tau \text{moves1.simps}$ )  
**from**  $\text{bisim1 have } s: xcp = \text{None } xs = \text{loc}$   
**and**  $\text{exec1: } \tau \text{Exec-mover-a } P t e1 h (stk, loc, pc, \text{None}) ([v], xs, \text{length } (compE2 e1), \text{None})$   
**by** ( $\text{auto dest: bisim1Val2D1}$ )  
**from**  $\text{exec1 have } \tau \text{Exec-mover-a } P t (e1 \llbracket \text{bop} \rrbracket e2) h (stk, loc, pc, \text{None}) ([v], xs, \text{length } (compE2 e1), \text{None})$   
**by** ( $\text{rule BinOp-}\tau\text{ExecrI1}$ )  
**moreover**  
**from**  $\text{IH2}[OF \text{red}] \text{bsok obtain } pc'' stk'' loc'' xcp''$   
**where**  $\text{bisim': } P, e2, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$   
**and**  $\text{exec': } ?\text{exec } ta e2 e2 E' h \square xs 0 \text{None } h' pc'' stk'' loc'' xcp''$  **by**  $\text{auto}$   
**have**  $?exec \text{ ta } (e1 \llbracket \text{bop} \rrbracket e2) (\text{Val } v \llbracket \text{bop} \rrbracket e2) (\text{Val } v \llbracket \text{bop} \rrbracket E') h (\square @ [v]) xs (\text{length } (compE2 e1) + 0) \text{None } h' (\text{length } (compE2 e1) + pc'') (stk'' @ [v]) loc'' xcp''$   
**proof** ( $\text{cases } \tau \text{move1 } P h (\text{Val } v \llbracket \text{bop} \rrbracket e2)$ )  
**case**  $\text{True}$   
**with**  $\text{exec' } \tau$  **have**  $[simp]: h = h'$  **and**  $e: \text{sim-move } e2 E' P t e2 h (\square, xs, 0, \text{None}) (stk'', loc'', pc'', xcp'')$  **by**  $\text{auto}$   
**from**  $e$  **have**  $\text{sim-move } (\text{Val } v \llbracket \text{bop} \rrbracket e2) (\text{Val } v \llbracket \text{bop} \rrbracket E') P t (e1 \llbracket \text{bop} \rrbracket e2) h (\square @ [v], xs, \text{length } (compE2 e1) + 0, \text{None}) (stk'' @ [v], loc'', \text{length } (compE2 e1) + pc'', xcp'')$   
**by** ( $\text{fastforce dest: BinOp-}\tau\text{ExecrI2 BinOp-}\tau\text{ExectI2}$ )  
**with**  $\text{True show ?thesis}$  **by**  $\text{auto}$   
**next**  
**case**  $\text{False}$   
**with**  $\text{exec' } \tau$  **obtain**  $pc' stk' loc' xcp'$   
**where**  $e: \tau \text{Exec-mover-a } P t e2 h (\square, xs, 0, \text{None}) (stk', loc', pc', xcp')$   
**and**  $e': \text{exec-move-a } P t e2 h (stk', loc', pc', xcp') (\text{extTA2JVM } (compP2 P) ta) h' (stk'', loc'', pc'', xcp'')$   
**and**  $\tau': \neg \tau \text{move2 } (compP2 P) h stk' e2 pc' xcp'$   
**and**  $\text{call: call1 } e2 = \text{None} \vee \text{no-call2 } e2 0 \vee pc' = 0 \wedge stk' = \square \wedge loc' = xs \wedge xcp' = \text{None}$  **by**  $\text{auto}$   
**from**  $e$  **have**  $\tau \text{Exec-mover-a } P t (e1 \llbracket \text{bop} \rrbracket e2) h (\square @ [v], xs, \text{length } (compE2 e1) + 0, \text{None}) (stk' @ [v], loc', \text{length } (compE2 e1) + pc', xcp')$  **by** ( $\text{rule BinOp-}\tau\text{ExecrI2}$ )  
**moreover from**  $e'$  **have**  $\text{exec-move-a } P t (e1 \llbracket \text{bop} \rrbracket e2) h (stk' @ [v], loc', \text{length } (compE2 e1) + pc', xcp') (\text{extTA2JVM } (compP2 P) ta) h' (stk'' @ [v], loc'', \text{length } (compE2 e1) + pc'', xcp'')$   
**by** ( $\text{rule exec-move-BinOpI2}$ )  
**moreover from**  $e'$  **have**  $pc' < \text{length } (compE2 e2)$  **by**  $\text{auto}$   
**with**  $\tau' e'$  **have**  $\neg \tau \text{move2 } (compP2 P) h (stk' @ [v]) (e1 \llbracket \text{bop} \rrbracket e2) (\text{length } (compE2 e1) + pc')$   
 $xcp'$   
**by** ( $\text{auto simp add: } \tau \text{instr-stk-drop-exec-move } \tau \text{move2-iff}$ )  
**moreover from**  $\text{red have call1 } (e1' \llbracket \text{bop} \rrbracket e2) = \text{call1 } e2$  **by** ( $\text{auto}$ )  
**moreover have**  $\text{no-call2 } e2 0 \implies \text{no-call2 } (e1 \llbracket \text{bop} \rrbracket e2) (\text{length } (compE2 e1))$   
**by** ( $\text{auto simp add: no-call2-def}$ )  
**ultimately show ?thesis using False call**  
**by** ( $\text{auto simp del: split-paired-Ex call1.simps calls1.simps}$ ) **blast**  
**qed**  
**moreover from**  $\text{bisim'}$   
**have**  $P, e1 \llbracket \text{bop} \rrbracket e2, h' \vdash (\text{Val } v \llbracket \text{bop} \rrbracket E', xs') \leftrightarrow ((stk'' @ [v]), loc'', \text{length } (compE2 e1) + pc'',$

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xcp'')
  by(rule bisim1-bisims1.bisim1BinOp2)
  moreover from bisim1 have pc ≠ length (compE2 e1) ⟶ no-call2 (e1 «bop» e2) pc
  by(auto simp add: no-call2-def dest: bisim-Val-pc-not-Invoke bisim1-pc-length-compE2)
  ultimately show ?thesis using τ exec1 s
  apply(auto simp del: split-paired-Ex call1.simps calls1.simps split: if-split-asm split del: if-split)
  apply(blast intro: τExec-mover-trans|fastforce elim!: τExec-mover-trans simp del: split-paired-Ex
call1.simps calls1.simps)+
  done
next
case (Red1BinOp v1 v2 v)
note [simp] = ⟨e1' = Val v1⟩ ⟨e2 = Val v2⟩ ⟨ta = ε⟩ ⟨e' = Val v⟩ ⟨h' = h⟩ ⟨xs' = xs⟩
  and binop = ⟨binop bop v1 v2 = [Inl v]⟩
have τ: τmove1 P h (Val v1 «bop» Val v2) by(rule τmove1BinOp)
from bisim1 have s: xcp = None xs = loc
  and τExec-mover-a P t e1 h (stk, loc, pc, xcp) ([v1], loc, length (compE2 e1), None)
  by(auto dest: bisim1Val2D1)
hence τExec-mover-a P t (e1 «bop» Val v2) h (stk, loc, pc, xcp) ([v1], loc, length (compE2 e1),
None)
  by-(rule BinOp-τExecrI1)
also have τmove2 (compP2 P) h [v1] (e1 «bop» Val v2) (length (compE2 e1) + 0) None
  by(rule τmove2BinOp2)(rule τmove2Val)
  with binop have τExec-mover-a P t (e1 «bop» Val v2) h ([v1], loc, length (compE2 e1), None)
([v2, v1], loc, Suc (length (compE2 e1)), None)
  by-(rule τExecr1step, auto intro!: exec-instr simp add: exec-move-def compP2-def)
also (rtranclp-trans) from binop
have exec-meth-a (compP2 P) (compE2 (e1 «bop» Val v2)) (compE2 (e1 «bop» Val v2) 0 0) t
  h ([v2, v1], loc, Suc (length (compE2 e1)), None) ε
  h ([v], loc, Suc (Suc (length (compE2 e1))), None)
  by-(rule exec-instr, auto)
moreover have τmove2 (compP2 P) h [v2, v1] (e1 «bop» Val v2) (Suc (length (compE2 e1)))
None by(simp add: τmove2-iff)
ultimately have τExec-mover-a P t (e1 «bop» Val v2) h (stk, loc, pc, xcp) ([v], loc, Suc (Suc
(length (compE2 e1))), None)
  by(fastforce intro: rtranclp.rtrancl-into-rtrancl τexec-moveI simp add: exec-move-def compP2-def)
moreover
have P, e1 «bop» Val v2, h ⊢ (Val v, loc) ↔ ([v], loc, length (compE2 (e1 «bop» Val v2)), None)
  by(rule bisim1Val2) simp
ultimately show ?thesis using s τ by auto
next
case (Red1BinOpFail v1 v2 a)
note [simp] = ⟨e1' = Val v1⟩ ⟨e2 = Val v2⟩ ⟨ta = ε⟩ ⟨e' = Throw a⟩ ⟨h' = h⟩ ⟨xs' = xs⟩
  and binop = ⟨binop bop v1 v2 = [Inr a]⟩
have τ: τmove1 P h (Val v1 «bop» Val v2) by(rule τmove1BinOp)
from bisim1 have s: xcp = None xs = loc
  and τExec-mover-a P t e1 h (stk, loc, pc, xcp) ([v1], loc, length (compE2 e1), None)
  by(auto dest: bisim1Val2D1)
hence τExec-mover-a P t (e1 «bop» Val v2) h (stk, loc, pc, xcp) ([v1], loc, length (compE2 e1),
None)
  by-(rule BinOp-τExecrI1)
also have τmove2 (compP2 P) h [v1] (e1 «bop» Val v2) (length (compE2 e1) + 0) None
  by(rule τmove2BinOp2)(rule τmove2Val)
  with binop have τExec-mover-a P t (e1 «bop» Val v2) h ([v1], loc, length (compE2 e1), None)
([v2, v1], loc, Suc (length (compE2 e1)), None)

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by—(rule  $\tau\text{Execr1step}$ , auto intro!:  $\text{exec-instr simp add: exec-move-def compP2-def}$ )  
 also (rtrancpl-trans) from binop  
 have  $\text{exec-meth-a (compP2 P) (compE2 (e1 \ll bop \gg Val v2)) (compxE2 (e1 \ll bop \gg Val v2) 0 0) t}$   
      $h ([v2, v1], \text{loc}, \text{Suc (length (compE2 e1))}, \text{None}) \varepsilon$   
      $h ([v2, v1], \text{loc}, \text{Suc (length (compE2 e1))}, [a])$   
 by—(rule  $\text{exec-instr}$ , auto)  
 moreover have  $\tau\text{move2 (compP2 P) h [v2, v1] (e1 \ll bop \gg Val v2) (Suc (length (compE2 e1)))}$   
 None by (simp add:  $\tau\text{move2-iff}$ )  
 ultimately have  $\tau\text{Exec-movet-a P t (e1 \ll bop \gg Val v2) h (stk, \text{loc}, \text{pc}, \text{xcp}) ([v2, v1], \text{loc}, \text{Suc (length (compE2 e1))}, [a])}$   
 by (fastforce intro: rtrancpl-into-trancpl1  $\tau\text{exec-moveI simp add: exec-move-def compP2-def}$ )  
 moreover  
 have  $P, e1 \ll bop \gg Val v2, h \vdash (\text{Throw a, loc}) \leftrightarrow ([v2, v1], \text{loc}, \text{length (compE2 e1)} + \text{length (compE2 (Val v2))}, [a])$   
 by (rule  $\text{bisim1BinOpThrow}$ )  
 ultimately show ?thesis using  $s \tau$  by auto  
 next  
 case (Bin1OpThrow1 a)  
 note  $[simp] = \langle e1' = \text{Throw a} \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw a} \rangle \langle h' = h \rangle \langle xs' = xs \rangle$   
 have  $\tau: \tau\text{move1 P h (Throw a \ll bop \gg e2)}$  by (rule  $\tau\text{move1BinOpThrow1}$ )  
 from bisim1 have  $\text{xcp} = [a] \vee \text{xcp} = \text{None}$  by (auto dest:  $\text{bisim1-ThrowD}$ )  
 thus ?thesis  
 proof  
 assume  $[simp]: \text{xcp} = [a]$   
 with bisim1 have  $P, e1 \ll bop \gg e2, h \vdash (\text{Throw a, xs}) \leftrightarrow (\text{stk, loc, pc, xcp})$   
 by (auto intro:  $\text{bisim1-bisims1.intros}$ )  
 thus ?thesis using  $\tau$  by (fastforce)  
 next  
 assume  $[simp]: \text{xcp} = \text{None}$   
 with bisim1 obtain  $\text{pc'}$  where  $\tau\text{Exec-mover-a P t e1 h (stk, loc, pc, None) ([Addr a], \text{loc}, \text{pc'}, [a])}$   
 and  $\text{bisim': } P, e1, h \vdash (\text{Throw a, xs}) \leftrightarrow ([Addr a], \text{loc}, \text{pc'}, [a])$   
 and  $[simp]: xs = \text{loc}$   
 by (auto dest:  $\text{bisim1-Throw-}\tau\text{Exec-mover}$ )  
 hence  $\tau\text{Exec-mover-a P t (e1 \ll bop \gg e2) h (stk, loc, pc, None) ([Addr a], \text{loc}, \text{pc'}, [a])}$   
 by—(rule  $\text{BinOp-}\tau\text{ExecrI1}$ )  
 moreover from bisim'  
 have  $P, e1 \ll bop \gg e2, h \vdash (\text{Throw a, xs}) \leftrightarrow ([Addr a], \text{loc}, \text{pc'}, [a])$   
 by (auto intro:  $\text{bisim1-bisims1.bisim1BinOpThrow1}$ )  
 ultimately show ?thesis using  $\tau$  by auto  
 qed  
 next  
 case (Bin1OpThrow2 v a)  
 note  $[simp] = \langle e1' = \text{Val v} \rangle \langle e2 = \text{Throw a} \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw a} \rangle \langle h' = h \rangle \langle xs' = xs \rangle$   
 from bisim1 have  $s: \text{xcp} = \text{None} \text{ xs} = \text{loc}$   
 and  $\tau\text{Exec-mover-a P t e1 h (stk, loc, pc, xcp) ([v], \text{loc}, \text{length (compE2 e1)}, \text{None})}$   
 by (auto dest:  $\text{bisim1Val2D1}$ )  
 hence  $\tau\text{Exec-mover-a P t (e1 \ll bop \gg Throw a) h (stk, loc, pc, xcp) ([v], \text{loc}, \text{length (compE2 e1)}, \text{None})}$   
 by—(rule  $\text{BinOp-}\tau\text{ExecrI1}$ )  
 also have  $\tau\text{Exec-mover-a P t (e1 \ll bop \gg Throw a) h ([v], \text{loc}, \text{length (compE2 e1)}, \text{None}) ([Addr a, v], \text{loc}, \text{Suc (length (compE2 e1))}, [a])}$   
 by (rule  $\tau\text{Execr2step}$ ) (auto simp add:  $\text{exec-move-def exec-meth-instr } \tau\text{move2-iff } \tau\text{move1.simps } \tau\text{moves1.simps}$ )

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    also (rtranclp-trans)
    have  $P, e1 \llbracket \text{bop} \rrbracket \text{Throw } a, h \vdash (\text{Throw } a, \text{loc}) \leftrightarrow ([\text{Addr } a] @ [v], \text{loc}, (\text{length } (\text{compE2 } e1) + \text{length } (\text{compE2 } (\text{addr } a))), [a])$ 
    by(rule bisim1BinOpThrow2[OF bisim1Throw2])
    moreover have  $\tau \text{move1 } P \ h \ (e1' \llbracket \text{bop} \rrbracket e2) \text{ by } (\text{auto intro: } \tau \text{move1BinOpThrow2})$ 
    ultimately show ?thesis using s by auto
  qed
next
case (bisim1BinOp2 e2 n e2' xs stk loc pc xcp e1 bop v1)
note IH2 = bisim1BinOp2.IH(2)
note bisim1 =  $\langle \bigwedge xs. P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$ 
note bisim2 =  $\langle P, e2, h \vdash (e2', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \rangle$ 
note red =  $\langle \text{True}, P, t \vdash 1 \ \langle \text{Val } v1 \llbracket \text{bop} \rrbracket e2', (h, xs) \rangle -ta\rightarrow \langle e', (h', xs') \rangle$ 
note bsok =  $\langle \text{bsok } (e1 \llbracket \text{bop} \rrbracket e2) \ n \rangle$ 
from red show ?case
proof cases
  case (Bin1OpRed2 E')
  note [simp] =  $\langle e' = \text{Val } v1 \llbracket \text{bop} \rrbracket E' \rangle$ 
  and red =  $\langle \text{True}, P, t \vdash 1 \ \langle e2', (h, xs) \rangle -ta\rightarrow \langle E', (h', xs') \rangle$ 
  from IH2[OF red] bsok obtain  $pc'' \ \text{stk}'' \ \text{loc}'' \ \text{xcp}''$ 
  where bisim':  $P, e2, h' \vdash (E', xs') \leftrightarrow (\text{stk}'', \text{loc}'', \text{pc}'', \text{xcp}'')$ 
  and exec':  $?exec \ ta \ e2 \ e2' \ E' \ h \ \text{stk} \ \text{loc} \ \text{pc} \ \text{xcp} \ h' \ \text{pc}'' \ \text{stk}'' \ \text{loc}'' \ \text{xcp}'' \text{ by } \text{auto}$ 
  from red have  $\tau: \tau \text{move1 } P \ h \ (\text{Val } v1 \llbracket \text{bop} \rrbracket e2') = \tau \text{move1 } P \ h \ e2' \text{ by } (\text{auto simp add: } \tau \text{move1.simps } \tau \text{moves1.simps})$ 
  have no-call2  $e2 \ \text{pc} \implies \text{no-call2 } (e1 \llbracket \text{bop} \rrbracket e2) \ (\text{length } (\text{compE2 } e1) + \text{pc}) \text{ by } (\text{auto simp add: no-call2-def})$ 
  hence  $?exec \ ta \ (e1 \llbracket \text{bop} \rrbracket e2) \ (\text{Val } v1 \llbracket \text{bop} \rrbracket e2') \ (\text{Val } v1 \llbracket \text{bop} \rrbracket E') \ h \ (\text{stk} @ [v1]) \ \text{loc} \ (\text{length } (\text{compE2 } e1) + \text{pc}) \ \text{xcp} \ h' \ (\text{length } (\text{compE2 } e1) + \text{pc}'') \ (\text{stk}'' @ [v1]) \ \text{loc}'' \ \text{xcp}''$ 
  using exec'  $\tau$ 
  apply(cases  $\tau \text{move1 } P \ h \ (\text{Val } v1 \llbracket \text{bop} \rrbracket e2')$ )
  apply(auto)
  apply(blast intro: BinOp- $\tau$ ExecrI2 BinOp- $\tau$ ExectI2 exec-move-BinOpI2)
  apply(blast intro: BinOp- $\tau$ ExecrI2 BinOp- $\tau$ ExectI2 exec-move-BinOpI2)
  apply(rule exI conjI BinOp- $\tau$ ExecrI2 exec-move-BinOpI2|assumption)+
  apply(fastforce simp add:  $\tau \text{instr-stk-drop-exec-move } \tau \text{move2-iff split: if-split-asm})$ 
  apply(rule exI conjI BinOp- $\tau$ ExecrI2 exec-move-BinOpI2|assumption)+
  apply(fastforce simp add:  $\tau \text{instr-stk-drop-exec-move } \tau \text{move2-iff split: if-split-asm})$ 
  apply(rule exI conjI BinOp- $\tau$ ExecrI2 exec-move-BinOpI2 rtranclp.rtrancl-refl|assumption)+
  apply(fastforce simp add:  $\tau \text{instr-stk-drop-exec-move } \tau \text{move2-iff split: if-split-asm})$ 
  done
  moreover from bisim'
  have  $P, e1 \llbracket \text{bop} \rrbracket e2, h' \vdash (\text{Val } v1 \llbracket \text{bop} \rrbracket E', xs') \leftrightarrow (\text{stk}'' @ [v1], \text{loc}'', \text{length } (\text{compE2 } e1) + \text{pc}'', \text{xcp}'')$ 
  by(rule bisim1-bisims1.bisim1BinOp2)
  ultimately show ?thesis using  $\tau$  by auto blast+
next
case (Red1BinOp v2 v)
note [simp] =  $\langle e2' = \text{Val } v2 \rangle \ \langle ta = \varepsilon \rangle \ \langle e' = \text{Val } v \rangle \ \langle h' = h \rangle \ \langle xs' = xs \rangle$ 
and binop =  $\langle \text{binop } bop \ v1 \ v2 = [\text{Inl } v] \rangle$ 
have  $\tau: \tau \text{move1 } P \ h \ (\text{Val } v1 \llbracket \text{bop} \rrbracket \text{Val } v2) \text{ by } (\text{rule } \tau \text{move1BinOp})$ 
from bisim2 have s:  $\text{xcp} = \text{None} \ xs = \text{loc}$ 
and  $\tau \text{Exec-mover-a } P \ t \ e2 \ h \ (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \ ([v2], \text{loc}, \text{length } (\text{compE2 } e2), \text{None})$ 
by(auto dest: bisim1Val2D1)
hence  $\tau \text{Exec-mover-a } P \ t \ (e1 \llbracket \text{bop} \rrbracket e2) \ h \ (\text{stk} @ [v1], \text{loc}, \text{length } (\text{compE2 } e1) + \text{pc}, \text{xcp}) \ ([v2] @ [v1], \text{loc}, \text{length } (\text{compE2 } e1) + \text{length } (\text{compE2 } e2), \text{None})$ 

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**by**—(rule *BinOp-τExecrI2*)  
**moreover from** *binop*  
**have** *exec-move-a*  $P \ t \ (e1 \llbracket bop \rrbracket e2) \ h \ ([v2, v1], \text{loc}, \text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2), \text{None}) \ \varepsilon$   
 $h \ ([v], \text{loc}, \text{Suc}(\text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2)), \text{None})$   
**unfolding** *exec-move-def* **by**—(rule *exec-instr*, *auto*)  
**moreover have**  $\tau \text{move2} \ (\text{compP2 } P) \ h \ [v2, v1] \ (e1 \llbracket bop \rrbracket e2) \ (\text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2)) \ \text{None}$   
**by**(*simp add: τmove2-iff*)  
**ultimately have**  $\tau \text{Exec-mover-a } P \ t \ (e1 \llbracket bop \rrbracket e2) \ h \ (\text{stk} @ [v1], \text{loc}, \text{length}(\text{compE2 } e1) + \text{pc}, \text{xcp}) \ ([v], \text{loc}, \text{Suc}(\text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2)), \text{None})$   
**by**(*auto intro: rtranclp.rtrancl-into-rtrancl τexec-moveI simp add: compP2-def*)  
**moreover**  
**have**  $P, e1 \llbracket bop \rrbracket e2, h \vdash (\text{Val } v, \text{loc}) \leftrightarrow ([v], \text{loc}, \text{length}(\text{compE2 } (e1 \llbracket bop \rrbracket e2)), \text{None})$   
**by**(rule *bisim1Val2*) *simp*  
**ultimately show** *?thesis* **using**  $s \ \tau$  **by**(*auto*)  
**next**  
**case** (*Red1BinOpFail v2 a*)  
**note**  $[simp] = \langle e2' = \text{Val } v2 \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw } a \rangle \langle h' = h \rangle \langle xs' = xs \rangle$   
**and**  $\text{binop} = \langle \text{binop } bop \ v1 \ v2 = \lfloor \text{Inr } a \rfloor \rangle$   
**have**  $\tau: \tau \text{move1 } P \ h \ (\text{Val } v1 \llbracket bop \rrbracket \text{Val } v2) \ \text{by}(\text{rule } \tau \text{move1BinOp})$   
**from** *bisim2* **have**  $s: \text{xcp} = \text{None} \ xs = \text{loc}$   
**and**  $\tau \text{Exec-mover-a } P \ t \ e2 \ h \ (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \ ([v2], \text{loc}, \text{length}(\text{compE2 } e2), \text{None})$   
**by**(*auto dest: bisim1Val2D1*)  
**hence**  $\tau \text{Exec-mover-a } P \ t \ (e1 \llbracket bop \rrbracket e2) \ h \ (\text{stk} @ [v1], \text{loc}, \text{length}(\text{compE2 } e1) + \text{pc}, \text{xcp}) \ ([v2] @ [v1], \text{loc}, \text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2), \text{None})$   
**by**—(rule *BinOp-τExecrI2*)  
**moreover from** *binop*  
**have** *exec-move-a*  $P \ t \ (e1 \llbracket bop \rrbracket e2) \ h \ ([v2, v1], \text{loc}, \text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2), \text{None}) \ \varepsilon$   
 $h \ ([v2, v1], \text{loc}, \text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2), \lfloor a \rfloor)$   
**unfolding** *exec-move-def* **by**—(rule *exec-instr*, *auto*)  
**moreover have**  $\tau \text{move2} \ (\text{compP2 } P) \ h \ [v2, v1] \ (e1 \llbracket bop \rrbracket e2) \ (\text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2)) \ \text{None}$   
**by**(*simp add: τmove2-iff*)  
**ultimately have**  $\tau \text{Exec-mover-a } P \ t \ (e1 \llbracket bop \rrbracket e2) \ h \ (\text{stk} @ [v1], \text{loc}, \text{length}(\text{compE2 } e1) + \text{pc}, \text{xcp}) \ ([v2, v1], \text{loc}, \text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2), \lfloor a \rfloor)$   
**by**(*auto intro: rtranclp-into-tranclp1 τexec-moveI simp add: compP2-def*)  
**moreover**  
**have**  $P, e1 \llbracket bop \rrbracket e2, h \vdash (\text{Throw } a, \text{loc}) \leftrightarrow ([v2, v1], \text{loc}, \text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2), \lfloor a \rfloor)$   
**by**(rule *bisim1BinOpThrow*)  
**ultimately show** *?thesis* **using**  $s \ \tau$  **by**(*auto*)  
**next**  
**case** (*Bin1OpThrow2 a*)  
**note**  $[simp] = \langle e2' = \text{Throw } a \rangle \langle ta = \varepsilon \rangle \langle h' = h \rangle \langle xs' = xs \rangle \langle e' = \text{Throw } a \rangle$   
**have**  $\tau: \tau \text{move1 } P \ h \ (\text{Val } v1 \llbracket bop \rrbracket \text{Throw } a) \ \text{by}(\text{rule } \tau \text{move1BinOpThrow2})$   
**from** *bisim2* **have**  $\text{xcp} = \lfloor a \rfloor \vee \text{xcp} = \text{None} \ \text{by}(\text{auto dest: bisim1-ThrowD})$   
**thus** *?thesis*  
**proof**  
**assume**  $[simp]: \text{xcp} = \lfloor a \rfloor$   
**with** *bisim2*  
**have**  $P, e1 \llbracket bop \rrbracket e2, h \vdash (\text{Throw } a, xs) \leftrightarrow (\text{stk} @ [v1], \text{loc}, \text{length}(\text{compE2 } e1) + \text{pc}, \text{xcp})$   
**by**(*auto intro: bisim1BinOpThrow2*)

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    thus ?thesis using  $\tau$  by (fastforce)
  next
    assume [simp]:  $xcp = None$ 
    with bisim2 obtain  $pc'$ 
      where  $\tau Exec\text{-}mover\text{-}a\ P\ t\ e2\ h\ (stk, loc, pc, None)\ ([Addr\ a], loc, pc', [a])$ 
      and  $bisim': P, e2, h \vdash (Throw\ a, xs) \leftrightarrow ([Addr\ a], loc, pc', [a])$  and [simp]:  $xs = loc$ 
      by (auto dest: bisim1-Throw- $\tau Exec\text{-}mover$ )
    hence  $\tau Exec\text{-}mover\text{-}a\ P\ t\ (e1 \ll bop \gg e2)\ h\ (stk\ @\ [v1], loc, length\ (compE2\ e1) + pc, None)\ ([Addr\ a]\ @\ [v1], loc, length\ (compE2\ e1) + pc', [a])$ 
      by (rule BinOp- $\tau ExecrI2$ )
    moreover from bisim'
      have  $P, e1 \ll bop \gg e2, h \vdash (Throw\ a, xs) \leftrightarrow ([Addr\ a]\ @[v1], loc, length\ (compE2\ e1) + pc', [a])$ 
      by (rule bisim1BinOpThrow2, auto)
    ultimately show ?thesis using  $\tau$  by auto
  qed
qed auto
next
  case bisim1BinOpThrow1 thus ?case by fastforce
next
  case bisim1BinOpThrow2 thus ?case by fastforce
next
  case bisim1BinOpThrow thus ?case by fastforce
next
  case (bisim1LAss1  $E\ n\ e\ xs\ stk\ loc\ pc\ xcp\ V$ )
    note  $IH = bisim1LAss1.IH(2)$ 
    note  $bisim = \langle P, E, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
    note  $red = \langle True, P, t \vdash 1\ \langle V := e, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$ 
    note  $bsok = \langle bsok\ (V := E)\ n \rangle$ 
    from red show ?case
  proof cases
    case (LAss1Red  $ee'$ )
      note [simp] =  $\langle e' = V := ee' \rangle$ 
      and  $red = \langle True, P, t \vdash 1\ \langle e, (h, xs) \rangle -ta \rightarrow \langle ee', (h', xs') \rangle \rangle$ 
    from red have  $\tau move1\ P\ h\ (V := e) = \tau move1\ P\ h\ e$  by (auto simp add:  $\tau move1.simps\ \tau moves1.simps$ )
    moreover from red have  $call1\ (V := e) = call1\ e$  by auto
    moreover from  $IH[OF\ red]\ bsok$ 
      obtain  $pc''\ stk''\ loc''\ xcp''$  where  $bisim: P, E, h' \vdash (ee', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$ 
      and redo:  $?exec\ ta\ E\ e\ ee'\ h\ stk\ loc\ pc\ xcp\ h'\ pc''\ stk''\ loc''\ xcp''$  by auto
    from bisim
      have  $P, V := E, h' \vdash (V := ee', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$ 
      by (rule bisim1-bisims1.bisim1LAss1)
    moreover {
      assume no-call2  $E\ pc$ 
      hence no-call2  $(V := E)\ pc$  by (auto simp add: no-call2-def) }
    ultimately show ?thesis using redo
      by (auto simp del:  $call1.simps\ calls1.simps\ split: if-split-asm\ split\ del: if-split$ ) (blast intro:
LAss- $\tau ExecrI\ LAss\text{-}\tau ExecrI\ exec\text{-}move\text{-}LAssI$ ) +
  next
    case (Red1LAss  $v$ )
      note [simp] =  $\langle e = Val\ v \rangle\ \langle ta = \varepsilon \rangle\ \langle e' = unit \rangle\ \langle h' = h \rangle\ \langle xs' = xs[V := v] \rangle$ 
      and  $V = \langle V < length\ xs \rangle$ 
      from bisim have  $s: xcp = None\ xs = loc$  by (auto dest: bisim-Val-loc-eq-xcp-None)
      from bisim have  $\tau Exec\text{-}mover\text{-}a\ P\ t\ E\ h\ (stk, loc, pc, xcp)\ ([v], loc, length\ (compE2\ E), None)$ 
      by (auto dest: bisim1Val2D1)

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hence  $\tau_{\text{Exec-mover-a}} P \ t \ (V:=E) \ h \ (stk, loc, pc, xcp) \ ([v], loc, length \ (compE2 \ E), None)$   
 by(rule *LAss- $\tau_{\text{ExecrI}}$* )  
 moreover have  $\text{exec-move-a } P \ t \ (V:=E) \ h \ ([v], loc, length \ (compE2 \ E), None) \ \varepsilon \ h \ ([], loc[V := v], Suc \ (length \ (compE2 \ E)), None)$   
 using  $V \ s$  by(auto intro: *exec-instr simp add: exec-move-def*)  
 moreover have  $\tau_{\text{move2}} \ (compP2 \ P) \ h \ [v] \ (V := E) \ (length \ (compE2 \ E)) \ None$  by(*simp add:  $\tau_{\text{move2-iff}}$* )  
 ultimately have  $\tau_{\text{Exec-mover-a}} P \ t \ (V:=E) \ h \ (stk, loc, pc, xcp) \ ([], loc[V := v], Suc \ (length \ (compE2 \ E)), None)$   
 by(auto intro: *rtrancpl.rtrancpl-into-rtrancpl  $\tau_{\text{exec-moveI}} \text{simp add: compP2-def}$* )  
 moreover have  $\tau_{\text{move1}} P \ h \ (V := Val \ v)$  by(rule  *$\tau_{\text{move1LAssRed}}$* )  
 moreover have  $P, V:=E, h \vdash (unit, loc[V := v]) \leftrightarrow ([], loc[V := v], Suc \ (length \ (compE2 \ E)), None)$   
 by(rule *bisim1LAss2*)  
 ultimately show *?thesis* using  $s$  by auto  
 next  
 case (*LAss1Throw a*)  
 note  $[simp] = \langle e = Throw \ a \rangle \langle h' = h \rangle \langle xs' = xs \rangle \langle ta = \varepsilon \rangle \langle e' = Throw \ a \rangle$   
 have  $\tau: \tau_{\text{move1}} P \ h \ (V:=e)$  by(auto intro:  *$\tau_{\text{move1LAssThrow}}$* )  
 from *bisim* have  $xcp = [a] \vee xcp = None$  by(auto dest: *bisim1-ThrowD*)  
 thus *?thesis*  
 proof  
 assume  $[simp]: xcp = [a]$   
 with *bisim* have  $P, V:=E, h \vdash (Throw \ a, xs) \leftrightarrow (stk, loc, pc, xcp)$  by(auto intro: *bisim1LAssThrow*)  
 thus *?thesis* using  $\tau$  by(*fastforce*)  
 next  
 assume  $[simp]: xcp = None$   
 with *bisim* obtain  $pc'$   
 where  $\tau_{\text{Exec-mover-a}} P \ t \ E \ h \ (stk, loc, pc, None) \ ([Addr \ a], loc, pc', [a])$   
 and *bisim'*:  $P, E, h \vdash (Throw \ a, xs) \leftrightarrow ([Addr \ a], loc, pc', [a])$  and  $[simp]: xs = loc$   
 by(auto dest: *bisim1-Throw- $\tau_{\text{Exec-mover}}$* )  
 hence  $\tau_{\text{Exec-mover-a}} P \ t \ (V:=E) \ h \ (stk, loc, pc, None) \ ([Addr \ a], loc, pc', [a])$   
 by-(rule *LAss- $\tau_{\text{ExecrI}}$* )  
 moreover from *bisim'* have  $P, V:=E, h \vdash (Throw \ a, xs) \leftrightarrow ([Addr \ a], loc, pc', [a])$   
 by-(rule *bisim1LAssThrow, auto*)  
 ultimately show *?thesis* using  $\tau$  by auto  
 qed  
 qed  
 next  
 case *bisim1LAss2* thus *?case* by *fastforce*  
 next  
 case *bisim1LAssThrow* thus *?case* by *fastforce*  
 next  
 case (*bisim1AAcc1 a n a' xs stk loc pc xcp i*)  
 note  $IH1 = bisim1AAcc1.IH(2)$   
 note  $IH2 = bisim1AAcc1.IH(4)$   
 note  $bisim1 = \langle P, a, h \vdash (a', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$   
 note  $bisim2 = \langle \bigwedge xs. P, i, h \vdash (i, xs) \leftrightarrow ([], xs, 0, None) \rangle$   
 note  $bsok = \langle bsok \ (a[i]) \ n \rangle$   
 from  $\langle True, P, t \vdash 1 \ \langle a'[i], (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$  show *?case*  
 proof cases  
 case (*AAcc1Red1 E'*)  
 note  $[simp] = \langle e' = E'[i] \rangle$   
 and  $red = \langle True, P, t \vdash 1 \ \langle a', (h, xs) \rangle -ta \rightarrow \langle E', (h', xs') \rangle \rangle$

**from** *red* **have**  $\tau \text{move1 } P \ h \ (a'[i]) = \tau \text{move1 } P \ h \ a'$  **by** (*auto simp add:  $\tau \text{move1.simps}$   $\tau \text{moves1.simps}$* )  
**moreover from** *red* **have**  $\text{call1 } (a'[i]) = \text{call1 } a'$  **by** *auto*  
**moreover from** *IH1[OF red]* *bsok*  
**obtain**  $pc'' \ stk'' \ loc'' \ xcp''$  **where**  $\text{bisim}: P, a, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$   
**and** *redo*:  $?exec \ ta \ a \ a' \ E' \ h \ stk \ loc \ pc \ xcp \ h' \ pc'' \ stk'' \ loc'' \ xcp''$  **by** *auto*  
**from** *bisim* **have**  $P, a[i], h' \vdash (E'[i], xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$   
**by** (*rule bisim1-bisims1.bisim1AAcc1*)  
**moreover** {  
**assume** *no-call2 a pc*  
**hence** *no-call2*  $(a[i]) \ pc \vee pc = \text{length } (\text{compE2 } a)$  **by** (*auto simp add: no-call2-def*) }  
**ultimately show** *?thesis* **using** *redo*  
**by** (*auto simp del: call1.simps calls1.simps split: if-split-asm split del: if-split*) (*blast intro: AAcc- $\tau$ ExecrI1 AAcc- $\tau$ ExectI1 exec-move-AAccI1*) +  
**next**  
**case** (*AAcc1Red2 E' v*)  
**note** [*simp*] =  $\langle a' = \text{Val } v \rangle \langle e' = \text{Val } v[E'] \rangle$   
**and** *red* =  $\langle \text{True}, P, t \vdash 1 \ \langle i, (h, xs) \rangle -ta \rightarrow \langle E', (h', xs') \rangle \rangle$   
**from** *red* **have**  $\tau: \tau \text{move1 } P \ h \ (\text{Val } v[i]) = \tau \text{move1 } P \ h \ i$  **by** (*auto simp add:  $\tau \text{move1.simps}$   $\tau \text{moves1.simps}$* )  
**from** *bisim1* **have**  $s: xcp = \text{None} \ xs = \text{loc}$   
**and** *exec1*:  $\tau \text{Exec-mover-a } P \ t \ a \ h \ (stk, loc, pc, \text{None}) \ ([v], xs, \text{length } (\text{compE2 } a), \text{None})$   
**by** (*auto dest: bisim1Val2D1*)  
**from** *exec1* **have**  $\tau \text{Exec-mover-a } P \ t \ (a[i]) \ h \ (stk, loc, pc, \text{None}) \ ([v], xs, \text{length } (\text{compE2 } a), \text{None})$   
**by** (*rule AAcc- $\tau$ ExecrI1*)  
**moreover**  
**from** *IH2[OF red]* *bsok* **obtain**  $pc'' \ stk'' \ loc'' \ xcp''$   
**where**  $\text{bisim}': P, i, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$   
**and** *exec'*:  $?exec \ ta \ i \ i \ E' \ h \ [] \ xs \ 0 \ \text{None} \ h' \ pc'' \ stk'' \ loc'' \ xcp''$  **by** *auto*  
**have**  $?exec \ ta \ (a[i]) \ (\text{Val } v[i]) \ (\text{Val } v[E']) \ h \ ([] @ [v]) \ xs \ (\text{length } (\text{compE2 } a) + 0) \ \text{None} \ h' \ (\text{length } (\text{compE2 } a) + pc'') \ (stk'' @ [v]) \ loc'' \ xcp''$   
**proof** (*cases  $\tau \text{move1 } P \ h \ (\text{Val } v[i])$* )  
**case** *True*  
**with** *exec'  $\tau$*  **have** [*simp*]:  $h = h'$  **and**  $e: \text{sim-move } i \ E' \ P \ t \ i \ h \ ( [], xs, 0, \text{None}) \ (stk'', loc'', pc'', xcp'')$  **by** *auto*  
**from**  $e$  **have**  $\text{sim-move } (a[i]) \ (a[E']) \ P \ t \ (a[i]) \ h \ ( [] @ [v], xs, \text{length } (\text{compE2 } a) + 0, \text{None}) \ (stk'' @ [v], loc'', \text{length } (\text{compE2 } a) + pc'', xcp'')$   
**by** (*fastforce dest: AAcc- $\tau$ ExecrI2 AAcc- $\tau$ ExectI2*)  
**with** *True* **show** *?thesis* **by** *auto*  
**next**  
**case** *False*  
**with** *exec'  $\tau$*  **obtain**  $pc' \ stk' \ loc' \ xcp'$   
**where**  $e: \tau \text{Exec-mover-a } P \ t \ i \ h \ ( [], xs, 0, \text{None}) \ (stk', loc', pc', xcp')$   
**and**  $e': \text{exec-move-a } P \ t \ i \ h \ (stk', loc', pc', xcp') \ (\text{extTA2JVM } (\text{compP2 } P) \ ta) \ h' \ (stk'', loc'', pc'', xcp'')$   
**and**  $\tau': \neg \tau \text{move2 } (\text{compP2 } P) \ h \ stk' \ i \ pc' \ xcp'$   
**and**  $\text{call1 } i = \text{None} \vee \text{no-call2 } i \ 0 \vee pc' = 0 \wedge stk' = [] \wedge loc' = xs \wedge xcp' = \text{None}$  **by** *auto*  
**from**  $e$  **have**  $\tau \text{Exec-mover-a } P \ t \ (a[i]) \ h \ ( [] @ [v], xs, \text{length } (\text{compE2 } a) + 0, \text{None}) \ (stk' @ [v], loc', \text{length } (\text{compE2 } a) + pc', xcp')$   
**by** (*rule AAcc- $\tau$ ExecrI2*)  
**moreover from**  $e'$  **have**  $\text{exec-move-a } P \ t \ (a[i]) \ h \ (stk' @ [v], loc', \text{length } (\text{compE2 } a) + pc', xcp') \ (\text{extTA2JVM } (\text{compP2 } P) \ ta) \ h' \ (stk'' @ [v], loc'', \text{length } (\text{compE2 } a) + pc'', xcp'')$   
**by** (*rule exec-move-AAccI2*)

**moreover from**  $e' \tau' \text{ have } \neg \tau \text{move2 } (\text{compP2 } P) \ h \ (stk' @ [v]) \ (a[i]) \ (\text{length } (\text{compE2 } a) + pc') \ xcp'$   
**by**(*auto simp add:  $\tau \text{instr-stk-drop-exec-move } \tau \text{move2-iff}$* )  
**moreover have**  $\text{call1 } (a'[i]) = \text{call1 } i \text{ by } \text{simp}$   
**moreover have**  $\text{no-call2 } i \ 0 \implies \text{no-call2 } (a[i]) \ (\text{length } (\text{compE2 } a))$   
**by**(*auto simp add: no-call2-def*)  
**ultimately show** *?thesis using False call*  
**by**(*auto simp del: split-paired-Ex call1.simps calls1.simps*) *blast*  
**qed**  
**moreover from** *bisim'*  
**have**  $P, a[i], h' \vdash (\text{Val } v[E'], xs') \leftrightarrow ((stk'' @ [v]), loc'', \text{length } (\text{compE2 } a) + pc'', xcp'')$   
**by**(*rule bisim1-bisims1.bisim1AAcc2*)  
**moreover from** *bisim1* **have**  $pc \neq \text{length } (\text{compE2 } a) \longrightarrow \text{no-call2 } (a[i]) \ pc$   
**by**(*auto simp add: no-call2-def dest: bisim-Val-pc-not-Invoke bisim1-pc-length-compE2*)  
**ultimately show** *?thesis using  $\tau \text{exec1 } s$*   
**apply**(*auto simp del: split-paired-Ex call1.simps calls1.simps split: if-split-asm split del: if-split*)  
**apply**(*blast intro:  $\tau \text{Exec-mover-trans}$  fastforce elim!:  $\tau \text{Exec-mover-trans}$  simp del: split-paired-Ex call1.simps calls1.simps*)  
**done**  
**next**  
**case** (*Red1AAcc A U len I v*)  
**hence** [*simp*]:  $a' = \text{addr } A \ e' = \text{Val } v \ i = \text{Val } (\text{Intg } I) \ h' = h \ xs' = xs$   
 $ta = \{\text{ReadMem } A \ (\text{ACell } (\text{nat } (\text{sint } I))) \ v\}$   
**and**  $hA: \text{typeof-addr } h \ A = \lfloor \text{Array-type } U \text{ len} \rfloor$  **and**  $I: 0 \leq s \ I \ \text{sint } I < \text{int len}$   
**and** *read: heap-read h A (ACell (nat (sint I))) v by auto*  
**have**  $\tau: \neg \tau \text{move1 } P \ h \ (\text{addr } A \lfloor \text{Val } (\text{Intg } I) \rfloor) \text{ by } (\text{auto simp add: } \tau \text{move1.simps } \tau \text{moves1.simps})$   
**from** *bisim1* **have**  $s: xcp = \text{None} \ xs = \text{loc}$   
**and**  $\tau \text{Exec-mover-a } P \ t \ a \ h \ (stk, \text{loc}, pc, xcp) \ ([\text{Addr } A], \text{loc}, \text{length } (\text{compE2 } a), \text{None})$   
**by**(*auto dest: bisim1Val2D1*)  
**hence**  $\tau \text{Exec-mover-a } P \ t \ (a \lfloor \text{Val } (\text{Intg } I) \rfloor) \ h \ (stk, \text{loc}, pc, xcp) \ ([\text{Addr } A], \text{loc}, \text{length } (\text{compE2 } a), \text{None})$   
**by**-(*rule AAacc- $\tau \text{ExecrI1}$* )  
**also have**  $\tau \text{move2 } (\text{compP2 } P) \ h \ [\text{Addr } A] \ (a \lfloor \text{Val } (\text{Intg } I) \rfloor) \ (\text{length } (\text{compE2 } a) + 0) \ \text{None}$   
**by**(*rule  $\tau \text{move2AAcc2}$  (rule  $\tau \text{move2Val}$ )*)  
**hence**  $\tau \text{Exec-mover-a } P \ t \ (a \lfloor \text{Val } (\text{Intg } I) \rfloor) \ h \ ([\text{Addr } A], \text{loc}, \text{length } (\text{compE2 } a), \text{None}) \ ([\text{Intg } I, \text{Addr } A], \text{loc}, \text{Suc } (\text{length } (\text{compE2 } a)), \text{None})$   
**by**-(*rule  $\tau \text{Execr1step}$ , auto intro!: exec-instr simp add: exec-move-def compP2-def*)  
**also** (*rtranclp-trans*) **from** *hA I read*  
**have**  $\text{exec-move-a } P \ t \ (a \lfloor \text{Val } (\text{Intg } I) \rfloor) \ h \ ([\text{Intg } I, \text{Addr } A], \text{loc}, \text{Suc } (\text{length } (\text{compE2 } a)), \text{None})$   
 $\{\text{ReadMem } A \ (\text{ACell } (\text{nat } (\text{sint } I))) \ v\}$   
 $h \ ([v], \text{loc}, \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } a))), \text{None})$   
**unfolding** *exec-move-def* **by**-(*rule exec-instr, auto simp add: is-Ref-def*)  
**moreover have**  $\tau \text{move2 } (\text{compP2 } P) \ h \ [\text{Intg } I, \text{Addr } A] \ (a \lfloor \text{Val } (\text{Intg } I) \rfloor) \ (\text{Suc } (\text{length } (\text{compE2 } a))) \ \text{None} \implies \text{False}$   
**by**(*simp add:  $\tau \text{move2-iff}$* )  
**moreover**  
**have**  $P, a \lfloor \text{Val } (\text{Intg } I) \rfloor, h \vdash (\text{Val } v, \text{loc}) \leftrightarrow ([v], \text{loc}, \text{length } (\text{compE2 } (a \lfloor \text{Val } (\text{Intg } I) \rfloor)), \text{None})$   
**by**(*rule bisim1Val2*) *simp*  
**ultimately show** *?thesis using  $s \ \tau$*   
**by**(*auto simp add: ta-upd-simps*) *blast*  
**next**  
**case** (*Red1AAccNull v*)  
**note** [*simp*] =  $\langle a' = \text{null} \rangle \langle i = \text{Val } v \rangle \langle ta = \varepsilon \rangle \langle e' = \text{THROW NullPointer} \rangle \langle h' = h \rangle \langle xs' = xs \rangle$   
**from** *bisim1* **have**  $s: xcp = \text{None} \ xs = \text{loc}$

**and**  $\tau\text{Exec-mover-a } P \ t \ a \ h \ (stk, loc, pc, xcp) \ ([Null], loc, length \ (compE2 \ a), None)$   
**by**(*auto dest: bisim1Val2D1 intro: AAcc- $\tau\text{ExecrI1}$* )  
**hence**  $\tau\text{Exec-mover-a } P \ t \ (a[i]) \ h \ (stk, loc, pc, xcp) \ ([Null], loc, length \ (compE2 \ a), None)$   
**by**-(*rule AAcc- $\tau\text{ExecrI1}$* )  
**also from** *bisim2[of loc]* **have**  $\tau\text{Exec-mover-a } P \ t \ i \ h \ ([], loc, 0, None) \ ([v], loc, length \ (compE2 \ i), None)$   
**by**(*auto dest: bisim1Val2D1*)  
**hence**  $\tau\text{Exec-mover-a } P \ t \ (a[i]) \ h \ ([[] @ [Null], loc, length \ (compE2 \ a) + 0, None) \ ([v] @ [Null], loc, length \ (compE2 \ a) + length \ (compE2 \ i), None)$   
**by**(*rule AAcc- $\tau\text{ExecrI2}$* )  
**hence**  $\tau\text{Exec-mover-a } P \ t \ (a[i]) \ h \ ([Null], loc, length \ (compE2 \ a), None) \ ([v, Null], loc, length \ (compE2 \ a) + length \ (compE2 \ i), None)$  **by** *simp*  
**also** (*rtrancpl-trans*) **have**  $\text{exec-move-a } P \ t \ (a[i]) \ h \ ([v, Null], loc, length \ (compE2 \ a) + length \ (compE2 \ i), None) \ \varepsilon \ h \ ([v, Null], loc, length \ (compE2 \ a) + length \ (compE2 \ i), \lfloor \text{addr-of-sys-xcpt NullPointer} \rfloor)$   
**unfolding** *exec-move-def* **by**-(*rule exec-instr, auto*)  
**moreover have**  $\neg \tau\text{move2} \ (compP2 \ P) \ h \ [v, Null] \ (a[i]) \ (length \ (compE2 \ a) + length \ (compE2 \ i)) \ None$   
**by**(*simp add:  $\tau\text{move2-iff}$* )  
**moreover have**  $\neg \tau\text{move1} \ P \ h \ (a'[i])$  **by**(*auto simp add:  $\tau\text{move1.simps}$   $\tau\text{moves1.simps}$* )  
**moreover**  
**have**  $P, a[i], h \vdash (THROW \text{NullPointer}, xs) \leftrightarrow ([v, Null], xs, length \ (compE2 \ a) + length \ (compE2 \ i), \lfloor \text{addr-of-sys-xcpt NullPointer} \rfloor)$   
**by**(*rule bisim1-bisims1.bisim1AAccFail*)  
**ultimately show** *?thesis* **using** *s* **by** *auto blast*  
**next**  
**case** (*Red1AAccBounds A U len I*)  
**hence** [*simp*]:  $a' = \text{addr } A \ e' = \text{THROW } \text{ArrayIndexOutOfBounds } i = \text{Val } (Intg \ I)$   
 $ta = \varepsilon \ h' = h \ xs' = xs$   
**and**  $hA: \text{typeof-addr } h \ A = \lfloor \text{Array-type } U \text{ len} \rfloor$  **and**  $I: I < s \ 0 \vee \text{int } len \leq \text{sint } I$  **by** *auto*  
**from** *bisim1* **have**  $s: xcp = None \ xs = loc$   
**and**  $\tau\text{Exec-mover-a } P \ t \ a \ h \ (stk, loc, pc, xcp) \ ([Addr \ A], loc, length \ (compE2 \ a), None)$   
**by**(*auto dest: bisim1Val2D1*)  
**hence**  $\tau\text{Exec-mover-a } P \ t \ (a[i]) \ h \ (stk, loc, pc, xcp) \ ([Addr \ A], loc, length \ (compE2 \ a), None)$   
**by**-(*rule AAcc- $\tau\text{ExecrI1}$* )  
**also from** *bisim2[of loc]* **have**  $\tau\text{Exec-mover-a } P \ t \ i \ h \ ([], loc, 0, None) \ ([Intg \ I], loc, length \ (compE2 \ i), None)$   
**by**(*auto dest: bisim1Val2D1*)  
**hence**  $\tau\text{Exec-mover-a } P \ t \ (a[i]) \ h \ ([[] @ [Addr \ A], loc, length \ (compE2 \ a) + 0, None) \ ([Intg \ I] @ [Addr \ A], loc, length \ (compE2 \ a) + length \ (compE2 \ i), None)$   
**by**(*rule AAcc- $\tau\text{ExecrI2}$* )  
**hence**  $\tau\text{Exec-mover-a } P \ t \ (a[i]) \ h \ ([Addr \ A], loc, length \ (compE2 \ a), None) \ ([Intg \ I, Addr \ A], loc, length \ (compE2 \ a) + length \ (compE2 \ i), None)$  **by** *simp*  
**also** (*rtrancpl-trans*) **from**  $I \ hA$   
**have**  $\text{exec-move-a } P \ t \ (a[i]) \ h \ ([Intg \ I, Addr \ A], loc, length \ (compE2 \ a) + length \ (compE2 \ i), None) \ \varepsilon \ h \ ([Intg \ I, Addr \ A], loc, length \ (compE2 \ a) + length \ (compE2 \ i), \lfloor \text{addr-of-sys-xcpt ArrayIndexOutOfBounds} \rfloor)$   
**unfolding** *exec-move-def* **by**-(*rule exec-instr, auto simp add: is-Ref-def*)  
**moreover have**  $\neg \tau\text{move2} \ (compP2 \ P) \ h \ [Intg \ I, Addr \ A] \ (a[i]) \ (length \ (compE2 \ a) + length \ (compE2 \ i)) \ None$   
**by**(*simp add:  $\tau\text{move2-iff}$* )  
**moreover have**  $\neg \tau\text{move1} \ P \ h \ (a'[i])$  **by**(*auto simp add:  $\tau\text{move1.simps}$   $\tau\text{moves1.simps}$* )  
**moreover**  
**have**  $P, a[i], h \vdash (THROW \text{ArrayIndexOutOfBounds}, xs) \leftrightarrow ([Intg \ I, Addr \ A], xs, length \ (compE2 \ i))$

```

a) + length (compE2 i), [addr-of-sys-xcpt ArrayIndexOutOfBounds])
  by(rule bisim1-bisims1.bisim1AAccFail)
  ultimately show ?thesis using s by auto blast
next
case (AAcc1Throw1 A)
note [simp] = ⟨a' = Throw A⟩ ⟨ta = ε⟩ ⟨e' = Throw A⟩ ⟨h' = h⟩ ⟨xs' = xs⟩
have τ: τmove1 P h (Throw A[i]) by(rule τmove1AAccThrow1)
from bisim1 have xcp = [A] ∨ xcp = None by(auto dest: bisim1-ThrowD)
thus ?thesis
proof
  assume [simp]: xcp = [A]
  with bisim1 have P, a[i], h ⊢ (Throw A, xs) ↔ (stk, loc, pc, xcp)
  by(auto intro: bisim1-bisims1.intros)
  thus ?thesis using τ by(fastforce)
next
assume [simp]: xcp = None
with bisim1 obtain pc' where τExec-mover-a P t a h (stk, loc, pc, None) ([Addr A], loc, pc',
[A])
  and bisim': P, a, h ⊢ (Throw A, xs) ↔ ([Addr A], loc, pc', [A])
  and [simp]: xs = loc
  by(auto dest: bisim1-Throw-τExec-mover)
hence τExec-mover-a P t (a[i]) h (stk, loc, pc, None) ([Addr A], loc, pc', [A])
  by-(rule AAcc-τExecrI1)
moreover from bisim'
have P, a[i], h ⊢ (Throw A, xs) ↔ ([Addr A], loc, pc', [A])
  by(auto intro: bisim1-bisims1.bisim1AAccThrow1)
ultimately show ?thesis using τ by auto
qed
next
case (AAcc1Throw2 v ad)
note [simp] = ⟨a' = Val v⟩ ⟨i = Throw ad⟩ ⟨ta = ε⟩ ⟨e' = Throw ad⟩ ⟨h' = h⟩ ⟨xs' = xs⟩
from bisim1 have s: xcp = None xs = loc
  and τExec-mover-a P t a h (stk, loc, pc, xcp) ([v], loc, length (compE2 a), None)
  by(auto dest: bisim1Val2D1)
hence τExec-mover-a P t (a[Throw ad]) h (stk, loc, pc, xcp) ([v], loc, length (compE2 a), None)
  by-(rule AAcc-τExecrI1)
also have τExec-mover-a P t (a[Throw ad]) h ([v], loc, length (compE2 a), None) ([Addr ad, v],
loc, Suc (length (compE2 a)), [ad])
  by(rule τExecr2step)(auto simp add: exec-move-def exec-meth-instr τmove2-iff τmove1.simps
τmoves1.simps)
also (rtranclp-trans)
have P, a[Throw ad], h ⊢ (Throw ad, loc) ↔ ([Addr ad] @ [v], loc, (length (compE2 a) + length
(compE2 (addr ad))), [ad])
  by(rule bisim1AAccThrow2[OF bisim1Throw2])
moreover have τmove1 P h (a'[Throw ad]) by(auto intro: τmove1AAccThrow2)
ultimately show ?thesis using s by auto
qed
next
case (bisim1AAcc2 i n i' xs stk loc pc xcp a v1)
note IH2 = bisim1AAcc2.IH(2)
note bisim1 = ⟨∧xs. P, a, h ⊢ (a, xs) ↔ ([], xs, 0, None)⟩
note bisim2 = ⟨P, i, h ⊢ (i', xs) ↔ (stk, loc, pc, xcp)⟩
note red = ⟨True, P, t ⊢ 1 ⟨Val v1[i], (h, xs)⟩ -ta→ ⟨e', (h', xs')⟩⟩
note bsok = ⟨bsok (a[i]) n⟩

```

```

from red show ?case
proof cases
  case (AAcc1Red2 E')
  note [simp] = ⟨e' = Val v1 [E'⟩
    and red = ⟨True, P, t ⊢ 1 ⟨i', (h, xs)⟩ - ta → ⟨E', (h', xs')⟩
  from IH2[OF red] bsok obtain pc'' stk'' loc'' xcp''
    where bisim': P, i, h' ⊢ (E', xs') ↔ (stk'', loc'', pc'', xcp'')
    and exec': ?exec ta i i' E' h stk loc pc xcp h' pc'' stk'' loc'' xcp'' by auto
  from red have τ: τmove1 P h (Val v1 [i]) = τmove1 P h i' by (auto simp add: τmove1.simps
τmoves1.simps)
  have no-call2 i pc ⇒ no-call2 (a [i]) (length (compE2 a) + pc) by (auto simp add: no-call2-def)
  hence ?exec ta (a [i]) (Val v1 [i]) (Val v1 [E']) h (stk @ [v1]) loc (length (compE2 a) + pc) xcp
h' (length (compE2 a) + pc'') (stk'' @ [v1]) loc'' xcp''
    using exec' τ
    apply (cases τmove1 P h (Val v1 [i]))
    apply (auto)
    apply (blast intro: AAcc-τExecrI2 AAcc-τExecI2 exec-move-AAccI2)
    apply (blast intro: AAcc-τExecrI2 AAcc-τExecI2 exec-move-AAccI2)
    apply (rule exI conjI AAcc-τExecrI2 exec-move-AAccI2|assumption) +
    apply (fastforce simp add: τinstr-stk-drop-exec-move τmove2-iff split: if-split-asm)
    apply (rule exI conjI AAcc-τExecrI2 exec-move-AAccI2|assumption) +
    apply (fastforce simp add: τinstr-stk-drop-exec-move τmove2-iff split: if-split-asm)
    apply (rule exI conjI AAcc-τExecrI2 exec-move-AAccI2 rtrancpl.rtrancpl-refl|assumption) +
    apply (fastforce simp add: τinstr-stk-drop-exec-move τmove2-iff split: if-split-asm) +
    done
  moreover from bisim'
  have P, a [i], h' ⊢ (Val v1 [E'], xs') ↔ (stk'' @ [v1], loc'', length (compE2 a) + pc'', xcp'')
    by (rule bisim1-bisims1.bisim1AAcc2)
  ultimately show ?thesis using τ by auto blast +
next
  case (Red1AAcc A U len I v)
  hence [simp]: v1 = Addr A e' = Val v i' = Val (Intg I)
    ta = {ReadMem A (ACell (nat (sint I))) v} h' = h xs' = xs
    and hA: typeof-addr h A = [Array-type U len] and I: 0 ≤ s I sint I < int len
    and read: heap-read h A (ACell (nat (sint I))) v by auto
  have τ: ¬ τmove1 P h (addr A [Val (Intg I)]) by (auto simp add: τmove1.simps τmoves1.simps)
  from bisim2 have s: xcp = None xs = loc
    and τExec-mover-a P t i h (stk, loc, pc, xcp) ([Intg I], loc, length (compE2 i), None)
    by (auto dest: bisim1Val2D1)
  hence τExec-mover-a P t (a [i]) h (stk @ [Addr A], loc, length (compE2 a) + pc, xcp) ([Intg I] @
[Addr A], loc, length (compE2 a) + length (compE2 i), None)
    by - (rule AAcc-τExecrI2)
  moreover from hA I read
  have exec-move-a P t (a [i]) h ([Intg I, Addr A], loc, length (compE2 a) + length (compE2 i),
None)
    {ReadMem A (ACell (nat (sint I))) v}
    h ([v], loc, Suc (length (compE2 a) + length (compE2 i)), None)
    unfolding exec-move-def by - (rule exec-instr, auto simp add: is-Ref-def)
  moreover have τmove2 (compP2 P) h [Intg I, Addr A] (a [i]) (length (compE2 a) + length
(compE2 i)) None ⇒ False
    by (simp add: τmove2-iff)
  moreover
  have P, a [i], h ⊢ (Val v, loc) ↔ ([v], loc, length (compE2 (a [i]))), None)
    by (rule bisim1Val2) simp

```



ultimately show *?thesis* using  $s \tau$  by (auto simp add: ta-upd-simps) blast

next

case (Red1AAccNull v)

note [simp] =  $\langle v1 = \text{Null} \rangle \langle i' = \text{Val } v \rangle \langle ta = \varepsilon \rangle \langle e' = \text{THROW NullPointer} \rangle \langle h' = h \rangle \langle xs' = xs \rangle$

from bisim2 have [simp]:  $xcp = \text{None } xs = \text{loc}$

and  $\tau \text{Exec-mover-a } P \ t \ i \ h \ (stk, \text{loc}, pc, xcp) \ ([v], \text{loc}, \text{length} (\text{compE2 } i), \text{None})$

by (auto dest: bisim1Val2D1)

hence  $\tau \text{Exec-mover-a } P \ t \ (a[i]) \ h \ (stk @ [\text{Null}], \text{loc}, \text{length} (\text{compE2 } a) + pc, xcp) \ ([v] @ [\text{Null}], \text{loc}, \text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i), \text{None})$

by (rule AAcc- $\tau \text{ExecrI2}$ )

moreover have  $\text{exec-move-a } P \ t \ (a[i]) \ h \ ([v, \text{Null}], \text{loc}, \text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i), \text{None}) \varepsilon \ h \ ([v, \text{Null}], \text{loc}, \text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i), [\text{addr-of-sys-xcpt NullPointer}])$

unfolding  $\text{exec-move-def}$  by (rule  $\text{exec-instr}$ , auto)

moreover have  $\neg \tau \text{move2} (\text{compP2 } P) \ h \ [v, \text{Null}] \ (a[i]) \ (\text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i)) \ \text{None}$

by (simp add:  $\tau \text{move2-iff}$ )

moreover have  $\neg \tau \text{move1 } P \ h \ (\text{null}[i'])$  by (auto simp add:  $\tau \text{move1.simps } \tau \text{moves1.simps}$ )

moreover

have  $P, a[i], h \vdash (\text{THROW NullPointer}, xs) \leftrightarrow ([v, \text{Null}], xs, \text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i), [\text{addr-of-sys-xcpt NullPointer}])$

by (rule bisim1-bisims1.bisim1AAccFail)

ultimately show *?thesis* by auto blast

next

case (Red1AAccBounds A U len I)

hence [simp]:  $v1 = \text{Addr } A \ e' = \text{THROW ArrayIndexOutOfBounds } i' = \text{Val } (\text{Intg } I)$

$ta = \varepsilon \ h' = h \ xs' = xs$

and  $hA: \text{typeof-addr } h \ A = [\text{Array-type } U \ \text{len}]$  and  $I: I < s \ 0 \vee \text{int } \text{len} \leq \text{sint } I$  by auto

from bisim2 have  $s: xcp = \text{None } xs = \text{loc}$

and  $\tau \text{Exec-mover-a } P \ t \ i \ h \ (stk, \text{loc}, pc, xcp) \ ([\text{Intg } I], \text{loc}, \text{length} (\text{compE2 } i), \text{None})$

by (auto dest: bisim1Val2D1)

hence  $\tau \text{Exec-mover-a } P \ t \ (a[i]) \ h \ (stk @ [\text{Addr } A], \text{loc}, \text{length} (\text{compE2 } a) + pc, xcp) \ ([\text{Intg } I] @ [\text{Addr } A], \text{loc}, \text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i), \text{None})$

by (rule AAcc- $\tau \text{ExecrI2}$ )

moreover from  $I \ hA$

have  $\text{exec-move-a } P \ t \ (a[i]) \ h \ ([\text{Intg } I, \text{Addr } A], \text{loc}, \text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i), \text{None}) \varepsilon \ h \ ([\text{Intg } I, \text{Addr } A], \text{loc}, \text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i), [\text{addr-of-sys-xcpt ArrayIndexOutOfBounds}])$

unfolding  $\text{exec-move-def}$  by (rule  $\text{exec-instr}$ , auto simp add:  $\text{is-Ref-def}$ )

moreover have  $\neg \tau \text{move2} (\text{compP2 } P) \ h \ [\text{Intg } I, \text{Addr } A] \ (a[i]) \ (\text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i)) \ \text{None}$

by (simp add:  $\tau \text{move2-iff}$ )

moreover have  $\neg \tau \text{move1 } P \ h \ (\text{addr } A[i'])$  by (auto simp add:  $\tau \text{move1.simps } \tau \text{moves1.simps}$ )

moreover

have  $P, a[i], h \vdash (\text{THROW ArrayIndexOutOfBounds}, xs) \leftrightarrow ([\text{Intg } I, \text{Addr } A], xs, \text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i), [\text{addr-of-sys-xcpt ArrayIndexOutOfBounds}])$

by (rule bisim1-bisims1.bisim1AAccFail)

ultimately show *?thesis* using  $s$  by auto blast

next

case (AAcc1Throw2 A)

note [simp] =  $\langle i' = \text{Throw } A \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw } A \rangle \langle h' = h \rangle \langle xs' = xs \rangle$

have  $\tau: \tau \text{move1 } P \ h \ (\text{Val } v1 \mid \text{Throw } A)$  by (rule  $\tau \text{move1AAccThrow2}$ )

from bisim2 have  $xcp = [A] \vee xcp = \text{None}$  by (auto dest: bisim1-ThrowD)

thus *?thesis*

proof

```

assume [simp]: xcp = [A]
with bisim2
have P, a[i], h ⊢ (Throw A, xs) ↔ (stk @ [v1], loc, length (compE2 a) + pc, xcp)
  by(auto intro: bisim1-bisims1.intros)
thus ?thesis using τ by(auto)
next
assume [simp]: xcp = None
with bisim2 obtain pc' where τExec-mover-a P t i h (stk, loc, pc, None) ([Addr A], loc, pc',
[A])
  and bisim': P, i, h ⊢ (Throw A, xs) ↔ ([Addr A], loc, pc', [A])
  and [simp]: xs = loc
  by(auto dest: bisim1-Throw-τExec-mover)
hence τExec-mover-a P t (a[i]) h (stk @ [v1], loc, length (compE2 a) + pc, None) ([Addr A] @
[v1], loc, length (compE2 a) + pc', [A])
  by-(rule AAcc-τExecrI2)
moreover from bisim'
have P, a[i], h ⊢ (Throw A, xs) ↔ ([Addr A] @ [v1], loc, length (compE2 a) + pc', [A])
  by(rule bisim1-bisims1.bisim1AAccThrow2)
ultimately show ?thesis using τ by auto
qed
qed auto
next
case bisim1AAccThrow1 thus ?case by auto
next
case bisim1AAccThrow2 thus ?case by auto
next
case bisim1AAccFail thus ?case by auto
next
case (bisim1AAss1 a n a' xs stk loc pc xcp i e)
note IH1 = bisim1AAss1.IH(2)
note IH2 = bisim1AAss1.IH(4)
note IH3 = bisim1AAss1.IH(6)
note bisim1 = ⟨P, a, h ⊢ (a', xs) ↔ (stk, loc, pc, xcp)⟩
note bisim2 = ⟨∧xs. P, i, h ⊢ (i, xs) ↔ ([], xs, 0, None)⟩
note bisim3 = ⟨∧xs. P, e, h ⊢ (e, xs) ↔ ([], xs, 0, None)⟩
note bsok = ⟨bsok (a[i] := e) n⟩
from ⟨True, P, t ⊢ 1 ⟨a'[i] := e, (h, xs)⟩ -ta→ ⟨e', (h', xs')⟩⟩ show ?case
proof cases
  case (AAss1Red1 E')
  note [simp] = ⟨e' = E'[i] := e⟩
  and red = ⟨True, P, t ⊢ 1 ⟨a', (h, xs)⟩ -ta→ ⟨E', (h', xs')⟩⟩
  from red have τmove1 P h (a'[i] := e) = τmove1 P h a' by(auto simp add: τmove1.simps
τmoves1.simps)
  moreover from red have call1 (a'[i] := e) = call1 a' by auto
  moreover from IH1[OF red] bsok
  obtain pc'' stk'' loc'' xcp'' where bisim: P, a, h' ⊢ (E', xs') ↔ (stk'', loc'', pc'', xcp'')
  and redo: ?exec ta a a' E' h stk loc pc xcp h' pc'' stk'' loc'' xcp'' by auto
  from bisim
  have P, a[i] := e, h' ⊢ (E'[i] := e, xs') ↔ (stk'', loc'', pc'', xcp'')
  by(rule bisim1-bisims1.bisim1AAss1)
  moreover {
    assume no-call2 a pc
    hence no-call2 (a[i] := e) pc ∨ pc = length (compE2 a) by(auto simp add: no-call2-def) }
  ultimately show ?thesis using redo

```

$\text{by}(\text{auto simp del: call1.simps calls1.simps split: if-split-asm split del: if-split})(\text{blast intro:}$   
 $\text{AAss-}\tau\text{ExecrI1 AAss-}\tau\text{ExectI1 exec-move-AAssI1})+$   
 $\text{next}$   
 $\text{case (AAss1Red2 } E' v)$   
 $\text{note [simp] = } \langle a' = \text{Val } v \rangle \langle e' = \text{Val } v[E'] := e \rangle$   
 $\text{and red} = \langle \text{True}, P, t \vdash 1 \langle i, (h, xs) \rangle -ta \rightarrow \langle E', (h', xs') \rangle \rangle$   
 $\text{from red have } \tau: \tau\text{move1 } P h (\text{Val } v[i] := e) = \tau\text{move1 } P h i \text{ by}(\text{auto simp add: } \tau\text{move1.simps}$   
 $\tau\text{moves1.simps})$   
 $\text{from bisim1 have } s: xcp = \text{None } xs = \text{loc}$   
 $\text{and exec1: } \tau\text{Exec-mover-a } P t a h (\text{stk}, \text{loc}, \text{pc}, \text{None}) ([v], xs, \text{length (compE2 } a), \text{None})$   
 $\text{by}(\text{auto dest: bisim1Val2D1})$   
 $\text{from exec1 have } \tau\text{Exec-mover-a } P t (a[i] := e) h (\text{stk}, \text{loc}, \text{pc}, \text{None}) ([v], xs, \text{length (compE2 } a),$   
 $\text{None})$   
 $\text{by}(\text{rule AAss-}\tau\text{ExecrI1})$   
 $\text{moreover}$   
 $\text{from IH2[OF red] bsok obtain } pc'' \text{ stk'' loc'' xcp''}$   
 $\text{where bisim': } P, i, h' \vdash (E', xs') \leftrightarrow (\text{stk'', loc'', pc'', xcp''})$   
 $\text{and exec': } ?\text{exec } ta \ i \ i \ E' h \ [] \ xs \ 0 \ \text{None } h' \ pc'' \ \text{stk'' loc'' xcp'' by auto}$   
 $\text{have } ?\text{exec } ta \ (a[i] := e) \ (\text{Val } v[i] := e) \ (\text{Val } v[E'] := e) \ h \ ([] @ [v]) \ xs \ (\text{length (compE2 } a) +$   
 $0) \ \text{None } h' \ (\text{length (compE2 } a) + pc'') \ (\text{stk''} @ [v]) \ \text{loc'' xcp''}$   
 $\text{proof(cases } \tau\text{move1 } P h (\text{Val } v[i] := e))$   
 $\text{case True}$   
 $\text{with exec' } \tau \text{ have [simp]: } h = h' \text{ and } e: \text{sim-move } i \ E' P t i h \ ([], xs, 0, \text{None}) \ (\text{stk'', loc'', pc''},$   
 $\text{xcp''}) \text{ by auto}$   
 $\text{from } e \text{ have sim-move } (a[i] := e) \ (a[E'] := e) \ P t \ (a[i] := e) \ h \ ([] @ [v], xs, \text{length (compE2}$   
 $a) + 0, \text{None}) \ (\text{stk''} @ [v], \text{loc'', length (compE2 } a) + pc'', \text{xcp''})$   
 $\text{by(fastforce dest: AAss-}\tau\text{ExecrI2 AAss-}\tau\text{ExectI2})$   
 $\text{with True show ?thesis by auto}$   
 $\text{next}$   
 $\text{case False}$   
 $\text{with exec' } \tau \text{ obtain } pc' \text{ stk' loc' xcp'}$   
 $\text{where } e: \tau\text{Exec-mover-a } P t i h \ ([], xs, 0, \text{None}) \ (\text{stk'}, \text{loc'}, \text{pc'}, \text{xcp'})$   
 $\text{and } e': \text{exec-move-a } P t i h \ (\text{stk'}, \text{loc'}, \text{pc'}, \text{xcp'}) \ (\text{extTA2JVM (compP2 } P) \ ta) \ h' \ (\text{stk'', loc''},$   
 $\text{pc'', xcp''})$   
 $\text{and } \tau': \neg \tau\text{move2 (compP2 } P) \ h \ \text{stk'} \ i \ \text{pc' xcp'}$   
 $\text{and call: call1 } i = \text{None} \vee \text{no-call2 } i \ 0 \vee \text{pc}' = 0 \wedge \text{stk}' = [] \wedge \text{loc}' = xs \wedge \text{xcp}' = \text{None by}$   
 $\text{auto}$   
 $\text{from } e \text{ have } \tau\text{Exec-mover-a } P t \ (a[i] := e) \ h \ ([] @ [v], xs, \text{length (compE2 } a) + 0, \text{None}) \ (\text{stk'}$   
 $@ [v], \text{loc'}, \text{length (compE2 } a) + \text{pc'}, \text{xcp'}) \text{ by}(\text{rule AAss-}\tau\text{ExecrI2})$   
 $\text{moreover from } e' \text{ have exec-move-a } P t \ (a[i] := e) \ h \ (\text{stk'} @ [v], \text{loc'}, \text{length (compE2 } a) +$   
 $\text{pc'}, \text{xcp'}) \ (\text{extTA2JVM (compP2 } P) \ ta) \ h' \ (\text{stk''} @ [v], \text{loc'', length (compE2 } a) + \text{pc'', xcp''})$   
 $\text{by}(\text{rule exec-move-AAssI2})$   
 $\text{moreover from } e' \text{ have } \text{pc}' < \text{length (compE2 } i) \text{ by}(\text{auto elim: exec-meth.cases})$   
 $\text{with } \tau' \ e' \text{ have } \neg \tau\text{move2 (compP2 } P) \ h \ (\text{stk'} @ [v]) \ (a[i] := e) \ (\text{length (compE2 } a) + \text{pc'}) \ \text{xcp'}$   
 $\text{by}(\text{auto simp add: } \tau\text{instr-stk-drop-exec-move } \tau\text{move2-iff})$   
 $\text{moreover from red have call1 } (a'[i] := e) = \text{call1 } i \text{ by auto}$   
 $\text{moreover have no-call2 } i \ 0 \implies \text{no-call2 } (a[i] := e) \ (\text{length (compE2 } a))$   
 $\text{by}(\text{auto simp add: no-call2-def})$   
 $\text{ultimately show ?thesis using False call}$   
 $\text{by}(\text{auto simp del: split-paired-Ex call1.simps calls1.simps}) \text{ blast}$   
 $\text{qed}$   
 $\text{moreover from bisim'}$   
 $\text{have } P, a[i] := e, h' \vdash (\text{Val } v[E'] := e, xs') \leftrightarrow ((\text{stk''} @ [v]), \text{loc'', length (compE2 } a) + \text{pc'', xcp''})$   
 $\text{by}(\text{rule bisim1-bisims1.bisim1AAss2})$

**moreover from** *bisim1* **have**  $pc \neq \text{length}(\text{compE2 } a) \longrightarrow \text{no-call2 } (a[i] := e) \text{ pc}$   
**by**(*auto simp add: no-call2-def dest: bisim-Val-pc-not-Invoke bisim1-pc-length-compE2*)  
**ultimately show** *?thesis* **using**  $\tau \text{ exec1 } s$   
**apply**(*auto simp del: split-paired-Ex call1.simps calls1.simps split: if-split-asm split del: if-split*)  
**apply**(*blast intro:  $\tau \text{Exec-mover-trans}$ |fastforce elim!:  $\tau \text{Exec-mover-trans}$  simp del: split-paired-Ex call1.simps calls1.simps*)  
**done**  
**next**  
**case** (*AAss1Red3 E' v v'*)  
**note** [*simp*] =  $\langle i = \text{Val } v' \rangle \langle a' = \text{Val } v \rangle \langle e' = \text{Val } v \mid \text{Val } v' \rangle := E'$   
**and**  $\text{red} = \langle \text{True}, P, t \vdash 1 \langle e, (h, xs) \rangle -ta \rightarrow \langle E', (h', xs') \rangle \rangle$   
**from** *red* **have**  $\tau: \tau \text{move1 } P \ h \ (\text{Val } v \mid \text{Val } v' \rangle := e) = \tau \text{move1 } P \ h \ e$  **by**(*auto simp add:  $\tau \text{move1}.simps$   $\tau \text{moves1}.simps$* )  
**from** *bisim1* **have**  $s: xcp = \text{None } xs = \text{loc}$   
**and** *exec1*:  $\tau \text{Exec-mover-a } P \ t \ a \ h \ (stk, \text{loc}, pc, \text{None}) \ (\square @ [v], xs, \text{length}(\text{compE2 } a) + 0, \text{None})$   
**by**(*auto dest: bisim1Val2D1*)  
**from** *exec1* **have**  $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (stk, \text{loc}, pc, \text{None}) \ (\square @ [v], xs, \text{length}(\text{compE2 } a) + 0, \text{None})$   
**by**(*rule AAss- $\tau \text{ExecrI1}$* )  
**also from** *bisim2*[*of xs*]  
**have**  $\tau \text{Exec-mover-a } P \ t \ i \ h \ (\square, xs, 0, \text{None}) \ ([v'], xs, \text{length}(\text{compE2 } i), \text{None})$   
**by**(*auto dest: bisim1Val2D1*)  
**hence**  $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (\square @ [v], xs, \text{length}(\text{compE2 } a) + 0, \text{None}) \ ([v'] @ [v], xs, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i), \text{None})$   
**by**(*rule AAss- $\tau \text{ExecrI2}$* )  
**also** (*rtranclp-trans*) **from** *IH3*[*OF red*] *bsok* **obtain**  $pc'' \ stk'' \ loc'' \ xcp''$   
**where** *bisim'*:  $P, e, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$   
**and** *exec'*:  $?exec \ ta \ e \ e \ E' \ h \ \square \ xs \ 0 \ \text{None} \ h' \ pc'' \ stk'' \ loc'' \ xcp''$  **by** *auto*  
**have**  $?exec \ ta \ (a[i] := e) \ (\text{Val } v \mid \text{Val } v' \rangle := e) \ (\text{Val } v \mid \text{Val } v' \rangle := E') \ h \ (\square @ [v', v], xs, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + 0) \ \text{None} \ h' \ (\text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + pc'') \ (stk'' @ [v', v]) \ loc'' \ xcp''$   
**proof**(*cases  $\tau \text{move1 } P \ h \ (\text{Val } v \mid \text{Val } v' \rangle := e)$* )  
**case** *True*  
**with** *exec'*  $\tau$  **have** [*simp*]:  $h = h'$  **and**  $e: \text{sim-move } e \ E' \ P \ t \ e \ h \ (\square, xs, 0, \text{None}) \ (stk'', loc'', pc'', xcp'')$  **by** *auto*  
**from**  $e$  **have**  $\text{sim-move } (\text{Val } v \mid \text{Val } v' \rangle := e) \ (\text{Val } v \mid \text{Val } v' \rangle := E') \ P \ t \ (a[i] := e) \ h \ (\square @ [v', v], xs, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + 0, \text{None}) \ (stk'' @ [v', v], loc'', \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + pc'', xcp'')$   
**by**(*fastforce dest: AAss- $\tau \text{ExecI3}$  AAss- $\tau \text{ExecrI3}$  simp del: compE2.simps compEs2.simps*)  
**with** *True* **show** *?thesis* **by** *auto*  
**next**  
**case** *False*  
**with** *exec'*  $\tau$  **obtain**  $pc' \ stk' \ loc' \ xcp'$   
**where**  $e: \tau \text{Exec-mover-a } P \ t \ e \ h \ (\square, xs, 0, \text{None}) \ (stk', loc', pc', xcp')$   
**and**  $e': \text{exec-move-a } P \ t \ e \ h \ (stk', loc', pc', xcp') \ (\text{extTA2JVM } (\text{compP2 } P) \ ta) \ h' \ (stk'', loc'', pc'', xcp'')$   
**and**  $\tau': \neg \tau \text{move2 } (\text{compP2 } P) \ h \ stk' \ e \ pc' \ xcp'$   
**and**  $\text{call: call1 } e = \text{None} \vee \text{no-call2 } e \ 0 \vee pc' = 0 \wedge stk' = \square \wedge loc' = xs \wedge xcp' = \text{None}$  **by** *auto*  
**from**  $e$  **have**  $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (\square @ [v', v], xs, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + 0, \text{None}) \ (stk' @ [v', v], loc', \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + pc', xcp')$   
**by**(*rule AAss- $\tau \text{ExecrI3}$* )  
**moreover from**  $e'$  **have**  $\text{exec-move-a } P \ t \ (a[i] := e) \ h \ (stk' @ [v', v], loc', \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + pc', xcp') \ (\text{extTA2JVM } (\text{compP2 } P) \ ta) \ h' \ (stk'' @ [v', v], loc'', \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + pc'', xcp'')$

$a) + \text{length}(\text{compE2 } i) + pc'', xcp'')$   
**by**(rule *exec-move-AAssI3*)  
**moreover from**  $e' \tau'$   
**have**  $\neg \tau \text{move2}(\text{compP2 } P) h (stk' @ [v', v]) (a[i] := e) (\text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + pc') xcp'$   
**by**(auto simp add:  $\tau \text{instr-stk-drop-exec-move } \tau \text{move2-iff}$ )  
**moreover have**  $\text{call1 } (a'[i] := e) = \text{call1 } e$  **by** simp  
**moreover have**  $\text{no-call2 } e 0 \implies \text{no-call2 } (a[i] := e) (\text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i))$   
**by**(auto simp add: *no-call2-def*)  
**ultimately show** *?thesis* **using** *False call*  
**by**(auto simp del: *split-paired-Ex call1.simps calls1.simps*) *blast*  
**qed**  
**moreover from** *bisim'*  
**have**  $P, a[i] := e, h' \vdash (Val v \lfloor Val v' \rfloor := E', xs') \leftrightarrow ((stk'' @ [v', v]), loc'', \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + pc'', xcp'')$   
**by**(rule *bisim1-bisims1.bisim1AAss3*)  
**moreover from** *bisim1* **have**  $pc \neq \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) \longrightarrow \text{no-call2 } (a[i] := e) pc$   
**by**(auto simp add: *no-call2-def dest: bisim-Val-pc-not-Invoke bisim1-pc-length-compE2*)  
**ultimately show** *?thesis* **using**  $\tau \text{exec1 } s$   
**apply**(auto simp del: *split-paired-Ex call1.simps calls1.simps split: if-split-asm split del: if-split*)  
**apply**(*blast intro: \tau Exec-mover-trans|fastforce elim!: \tau Exec-mover-trans simp del: split-paired-Ex call1.simps calls1.simps*)  
**done**  
**next**  
**case** (*Red1AAss A U len I v U'*)  
**hence**  $[simp]: a' = \text{addr } A \ e' = \text{unit } i = Val (\text{Intg } I)$   
 $ta = \{ \text{WriteMem } A (ACell (\text{nat } (\text{sint } I))) \ v \} \ xs' = xs \ e = Val \ v$   
**and**  $hA: \text{typeof-addr } h \ A = \lfloor \text{Array-type } U \text{ len} \rfloor$  **and**  $I: 0 \leq s \ I \ \text{sint } I < \text{int } len$   
**and**  $v: \text{typeof}_h \ v = \lfloor U' \rfloor \ P \vdash U' \leq U$   
**and**  $h': \text{heap-write } h \ A (ACell (\text{nat } (\text{sint } I))) \ v \ h'$  **by** auto  
**have**  $\tau: \neg \tau \text{move1 } P \ h \ (AAss (\text{addr } A) (Val (\text{Intg } I)) (Val \ v))$  **by**(auto simp add:  $\tau \text{move1.simps}$   $\tau \text{moves1.simps}$ )  
**from** *bisim1* **have**  $s: xcp = \text{None} \ xs = loc$   
**and**  $\tau \text{Exec-mover-a } P \ t \ a \ h \ (stk, loc, pc, xcp) (\lfloor @ [\text{Addr } A], loc, \text{length}(\text{compE2 } a) + 0, \text{None})$   
**by**(auto dest: *bisim1Val2D1*)  
**hence**  $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (stk, loc, pc, xcp) (\lfloor @ [\text{Addr } A], loc, \text{length}(\text{compE2 } a) + 0, \text{None})$   
**by**-(rule *AAss-\tau ExecrI1*)  
**also from** *bisim2[of loc]*  
**have**  $\tau \text{Exec-mover-a } P \ t \ i \ h \ (\lfloor, loc, 0, \text{None}) ([\text{Intg } I], loc, \text{length}(\text{compE2 } i) + 0, \text{None})$   
**by**(auto dest: *bisim1Val2D1*)  
**hence**  $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (\lfloor @ [\text{Addr } A], loc, \text{length}(\text{compE2 } a) + 0, \text{None}) ([\text{Intg } I] @ [\text{Addr } A], loc, \text{length}(\text{compE2 } a) + (\text{length}(\text{compE2 } i) + 0), \text{None})$   
**by**(rule *AAss-\tau ExecrI2*)  
**also** (*rtrancp-trans*) **have**  $[\text{Intg } I] @ [\text{Addr } A] = \lfloor @ [\text{Intg } I, \text{Addr } A]$  **by** simp  
**also note** *add.assoc[symmetric]*  
**also from** *bisim3[of loc]* **have**  $\tau \text{Exec-mover-a } P \ t \ e \ h \ (\lfloor, loc, 0, \text{None}) ([v], loc, \text{length}(\text{compE2 } e), \text{None})$   
**by**(auto dest: *bisim1Val2D1*)  
**hence**  $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (\lfloor @ [\text{Intg } I, \text{Addr } A], loc, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + 0, \text{None}) ([v] @ [\text{Intg } I, \text{Addr } A], loc, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + \text{length}(\text{compE2 } e), \text{None})$   
**by**(rule *AAss-\tau ExecrI3*)

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also (rtranclp-trans) from  $hA \ I \ v \ h'$ 
have exec-move-a  $P \ t \ (a[i] := e) \ h \ ([v, \text{Intg } I, \text{Addr } A], \text{loc}, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + \text{length}(\text{compE2 } e), \text{None})$ 
 $\{ \text{WriteMem } A \ (A\text{Cell } (\text{nat } (\text{sint } I))) \ v \}$ 
 $h' \ ([], \text{loc}, \text{Suc}(\text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + \text{length}(\text{compE2 } e))), \text{None})$ 
unfolding exec-move-def by-(rule exec-instr, auto simp add: compP2-def is-Ref-def)
moreover have  $\tau\text{move2}(\text{compP2 } P) \ h \ [v, \text{Intg } I, \text{Addr } A] \ (a[i] := e) \ (\text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + \text{length}(\text{compE2 } e)) \ \text{None} \implies \text{False}$ 
by(simp add:  $\tau\text{move2-iff}$ )
moreover
have  $P, a[i] := e, h' \vdash (\text{unit}, \text{loc}) \leftrightarrow ([], \text{loc}, \text{Suc}(\text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + \text{length}(\text{compE2 } e))), \text{None})$ 
by(rule bisim1-bisims1.bisim1AAss4)
ultimately show ?thesis using  $s \ \tau$  by(auto simp add: ta-upd-simps) blast
next
case (Red1AAssNull  $v \ v'$ )
note  $[simp] = \langle a' = \text{null} \rangle \langle e' = \text{THROW NullPointer} \rangle \langle i = \text{Val } v \rangle \langle xs' = xs \rangle \langle ta = \varepsilon \rangle \langle h' = h \rangle$ 
 $\langle e = \text{Val } v' \rangle$ 
have  $\tau: \neg \tau\text{move1 } P \ h \ (AAss \ \text{null} \ (Val \ v) \ (Val \ v'))$  by(auto simp add:  $\tau\text{move1.simps}$   $\tau\text{moves1.simps}$ )
from bisim1 have  $s: xcp = \text{None} \ xs = \text{loc}$ 
and  $\tau\text{Exec-mover-a } P \ t \ a \ h \ (stk, \text{loc}, pc, xcp) \ ([[] @ [\text{Null}], \text{loc}, \text{length}(\text{compE2 } a) + 0, \text{None})$ 
by(auto dest: bisim1Val2D1)
hence  $\tau\text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (stk, \text{loc}, pc, xcp) \ ([[] @ [\text{Null}], \text{loc}, \text{length}(\text{compE2 } a) + 0, \text{None})$ 
by-(rule AAss- $\tau\text{ExecrI1}$ )
also from bisim2[of loc] have  $\tau\text{Exec-mover-a } P \ t \ i \ h \ ([], \text{loc}, 0, \text{None}) \ ([v], \text{loc}, \text{length}(\text{compE2 } i) + 0, \text{None})$ 
by(auto dest: bisim1Val2D1)
hence  $\tau\text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ ([[] @ [\text{Null}], \text{loc}, \text{length}(\text{compE2 } a) + 0, \text{None}) \ ([v] @ [\text{Null}], \text{loc}, \text{length}(\text{compE2 } a) + (\text{length}(\text{compE2 } i) + 0), \text{None})$ 
by(rule AAss- $\tau\text{ExecrI2}$ )
also (rtranclp-trans) have  $[v] @ [\text{Null}] = [] @ [v, \text{Null}]$  by simp
also note add.assoc[symmetric]
also from bisim3[of loc] have  $\tau\text{Exec-mover-a } P \ t \ e \ h \ ([], \text{loc}, 0, \text{None}) \ ([v'], \text{loc}, \text{length}(\text{compE2 } e), \text{None})$ 
by(auto dest: bisim1Val2D1)
hence  $\tau\text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ ([[] @ [v, \text{Null}], \text{loc}, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + 0, \text{None}) \ ([v'] @ [v, \text{Null}], \text{loc}, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + \text{length}(\text{compE2 } e), \text{None})$ 
by(rule AAss- $\tau\text{ExecrI3}$ )
also (rtranclp-trans)
have exec-move-a  $P \ t \ (a[i] := e) \ h \ ([v', v, \text{Null}], \text{loc}, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + \text{length}(\text{compE2 } e), \text{None}) \ \varepsilon$ 
 $h \ ([v', v, \text{Null}], \text{loc}, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + \text{length}(\text{compE2 } e), [\text{addr-of-sys-xcpt NullPointer}])$ 
unfolding exec-move-def by-(rule exec-instr, auto simp add: is-Ref-def)
moreover have  $\tau\text{move2}(\text{compP2 } P) \ h \ [v', v, \text{Null}] \ (a[i] := e) \ (\text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + \text{length}(\text{compE2 } e)) \ \text{None} \implies \text{False}$ 
by(simp add:  $\tau\text{move2-iff}$ )
moreover
have  $P, a[i] := e, h' \vdash (\text{THROW NullPointer}, \text{loc}) \leftrightarrow ([v', v, \text{Null}], \text{loc}, \text{length}(\text{compE2 } a) + \text{length}(\text{compE2 } i) + \text{length}(\text{compE2 } e), [\text{addr-of-sys-xcpt NullPointer}])$ 
by(rule bisim1-bisims1.bisim1AAssFail)

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ultimately show *?thesis* using  $s \tau$  by auto blast

next

case (Red1AAssBounds A U len I v)

hence [simp]:  $a' = \text{addr } A \ e' = \text{THROW ArrayIndexOutOfBounds } i = \text{Val } (\text{Intg } I) \ xs' = xs \ ta = \varepsilon \ h' = h \ e = \text{Val } v$

and  $hA: \text{typeof-addr } h \ A = \lfloor \text{Array-type } U \ \text{len} \rfloor$  and  $I: I < s \ 0 \vee \text{int } \text{len} \leq \text{sint } I$  by auto

have  $\tau: \neg \tau \text{move1 } P \ h \ (AAss \ (\text{addr } A) \ i \ e)$  by (auto simp add:  $\tau \text{move1.simps}$   $\tau \text{moves1.simps}$ )

from bisim1 have  $s: xcp = \text{None} \ xs = \text{loc}$

and  $\tau \text{Exec-mover-a } P \ t \ a \ h \ (stk, \text{loc}, pc, xcp) \ (\llbracket \rrbracket @ [\text{Addr } A], \text{loc}, \text{length } (\text{compE2 } a) + 0, \text{None})$

by (auto dest: bisim1Val2D1)

hence  $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (stk, \text{loc}, pc, xcp) \ (\llbracket \rrbracket @ [\text{Addr } A], \text{loc}, \text{length } (\text{compE2 } a) + 0, \text{None})$

by (rule AAss- $\tau \text{ExecrI1}$ )

also from bisim2[of loc]

have  $\tau \text{Exec-mover-a } P \ t \ i \ h \ (\llbracket \rrbracket, \text{loc}, 0, \text{None}) \ ([\text{Intg } I], \text{loc}, \text{length } (\text{compE2 } i) + 0, \text{None})$

by (auto dest: bisim1Val2D1)

hence  $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (\llbracket \rrbracket @ [\text{Addr } A], \text{loc}, \text{length } (\text{compE2 } a) + 0, \text{None}) \ ([\text{Intg } I] @ [\text{Addr } A], \text{loc}, \text{length } (\text{compE2 } a) + (\text{length } (\text{compE2 } i) + 0), \text{None})$

by (rule AAss- $\tau \text{ExecrI2}$ )

also (rtranclp-trans) have  $[\text{Intg } I] @ [\text{Addr } A] = \llbracket \rrbracket @ [\text{Intg } I, \text{Addr } A]$  by simp

also note add.assoc[symmetric]

also from bisim3[of loc]

have  $\tau \text{Exec-mover-a } P \ t \ e \ h \ (\llbracket \rrbracket, \text{loc}, 0, \text{None}) \ ([v], \text{loc}, \text{length } (\text{compE2 } e), \text{None})$

by (auto dest: bisim1Val2D1)

hence  $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (\llbracket \rrbracket @ [\text{Intg } I, \text{Addr } A], \text{loc}, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + 0, \text{None}) \ ([v] @ [\text{Intg } I, \text{Addr } A], \text{loc}, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + \text{length } (\text{compE2 } e), \text{None})$

by (rule AAss- $\tau \text{ExecrI3}$ )

also (rtranclp-trans) from  $hA \ I$

have  $\text{exec-move-a } P \ t \ (a[i] := e) \ h \ ([v, \text{Intg } I, \text{Addr } A], \text{loc}, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + \text{length } (\text{compE2 } e), \text{None}) \ \varepsilon$

$h \ ([v, \text{Intg } I, \text{Addr } A], \text{loc}, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + \text{length } (\text{compE2 } e), \lfloor \text{addr-of-sys-xcpt ArrayIndexOutOfBounds} \rfloor)$

unfolding  $\text{exec-move-def}$  by (rule  $\text{exec-instr}$ , auto simp add:  $\text{is-Ref-def}$ )

moreover have  $\tau \text{move2 } (\text{compP2 } P) \ h \ [v, \text{Intg } I, \text{Addr } A] \ (a[i] := e) \ (\text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + \text{length } (\text{compE2 } e)) \ \text{None} \implies \text{False}$

by (simp add:  $\tau \text{move2-iff}$ )

moreover

have  $P, a[i] := e, h' \vdash (\text{THROW ArrayIndexOutOfBounds}, \text{loc}) \leftrightarrow ([v, \text{Intg } I, \text{Addr } A], \text{loc}, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + \text{length } (\text{compE2 } e), \lfloor \text{addr-of-sys-xcpt ArrayIndexOutOfBounds} \rfloor)$

by (rule bisim1-bisims1.bisim1AAssFail)

ultimately show *?thesis* using  $s \tau$  by auto blast

next

case (Red1AAssStore A U len I v U')

hence [simp]:  $a' = \text{addr } A \ e' = \text{THROW ArrayStore } i = \text{Val } (\text{Intg } I) \ xs' = xs \ ta = \varepsilon \ h' = h \ e = \text{Val } v$

and  $hA: \text{typeof-addr } h \ A = \lfloor \text{Array-type } U \ \text{len} \rfloor$  and  $I: 0 \leq s \ I \ \text{sint } I < \text{int } \text{len}$

and  $U: \neg P \vdash U' \leq U \ \text{typeof}_h \ v = \lfloor U' \rfloor$  by auto

have  $\tau: \neg \tau \text{move1 } P \ h \ (AAss \ (\text{addr } A) \ i \ e)$  by (auto simp add:  $\tau \text{move1.simps}$   $\tau \text{moves1.simps}$ )

from bisim1 have  $s: xcp = \text{None} \ xs = \text{loc}$

and  $\tau \text{Exec-mover-a } P \ t \ a \ h \ (stk, \text{loc}, pc, xcp) \ (\llbracket \rrbracket @ [\text{Addr } A], \text{loc}, \text{length } (\text{compE2 } a) + 0, \text{None})$

by (auto dest: bisim1Val2D1)

hence  $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (stk, \text{loc}, pc, xcp) \ (\llbracket \rrbracket @ [\text{Addr } A], \text{loc}, \text{length } (\text{compE2 } a) + 0, \text{None})$

```

    by-(rule AAss-τExecrI1)
  also from bisim2[of loc]
  have τExec-mover-a P t i h ([], loc, 0, None) ([Intg I], loc, length (compE2 i) + 0, None)
    by(auto dest: bisim1Val2D1)
  hence τExec-mover-a P t (a[i] := e) h ([[] @ [Addr A], loc, length (compE2 a) + 0, None) ([Intg
I] @ [Addr A], loc, length (compE2 a) + (length (compE2 i) + 0), None)
    by(rule AAss-τExecrI2)
  also (rtrancpl-trans) have [Intg I] @ [Addr A] = [] @ [Intg I, Addr A] by simp
  also note add.assoc[symmetric]
  also from bisim3[of loc]
  have τExec-mover-a P t e h ([], loc, 0, None) ([v], loc, length (compE2 e), None)
    by(auto dest: bisim1Val2D1)
  hence τExec-mover-a P t (a[i] := e) h ([[] @ [Intg I, Addr A], loc, length (compE2 a) + length
(compE2 i) + 0, None) ([v] @ [Intg I, Addr A], loc, length (compE2 a) + length (compE2 i) + length
(compE2 e), None)
    by(rule AAss-τExecrI3)
  also (rtrancpl-trans) from hA I U
  have exec-move-a P t (a[i] := e) h ([v, Intg I, Addr A], loc, length (compE2 a) + length (compE2
i) + length (compE2 e), None) ε
    h ([v, Intg I, Addr A], loc, length (compE2 a) + length (compE2 i) +
length (compE2 e), [addr-of-sys-xcpt ArrayStore])
  unfolding exec-move-def by-(rule exec-instr, auto simp add: is-Ref-def compP2-def)
  moreover have τmove2 (compP2 P) h [v, Intg I, Addr A] (a[i] := e) (length (compE2 a) +
length (compE2 i) + length (compE2 e)) None ⇒ False
    by(simp add: τmove2-iff)
  moreover
  have P, a[i] := e, h' ⊢ (THROW ArrayStore, loc) ↔ ([v, Intg I, Addr A], loc, length (compE2 a)
+ length (compE2 i) + length (compE2 e), [addr-of-sys-xcpt ArrayStore])
    by(rule bisim1-bisims1.bisim1AAssFail)
  ultimately show ?thesis using s τ by auto blast
next
case (AAss1Throw1 A)
hence [simp]: a' = Throw A ta = ε e' = Throw A h' = h xs' = xs by auto
have τ: τmove1 P h (Throw A[i] := e) by(rule τmove1AAssThrow1)
from bisim1 have xcp = [A] ∨ xcp = None by(auto dest: bisim1-ThrowD)
thus ?thesis
proof
  assume [simp]: xcp = [A]
  with bisim1 have P, a[i] := e, h ⊢ (Throw A, xs) ↔ (stk, loc, pc, xcp)
    by(auto intro: bisim1-bisims1.intros)
  thus ?thesis using τ by(fastforce)
next
  assume [simp]: xcp = None
  with bisim1 obtain pc' where τExec-mover-a P t a h (stk, loc, pc, None) ([Addr A], loc, pc',
[A])
    and bisim': P, a, h ⊢ (Throw A, xs) ↔ ([Addr A], loc, pc', [A])
    and [simp]: xs = loc
    by(auto dest: bisim1-Throw-τExec-mover)
  hence τExec-mover-a P t (a[i] := e) h (stk, loc, pc, None) ([Addr A], loc, pc', [A])
    by-(rule AAss-τExecrI1)
  moreover from bisim'
  have P, a[i] := e, h ⊢ (Throw A, xs) ↔ ([Addr A], loc, pc', [A])
    by(auto intro: bisim1-bisims1.bisim1AAssThrow1)
  ultimately show ?thesis using τ by auto

```



qed  
next  
case (AAss1Throw2 v ad)  
note [simp] =  $\langle a' = \text{Val } v \rangle \langle i = \text{Throw } ad \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw } ad \rangle \langle h' = h \rangle \langle xs' = xs \rangle$   
from bisim1 have s: xcp = None xs = loc  
and  $\tau\text{Exec-mover-a } P \ t \ a \ h \ (stk, loc, pc, xcp) \ ([v], loc, length \ (compE2 \ a), None)$   
by(auto dest: bisim1Val2D1)  
hence  $\tau\text{Exec-mover-a } P \ t \ (a[\text{Throw } ad] := e) \ h \ (stk, loc, pc, xcp) \ ([v], loc, length \ (compE2 \ a), None)$   
by-(rule AAAss- $\tau\text{ExecrI1}$ )  
also have  $\tau\text{Exec-mover-a } P \ t \ (a[\text{Throw } ad] := e) \ h \ ([v], loc, length \ (compE2 \ a), None) \ ([Addr \ ad, v], loc, Suc \ (length \ (compE2 \ a)), [ad])$   
by(rule  $\tau\text{Execr2step}$ )(auto simp add: exec-move-def exec-meth-instr  $\tau\text{move2-iff}$   $\tau\text{move1.simps}$   $\tau\text{moves1.simps}$ )  
also (rtrancpl-trans)  
have  $P, a[\text{Throw } ad] := e, h \vdash (\text{Throw } ad, loc) \leftrightarrow ([Addr \ ad] @ [v], loc, (length \ (compE2 \ a) + length \ (compE2 \ (addr \ ad))), [ad])$   
by(rule bisim1AAssThrow2[OF bisim1Throw2])  
moreover have  $\tau\text{move1 } P \ h \ (a'[\text{Throw } ad] := e)$  by(auto intro:  $\tau\text{move1AAssThrow2}$ )  
ultimately show ?thesis using s by auto  
next  
case (AAss1Throw3 va vi ad)  
note [simp] =  $\langle a' = \text{Val } va \rangle \langle i = \text{Val } vi \rangle \langle e = \text{Throw } ad \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw } ad \rangle \langle h' = h \rangle \langle xs' = xs \rangle$   
from bisim1 have s: xcp = None xs = loc  
and  $\tau\text{Exec-mover-a } P \ t \ a \ h \ (stk, loc, pc, xcp) \ ([va], loc, length \ (compE2 \ a), None)$   
by(auto dest: bisim1Val2D1)  
hence  $\tau\text{Exec-mover-a } P \ t \ (a[i] := \text{Throw } ad) \ h \ (stk, loc, pc, xcp) \ ([va], loc, length \ (compE2 \ a), None)$   
by-(rule AAAss- $\tau\text{ExecrI1}$ )  
also from bisim2[of loc] have  $\tau\text{Exec-mover-a } P \ t \ i \ h \ ([], loc, 0, None) \ ([vi], loc, length \ (compE2 \ i), None)$   
by(auto dest: bisim1Val2D1)  
from AAAss- $\tau\text{ExecrI2}$ [OF this, of a e va]  
have  $\tau\text{Exec-mover-a } P \ t \ (a[i] := \text{Throw } ad) \ h \ ([va], loc, length \ (compE2 \ a), None) \ ([vi, va], loc, length \ (compE2 \ a) + length \ (compE2 \ i), None)$  by simp  
also (rtrancpl-trans)  
have  $\tau\text{Exec-mover-a } P \ t \ (a[i] := \text{Throw } ad) \ h \ ([vi, va], loc, length \ (compE2 \ a) + length \ (compE2 \ i), None) \ ([Addr \ ad, vi, va], loc, Suc \ (length \ (compE2 \ a) + length \ (compE2 \ i)), [ad])$   
by(rule  $\tau\text{Execr2step}$ )(auto simp add: exec-move-def exec-meth-instr  $\tau\text{move2-iff}$   $\tau\text{move1.simps}$   $\tau\text{moves1.simps}$ )  
also (rtrancpl-trans)  
have  $P, a[i] := \text{Throw } ad, h \vdash (\text{Throw } ad, loc) \leftrightarrow ([Addr \ ad] @ [vi, va], loc, (length \ (compE2 \ a) + length \ (compE2 \ i) + length \ (compE2 \ (addr \ ad))), [ad])$   
by(rule bisim1AAssThrow3[OF bisim1Throw2])  
moreover have  $\tau\text{move1 } P \ h \ (AAss \ a' \ (\text{Val } vi) \ (\text{Throw } ad))$  by(auto intro:  $\tau\text{move1AAssThrow3}$ )  
ultimately show ?thesis using s by auto  
qed  
next  
case (bisim1AAss2 i n i' xs stk loc pc xcp a e v1)  
note IH2 = bisim1AAss2.IH(2)  
note IH3 = bisim1AAss2.IH(6)  
note bisim2 =  $\langle P, i, h \vdash (i', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$   
note bisim1 =  $\langle \bigwedge xs. P, a, h \vdash (a, xs) \leftrightarrow ([], xs, 0, None) \rangle$

```

note  $\text{bisim3} = \langle \bigwedge xs. P, e, h \vdash (e, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$ 
note  $\text{bsok} = \langle \text{bsok } (a[i] := e) \ n \rangle$ 
from  $\langle \text{True}, P, t \vdash 1 \ \langle \text{Val } v1[i] := e, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$  show  $?case$ 
proof cases
  case  $(AAss1Red2 \ E')$ 
    note  $[simp] = \langle e' = \text{Val } v1[E] := e \rangle$ 
    and  $\text{red} = \langle \text{True}, P, t \vdash 1 \ \langle i', (h, xs) \rangle -ta \rightarrow \langle E', (h', xs') \rangle \rangle$ 
    from  $\text{red}$  have  $\tau: \tau \text{move1 } P \ h \ (\text{Val } v1[i] := e) = \tau \text{move1 } P \ h \ i'$  by  $(\text{auto simp add: } \tau \text{move1.simps } \tau \text{moves1.simps})$ 
    from  $IH2[OF \ \text{red}] \ \text{bsok}$  obtain  $pc'' \ stk'' \ loc'' \ xcp''$ 
    where  $\text{bisim}' : P, i, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$ 
    and  $\text{exec}' : ?exec \ ta \ i \ i' \ E' \ h \ stk \ loc \ pc \ xcp \ h' \ pc'' \ stk'' \ loc'' \ xcp''$  by auto
    have  $?exec \ ta \ (a[i] := e) \ (\text{Val } v1[i] := e) \ (\text{Val } v1[E] := e) \ h \ (stk \ @ \ [v1]) \ loc \ (\text{length} \ (\text{compE2} \ a) + pc) \ xcp \ h' \ (\text{length} \ (\text{compE2} \ a) + pc'') \ (stk'' \ @ \ [v1]) \ loc'' \ xcp''$ 
    proof  $(\text{cases } \tau \text{move1 } P \ h \ (\text{Val } v1[i] := e))$ 
      case True
        with  $\text{exec}' \ \tau$  have  $[simp]: h = h'$  and  $e: \text{sim-move } i' \ E' \ P \ t \ i \ h \ (stk, loc, pc, xcp) \ (stk'', loc'', pc'', xcp'')$  by auto
        from  $e$  have  $\text{sim-move } (\text{Val } v1[i] := e) \ (\text{Val } v1[E] := e) \ P \ t \ (a[i] := e) \ h \ (stk \ @ \ [v1], loc, \text{length} \ (\text{compE2} \ a) + pc, xcp) \ (stk'' \ @ \ [v1], loc'', \text{length} \ (\text{compE2} \ a) + pc'', xcp'')$ 
        by  $(\text{fastforce dest: } AAss\text{-}\tau \text{ExecrI2} \ AAss\text{-}\tau \text{ExectI2} \ \text{simp del: compE2.simps compEs2.simps})$ 
        with True show  $?thesis$  by auto
      next
        case False
          with  $\text{exec}' \ \tau$  obtain  $pc' \ stk' \ loc' \ xcp'$ 
          where  $e: \tau \text{Exec-mover-a } P \ t \ i \ h \ (stk, loc, pc, xcp) \ (stk', loc', pc', xcp')$ 
          and  $e': \text{exec-move-a } P \ t \ i \ h \ (stk', loc', pc', xcp') \ (\text{extTA2JVM} \ (\text{compP2} \ P) \ ta) \ h' \ (stk'', loc'', pc'', xcp'')$ 
          and  $\tau': \neg \tau \text{move2} \ (\text{compP2} \ P) \ h \ stk' \ i \ pc' \ xcp'$ 
          and  $\text{call}: \text{call1 } i' = \text{None} \vee \text{no-call2 } i \ pc \vee pc' = pc \wedge stk' = stk \wedge loc' = loc \wedge xcp' = xcp$  by auto
          from  $e$  have  $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (stk \ @ \ [v1], loc, \text{length} \ (\text{compE2} \ a) + pc, xcp) \ (stk' \ @ \ [v1], loc', \text{length} \ (\text{compE2} \ a) + pc', xcp')$  by  $(\text{rule } AAss\text{-}\tau \text{ExecrI2})$ 
          moreover from  $e'$  have  $\text{exec-move-a } P \ t \ (a[i] := e) \ h \ (stk' \ @ \ [v1], loc', \text{length} \ (\text{compE2} \ a) + pc', xcp') \ (\text{extTA2JVM} \ (\text{compP2} \ P) \ ta) \ h' \ (stk'' \ @ \ [v1], loc'', \text{length} \ (\text{compE2} \ a) + pc'', xcp'')$ 
          by  $(\text{rule } \text{exec-move-AAssI2})$ 
          moreover from  $e'$  have  $pc' < \text{length} \ (\text{compE2} \ i)$  by  $(\text{auto elim: exec-meth.cases})$ 
          with  $\tau' \ e'$  have  $\neg \tau \text{move2} \ (\text{compP2} \ P) \ h \ (stk' \ @ \ [v1]) \ (a[i] := e) \ (\text{length} \ (\text{compE2} \ a) + pc')$ 
          by  $(\text{auto simp add: } \tau \text{instr-stk-drop-exec-move } \tau \text{move2-iff})$ 
          moreover from  $\text{red}$  have  $\text{call1} \ (\text{Val } v1[i] := e) = \text{call1 } i'$  by auto
          moreover have  $\text{no-call2 } i \ pc \implies \text{no-call2 } (a[i] := e) \ (\text{length} \ (\text{compE2} \ a) + pc)$ 
          by  $(\text{auto simp add: no-call2-def})$ 
          ultimately show  $?thesis$  using False call by  $(\text{auto simp del: split-paired-Ex call1.simps calls1.simps})$ 
    qed
    moreover from  $\text{bisim}'$ 
    have  $P, a[i] := e, h' \vdash (\text{Val } v1[E] := e, xs') \leftrightarrow ((stk'' \ @ \ [v1]), loc'', \text{length} \ (\text{compE2} \ a) + pc'', xcp'')$ 
    by  $(\text{rule } \text{bisim1-bisims1.bisim1AAss2})$ 
    ultimately show  $?thesis$ 
    apply  $(\text{auto simp del: split-paired-Ex call1.simps calls1.simps split: if-split-asm split del: if-split})$ 
    apply  $(\text{blast intro: } \tau \text{Exec-mover-trans})$ 
    done

```

**next**  
**case** (*AAss1Red3*  $E' v'$ )  
**note** [*simp*] =  $\langle i' = \text{Val } v' \rangle \langle e' = \text{Val } v1 \mid \text{Val } v' \rangle := E'$   
**and**  $\text{red} = \langle \text{True}, P, t \vdash 1 \langle e, (h, xs) \rangle -ta \rightarrow \langle E', (h', xs') \rangle \rangle$   
**from**  $\text{red}$  **have**  $\tau: \tau \text{move1 } P h (\text{Val } v1 \mid \text{Val } v' \rangle := e) = \tau \text{move1 } P h e$   
**by**(*auto simp add:  $\tau \text{move1.simps}$   $\tau \text{moves1.simps}$* )  
**from** *bisim2* **have**  $s: xcp = \text{None } xs = \text{loc}$   
**and**  $\text{exec1}: \tau \text{Exec-mover-a } P t i h (\text{stk}, \text{loc}, \text{pc}, xcp) ([v'], xs, \text{length } (\text{compE2 } i), \text{None})$   
**by**(*auto dest: bisim1Val2D1*)  
**hence**  $\tau \text{Exec-mover-a } P t (a[i] := e) h (\text{stk} @ [v1], \text{loc}, \text{length } (\text{compE2 } a) + \text{pc}, xcp) ([v'] @ [v1], xs, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i), \text{None})$   
**by**-(*rule AAss- $\tau \text{ExecrI2}$* )  
**moreover from** *IH3[OF red] bsok* **obtain**  $pc'' \text{stk}'' \text{loc}'' xcp''$   
**where**  $\text{bisim}': P, e, h' \vdash (E', xs') \leftrightarrow (\text{stk}'', \text{loc}'', pc'', xcp'')$   
**and**  $\text{exec}': ?\text{exec } ta e e E' h [] xs 0 \text{None } h' pc'' \text{stk}'' \text{loc}'' xcp''$  **by** *auto*  
**have**  $?\text{exec } ta (a[i] := e) (\text{Val } v1 \mid \text{Val } v' \rangle := e) (\text{Val } v1 \mid \text{Val } v' \rangle := E') h ([v'] @ [v1], xs, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + 0) \text{None } h' (\text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + pc'') (\text{stk}'' @ [v', v1]) \text{loc}'' xcp''$   
**proof**(*cases  $\tau \text{move1 } P h (\text{Val } v1 \mid \text{Val } v' \rangle := e)$* )  
**case** *True*  
**with**  $\text{exec}' \tau$  **have** [*simp*]:  $h = h'$   
**and**  $e: \text{sim-move } e E' P t e h ([v'], xs, 0, \text{None}) (\text{stk}'', \text{loc}'', pc'', xcp'')$  **by** *auto*  
**from**  $e$  **have**  $\text{sim-move } (\text{Val } v1 \mid \text{Val } v' \rangle := e) (\text{Val } v1 \mid \text{Val } v' \rangle := E') P t (a[i] := e) h ([v'] @ [v'], xs, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + 0, \text{None}) (\text{stk}'' @ [v', v1], \text{loc}'', \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + pc'', xcp'')$   
**by**(*fastforce dest: AAss- $\tau \text{ExecI3}$  AAss- $\tau \text{ExecrI3}$  simp del: compE2.simps compEs2.simps*)  
**with** *True* **show** *?thesis* **by** *auto*  
**next**  
**case** *False*  
**with**  $\text{exec}' \tau$  **obtain**  $pc' \text{stk}' \text{loc}' xcp'$   
**where**  $e: \tau \text{Exec-mover-a } P t e h ([v'], xs, 0, \text{None}) (\text{stk}', \text{loc}', pc', xcp')$   
**and**  $e': \text{exec-move-a } P t e h (\text{stk}', \text{loc}', pc', xcp') (\text{extTA2JVM } (\text{compP2 } P) ta) h' (\text{stk}'', \text{loc}'', pc'', xcp'')$   
**and**  $\tau': \neg \tau \text{move2 } (\text{compP2 } P) h \text{stk}' e pc' xcp'$   
**and**  $\text{call}: \text{call1 } e = \text{None} \vee \text{no-call2 } e 0 \vee pc' = 0 \wedge \text{stk}' = [] \wedge \text{loc}' = xs \wedge xcp' = \text{None}$  **by** *auto*  
**from**  $e$  **have**  $\tau \text{Exec-mover-a } P t (a[i] := e) h ([v'] @ [v', v1], xs, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + 0, \text{None}) (\text{stk}' @ [v', v1], \text{loc}', \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + pc', xcp')$   
**by**(*rule AAss- $\tau \text{ExecrI3}$* )  
**moreover from**  $e'$  **have**  $\text{exec-move-a } P t (a[i] := e) h (\text{stk}' @ [v', v1], \text{loc}', \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + pc', xcp') (\text{extTA2JVM } (\text{compP2 } P) ta) h' (\text{stk}'' @ [v', v1], \text{loc}'', \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + pc'', xcp'')$   
**by**(*rule exec-move-AAssI3*)  
**moreover from**  $e' \tau'$  **have**  $\neg \tau \text{move2 } (\text{compP2 } P) h (\text{stk}' @ [v', v1]) (a[i] := e) (\text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + pc') xcp'$   
**by**(*auto simp add:  $\tau \text{instr-stk-drop-exec-move}$   $\tau \text{move2-iff}$* )  
**moreover from**  $\text{red}$  **have**  $\text{call1 } (\text{Val } v1 \mid \text{Val } v' \rangle := e) = \text{call1 } e$  **by** *auto*  
**moreover have**  $\text{no-call2 } e 0 \implies \text{no-call2 } (a[i] := e) (\text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i))$   
**by**(*auto simp add: no-call2-def*)  
**ultimately show** *?thesis* **using** *False call* **by**(*auto simp del: split-paired-Ex call1.simps calls1.simps*)  
**blast**  
**qed**  
**moreover from** *bisim'*  
**have**  $P, a[i] := e, h' \vdash (\text{Val } v1 \mid \text{Val } v' \rangle := E', xs') \leftrightarrow ((\text{stk}'' @ [v', v1]), \text{loc}'', \text{length } (\text{compE2 } a))$

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+ length (compE2 i) + pc'', xcp'')
  by(rule bisim1-bisims1.bisim1AAss3)
  moreover from bisim2 have pc ≠ length (compE2 i) ⟶ no-call2 (a[i] := e) (length (compE2
a) + pc)
  by(auto simp add: no-call2-def dest: bisim-Val-pc-not-Invoke bisim1-pc-length-compE2)
  ultimately show ?thesis using τ exec1 s
  apply(auto simp del: split-paired-Ex call1.simps calls1.simps split: if-split-asm split del: if-split)
  apply(blast intro: τExec-mover-trans|fastforce elim!: τExec-mover-trans simp del: split-paired-Ex
call1.simps calls1.simps)+
  done
next
case (Red1AAss A U len I v U')
hence [simp]: v1 = Addr A e' = unit i' = Val (Intg I)
  ta = {WriteMem A (ACell (nat (sint I))) v} xs' = xs e = Val v
  and hA: typeof-addr h A = [Array-type U len] and I: 0 ≤ s I sint I < int len
  and v: typeofh v = [U'] P ⊢ U' ≤ U
  and h': heap-write h A (ACell (nat (sint I))) v h' by auto
have τ: ⊢ τmove1 P h (AAss (addr A) (Val (Intg I)) (Val v)) by(auto simp add: τmove1.simps
τmoves1.simps)
from bisim2 have s: xcp = None xs = loc
  and τExec-mover-a P t i h (stk, loc, pc, xcp) ([Intg I], loc, length (compE2 i), None)
  by(auto dest: bisim1Val2D1)
hence τExec-mover-a P t (a[i] := e) h (stk @ [Addr A], loc, length (compE2 a) + pc, xcp) ([Intg
I] @ [Addr A], loc, length (compE2 a) + length (compE2 i), None)
  by-(rule AAAss-τExecrI2)
hence τExec-mover-a P t (a[i] := e) h (stk @ [Addr A], loc, length (compE2 a) + pc, xcp) ([ @
[Intg I, Addr A], loc, length (compE2 a) + length (compE2 i) + 0, None) by simp
also from bisim3[of loc] have τExec-mover-a P t e h ([, loc, 0, None) ([v], loc, length (compE2
e), None)
  by(auto dest: bisim1Val2D1)
hence τExec-mover-a P t (a[i] := e) h ([ @ [Intg I, Addr A], loc, length (compE2 a) + length
(compE2 i) + 0, None) ([v] @ [Intg I, Addr A], loc, length (compE2 a) + length (compE2 i) + length
(compE2 e), None)
  by(rule AAAss-τExecrI3)
also (rtranclp-trans) from hA I v h'
have exec-move-a P t (a[i] := e) h ([v, Intg I, Addr A], loc, length (compE2 a) + length (compE2
i) + length (compE2 e), None)
  {WriteMem A (ACell (nat (sint I))) v}
  h' ([, loc, Suc (length (compE2 a) + length (compE2 i) + length (compE2
e)), None)
  unfolding exec-move-def by-(rule exec-instr, auto simp add: compP2-def is-Ref-def)
moreover have τmove2 (compP2 P) h [v, Intg I, Addr A] (a[i] := e) (length (compE2 a) +
length (compE2 i) + length (compE2 e)) None ⟹ False
  by(simp add: τmove2-iff)
moreover
have P, a[i] := e, h' ⊢ (unit, loc) ↔ ([, loc, Suc (length (compE2 a) + length (compE2 i) +
length (compE2 e)), None)
  by(rule bisim1-bisims1.bisim1AAss4)
ultimately show ?thesis using s τ by(auto simp add: ta-upd-simps) blast
next
case (Red1AAssNull v v')
note [simp] = ⟨v1 = Null⟩ ⟨e' = THROW NullPointer⟩ ⟨i' = Val v⟩ ⟨xs' = xs⟩ ⟨ta = ε⟩ ⟨h' = h⟩
⟨e = Val v'⟩
have τ: ⊢ τmove1 P h (AAss null (Val v) (Val v')) by(auto simp add: τmove1.simps τmoves1.simps)

```

**from** *bisim2* **have**  $s: xcp = \text{None}$   $xs = \text{loc}$   
**and**  $\tau \text{Exec-mover-a } P \ t \ i \ h \ (stk, \text{loc}, pc, xcp) \ ([v], \text{loc}, \text{length} \ (\text{compE2 } i), \text{None})$   
**by**(*auto dest: bisim1Val2D1*)  
**hence**  $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (stk \ @ \ [\text{Null}], \text{loc}, \text{length} \ (\text{compE2 } a) + pc, xcp) \ ([v] \ @ \ [\text{Null}], \text{loc}, \text{length} \ (\text{compE2 } a) + \text{length} \ (\text{compE2 } i), \text{None})$   
**by**-(*rule AAss- $\tau$ ExecrI2*)  
**hence**  $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (stk \ @ \ [\text{Null}], \text{loc}, \text{length} \ (\text{compE2 } a) + pc, xcp) \ ([\ ] \ @ \ [v, \text{Null}], \text{loc}, \text{length} \ (\text{compE2 } a) + \text{length} \ (\text{compE2 } i) + 0, \text{None})$  **by** *simp*  
**also from** *bisim3[of loc]* **have**  $\tau \text{Exec-mover-a } P \ t \ e \ h \ ([\ ], \text{loc}, 0, \text{None}) \ ([v'], \text{loc}, \text{length} \ (\text{compE2 } e), \text{None})$   
**by**(*auto dest: bisim1Val2D1*)  
**hence**  $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ ([\ ] \ @ \ [v, \text{Null}], \text{loc}, \text{length} \ (\text{compE2 } a) + \text{length} \ (\text{compE2 } i) + 0, \text{None}) \ ([v'] \ @ \ [v, \text{Null}], \text{loc}, \text{length} \ (\text{compE2 } a) + \text{length} \ (\text{compE2 } i) + \text{length} \ (\text{compE2 } e), \text{None})$   
**by**(*rule AAss- $\tau$ ExecrI3*)  
**also** (*rtranclp-trans*)  
**have**  $\text{exec-move-a } P \ t \ (a[i] := e) \ h \ ([v', v, \text{Null}], \text{loc}, \text{length} \ (\text{compE2 } a) + \text{length} \ (\text{compE2 } i) + \text{length} \ (\text{compE2 } e), \text{None}) \ \varepsilon$   
 $h \ ([v', v, \text{Null}], \text{loc}, \text{length} \ (\text{compE2 } a) + \text{length} \ (\text{compE2 } i) + \text{length} \ (\text{compE2 } e), \text{[addr-of-sys-xcpt NullPointer]})$   
**unfolding** *exec-move-def* **by**-(*rule exec-instr, auto simp add: is-Ref-def*)  
**moreover have**  $\tau \text{move2} \ (\text{compP2 } P) \ h \ [v', v, \text{Null}] \ (a[i] := e) \ (\text{length} \ (\text{compE2 } a) + \text{length} \ (\text{compE2 } i) + \text{length} \ (\text{compE2 } e)) \ \text{None} \implies \text{False}$   
**by**(*simp add:  $\tau \text{move2-iff}$* )  
**moreover**  
**have**  $P, a[i] := e, h' \vdash (\text{THROW NullPointer}, \text{loc}) \leftrightarrow ([v', v, \text{Null}], \text{loc}, \text{length} \ (\text{compE2 } a) + \text{length} \ (\text{compE2 } i) + \text{length} \ (\text{compE2 } e), \text{[addr-of-sys-xcpt NullPointer]})$   
**by**(*rule bisim1-bisims1.bisim1AAssFail*)  
**ultimately show** *?thesis* **using**  $s \ \tau$  **by** *auto blast*  
**next**  
**case** (*Red1AAssBounds A U len I v*)  
**hence** [*simp*]:  $v1 = \text{Addr } A \ e' = \text{THROW ArrayIndexOutOfBounds } i' = \text{Val } (\text{Intg } I) \ xs' = xs \ ta = \varepsilon \ h' = h \ e = \text{Val } v$   
**and**  $hA: \text{typeof-addr } h \ A = \text{[Array-type } U \ \text{len}]$  **and**  $I: I < s \ 0 \vee \text{int len} \leq \text{sint } I$  **by** *auto*  
**have**  $\tau: \neg \tau \text{move1 } P \ h \ (\text{addr } A[i'] := e)$  **by**(*auto simp add:  $\tau \text{move1.simps}$   $\tau \text{moves1.simps}$* )  
**from** *bisim2* **have**  $s: xcp = \text{None}$   $xs = \text{loc}$   
**and**  $\tau \text{Exec-mover-a } P \ t \ i \ h \ (stk, \text{loc}, pc, xcp) \ ([\text{Intg } I], \text{loc}, \text{length} \ (\text{compE2 } i), \text{None})$   
**by**(*auto dest: bisim1Val2D1*)  
**hence**  $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (stk \ @ \ [\text{Addr } A], \text{loc}, \text{length} \ (\text{compE2 } a) + pc, xcp) \ ([\text{Intg } I] \ @ \ [\text{Addr } A], \text{loc}, \text{length} \ (\text{compE2 } a) + \text{length} \ (\text{compE2 } i), \text{None})$   
**by**-(*rule AAss- $\tau$ ExecrI2*)  
**hence**  $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (stk \ @ \ [\text{Addr } A], \text{loc}, \text{length} \ (\text{compE2 } a) + pc, xcp) \ ([\ ] \ @ \ [\text{Intg } I, \text{Addr } A], \text{loc}, \text{length} \ (\text{compE2 } a) + \text{length} \ (\text{compE2 } i) + 0, \text{None})$  **by** *simp*  
**also from** *bisim3[of loc]* **have**  $\tau \text{Exec-mover-a } P \ t \ e \ h \ ([\ ], \text{loc}, 0, \text{None}) \ ([v], \text{loc}, \text{length} \ (\text{compE2 } e), \text{None})$   
**by**(*auto dest: bisim1Val2D1*)  
**hence**  $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ ([\ ] \ @ \ [\text{Intg } I, \text{Addr } A], \text{loc}, \text{length} \ (\text{compE2 } a) + \text{length} \ (\text{compE2 } i) + 0, \text{None}) \ ([v] \ @ \ [\text{Intg } I, \text{Addr } A], \text{loc}, \text{length} \ (\text{compE2 } a) + \text{length} \ (\text{compE2 } i) + \text{length} \ (\text{compE2 } e), \text{None})$   
**by**(*rule AAss- $\tau$ ExecrI3*)  
**also** (*rtranclp-trans*) **from**  $hA \ I$   
**have**  $\text{exec-move-a } P \ t \ (a[i] := e) \ h \ ([v, \text{Intg } I, \text{Addr } A], \text{loc}, \text{length} \ (\text{compE2 } a) + \text{length} \ (\text{compE2 } i) + \text{length} \ (\text{compE2 } e), \text{None}) \ \varepsilon$   
 $h \ ([v, \text{Intg } I, \text{Addr } A], \text{loc}, \text{length} \ (\text{compE2 } a) + \text{length} \ (\text{compE2 } i) + \text{length}$

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(compE2 e), [addr-of-sys-xcpt ArrayIndexOutOfBounds])
  unfolding exec-move-def by- (rule exec-instr, auto simp add: is-Ref-def)
  moreover have  $\tau\text{move2}$  (compP2 P) h [v, Intg I, Addr A] (a[i] := e) (length (compE2 a) +
length (compE2 i) + length (compE2 e)) None  $\implies$  False
  by(simp add:  $\tau\text{move2}$ -iff)
  moreover
  have P, a[i] := e, h'  $\vdash$  (THROW ArrayIndexOutOfBounds, loc)  $\leftrightarrow$  ([v, Intg I, Addr A], loc, length
(compE2 a) + length (compE2 i) + length (compE2 e), [addr-of-sys-xcpt ArrayIndexOutOfBounds])
  by(rule bisim1-bisims1.bisim1AAssFail)
  ultimately show ?thesis using s  $\tau$  by auto blast
next
case (Red1AAssStore A U len I v U')
  hence [simp]: v1 = Addr A e' = THROW ArrayStore i' = Val (Intg I) xs' = xs ta =  $\varepsilon$  h' = h e
= Val v
  and hA: typeof-addr h A = [Array-type U len] and I: 0  $\leq$  s I sint I < int len
  and U:  $\neg P \vdash U' \leq U$  typeofh v = [U'] by auto
  have  $\tau$ :  $\neg \tau\text{move1}$  P h (addr A[i'] := e) by(auto simp add:  $\tau\text{move1}$ .simps  $\tau\text{moves1}$ .simps)
  from bisim2 have s: xcp = None xs = loc
  and  $\tau\text{Exec-mover-a}$  P t i h (stk, loc, pc, xcp) ([Intg I], loc, length (compE2 i), None)
  by(auto dest: bisim1Val2D1)
  hence  $\tau\text{Exec-mover-a}$  P t (a[i] := e) h (stk @ [Addr A], loc, length (compE2 a) + pc, xcp) ([Intg
I] @ [Addr A], loc, length (compE2 a) + length (compE2 i), None)
  by-(rule AAAss- $\tau\text{ExecrI2}$ )
  hence  $\tau\text{Exec-mover-a}$  P t (a[i] := e) h (stk @ [Addr A], loc, length (compE2 a) + pc, xcp) ([ @
[Intg I, Addr A], loc, length (compE2 a) + length (compE2 i) + 0, None) by simp
  also from bisim3[of loc]
  have  $\tau\text{Exec-mover-a}$  P t e h ([], loc, 0, None) ([v], loc, length (compE2 e), None)
  by(auto dest: bisim1Val2D1)
  hence  $\tau\text{Exec-mover-a}$  P t (a[i] := e) h ([ @ [Intg I, Addr A], loc, length (compE2 a) + length
(compE2 i) + 0, None) ([v] @ [Intg I, Addr A], loc, length (compE2 a) + length (compE2 i) + length
(compE2 e), None)
  by(rule AAAss- $\tau\text{ExecrI3}$ )
  also (rtranclp-trans) from hA I U
  have exec-move-a P t (a[i] := e) h ([v, Intg I, Addr A], loc, length (compE2 a) + length (compE2
i) + length (compE2 e), None)  $\varepsilon$ 
  h ([v, Intg I, Addr A], loc, length (compE2 a) + length (compE2 i) + length
(compE2 e), [addr-of-sys-xcpt ArrayStore])
  unfolding exec-move-def by- (rule exec-instr, auto simp add: is-Ref-def compP2-def)
  moreover have  $\tau\text{move2}$  (compP2 P) h [v, Intg I, Addr A] (a[i] := e) (length (compE2 a) +
length (compE2 i) + length (compE2 e)) None  $\implies$  False
  by(simp add:  $\tau\text{move2}$ -iff)
  moreover
  have P, a[i] := e, h'  $\vdash$  (THROW ArrayStore, loc)  $\leftrightarrow$  ([v, Intg I, Addr A], loc, length (compE2 a)
+ length (compE2 i) + length (compE2 e), [addr-of-sys-xcpt ArrayStore])
  by(rule bisim1-bisims1.bisim1AAssFail)
  ultimately show ?thesis using s  $\tau$  by auto fast
next
case (AAss1Throw2 A)
  note [simp] =  $\langle i' = \text{Throw } A \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw } A \rangle \langle h' = h \rangle \langle xs' = xs \rangle$ 
  have  $\tau$ :  $\tau\text{move1}$  P h (Val v1 [Throw A] := e) by(rule  $\tau\text{move1AAssThrow2}$ )
  from bisim2 have xcp = [A]  $\vee$  xcp = None by(auto dest: bisim1-ThrowD)
  thus ?thesis
proof
  assume [simp]: xcp = [A]

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with bisim2
have  $P, a[i] := e, h \vdash (\text{Throw } A, xs) \leftrightarrow (stk @ [v1], loc, \text{length } (compE2 \ a) + pc, xcp)$ 
  by(auto intro: bisim1-bisims1.intros)
thus ?thesis using  $\tau$  by(fastforce)
next
assume [simp]:  $xcp = \text{None}$ 
with bisim2 obtain  $pc'$  where  $\tau \text{Exec-mover-a } P \ t \ i \ h \ (stk, loc, pc, \text{None}) \ ([Addr \ A], loc, pc', [A])$ 
  and bisim':  $P, i, h \vdash (\text{Throw } A, xs) \leftrightarrow ([Addr \ A], loc, pc', [A])$ 
  and [simp]:  $xs = loc$ 
  by(auto dest: bisim1-Throw- $\tau$ Exec-mover)
hence  $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (stk @ [v1], loc, \text{length } (compE2 \ a) + pc, \text{None}) \ ([Addr \ A] @ [v1], loc, \text{length } (compE2 \ a) + pc', [A])$ 
  by-(rule AAss- $\tau$ ExecrI2)
moreover from bisim'
have  $P, a[i] := e, h \vdash (\text{Throw } A, xs) \leftrightarrow ([Addr \ A] @ [v1], loc, \text{length } (compE2 \ a) + pc', [A])$ 
  by(rule bisim1-bisims1.bisim1AAssThrow2)
ultimately show ?thesis using  $\tau$  by auto
qed
next
case (AAss1Throw3 vi ad)
note [simp] =  $\langle i' = \text{Val } vi \rangle \langle e = \text{Throw } ad \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw } ad \rangle \langle h' = h \rangle \langle xs' = xs \rangle$ 
from bisim2 have  $s: xcp = \text{None} \ xs = loc$ 
  and  $\tau \text{Exec-mover-a } P \ t \ i \ h \ (stk, loc, pc, xcp) \ ([vi], loc, \text{length } (compE2 \ i), \text{None})$ 
  by(auto dest: bisim1Val2D1)
hence  $\tau \text{Exec-mover-a } P \ t \ (a[i] := \text{Throw } ad) \ h \ (stk @ [v1], loc, \text{length } (compE2 \ a) + pc, xcp)$ 
  ( $[vi] @ [v1], loc, \text{length } (compE2 \ a) + \text{length } (compE2 \ i), \text{None})$ 
  by-(rule AAss- $\tau$ ExecrI2)
also have  $\tau \text{Exec-mover-a } P \ t \ (a[i] := \text{Throw } ad) \ h \ ([vi] @ [v1], loc, \text{length } (compE2 \ a) + \text{length } (compE2 \ i), \text{None}) \ ([Addr \ ad, vi, v1], loc, \text{Suc } (\text{length } (compE2 \ a) + \text{length } (compE2 \ i)), [ad])$ 
  by(rule  $\tau \text{Execr2step}$ )(auto simp add: exec-move-def exec-meth-instr  $\tau$ move2-iff  $\tau$ move1.simps  $\tau$ moves1.simps)
also (rtranclp-trans)
have  $P, a[i] := \text{Throw } ad, h \vdash (\text{Throw } ad, loc) \leftrightarrow ([Addr \ ad] @ [vi, v1], loc, (\text{length } (compE2 \ a) + \text{length } (compE2 \ i) + \text{length } (compE2 \ (addr \ ad))), [ad])$ 
  by(rule bisim1AAssThrow3[OF bisim1Throw2])
moreover have  $\tau \text{move1 } P \ h \ (AAss \ (\text{Val } v1) \ (\text{Val } vi) \ (\text{Throw } ad))$  by(auto intro:  $\tau \text{move1AAssThrow3}$ )
ultimately show ?thesis using  $s$  by auto
qed auto
next
case (bisim1AAss3 e n ee xs stk loc pc xcp a i v v')
note IH3 = bisim1AAss3.IH(2)
note bisim3 =  $\langle P, e, h \vdash (ee, xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
note bsok =  $\langle bsok \ (a[i] := e) \ n \rangle$ 
from  $\langle \text{True}, P, t \vdash 1 \ \langle \text{Val } v \ [ \text{Val } v' ] := ee, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$  show ?case
proof cases
case (AAss1Red3 E')
note [simp] =  $\langle e' = \text{Val } v \ [ \text{Val } v' ] := E' \rangle$ 
and  $red = \langle \text{True}, P, t \vdash 1 \ \langle ee, (h, xs) \rangle -ta \rightarrow \langle E', (h', xs') \rangle \rangle$ 
from red have  $\tau: \tau \text{move1 } P \ h \ (\text{Val } v \ [ \text{Val } v' ] := ee) = \tau \text{move1 } P \ h \ ee$  by(auto simp add:  $\tau \text{move1.simps } \tau \text{moves1.simps}$ )
from IH3[OF red] bsok obtain  $pc'' \ stk'' \ loc'' \ xcp''$ 
  where bisim':  $P, e, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$ 
  and exec':  $?exec \ ta \ e \ ee \ E' \ h \ stk \ loc \ pc \ xcp \ h' \ pc'' \ stk'' \ loc'' \ xcp''$  by auto

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have no-call2 e pc  $\implies$  no-call2 (a[i] := e) (length (compE2 a) + length (compE2 i) + pc)
by(auto simp add: no-call2-def)
hence ?exec ta (a[i] := e) (Val v[Val v'] := ee) (Val v[Val v'] := E') h (stk @ [v', v]) loc (length
(compE2 a) + length (compE2 i) + pc) xcp h' (length (compE2 a) + length (compE2 i) + pc'') (stk''
@ [v', v]) loc'' xcp''
using exec'  $\tau$ 
apply(cases  $\tau$ move1 P h (Val v[Val v'] := ee))
apply(auto)
apply(blast intro: AAss- $\tau$ ExecrI3 AAss- $\tau$ ExecI3 exec-move-AAssI3)
apply(blast intro: AAss- $\tau$ ExecrI3 AAss- $\tau$ ExecI3 exec-move-AAssI3)
apply(rule exI conjI AAss- $\tau$ ExecrI3 exec-move-AAssI3|assumption)+
apply(fastforce simp add:  $\tau$ instr-stk-drop-exec-move  $\tau$ move2-iff split: if-split-asm)
apply(rule exI conjI AAss- $\tau$ ExecrI3 exec-move-AAssI3|assumption)+
apply(fastforce simp add:  $\tau$ instr-stk-drop-exec-move  $\tau$ move2-iff split: if-split-asm)
apply(rule exI conjI AAss- $\tau$ ExecrI3 exec-move-AAssI3 rtranclp.rtrancl-refl|assumption)+
apply(fastforce simp add:  $\tau$ instr-stk-drop-exec-move  $\tau$ move2-iff split: if-split-asm)+
done
moreover from bisim'
have P, a[i] := e, h'  $\vdash$  (Val v[Val v'] := E', xs')  $\leftrightarrow$  (stk''@[v', v], loc'', length (compE2 a) + length
(compE2 i) + pc'', xcp'')
by(rule bisim1-bisims1.bisim1AAss3)
ultimately show ?thesis using  $\tau$  by auto blast+
next
case (Red1AAss A U len I V U')
hence [simp]: v = Addr A e' = unit v' = Intg I xs' = xs ee = Val V
ta = {WriteMem A (ACell (nat (sint I))) V}
and hA: typeof-addr h A = [Array-type U len] and I: 0  $\leq$  s I sint I < int len
and v: typeofh V = [U'] P  $\vdash$  U'  $\leq$  U
and h': heap-write h A (ACell (nat (sint I))) V h' by auto
have  $\tau$ :  $\neg$   $\tau$ move1 P h (AAss (addr A) (Val (Intg I)) (Val V)) by(auto simp add:  $\tau$ move1.simps
 $\tau$ moves1.simps)
from bisim3 have s: xcp = None xs = loc
and exec1:  $\tau$ Exec-mover-a P t e h (stk, loc, pc, xcp) ([V], loc, length (compE2 e), None)
by(auto dest: bisim1Val2D1)
hence  $\tau$ Exec-mover-a P t (a[i] := e) h (stk @ [Intg I, Addr A], loc, length (compE2 a) + length
(compE2 i) + pc, xcp) ([V] @ [Intg I, Addr A], loc, length (compE2 a) + length (compE2 i) + length
(compE2 e), None)
by-(rule AAss- $\tau$ ExecrI3)
moreover from hA I v h'
have exec-move-a P t (a[i] := e) h ([V, Intg I, Addr A], loc, length (compE2 a) + length (compE2
i) + length (compE2 e), None)
{WriteMem A (ACell (nat (sint I))) V}
h' ([], loc, Suc (length (compE2 a) + length (compE2 i) + length (compE2
e)), None)
unfolding exec-move-def by-(rule exec-instr, auto simp add: compP2-def is-Ref-def)
moreover have  $\tau$ move2 (compP2 P) h [V, Intg I, Addr A] (a[i] := e) (length (compE2 a) +
length (compE2 i) + length (compE2 e)) None  $\implies$  False
by(simp add:  $\tau$ move2-iff)
moreover
have P, a[i] := e, h'  $\vdash$  (unit, loc)  $\leftrightarrow$  ([], loc, Suc (length (compE2 a) + length (compE2 i) +
length (compE2 e)), None)
by(rule bisim1-bisims1.bisim1AAss4)
ultimately show ?thesis using s  $\tau$  by(auto simp add: ta-upd-simps) blast
next

```



```

case (Red1AAssNull V')
note [simp] =  $\langle v = \text{Null} \rangle \langle e' = \text{THROW NullPointer} \rangle \langle xs' = xs \rangle \langle ta = \varepsilon \rangle \langle h' = h \rangle \langle ee = \text{Val } V' \rangle$ 
have  $\tau: \neg \tau \text{move1 } P \ h \ (AAss \ \text{null} \ (\text{Val } v') \ (\text{Val } V'))$  by(auto simp add:  $\tau \text{move1.simps}$   $\tau \text{moves1.simps}$ )
from bisim3 have s: xcp = None xs = loc
  and  $\tau \text{Exec-mover-a } P \ t \ e \ h \ (stk, loc, pc, xcp) \ ([V'], loc, length \ (compE2 \ e), None)$ 
  by(auto dest: bisim1Val2D1)
hence  $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (stk @ [v', Null], loc, length \ (compE2 \ a) + length \ (compE2 \ i) + pc, xcp) \ ([V'] @ [v', Null], loc, length \ (compE2 \ a) + length \ (compE2 \ i) + length \ (compE2 \ e), None)$ 
  by-(rule AAss- $\tau \text{ExecrI3}$ )
moreover
have exec-move-a P t (a[i] := e) h ([V', v', Null], loc, length (compE2 a) + length (compE2 i) + length (compE2 e), None)  $\varepsilon$ 
  h ([V', v', Null], loc, length (compE2 a) + length (compE2 i) + length (compE2 e), [addr-of-sys-xcpt NullPointer])
  unfolding exec-move-def by-(rule exec-instr, auto simp add: is-Ref-def)
moreover have  $\tau \text{move2} \ (compP2 \ P) \ h \ [V', v', Null] \ (a[i] := e) \ (length \ (compE2 \ a) + length \ (compE2 \ i) + length \ (compE2 \ e)) \ None \implies False$ 
  by(simp add:  $\tau \text{move2-iff}$ )
moreover
have P, a[i] := e, h'  $\vdash$  (THROW NullPointer, loc)  $\leftrightarrow$  ([V', v', Null], loc, length (compE2 a) + length (compE2 i) + length (compE2 e), [addr-of-sys-xcpt NullPointer])
  by(rule bisim1-bisims1.bisim1AAssFail)
ultimately show ?thesis using s  $\tau$  by auto blast
next
case (Red1AAssBounds A U len I V)
hence [simp]: v = Addr A e' = THROW ArrayIndexOutOfBounds v' = Intg I xs' = xs ta =  $\varepsilon$  h'
= h ee = Val V
  and hA: typeof-addr h A = [Array-type U len] and I: I < s 0  $\vee$  int len  $\leq$  sint I by auto
have  $\tau: \neg \tau \text{move1 } P \ h \ (addr \ A \ [Val \ (Intg \ I)] := ee)$  by(auto simp add:  $\tau \text{move1.simps}$   $\tau \text{moves1.simps}$ )
from bisim3 have s: xcp = None xs = loc
  and  $\tau \text{Exec-mover-a } P \ t \ e \ h \ (stk, loc, pc, xcp) \ ([V], loc, length \ (compE2 \ e), None)$ 
  by(auto dest: bisim1Val2D1)
hence  $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (stk @ [Intg \ I, Addr \ A], loc, length \ (compE2 \ a) + length \ (compE2 \ i) + pc, xcp) \ ([V] @ [Intg \ I, Addr \ A], loc, length \ (compE2 \ a) + length \ (compE2 \ i) + length \ (compE2 \ e), None)$ 
  by-(rule AAss- $\tau \text{ExecrI3}$ )
moreover from hA I
have exec-move-a P t (a[i] := e) h ([V, Intg I, Addr A], loc, length (compE2 a) + length (compE2 i) + length (compE2 e), None)  $\varepsilon$ 
  h ([V, Intg I, Addr A], loc, length (compE2 a) + length (compE2 i) + length (compE2 e), [addr-of-sys-xcpt ArrayIndexOutOfBounds])
  unfolding exec-move-def by-(rule exec-instr, auto simp add: is-Ref-def)
moreover have  $\tau \text{move2} \ (compP2 \ P) \ h \ [V, Intg \ I, Addr \ A] \ (a[i] := e) \ (length \ (compE2 \ a) + length \ (compE2 \ i) + length \ (compE2 \ e)) \ None \implies False$ 
  by(simp add:  $\tau \text{move2-iff}$ )
moreover
have P, a[i] := e, h'  $\vdash$  (THROW ArrayIndexOutOfBounds, loc)  $\leftrightarrow$  ([V, Intg I, Addr A], loc, length (compE2 a) + length (compE2 i) + length (compE2 e), [addr-of-sys-xcpt ArrayIndexOutOfBounds])
  by(rule bisim1-bisims1.bisim1AAssFail)
ultimately show ?thesis using s  $\tau$  by auto blast
next
case (Red1AAssStore A U len I V U')
hence [simp]: v = Addr A e' = THROW ArrayStore v' = Intg I xs' = xs ta =  $\varepsilon$  h' = h ee = Val V

```

**and**  $hA$ :  $\text{typeof-addr } h \ A = \lfloor \text{Array-type } U \ \text{len} \rfloor$  **and**  $I$ :  $0 \leq I \text{ sint } I < \text{int } \text{len}$   
**and**  $U$ :  $\neg P \vdash U' \leq U \text{ typeof}_h V = \lfloor U' \rfloor$  **by** *auto*  
**have**  $\tau$ :  $\neg \tau \text{move1 } P \ h \ (\text{addr } A \lfloor \text{Val } (\text{Intg } I) \rfloor := ee)$  **by** (*auto simp add:  $\tau \text{move1.simps}$   $\tau \text{moves1.simps}$* )  
**from** *bisim3* **have**  $s$ :  $xcp = \text{None}$   $xs = \text{loc}$   
**and**  $\tau \text{Exec-mover-a } P \ t \ e \ h \ (\text{stk}, \text{loc}, \text{pc}, xcp) \ ([V], \text{loc}, \text{length } (\text{compE2 } e), \text{None})$   
**by** (*auto dest: bisim1Val2D1*)  
**hence**  $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (\text{stk} @ [\text{Intg } I, \text{Addr } A], \text{loc}, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + \text{pc}, xcp) \ ([V] @ [\text{Intg } I, \text{Addr } A], \text{loc}, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + \text{length } (\text{compE2 } e), \text{None})$   
**by**  $-(\text{rule } AAss\text{-}\tau \text{ExecrI3})$   
**moreover from**  $hA \ I \ U$   
**have**  $\text{exec-move-a } P \ t \ (a[i] := e) \ h \ ([V, \text{Intg } I, \text{Addr } A], \text{loc}, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + \text{length } (\text{compE2 } e), \text{None}) \ \varepsilon$   
 $h \ ([V, \text{Intg } I, \text{Addr } A], \text{loc}, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + \text{length } (\text{compE2 } e), \lfloor \text{addr-of-sys-xcpt } \text{ArrayStore} \rfloor)$   
**unfolding**  $\text{exec-move-def}$  **by**  $-(\text{rule } \text{exec-instr}, \text{auto simp add: is-Ref-def compP2-def})$   
**moreover have**  $\tau \text{move2 } (\text{compP2 } P) \ h \ [V, \text{Intg } I, \text{Addr } A] \ (a[i] := e) \ (\text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + \text{length } (\text{compE2 } e)) \ \text{None} \implies \text{False}$   
**by** (*simp add:  $\tau \text{move2-iff}$* )  
**moreover**  
**have**  $P, a[i] := e, h' \vdash (\text{THROW } \text{ArrayStore}, \text{loc}) \leftrightarrow ([V, \text{Intg } I, \text{Addr } A], \text{loc}, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + \text{length } (\text{compE2 } e), \lfloor \text{addr-of-sys-xcpt } \text{ArrayStore} \rfloor)$   
**by** (*rule bisim1-bisims1.bisim1AAssFail*)  
**ultimately show** *?thesis* **using**  $s \ \tau$  **by** *auto blast*  
**next**  
**case** (*AAss1Throw3 A*)  
**note**  $[simp] = \langle ee = \text{Throw } A \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw } A \rangle \langle h' = h \rangle \langle xs' = xs \rangle$   
**have**  $\tau$ :  $\tau \text{move1 } P \ h \ (AAss \ (\text{Val } v) \ (\text{Val } v') \ (\text{Throw } A))$  **by** (*rule  $\tau \text{move1AAssThrow3}$* )  
**from** *bisim3* **have**  $xcp = \lfloor A \rfloor \vee xcp = \text{None}$  **by** (*auto dest: bisim1-ThrowD*)  
**thus** *?thesis*  
**proof**  
**assume**  $[simp]$ :  $xcp = \lfloor A \rfloor$   
**with** *bisim3*  
**have**  $P, a[i] := e, h \vdash (\text{Throw } A, xs) \leftrightarrow (\text{stk} @ [v', v], \text{loc}, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + \text{pc}, xcp)$   
**by** (*auto intro: bisim1-bisims1.intros*)  
**thus** *?thesis* **using**  $\tau$  **by** (*fastforce*)  
**next**  
**assume**  $[simp]$ :  $xcp = \text{None}$   
**with** *bisim3* **obtain**  $pc'$  **where**  $\tau \text{Exec-mover-a } P \ t \ e \ h \ (\text{stk}, \text{loc}, \text{pc}, \text{None}) \ ([\text{Addr } A], \text{loc}, pc', \lfloor A \rfloor)$   
**and**  $\text{bisim}'$ :  $P, e, h \vdash (\text{Throw } A, xs) \leftrightarrow ([\text{Addr } A], \text{loc}, pc', \lfloor A \rfloor)$   
**and**  $[simp]$ :  $xs = \text{loc}$   
**by** (*auto dest: bisim1-Throw- $\tau \text{Exec-mover}$* )  
**hence**  $\tau \text{Exec-mover-a } P \ t \ (a[i] := e) \ h \ (\text{stk} @ [v', v], \text{loc}, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + \text{pc}, \text{None}) \ ([\text{Addr } A] @ [v', v], \text{loc}, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + pc', \lfloor A \rfloor)$   
**by**  $-(\text{rule } AAss\text{-}\tau \text{ExecrI3})$   
**moreover from**  $\text{bisim}'$   
**have**  $P, a[i] := e, h \vdash (\text{Throw } A, xs) \leftrightarrow ([\text{Addr } A] @ [v', v], \text{loc}, \text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i) + pc', \lfloor A \rfloor)$   
**by** (*rule bisim1-bisims1.bisim1AAssThrow3*)  
**ultimately show** *?thesis* **using**  $\tau$  **by** *auto*  
**qed**  
**qed** *auto*

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next
  case bisim1AAssThrow1 thus ?case by auto
next
  case bisim1AAssThrow2 thus ?case by auto
next
  case bisim1AAssThrow3 thus ?case by auto
next
  case bisim1AAssFail thus ?case by auto
next
  case bisim1AAss4 thus ?case by auto
next
  case (bisim1ALength a n a' xs stk loc pc xcp)
  note IH = bisim1ALength.IH(2)
  note bisim =  $\langle P, a, h \vdash (a', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
  note red =  $\langle \text{True}, P, t \vdash 1 \langle a' \cdot \text{length}, (h, xs) \rangle - ta \rightarrow \langle e', (h', xs') \rangle \rangle$ 
  note bsok =  $\langle \text{bsok } (a \cdot \text{length}) \ n \rangle$ 
  from red show ?case
  proof cases
    case (ALength1Red ee')
    note [simp] =  $\langle e' = ee' \cdot \text{length} \rangle$ 
    and red =  $\langle \text{True}, P, t \vdash 1 \langle a', (h, xs) \rangle - ta \rightarrow \langle ee', (h', xs') \rangle \rangle$ 
    from red have  $\tau \text{move1 } P \ h \ (a' \cdot \text{length}) = \tau \text{move1 } P \ h \ a'$  by (auto simp add:  $\tau \text{move1.simps}$ 
 $\tau \text{moves1.simps}$ )
    moreover have  $\text{call1 } (a' \cdot \text{length}) = \text{call1 } a'$  by auto
    moreover from IH[OF red] bsok
    obtain pc'' stk'' loc'' xcp'' where bisim:  $P, a, h' \vdash (ee', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$ 
    and redo:  $?exec \ ta \ a \ a' \ ee' \ h \ stk \ loc \ pc \ xcp \ h' \ pc'' \ stk'' \ loc'' \ xcp''$  by auto
    from bisim have  $P, a \cdot \text{length}, h' \vdash (ee' \cdot \text{length}, xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$ 
    by (rule bisim1-bisims1.bisim1ALength)
    moreover {
      assume no-call2 a pc
      hence no-call2 (a · length) pc by (auto simp add: no-call2-def) }
    ultimately show ?thesis using redo
    by (auto simp del:  $\text{call1.simps}$   $\text{calls1.simps}$   $\text{split: if-split-asm}$   $\text{split del: if-split}$ ) (blast intro:
 $\text{ALength-}\tau \text{ExecrI}$   $\text{ALength-}\tau \text{ExecI}$   $\text{exec-move-ALengthI}$ ) +
  next
    case (Red1ALength A U len)
    hence [simp]:  $a' = \text{addr } A \ ta = \varepsilon \ e' = \text{Val } (\text{Intg } (\text{word-of-int } (\text{int } \text{len})))$ 
     $h' = h \ xs' = xs$ 
    and hA:  $\text{typeof-addr } h \ A = [\text{Array-type } U \ \text{len}]$  by auto
    from bisim have s:  $xcp = \text{None} \ xs = \text{loc}$  by (auto dest: bisim-Val-loc-eq-xcp-None)
    from bisim have  $\tau \text{Exec-mover-a } P \ t \ a \ h \ (stk, loc, pc, xcp) \ ([\text{Addr } A], \text{loc}, \text{length } (\text{compE2 } a),$ 
 $\text{None})$ 
    by (auto dest: bisim1Val2D1)
    hence  $\tau \text{Exec-mover-a } P \ t \ (a \cdot \text{length}) \ h \ (stk, loc, pc, xcp) \ ([\text{Addr } A], \text{loc}, \text{length } (\text{compE2 } a), \text{None})$ 
    by (rule ALength- $\tau \text{ExecrI}$ )
    moreover from hA
    have  $\text{exec-move-a } P \ t \ (a \cdot \text{length}) \ h \ ([\text{Addr } A], \text{loc}, \text{length } (\text{compE2 } a), \text{None}) \ \varepsilon \ h' \ ([\text{Intg } (\text{word-of-int } (\text{int } \text{len}))], \text{loc}, \text{Suc } (\text{length } (\text{compE2 } a)), \text{None})$ 
    by (auto intro!:  $\text{exec-instr}$  simp add:  $\text{is-Ref-def}$   $\text{exec-move-def}$ )
    moreover have  $\tau \text{move2 } (\text{compP2 } P) \ h \ [\text{Addr } A] \ (a \cdot \text{length}) \ (\text{length } (\text{compE2 } a)) \ \text{None} \implies \text{False}$ 
    by (simp add:  $\tau \text{move2-iff}$ )
    moreover have  $\neg \tau \text{move1 } P \ h \ (\text{addr } A \cdot \text{length})$  by (auto simp add:  $\tau \text{move1.simps}$   $\tau \text{moves1.simps}$ )
    moreover

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have  $P, a \cdot \text{length}, h' \vdash (\text{Val } (\text{Intg } (\text{word-of-int } (\text{int len}))), \text{loc}) \leftrightarrow ([\text{Intg } (\text{word-of-int } (\text{int len}))],$ 
 $\text{loc}, \text{length } (\text{compE2 } (a \cdot \text{length})), \text{None})$ 
by(rule bisim1Val2) simp
ultimately show ?thesis using s by(auto) blast
next
case Red1ALengthNull
note [simp] =  $\langle a' = \text{null} \rangle \langle e' = \text{THROW NullPointer} \rangle \langle h' = h \rangle \langle xs' = xs \rangle \langle ta = \varepsilon \rangle$ 
have  $\neg \tau \text{move1 } P \ h \ (\text{null} \cdot \text{length})$  by(auto simp add: \tau move1.simps \tau moves1.simps)
moreover from bisim have  $s: xcp = \text{None } xs = \text{loc}$ 
and  $\tau \text{Exec-mover-a } P \ t \ a \ h \ (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \ ([\text{Null}], \text{loc}, \text{length } (\text{compE2 } a), \text{None})$ 
by(auto dest: bisim1Val2D1)
hence  $\tau \text{Exec-mover-a } P \ t \ (a \cdot \text{length}) \ h \ (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \ ([\text{Null}], \text{loc}, \text{length } (\text{compE2 } a), \text{None})$ 
by-(rule ALength-\tau ExecrI)
moreover have  $\text{exec-move-a } P \ t \ (a \cdot \text{length}) \ h \ ([\text{Null}], \text{loc}, \text{length } (\text{compE2 } a), \text{None}) \in h \ ([\text{Null}],$ 
 $\text{loc}, \text{length } (\text{compE2 } a), \lfloor \text{addr-of-sys-xcpt NullPointer} \rfloor)$ 
unfolding exec-move-def by -(rule exec-instr, auto simp add: is-Ref-def)
moreover have  $\tau \text{move2 } (\text{compP2 } P) \ h \ [\text{Null}] \ (a \cdot \text{length}) \ (\text{length } (\text{compE2 } a)) \ \text{None} \implies \text{False}$ 
by(simp add: \tau move2-iff)
moreover
have  $P, a \cdot \text{length}, h \vdash (\text{THROW NullPointer}, \text{loc}) \leftrightarrow ([\text{Null}], \text{loc}, \text{length } (\text{compE2 } a), \lfloor \text{addr-of-sys-xcpt}$ 
 $\text{NullPointer} \rfloor)$ 
by(auto intro!: bisim1-bisims1.bisim1ALengthNull)
ultimately show ?thesis using s by auto blast
next
case (ALength1Throw A)
note [simp] =  $\langle a' = \text{Throw } A \rangle \langle h' = h \rangle \langle xs' = xs \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw } A \rangle$ 
have  $\tau: \tau \text{move1 } P \ h \ (\text{Throw } A \cdot \text{length})$  by(auto intro: \tau move1ALengthThrow)
from bisim have  $xcp = \lfloor A \rfloor \vee xcp = \text{None}$  by(auto dest: bisim1-ThrowD)
thus ?thesis
proof
assume [simp]:  $xcp = \lfloor A \rfloor$ 
with bisim have  $P, a \cdot \text{length}, h \vdash (\text{Throw } A, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp})$ 
by(auto intro: bisim1-bisims1.bisim1ALengthThrow)
thus ?thesis using  $\tau$  by(fastforce)
next
assume [simp]:  $xcp = \text{None}$ 
with bisim obtain  $pc'$ 
where  $\tau \text{Exec-mover-a } P \ t \ a \ h \ (\text{stk}, \text{loc}, \text{pc}, \text{None}) \ ([\text{Addr } A], \text{loc}, \text{pc}', \lfloor A \rfloor)$ 
and  $\text{bisim}' : P, a, h \vdash (\text{Throw } A, xs) \leftrightarrow ([\text{Addr } A], \text{loc}, \text{pc}', \lfloor A \rfloor)$  and [simp]:  $xs = \text{loc}$ 
by(auto dest: bisim1-Throw-\tau Exec-mover)
hence  $\tau \text{Exec-mover-a } P \ t \ (a \cdot \text{length}) \ h \ (\text{stk}, \text{loc}, \text{pc}, \text{None}) \ ([\text{Addr } A], \text{loc}, \text{pc}', \lfloor A \rfloor)$ 
by-(rule ALength-\tau ExecrI)
moreover from bisim' have  $P, a \cdot \text{length}, h \vdash (\text{Throw } A, xs) \leftrightarrow ([\text{Addr } A], \text{loc}, \text{pc}', \lfloor A \rfloor)$ 
by(rule bisim1-bisims1.bisim1ALengthThrow)
ultimately show ?thesis using  $\tau$  by auto
qed
qed
next
case bisim1ALengthThrow thus ?case by auto
next
case bisim1ALengthNull thus ?case by auto
next
case (bisim1FAcc E n e xs stk loc pc xcp F D)
note IH = bisim1FAcc.IH(2)

```

```

note bisim =  $\langle P, E, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
note red =  $\langle True, P, t \vdash 1 \langle e \cdot F\{D\}, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$ 
note bsok =  $\langle bsok (E \cdot F\{D\}) \ n \rangle$ 
from red show ?case
proof cases
  case (FAcc1Red ee')
    note [simp] =  $\langle e' = ee' \cdot F\{D\} \rangle$ 
    and red =  $\langle True, P, t \vdash 1 \langle e, (h, xs) \rangle -ta \rightarrow \langle ee', (h', xs') \rangle \rangle$ 
    from red have  $\tau move1 \ P \ h \ (e \cdot F\{D\}) = \tau move1 \ P \ h \ e$  by(auto simp add:  $\tau move1.simps$ 
 $\tau moves1.simps$ )
    moreover have  $call1 \ (e \cdot F\{D\}) = call1 \ e$  by auto
    moreover from IH[OF red] bsok
    obtain pc'' stk'' loc'' xcp'' where bisim:  $P, E, h' \vdash (ee', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$ 
    and redo: ?exec ta E e ee' h stk loc pc xcp h' pc'' stk'' loc'' xcp'' by auto
    from bisim
    have  $P, E \cdot F\{D\}, h' \vdash (ee' \cdot F\{D\}, xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$ 
    by(rule bisim1-bisims1.bisim1FAcc)
    moreover {
      assume no-call2 E pc
      hence no-call2 ( $E \cdot F\{D\}$ ) pc by(auto simp add: no-call2-def) }
    ultimately show ?thesis using redo
    by(auto simp del: call1.simps calls1.simps split: if-split-asm split del: if-split)(blast intro:
FAcc- $\tau$ ExecrI FAcc- $\tau$ ExectI exec-move-FAccI)+
  next
    case (Red1FAcc a v)
    hence [simp]:  $e = addr \ a \ ta = \{\!| ReadMem \ a \ (CField \ D \ F) \ v \!\} \ e' = Val \ v \ h' = h \ xs' = xs$ 
    and read: heap-read h a (CField D F) v by auto
    from bisim have  $s: xcp = None \ xs = loc$  by(auto dest: bisim-Val-loc-eq-xcp-None)
    from bisim have  $\tau Exec-mover-a \ P \ t \ E \ h \ (stk, loc, pc, xcp) ([Addr \ a], loc, length \ (compE2 \ E),$ 
None)
    by(auto dest: bisim1Val2D1)
    hence  $\tau Exec-mover-a \ P \ t \ (E \cdot F\{D\}) \ h \ (stk, loc, pc, xcp) ([Addr \ a], loc, length \ (compE2 \ E), None)$ 
    by(rule FAcc- $\tau$ ExecrI)
    moreover from read
    have  $exec-move-a \ P \ t \ (E \cdot F\{D\}) \ h \ ([Addr \ a], loc, length \ (compE2 \ E), None)$ 
     $\{\!| ReadMem \ a \ (CField \ D \ F) \ v \!\} \ h' \ ([v], loc, Suc \ (length \ (compE2 \ E)), None)$ 
    unfolding exec-move-def by(auto intro!: exec-instr)
    moreover have  $\tau move2 \ (compP2 \ P) \ h \ [Addr \ a] \ (E \cdot F\{D\}) \ (length \ (compE2 \ E)) \ None \implies False$ 
by(simp add:  $\tau move2-iff$ )
    moreover have  $\neg \tau move1 \ P \ h \ (addr \ a \cdot F\{D\})$  by(auto simp add:  $\tau move1.simps \tau moves1.simps$ )
    moreover
    have  $P, E \cdot F\{D\}, h' \vdash (Val \ v, loc) \leftrightarrow ([v], loc, length \ (compE2 \ (E \cdot F\{D\})), None)$ 
    by(rule bisim1Val2) simp
    ultimately show ?thesis using s by(auto simp add: ta-upd-simps) blast
  next
    case Red1FAccNull
    note [simp] =  $\langle e = null \rangle \langle e' = THROW \ NullPointer \rangle \langle h' = h \rangle \langle xs' = xs \rangle \langle ta = \varepsilon \rangle$ 
    have  $\neg \tau move1 \ P \ h \ (null \cdot F\{D\})$  by(auto simp add:  $\tau move1.simps \tau moves1.simps$ )
    moreover from bisim have  $s: xcp = None \ xs = loc$ 
    and  $\tau Exec-mover-a \ P \ t \ E \ h \ (stk, loc, pc, xcp) ([Null], loc, length \ (compE2 \ E), None)$ 
    by(auto dest: bisim1Val2D1)
    hence  $\tau Exec-mover-a \ P \ t \ (E \cdot F\{D\}) \ h \ (stk, loc, pc, xcp) ([Null], loc, length \ (compE2 \ E), None)$ 
    by-(rule FAcc- $\tau$ ExecrI)
    moreover

```

```

  have exec-move-a  $P \ t \ (E \cdot F\{D\}) \ h \ ([Null], \text{loc}, \text{length} \ (\text{compE2} \ E), \text{None}) \varepsilon \ h \ ([Null], \text{loc}, \text{length} \ (\text{compE2} \ E), \lfloor \text{addr-of-sys-xcpt} \ \text{NullPointer} \rfloor)$ 
    unfolding exec-move-def by  $\neg(\text{rule} \ \text{exec-instr}, \text{auto} \ \text{simp} \ \text{add: compP2-def} \ \text{dest: sees-field-idemp})$ 
    moreover have  $\tau \text{move2} \ (\text{compP2} \ P) \ h \ [Null] \ (E \cdot F\{D\}) \ (\text{length} \ (\text{compE2} \ E)) \ \text{None} \implies \text{False}$ 
  by(simp add:  $\tau \text{move2-iff}$ )
  moreover
  have  $P, E \cdot F\{D\}, h \vdash (\text{THROW} \ \text{NullPointer}, \text{loc}) \leftrightarrow ([Null], \text{loc}, \text{length} \ (\text{compE2} \ E), \lfloor \text{addr-of-sys-xcpt} \ \text{NullPointer} \rfloor)$ 
    by(rule bisim1-bisims1.bisim1FAccNull)
  ultimately show ?thesis using s by auto blast
next
case (FAcc1Throw a)
note [simp] =  $\langle e = \text{Throw} \ a \rangle \langle h' = h \rangle \langle xs' = xs \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw} \ a \rangle$ 
have  $\tau: \tau \text{move1} \ P \ h \ (e \cdot F\{D\})$  by(auto intro:  $\tau \text{move1FAccThrow}$ )
from bisim have  $xcp = \lfloor a \rfloor \vee xcp = \text{None}$  by(auto dest: bisim1-ThrowD)
thus ?thesis
proof
  assume [simp]:  $xcp = \lfloor a \rfloor$ 
  with bisim have  $P, E \cdot F\{D\}, h \vdash (\text{Throw} \ a, xs) \leftrightarrow (stk, \text{loc}, pc, xcp)$ 
    by(auto intro: bisim1-bisims1.bisim1FAccThrow)
  thus ?thesis using  $\tau$  by(fastforce)
next
  assume [simp]:  $xcp = \text{None}$ 
  with bisim obtain  $pc'$ 
    where  $\tau \text{Exec-mover-a} \ P \ t \ E \ h \ (stk, \text{loc}, pc, \text{None}) \ ([Addr \ a], \text{loc}, pc', \lfloor a \rfloor)$ 
    and  $\text{bisim}' : P, E, h \vdash (\text{Throw} \ a, xs) \leftrightarrow ([Addr \ a], \text{loc}, pc', \lfloor a \rfloor)$  and [simp]:  $xs = \text{loc}$ 
    by(auto dest: bisim1-Throw- $\tau \text{Exec-mover}$ )
  hence  $\tau \text{Exec-mover-a} \ P \ t \ (E \cdot F\{D\}) \ h \ (stk, \text{loc}, pc, \text{None}) \ ([Addr \ a], \text{loc}, pc', \lfloor a \rfloor)$ 
    by $\neg(\text{rule} \ \text{FAcc-}\tau \text{ExecrI})$ 
  moreover from bisim' have  $P, E \cdot F\{D\}, h \vdash (\text{Throw} \ a, xs) \leftrightarrow ([Addr \ a], \text{loc}, pc', \lfloor a \rfloor)$ 
    by(rule bisim1-bisims1.bisim1FAccThrow)
  ultimately show ?thesis using  $\tau$  by auto
qed
qed
next
case bisim1FAccThrow thus ?case by auto
next
case bisim1FAccNull thus ?case by auto
next
case (bisim1FAss1  $e1 \ n \ e1' \ xs \ stk \ \text{loc} \ pc \ xcp \ e2 \ F \ D$ )
note IH1 = bisim1FAss1.IH(2)
note IH2 = bisim1FAss1.IH(4)
note  $\text{bisim1} = \langle P, e1, h \vdash (e1', xs) \leftrightarrow (stk, \text{loc}, pc, xcp) \rangle$ 
note  $\text{bisim2} = \langle \bigwedge xs. P, e2, h \vdash (e2, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$ 
note  $\text{bsok} = \langle \text{bsok} \ (e1 \cdot F\{D\} := e2) \ n \rangle$ 
from  $\langle \text{True}, P, t \vdash 1 \ \langle e1' \cdot F\{D\} := e2, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$  show ?case
proof cases
  case (FAss1Red1  $E'$ )
  note [simp] =  $\langle e' = E' \cdot F\{D\} := e2 \rangle$ 
  and  $\text{red} = \langle \text{True}, P, t \vdash 1 \ \langle e1', (h, xs) \rangle -ta \rightarrow \langle E', (h', xs') \rangle \rangle$ 
  from red have  $\tau \text{move1} \ P \ h \ (e1' \cdot F\{D\} := e2) = \tau \text{move1} \ P \ h \ e1'$  by(auto simp add:  $\tau \text{move1.simps}$ 
 $\tau \text{moves1.simps}$ )
  moreover from red have  $\text{call1} \ (e1' \cdot F\{D\} := e2) = \text{call1} \ e1'$  by auto
  moreover from IH1[OF red] bsok

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obtain  $pc'' \ stk'' \ loc'' \ xcp''$  where  $bisim: P, e1, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$ 
and  $redo: ?exec \ ta \ e1 \ e1' \ E' \ h \ stk \ loc \ pc \ xcp \ h' \ pc'' \ stk'' \ loc'' \ xcp''$  by auto
from bisim
have  $P, e1 \cdot F\{D\} := e2, h' \vdash (E' \cdot F\{D\} := e2, xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$ 
by(rule bisim1-bisims1.bisim1FAss1)
moreover {
  assume no-call2 e1 pc
  hence no-call2 (e1 · F{D} := e2) pc  $\vee$  pc = length (compE2 e1) by(auto simp add: no-call2-def)
}
ultimately show ?thesis using redo
by(auto simp del: call1.simps calls1.simps split: if-split-asm split del: if-split)(blast intro: FAss- $\tau$ ExecrI1 FAss- $\tau$ ExecI1 exec-move-FAssI1)+
next
case (FAss1Red2 E' v)
note [simp] =  $\langle e1' = Val \ v \rangle \langle e' = Val \ v \cdot F\{D\} := E' \rangle$ 
and  $red = \langle True, P, t \vdash 1 \ \langle e2, (h, xs) \rangle - ta \rightarrow \langle E', (h', xs') \rangle \rangle$ 
from red have  $\tau: \tau move1 \ P \ h \ (Val \ v \cdot F\{D\} := e2) = \tau move1 \ P \ h \ e2$  by(auto simp add:  $\tau move1.simps \ \tau moves1.simps$ )
from bisim1 have  $s: xcp = None \ xs = loc$ 
and  $exec1: \tau Exec-mover-a \ P \ t \ e1 \ h \ (stk, loc, pc, None) ([v], xs, length \ (compE2 \ e1), None)$ 
by(auto dest: bisim1Val2D1)
from exec1 have  $\tau Exec-mover-a \ P \ t \ (e1 \cdot F\{D\} := e2) \ h \ (stk, loc, pc, None) ([v], xs, length \ (compE2 \ e1), None)$ 
by(rule FAss- $\tau$ ExecrI1)
moreover
from IH2[OF red] bsok obtain  $pc'' \ stk'' \ loc'' \ xcp''$ 
where  $bisim': P, e2, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$ 
and  $exec': ?exec \ ta \ e2 \ e2 \ E' \ h \ [] \ xs \ 0 \ None \ h' \ pc'' \ stk'' \ loc'' \ xcp''$  by auto
have  $?exec \ ta \ (e1 \cdot F\{D\} := e2) \ (Val \ v \cdot F\{D\} := e2) \ (Val \ v \cdot F\{D\} := E') \ h \ ([] \ @ \ [v]) \ xs \ (length \ (compE2 \ e1) + 0) \ None \ h' \ (length \ (compE2 \ e1) + pc'') \ (stk'' \ @ \ [v]) \ loc'' \ xcp''$ 
proof(cases  $\tau move1 \ P \ h \ (Val \ v \cdot F\{D\} := e2)$ )
case True
with  $exec' \ \tau$  have [simp]:  $h = h'$  and  $e: sim-move \ e2 \ E' \ P \ t \ e2 \ h \ ([] \ , \ xs, \ 0, \ None) \ (stk'', loc'', pc'', xcp'')$  by auto
from  $e$  have  $sim-move \ (Val \ v \cdot F\{D\} := e2) \ (Val \ v \cdot F\{D\} := E') \ P \ t \ (e1 \cdot F\{D\} := e2) \ h \ ([] \ @ \ [v], \ xs, \ length \ (compE2 \ e1) + 0, \ None) \ (stk'' \ @ \ [v], \ loc'', \ length \ (compE2 \ e1) + pc'', \ xcp'')$ 
by(fastforce dest: FAss- $\tau$ ExecrI2 FAss- $\tau$ ExecI2 simp del: compE2.simps compEs2.simps)
with True show ?thesis by auto
next
case False
with  $exec' \ \tau$  obtain  $pc' \ stk' \ loc' \ xcp'$ 
where  $e: \tau Exec-mover-a \ P \ t \ e2 \ h \ ([] \ , \ xs, \ 0, \ None) \ (stk', loc', pc', xcp')$ 
and  $e': exec-move-a \ P \ t \ e2 \ h \ (stk', loc', pc', xcp') \ (extTA2JVM \ (compP2 \ P) \ ta) \ h' \ (stk'', loc'', pc'', xcp'')$ 
and  $\tau': \neg \tau move2 \ (compP2 \ P) \ h \ stk' \ e2 \ pc' \ xcp'$ 
and  $call: call1 \ e2 = None \vee no-call2 \ e2 \ 0 \vee pc' = 0 \wedge stk' = [] \wedge loc' = xs \wedge xcp' = None$  by auto
from  $e$  have  $\tau Exec-mover-a \ P \ t \ (e1 \cdot F\{D\} := e2) \ h \ ([] \ @ \ [v], \ xs, \ length \ (compE2 \ e1) + 0, \ None) \ (stk' \ @ \ [v], \ loc', \ length \ (compE2 \ e1) + pc', \ xcp')$ 
by(rule FAss- $\tau$ ExecrI2)
moreover from  $e'$  have  $exec-move-a \ P \ t \ (e1 \cdot F\{D\} := e2) \ h \ (stk' \ @ \ [v], \ loc', \ length \ (compE2 \ e1) + pc', \ xcp') \ (extTA2JVM \ (compP2 \ P) \ ta) \ h' \ (stk'' \ @ \ [v], \ loc'', \ length \ (compE2 \ e1) + pc'', \ xcp'')$ 
by(rule exec-move-FAssI2)
moreover from  $e'$  have  $pc' < length \ (compE2 \ e2)$  by(auto elim: exec-meth.cases)

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with  $\tau' e'$  have  $\neg \tau \text{move2} (\text{compP2 } P) h (\text{stk}' @ [v]) (e1 \cdot F\{D\} := e2) (\text{length} (\text{compE2 } e1) +$ 
 $pc') xcp'$ 
  by(auto simp add:  $\tau \text{move2-iff}$   $\tau \text{instr-stk-drop-exec-move}$ )
moreover have  $\text{call1 } (e1' \cdot F\{D\} := e2) = \text{call1 } e2$  by simp
moreover have  $\text{no-call2 } e2 0 \implies \text{no-call2 } (e1 \cdot F\{D\} := e2) (\text{length} (\text{compE2 } e1))$ 
  by(auto simp add: no-call2-def)
ultimately show ?thesis using False call
  by(auto simp del: split-paired-Ex call1.simps calls1.simps) blast
qed
moreover from bisim'
have  $P, e1 \cdot F\{D\} := e2, h' \vdash (\text{Val } v \cdot F\{D\} := E', xs') \leftrightarrow ((\text{stk}'' @ [v]), \text{loc}'', \text{length} (\text{compE2 } e1)$ 
 $+ pc'', xcp'')$ 
  by(rule bisim1-bisims1.bisim1FAss2)
moreover from bisim1 have  $pc \neq \text{length} (\text{compE2 } e1) \longrightarrow \text{no-call2 } (e1 \cdot F\{D\} := e2) pc$ 
  by(auto simp add: no-call2-def dest: bisim-Val-pc-not-Invoke bisim1-pc-length-compE2)
ultimately show ?thesis using  $\tau \text{exec1 } s$ 
  apply(auto simp del: split-paired-Ex call1.simps calls1.simps split: if-split-asm split del: if-split)
  apply(blast intro:  $\tau \text{Exec-mover-trans}$  fastforce elim!:  $\tau \text{Exec-mover-trans}$  simp del: split-paired-Ex
 $\text{call1.simps calls1.simps}$ )
  done
next
case (Red1FAss a v)
note [simp] =  $\langle e1' = \text{addr } a \rangle \langle e2 = \text{Val } v \rangle \langle ta = \llbracket \text{WriteMem } a \text{ (CField } D \text{ F)} \text{ } v \rrbracket \rangle \langle e' = \text{unit} \rangle \langle xs' = xs \rangle$ 
  and  $\text{write} = \langle \text{heap-write } h \text{ } a \text{ (CField } D \text{ F)} \text{ } v \text{ } h' \rangle$ 
have  $\tau: \neg \tau \text{move1 } P h (e1' \cdot F\{D\} := e2)$  by(auto simp add:  $\tau \text{move1.simps}$   $\tau \text{moves1.simps}$ )
from bisim1 have  $s: xcp = \text{None } xs = \text{loc}$ 
  and  $\tau \text{Exec-mover-a } P t e1 h (\text{stk}, \text{loc}, pc, xcp) ([\text{Addr } a], \text{loc}, \text{length} (\text{compE2 } e1), \text{None})$ 
  by(auto dest: bisim1Val2D1)
hence  $\tau \text{Exec-mover-a } P t (e1 \cdot F\{D\} := e2) h (\text{stk}, \text{loc}, pc, xcp) ([\text{Addr } a], \text{loc}, \text{length} (\text{compE2 } e1), \text{None})$ 
  by-(rule FAss- $\tau \text{ExecrI1}$ )
also have  $\tau \text{move2} (\text{compP2 } P) h [\text{Addr } a] (e1 \cdot F\{D\} := \text{Val } v) (\text{length} (\text{compE2 } e1)) \text{None}$  by(simp add:  $\tau \text{move2-iff}$ )
hence  $\tau \text{Exec-mover-a } P t (e1 \cdot F\{D\} := e2) h ([\text{Addr } a], \text{loc}, \text{length} (\text{compE2 } e1), \text{None}) ([v, \text{Addr } a], \text{loc}, \text{Suc} (\text{length} (\text{compE2 } e1)), \text{None})$ 
  by-(rule  $\tau \text{Execr1step}$ , auto intro!: exec-instr simp add: exec-move-def compP2-def)
also (rtranclp-trans) from write
  have  $\text{exec-move-a } P t (e1 \cdot F\{D\} := e2) h ([v, \text{Addr } a], \text{loc}, \text{Suc} (\text{length} (\text{compE2 } e1)), \text{None})$ 
 $\llbracket \text{WriteMem } a \text{ (CField } D \text{ F)} \text{ } v \rrbracket$ 
 $h' ([], \text{loc}, \text{Suc} (\text{Suc} (\text{length} (\text{compE2 } e1))), \text{None})$ 
  unfolding exec-move-def by(auto intro!: exec-instr)
moreover have  $\tau \text{move2} (\text{compP2 } P) h [v, \text{Addr } a] (e1 \cdot F\{D\} := e2) (\text{Suc} (\text{length} (\text{compE2 } e1)))$ 
 $\text{None} \implies \text{False}$ 
  by(simp add:  $\tau \text{move2-iff}$ )
moreover
have  $P, e1 \cdot F\{D\} := e2, h' \vdash (\text{unit}, \text{loc}) \leftrightarrow ([], \text{loc}, \text{Suc} (\text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2)), \text{None})$ 
  by(rule bisim1-bisims1.bisim1FAss3)
ultimately show ?thesis using  $s \tau$  by(auto simp del: fun-upd-apply simp add: ta-upd-simps) blast
next
case (Red1FAssNull v)
note [simp] =  $\langle e1' = \text{null} \rangle \langle e2 = \text{Val } v \rangle \langle xs' = xs \rangle \langle ta = \varepsilon \rangle \langle e' = \text{THROW NullPointer} \rangle \langle h' = h \rangle$ 
have  $\tau: \neg \tau \text{move1 } P h (e1' \cdot F\{D\} := e2)$  by(auto simp add:  $\tau \text{move1.simps}$   $\tau \text{moves1.simps}$ )

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from bisim1 have s: xcp = None xs = loc
  and  $\tau\text{Exec-mover-a } P \ t \ e1 \ h \ (stk, loc, pc, xcp) \ ([Null], loc, length \ (compE2 \ e1), None)$ 
  by(auto dest: bisim1Val2D1)
hence  $\tau\text{Exec-mover-a } P \ t \ (e1 \cdot F\{D\} := e2) \ h \ (stk, loc, pc, xcp) \ ([Null], loc, length \ (compE2 \ e1),$ 
None)
  by-(rule FAss- $\tau\text{ExecrI1}$ )
also have  $\tau\text{move2} \ (compP2 \ P) \ h \ [Null] \ (e1 \cdot F\{D\} := Val \ v) \ (length \ (compE2 \ e1)) \ None$  by(simp
add:  $\tau\text{move2-iff}$ )
hence  $\tau\text{Exec-mover-a } P \ t \ (e1 \cdot F\{D\} := e2) \ h \ ([Null], loc, length \ (compE2 \ e1), None) \ ([v, Null],$ 
loc, Suc \ (length \ (compE2 \ e1)), None)
  by-(rule  $\tau\text{Execr1step}$ , auto intro!: exec-instr simp add: exec-move-def compP2-def)
also (rtranclp-trans)
have exec-move-a P t (e1·F{D} := e2) h ([v, Null], loc, Suc \ (length \ (compE2 \ e1)), None)  $\varepsilon$ 
  h' ([v, Null], loc, Suc \ (length \ (compE2 \ e1)), [addr-of-sys-xcpt NullPointer])
  by(auto intro!: exec-instr simp add: exec-move-def)
moreover have  $\tau\text{move2} \ (compP2 \ P) \ h \ [v, Null] \ (e1 \cdot F\{D\} := e2) \ (Suc \ (length \ (compE2 \ e1)))$ 
None  $\implies False$ 
  by(simp add:  $\tau\text{move2-iff}$ )
moreover
have P, e1·F{D} := e2, h  $\vdash (THROW \ NullPointer, loc) \leftrightarrow ([v, Null], loc, length \ (compE2 \ e1) +$ 
length \ (compE2 \ e2), [addr-of-sys-xcpt NullPointer])
  by(rule bisim1-bisims1.bisim1FAssNull)
ultimately show ?thesis using s  $\tau$  by(auto simp del: fun-upd-apply) blast
next
case (FAss1Throw1 a)
note [simp] =  $\langle e1' = Throw \ a \rangle \langle ta = \varepsilon \rangle \langle e' = Throw \ a \rangle \langle h' = h \rangle \langle xs' = xs \rangle$ 
have  $\tau: \tau\text{move1} \ P \ h \ (Throw \ a \cdot F\{D\} := e2)$  by(rule  $\tau\text{move1FAssThrow1}$ )
from bisim1 have xcp = [a]  $\vee$  xcp = None by(auto dest: bisim1-ThrowD)
thus ?thesis
proof
  assume [simp]: xcp = [a]
  with bisim1
have P, e1·F{D} := e2, h  $\vdash (Throw \ a, xs) \leftrightarrow (stk, loc, pc, xcp)$ 
  by(auto intro: bisim1-bisims1.intros)
thus ?thesis using  $\tau$  by(fastforce)
next
assume [simp]: xcp = None
with bisim1 obtain pc' where  $\tau\text{Exec-mover-a } P \ t \ e1 \ h \ (stk, loc, pc, None) \ ([Addr \ a], loc, pc',$ 
[a])
  and bisim': P, e1, h  $\vdash (Throw \ a, xs) \leftrightarrow ([Addr \ a], loc, pc', [a])$ 
  and [simp]: xs = loc
  by(auto dest: bisim1-Throw- $\tau\text{Exec-mover}$ )
hence  $\tau\text{Exec-mover-a } P \ t \ (e1 \cdot F\{D\} := e2) \ h \ (stk, loc, pc, None) \ ([Addr \ a], loc, pc', [a])$ 
  by-(rule FAss- $\tau\text{ExecrI1}$ )
moreover from bisim'
have P, e1·F{D} := e2, h  $\vdash (Throw \ a, xs) \leftrightarrow ([Addr \ a], loc, pc', [a])$ 
  by(rule bisim1-bisims1.bisim1FAssThrow1)
ultimately show ?thesis using  $\tau$  by auto
qed
next
case (FAss1Throw2 v ad)
note [simp] =  $\langle e1' = Val \ v \rangle \langle e2 = Throw \ ad \rangle \langle e' = Throw \ ad \rangle \langle h' = h \rangle \langle xs' = xs \rangle$ 
from bisim1 have s: xcp = None xs = loc
  and  $\tau\text{Exec-mover-a } P \ t \ e1 \ h \ (stk, loc, pc, xcp) \ ([v], loc, length \ (compE2 \ e1), None)$ 

```

**by**(*auto dest: bisim1Val2D1*)  
**hence**  $\tau\text{Exec-mover-a } P \ t \ (e1 \cdot F\{D\} := \text{Throw } ad) \ h \ (stk, loc, pc, xcp) \ ([v], loc, length \ (compE2 \ e1), None)$   
**by**-(*rule FAss- $\tau$ ExecrI1*)  
**also have**  $\tau\text{Exec-mover-a } P \ t \ (e1 \cdot F\{D\} := \text{Throw } ad) \ h \ ([v], loc, length \ (compE2 \ e1), None)$   
 $([Addr \ ad, v], loc, Suc \ (length \ (compE2 \ e1)), [ad])$   
**by**(*rule  $\tau\text{Execr2step}$* )(*auto simp add: exec-move-def exec-meth-instr  $\tau\text{move2-iff}$   $\tau\text{move1.simps}$   $\tau\text{moves1.simps}$* )  
**also** (*rtranclp-trans*)  
**have**  $P, e1 \cdot F\{D\} := \text{Throw } ad, h \vdash (\text{Throw } ad, loc) \leftrightarrow ([Addr \ ad] @ [v], loc, (length \ (compE2 \ e1) + length \ (compE2 \ (addr \ ad))), [ad])$   
**by**(*rule bisim1FAssThrow2[OF bisim1Throw2]*)  
**moreover have**  $\tau\text{move1 } P \ h \ (FAss \ e1' \ F \ D \ (\text{Throw } ad))$  **by**(*auto intro:  $\tau\text{move1FAssThrow2}$* )  
**ultimately show** *?thesis* **using** *s* **by** *auto*  
**qed**  
**next**  
**case** (*bisim1FAss2 e2 n e2' xs stk loc pc xcp e1 F D v1*)  
**note**  $IH2 = bisim1FAss2.IH(2)$   
**note**  $bisim1 = \langle \bigwedge xs. P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, None) \rangle$   
**note**  $bisim2 = \langle P, e2, h \vdash (e2', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$   
**note**  $bsok = \langle bsok \ (e1 \cdot F\{D\} := e2) \ n \rangle$   
**note**  $red = \langle True, P, t \vdash 1 \ \langle Val \ v1 \cdot F\{D\} := e2', (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle$   
**from** *red* **show** *?case*  
**proof** *cases*  
**case** (*FAss1Red2 E'*)  
**note**  $[simp] = \langle e' = Val \ v1 \cdot F\{D\} := E' \rangle$   
**and**  $red = \langle True, P, t \vdash 1 \ \langle e2', (h, xs) \rangle -ta \rightarrow \langle E', (h', xs') \rangle$   
**from**  $IH2[OF \ red] \ bsok$  **obtain**  $pc'' \ stk'' \ loc'' \ xcp''$   
**where**  $bisim': P, e2, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$   
**and**  $exec': ?exec \ ta \ e2 \ e2' \ E' \ h \ stk \ loc \ pc \ xcp \ h' \ pc'' \ stk'' \ loc'' \ xcp''$  **by** *auto*  
**from** *red* **have**  $\tau: \tau\text{move1 } P \ h \ (Val \ v1 \cdot F\{D\} := e2') = \tau\text{move1 } P \ h \ e2'$  **by**(*auto simp add:  $\tau\text{move1.simps}$   $\tau\text{moves1.simps}$* )  
**have**  $no\text{-call2 } e2 \ pc \implies no\text{-call2 } (e1 \cdot F\{D\} := e2) \ (length \ (compE2 \ e1) + pc)$  **by**(*auto simp add: no-call2-def*)  
**hence**  $?exec \ ta \ (e1 \cdot F\{D\} := e2) \ (Val \ v1 \cdot F\{D\} := e2') \ (Val \ v1 \cdot F\{D\} := E') \ h \ (stk @ [v1]) \ loc$   
 $(length \ (compE2 \ e1) + pc) \ xcp \ h' \ (length \ (compE2 \ e1) + pc'') \ (stk'' @ [v1]) \ loc'' \ xcp''$   
**using**  $exec' \ \tau$   
**apply**(*cases  $\tau\text{move1 } P \ h \ (Val \ v1 \cdot F\{D\} := e2')$* )  
**apply**(*auto*)  
**apply**(*blast intro: FAss- $\tau$ ExecrI2 FAss- $\tau$ ExecI2 exec-move-FAssI2*)  
**apply**(*blast intro: FAss- $\tau$ ExecrI2 FAss- $\tau$ ExecI2 exec-move-FAssI2*)  
**apply**(*rule exI conjI FAss- $\tau$ ExecrI2 exec-move-FAssI2|assumption*)  
**apply**(*fastforce simp add:  $\tau\text{instr-stk-drop-exec-move}$   $\tau\text{move2-iff}$  split: if-split-asm*)  
**apply**(*rule exI conjI FAss- $\tau$ ExecrI2 exec-move-FAssI2|assumption*)  
**apply**(*fastforce simp add:  $\tau\text{instr-stk-drop-exec-move}$   $\tau\text{move2-iff}$  split: if-split-asm*)  
**apply**(*rule exI conjI FAss- $\tau$ ExecrI2 exec-move-FAssI2 rtranclp.rtrancl-refl|assumption*)  
**apply**(*fastforce simp add:  $\tau\text{instr-stk-drop-exec-move}$   $\tau\text{move2-iff}$  split: if-split-asm*)  
**done**  
**moreover from**  $bisim'$   
**have**  $P, e1 \cdot F\{D\} := e2, h' \vdash (Val \ v1 \cdot F\{D\} := E', xs') \leftrightarrow (stk'' @ [v1], loc'', length \ (compE2 \ e1) + pc'', xcp'')$   
**by**(*rule bisim1-bisims1.bisim1FAss2*)  
**ultimately show** *?thesis* **using**  $\tau$  **by** *auto blast+*  
**next**

```

case (Red1FAss a v)
note [simp] =  $\langle v1 = \text{Addr } a \rangle \langle e2' = \text{Val } v \rangle \langle ta = \llbracket \text{WriteMem } a \text{ (CField } D \text{ F)} \text{ } v \rrbracket \rangle \langle e' = \text{unit} \rangle \langle xs' = xs \rangle$ 
  and ha =  $\langle \text{heap-write } h \text{ } a \text{ (CField } D \text{ F)} \text{ } v \text{ } h' \rangle$ 
  have  $\tau: \neg \tau \text{move1 } P \text{ } h \text{ (addr } a \cdot F\{D\} := e2')$  by(auto simp add:  $\tau \text{move1.simps}$   $\tau \text{moves1.simps}$ )
  from bisim2 have s: xcp = None xs = loc
    and  $\tau \text{Exec-mover-a } P \text{ } t \text{ } e2 \text{ } h \text{ (stk, loc, pc, xcp) ([v], loc, length (compE2 } e2), \text{None})}$ 
    by(auto dest: bisim1Val2D1)
  hence  $\tau \text{Exec-mover-a } P \text{ } t \text{ (} e1 \cdot F\{D\} := e2) \text{ } h \text{ (stk @ [v1], loc, length (compE2 } e1) + \text{pc, xcp) ([v] @ [v1], loc, length (compE2 } e1) + \text{length (compE2 } e2), \text{None})}$ 
    by-(rule FAss- $\tau \text{ExecrI2}$ )
  moreover from ha
  have exec-move-a P t (e1·F{D} := e2) h ([v, Addr a], loc, length (compE2 e1) + length (compE2 e2), None)  $\llbracket \text{WriteMem } a \text{ (CField } D \text{ F)} \text{ } v \rrbracket$ 
     $h' ([], \text{loc, Suc (length (compE2 } e1) + \text{length (compE2 } e2)), \text{None})$ 
    by(auto intro!: exec-instr simp add: exec-move-def)
  moreover have  $\tau \text{move2 (compP2 } P) \text{ } h \text{ [v, Addr a] (} e1 \cdot F\{D\} := e2) (\text{length (compE2 } e1) + \text{length (compE2 } e2)) \text{None} \implies \text{False}$ 
    by(simp add:  $\tau \text{move2-iff}$ )
  moreover
  have P, e1·F{D} := e2, h' ⊢ (unit, loc) ↔ ([], loc, Suc (length (compE2 e1) + length (compE2 e2)), None)
    by(rule bisim1-bisims1.bisim1FAss3)
  ultimately show ?thesis using s  $\tau$  by(auto simp del: fun-upd-apply simp add: ta-upd-simps) blast
next
  case (Red1FAssNull v)
  note [simp] =  $\langle v1 = \text{Null} \rangle \langle e2' = \text{Val } v \rangle \langle xs' = xs \rangle \langle ta = \varepsilon \rangle \langle e' = \text{THROW NullPointer} \rangle \langle h' = h \rangle$ 
  have  $\tau: \neg \tau \text{move1 } P \text{ } h \text{ (null} \cdot F\{D\} := e2')$  by(auto simp add:  $\tau \text{move1.simps}$   $\tau \text{moves1.simps}$ )
  from bisim2 have s: xcp = None xs = loc
    and  $\tau \text{Exec-mover-a } P \text{ } t \text{ } e2 \text{ } h \text{ (stk, loc, pc, xcp) ([v], loc, length (compE2 } e2), \text{None})}$ 
    by(auto dest: bisim1Val2D1)
  hence  $\tau \text{Exec-mover-a } P \text{ } t \text{ (} e1 \cdot F\{D\} := e2) \text{ } h \text{ (stk @ [Null], loc, length (compE2 } e1) + \text{pc, xcp) ([v] @ [Null], loc, length (compE2 } e1) + \text{length (compE2 } e2), \text{None})}$ 
    by-(rule FAss- $\tau \text{ExecrI2}$ )
  moreover have exec-move-a P t (e1·F{D} := e2) h ([v, Null], loc, length (compE2 e1) + length (compE2 e2), None)  $\varepsilon$ 
     $h' ([v, Null], \text{loc, length (compE2 } e1) + \text{length (compE2 } e2), \llbracket \text{addr-of-sys-xcpt NullPointer} \rrbracket)$ 
    by(auto intro!: exec-instr simp add: exec-move-def)
  moreover have  $\tau \text{move2 (compP2 } P) \text{ } h \text{ [v, Null] (} e1 \cdot F\{D\} := e2) (\text{length (compE2 } e1) + \text{length (compE2 } e2)) \text{None} \implies \text{False}$ 
    by(simp add:  $\tau \text{move2-iff}$ )
  moreover
  have P, e1·F{D} := e2, h ⊢ (THROW NullPointer, loc) ↔ ([v, Null], loc, length (compE2 e1) + length (compE2 e2),  $\llbracket \text{addr-of-sys-xcpt NullPointer} \rrbracket$ )
    by(rule bisim1-bisims1.bisim1FAssNull)
  ultimately show ?thesis using s  $\tau$  by(auto simp del: fun-upd-apply) blast
next
  case (FAss1Throw2 a)
  note [simp] =  $\langle e2' = \text{Throw } a \rangle \langle ta = \varepsilon \rangle \langle h' = h \rangle \langle xs' = xs \rangle \langle e' = \text{Throw } a \rangle$ 
  have  $\tau: \tau \text{move1 } P \text{ } h \text{ (FAss (Val } v1) \text{ } F \text{ } D \text{ (Throw } a))}$  by(rule  $\tau \text{move1FAssThrow2}$ )
  from bisim2 have xcp = [a] ∨ xcp = None by(auto dest: bisim1-ThrowD)
  thus ?thesis
proof

```

```

assume [simp]: xcp = [a]
with bisim2
have P, e1.F{D} := e2, h ⊢ (Throw a, xs) ↔ (stk @ [v1], loc, length (compE2 e1) + pc, xcp)
  by(auto intro: bisim1FAssThrow2)
thus ?thesis using τ by(fastforce)
next
assume [simp]: xcp = None
with bisim2 obtain pc'
  where τExec-mover-a P t e2 h (stk, loc, pc, None) ([Addr a], loc, pc', [a])
  and bisim': P, e2, h ⊢ (Throw a, xs) ↔ ([Addr a], loc, pc', [a]) and [simp]: xs = loc
  by(auto dest: bisim1-Throw-τExec-mover)
  hence τExec-mover-a P t (e1.F{D} := e2) h (stk @ [v1], loc, length (compE2 e1) + pc, None)
    ([Addr a] @ [v1], loc, length (compE2 e1) + pc', [a])
    by-(rule FAss-τExecrI2)
  moreover from bisim'
  have P, e1.F{D} := e2, h ⊢ (Throw a, xs) ↔ ([Addr a]@[v1], loc, length (compE2 e1) + pc',
    [a])
    by-(rule bisim1FAssThrow2, auto)
  ultimately show ?thesis using τ by auto
qed
qed auto
next
case bisim1FAssThrow1 thus ?case by fastforce
next
case bisim1FAssThrow2 thus ?case by fastforce
next
case bisim1FAssNull thus ?case by fastforce
next
case bisim1FAss3 thus ?case by fastforce
next
case (bisim1CAS1 e1 n e1' xs stk loc pc xcp e2 e3 D F)
note IH1 = bisim1CAS1.IH(2)
note IH2 = bisim1CAS1.IH(4)
note IH3 = bisim1CAS1.IH(6)
note bisim1 = ⟨P, e1, h ⊢ (e1', xs) ↔ (stk, loc, pc, xcp)⟩
note bisim2 = ⟨∧xs. P, e2, h ⊢ (e2, xs) ↔ ([], xs, 0, None)⟩
note bisim3 = ⟨∧xs. P, e3, h ⊢ (e3, xs) ↔ ([], xs, 0, None)⟩
note bsok = ⟨bsok - n⟩
from ⟨True, P, t ⊢ 1 ⟨-, (h, xs)⟩ -ta→ ⟨e', (h', xs')⟩⟩ show ?case
proof cases
  case (CAS1Red1 E')
    note [simp] = ⟨e' = E'.compareAndSwap(D.F, e2, e3)⟩
    and red = ⟨True, P, t ⊢ 1 ⟨e1', (h, xs)⟩ -ta→ ⟨E', (h', xs')⟩⟩
    from red have τmove1 P h (e1'.compareAndSwap(D.F, e2, e3)) = τmove1 P h e1' by(auto simp
      add: τmove1.simps τmoves1.simps)
    moreover from red have call1 (e1'.compareAndSwap(D.F, e2, e3)) = call1 e1' by auto
    moreover from IH1[OF red] bsok
    obtain pc'' stk'' loc'' xcp'' where bisim: P, e1, h' ⊢ (E', xs') ↔ (stk'', loc'', pc'', xcp'')
    and redo: ?exec ta e1 e1' E' h stk loc pc xcp h' pc'' stk'' loc'' xcp'' by auto
    from bisim
    have P, e1.compareAndSwap(D.F, e2, e3), h' ⊢ (E'.compareAndSwap(D.F, e2, e3), xs') ↔ (stk'',
      loc'', pc'', xcp'')
    by(rule bisim1-bisims1.bisim1CAS1)
    moreover {

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assume no-call2 e1 pc
hence no-call2 (e1 • compareAndSwap(D•F, e2, e3)) pc ∨ pc = length (compE2 e1) by(auto simp
add: no-call2-def) }
ultimately show ?thesis using redo
by(auto simp del: call1.simps calls1.simps split: if-split-asm split del: if-split)(blast intro:
CAS-τExecrI1 CAS-τExectI1 exec-move-CASI1)+
next
case (CAS1Red2 E' v)
note [simp] = ⟨e1' = Val v⟩ ⟨e' = Val v • compareAndSwap(D•F, E', e3)⟩
and red = ⟨True, P, t ⊢ 1 ⟨e2, (h, xs)⟩ -ta→ ⟨E', (h', xs')⟩⟩
from red have τ: τmove1 P h (Val v • compareAndSwap(D•F, e2, e3)) = τmove1 P h e2 by(auto
simp add: τmove1.simps τmoves1.simps)
from bisim1 have s: xcp = None xs = loc
and exec1: τExec-mover-a P t e1 h (stk, loc, pc, None) ([v], xs, length (compE2 e1), None)
by(auto dest: bisim1Val2D1)
from exec1 have τExec-mover-a P t (e1 • compareAndSwap(D•F, e2, e3)) h (stk, loc, pc, None)
([v], xs, length (compE2 e1), None)
by(rule CAS-τExecrI1)
moreover
from IH2[OF red] bsok obtain pc'' stk'' loc'' xcp''
where bisim': P, e2, h' ⊢ (E', xs') ↔ (stk'', loc'', pc'', xcp'')
and exec': ?exec ta e2 e2 E' h [] xs 0 None h' pc'' stk'' loc'' xcp'' by auto
have ?exec ta (e1 • compareAndSwap(D•F, e2, e3)) (Val v • compareAndSwap(D•F, e2, e3)) (Val
v • compareAndSwap(D•F, E', e3)) h ([v] @ [v]) xs (length (compE2 e1) + 0) None h' (length (compE2
e1) + pc'') (stk'' @ [v]) loc'' xcp''
proof(cases τmove1 P h (Val v • compareAndSwap(D•F, e2, e3)))
case True
with exec' τ have [simp]: h = h' and e: sim-move e2 E' P t e2 h ([v], xs, 0, None) (stk'', loc'',
pc'', xcp'') by auto
from e have sim-move (e1 • compareAndSwap(D•F, e2, e3)) (e1 • compareAndSwap(D•F, E', e3))
P t (e1 • compareAndSwap(D•F, e2, e3)) h ([v] @ [v], xs, length (compE2 e1) + 0, None) (stk'' @ [v],
loc'', length (compE2 e1) + pc'')
by(fastforce dest: CAS-τExecrI2 CAS-τExectI2)
with True show ?thesis by auto
next
case False
with exec' τ obtain pc' stk' loc' xcp'
where e: τExec-mover-a P t e2 h ([v], xs, 0, None) (stk', loc', pc', xcp')
and e': exec-move-a P t e2 h (stk', loc', pc', xcp') (extTA2JVM (compP2 P) ta) h' (stk'', loc'',
pc'', xcp'')
and τ': ¬ τmove2 (compP2 P) h stk' e2 pc' xcp'
and call: call1 e2 = None ∨ no-call2 e2 0 ∨ pc' = 0 ∧ stk' = [] ∧ loc' = xs ∧ xcp' = None by
auto
from e have τExec-mover-a P t (e1 • compareAndSwap(D•F, e2, e3)) h ([v] @ [v], xs, length
(compE2 e1) + 0, None) (stk' @ [v], loc', length (compE2 e1) + pc', xcp') by(rule CAS-τExecrI2)
moreover from e' have exec-move-a P t (e1 • compareAndSwap(D•F, e2, e3)) h (stk' @ [v], loc',
length (compE2 e1) + pc', xcp') (extTA2JVM (compP2 P) ta) h' (stk'' @ [v], loc'', length (compE2
e1) + pc'')
by(rule exec-move-CASI2)
moreover from e' have pc' < length (compE2 e2) by(auto elim: exec-meth.cases)
with τ' e' have ¬ τmove2 (compP2 P) h (stk' @ [v]) (e1 • compareAndSwap(D•F, e2, e3)) (length
(compE2 e1) + pc') xcp'
by(auto simp add: τinstr-stk-drop-exec-move τmove2-iff)
moreover from red have call1 (e1' • compareAndSwap(D•F, e2, e3)) = call1 e2 by auto

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moreover have no-call2 e2 0  $\implies$  no-call2 (e1.compareAndSwap(D·F, e2, e3)) (length (compE2 e1))
by(auto simp add: no-call2-def)
ultimately show ?thesis using False call
by(auto simp del: split-paired-Ex call1.simps calls1.simps) blast
qed
moreover from bisim'
have P, e1.compareAndSwap(D·F, e2, e3), h'  $\vdash$  (Val v.compareAndSwap(D·F, E', e3), xs')  $\leftrightarrow$ 
((stk'' @ [v]), loc'', length (compE2 e1) + pc'', xcp'')
by(rule bisim1-bisims1.bisim1CAS2)
moreover from bisim1 have pc  $\neq$  length (compE2 e1)  $\longrightarrow$  no-call2 (e1.compareAndSwap(D·F,
e2, e3)) pc
by(auto simp add: no-call2-def dest: bisim-Val-pc-not-Invoke bisim1-pc-length-compE2)
ultimately show ?thesis using  $\tau$  exec1 s
apply(auto simp del: split-paired-Ex call1.simps calls1.simps split: if-split-asm split del: if-split)
apply(blast intro:  $\tau$ Exec-mover-trans|fastforce elim!:  $\tau$ Exec-mover-trans simp del: split-paired-Ex
call1.simps calls1.simps)+
done
next
case (CAS1Red3 E' v v')
note [simp] =  $\langle e2 = \text{Val } v' \rangle \langle e1' = \text{Val } v \rangle \langle e' = \text{Val } v.\text{compareAndSwap}(D\cdot F, \text{Val } v', E') \rangle$ 
and red =  $\langle \text{True}, P, t \vdash 1 \langle e3, (h, xs) \rangle -ta \rightarrow \langle E', (h', xs') \rangle \rangle$ 
from red have  $\tau: \tau\text{move1 } P h (\text{Val } v.\text{compareAndSwap}(D\cdot F, \text{Val } v', e3)) = \tau\text{move1 } P h e3$  by(auto
simp add:  $\tau\text{move1}.simps \tau\text{moves1}.simps$ )
from bisim1 have s: xcp = None xs = loc
and exec1:  $\tau\text{Exec-mover-a } P t e1 h (stk, loc, pc, \text{None}) ([\ ] @ [v], xs, \text{length } (compE2 e1) + 0,$ 
None)
by(auto dest: bisim1Val2D1)
from exec1 have  $\tau\text{Exec-mover-a } P t (e1.\text{compareAndSwap}(D\cdot F, e2, e3)) h (stk, loc, pc, \text{None})$ 
 $([\ ] @ [v], xs, \text{length } (compE2 e1) + 0, \text{None})$ 
by(rule CAS- $\tau\text{ExecrI1}$ )
also from bisim2[of xs]
have  $\tau\text{Exec-mover-a } P t e2 h ([\ ], xs, 0, \text{None}) ([v'], xs, \text{length } (compE2 e2), \text{None})$ 
by(auto dest: bisim1Val2D1)
hence  $\tau\text{Exec-mover-a } P t (e1.\text{compareAndSwap}(D\cdot F, e2, e3)) h ([\ ] @ [v], xs, \text{length } (compE2 e1)$ 
 $+ 0, \text{None}) ([v'] @ [v], xs, \text{length } (compE2 e1) + \text{length } (compE2 e2), \text{None})$ 
by(rule CAS- $\tau\text{ExecrI2}$ )
also (rtranclp-trans) from IH3[OF red] bsok obtain pc'' stk'' loc'' xcp''
where bisim': P, e3, h'  $\vdash$  (E', xs')  $\leftrightarrow$  (stk'', loc'', pc'', xcp'')
and exec': ?exec ta e3 e3 E' h  $\square$  xs 0 None h' pc'' stk'' loc'' xcp'' by auto
have ?exec ta (e1.compareAndSwap(D·F, e2, e3)) (Val v.compareAndSwap(D·F, Val v', e3)) (Val
v.compareAndSwap(D·F, Val v', E')) h ([\ ] @ [v'], v] xs (length (compE2 e1) + length (compE2 e2) +
0) None h' (length (compE2 e1) + length (compE2 e2) + pc'') (stk'' @ [v', v]) loc'' xcp''
proof(cases  $\tau\text{move1 } P h (\text{Val } v.\text{compareAndSwap}(D\cdot F, \text{Val } v', e3))$ )
case True
with exec'  $\tau$  have [simp]: h = h' and e: sim-move e3 E' P t e3 h ([\ ], xs, 0, None) (stk'', loc'',
pc'', xcp'') by auto
from e have sim-move (Val v.compareAndSwap(D·F, Val v', e3)) (Val v.compareAndSwap(D·F,
Val v', E')) P t (e1.compareAndSwap(D·F, e2, e3)) h ([\ ] @ [v', v], xs, length (compE2 e1) + length
(compE2 e2) + 0, None) (stk'' @ [v', v], loc'', length (compE2 e1) + length (compE2 e2) + pc'', xcp'')
by(fastforce dest: CAS- $\tau\text{ExecrI3}$  CAS- $\tau\text{ExecrI3}$  simp del: compE2.simps compEs2.simps)
with True show ?thesis by auto
next
case False

```

**with**  $exec' \tau$  **obtain**  $pc' \ stk' \ loc' \ xcp'$   
**where**  $e: \tau Exec\text{-}mover\text{-}a \ P \ t \ e3 \ h \ (\ [], \ xs, \ 0, \ None) \ (stk', \ loc', \ pc', \ xcp')$   
**and**  $e': exec\text{-}move\text{-}a \ P \ t \ e3 \ h \ (stk', \ loc', \ pc', \ xcp') \ (extTA2JVM \ (compP2 \ P) \ ta) \ h' \ (stk'', \ loc'', \ pc'', \ xcp'')$   
**and**  $\tau': \neg \tau move2 \ (compP2 \ P) \ h \ stk' \ e3 \ pc' \ xcp'$   
**and**  $call: call1 \ e3 = None \vee no\text{-}call2 \ e3 \ 0 \vee pc' = 0 \wedge stk' = [] \wedge loc' = xs \wedge xcp' = None$  **by**  
*auto*  
**from**  $e$  **have**  $\tau Exec\text{-}mover\text{-}a \ P \ t \ (e1 \cdot compareAndSwap(D \cdot F, \ e2, \ e3)) \ h \ (\ [] \ @ \ [v', \ v], \ xs, \ length \ (compE2 \ e1) + length \ (compE2 \ e2) + 0, \ None) \ (stk' \ @ \ [v', \ v], \ loc', \ length \ (compE2 \ e1) + length \ (compE2 \ e2) + pc', \ xcp')$  **by**(rule *CAS- $\tau ExecrI3$* )  
**moreover from**  $e'$  **have**  $exec\text{-}move\text{-}a \ P \ t \ (e1 \cdot compareAndSwap(D \cdot F, \ e2, \ e3)) \ h \ (stk' \ @ \ [v', \ v], \ loc', \ length \ (compE2 \ e1) + length \ (compE2 \ e2) + pc', \ xcp') \ (extTA2JVM \ (compP2 \ P) \ ta) \ h' \ (stk'' \ @ \ [v', \ v], \ loc'', \ length \ (compE2 \ e1) + length \ (compE2 \ e2) + pc'', \ xcp'')$   
**by**(rule *exec-move-CASI3*)  
**moreover from**  $e' \ \tau'$   
**have**  $\neg \tau move2 \ (compP2 \ P) \ h \ (stk' \ @ \ [v', \ v]) \ (e1 \cdot compareAndSwap(D \cdot F, \ e2, \ e3)) \ (length \ (compE2 \ e1) + length \ (compE2 \ e2) + pc')$   $xcp'$   
**by**(*auto simp add:  $\tau instr\text{-}stk\text{-}drop\text{-}exec\text{-}move \ \tau move2\text{-}iff$* )  
**moreover have**  $call1 \ (e1' \cdot compareAndSwap(D \cdot F, \ e2, \ e3)) = call1 \ e3$  **by** *simp*  
**moreover have**  $no\text{-}call2 \ e3 \ 0 \implies no\text{-}call2 \ (e1 \cdot compareAndSwap(D \cdot F, \ e2, \ e3)) \ (length \ (compE2 \ e1) + length \ (compE2 \ e2))$   
**by**(*auto simp add: no-call2-def*)  
**ultimately show** *?thesis* **using** *False call*  
**by**(*auto simp del: split-paired-Ex call1.simps calls1.simps*) **blast**  
**qed**  
**moreover from** *bisim'*  
**have**  $P, e1 \cdot compareAndSwap(D \cdot F, \ e2, \ e3), h' \vdash (Val \ v \cdot compareAndSwap(D \cdot F, \ Val \ v', \ E'), \ xs') \leftrightarrow ((stk'' \ @ \ [v', \ v]), \ loc'', \ length \ (compE2 \ e1) + length \ (compE2 \ e2) + pc'', \ xcp'')$   
**by**(rule *bisim1-bisims1.bisim1CAS3*)  
**moreover from** *bisim1* **have**  $pc \neq length \ (compE2 \ e1) + length \ (compE2 \ e2) \longrightarrow no\text{-}call2 \ (e1 \cdot compareAndSwap(D \cdot F, \ e2, \ e3)) \ pc$   
**by**(*auto simp add: no-call2-def dest: bisim-Val-pc-not-Invoke bisim1-pc-length-compE2*)  
**ultimately show** *?thesis* **using**  $\tau exec1 \ s$   
**apply**(*auto simp del: split-paired-Ex call1.simps calls1.simps split: if-split-asm split del: if-split*)  
**apply**(*blast intro:  $\tau Exec\text{-}mover\text{-}trans$  fastforce elim!:  $\tau Exec\text{-}mover\text{-}trans$  simp del: split-paired-Ex call1.simps calls1.simps*)  
**done**  
**next**  
**case** (*CAS1Null v v'*)  
**note** [*simp*] =  $\langle e1' = null \rangle \langle e' = THROW \ NullPointer \rangle \langle e2 = Val \ v \rangle \langle xs' = xs \rangle \langle ta = \varepsilon \rangle \langle h' = h \rangle \langle e3 = Val \ v' \rangle$   
**have**  $\tau: \neg \tau move1 \ P \ h \ (AAss \ null \ (Val \ v) \ (Val \ v'))$  **by**(*auto simp add:  $\tau move1.simps \ \tau moves1.simps$* )  
**from** *bisim1* **have**  $s: xcp = None \ xs = loc$   
**and**  $\tau Exec\text{-}mover\text{-}a \ P \ t \ e1 \ h \ (stk, \ loc, \ pc, \ xcp) \ (\ [] \ @ \ [Null], \ loc, \ length \ (compE2 \ e1) + 0, \ None)$   
**by**(*auto dest: bisim1Val2D1*)  
**hence**  $\tau Exec\text{-}mover\text{-}a \ P \ t \ (e1 \cdot compareAndSwap(D \cdot F, \ e2, \ e3)) \ h \ (stk, \ loc, \ pc, \ xcp) \ (\ [] \ @ \ [Null], \ loc, \ length \ (compE2 \ e1) + 0, \ None)$   
**by**-(rule *CAS- $\tau ExecrI1$* )  
**also from** *bisim2[of loc]* **have**  $\tau Exec\text{-}mover\text{-}a \ P \ t \ e2 \ h \ (\ [], \ loc, \ 0, \ None) \ ([v], \ loc, \ length \ (compE2 \ e2) + 0, \ None)$   
**by**(*auto dest: bisim1Val2D1*)  
**hence**  $\tau Exec\text{-}mover\text{-}a \ P \ t \ (e1 \cdot compareAndSwap(D \cdot F, \ e2, \ e3)) \ h \ (\ [] \ @ \ [Null], \ loc, \ length \ (compE2 \ e1) + 0, \ None) \ ([v] \ @ \ [Null], \ loc, \ length \ (compE2 \ e1) + (length \ (compE2 \ e2) + 0), \ None)$   
**by**(rule *CAS- $\tau ExecrI2$* )

**also** (*rtranclp-trans*) **have**  $[v] @ [Null] = [] @ [v, Null]$  **by** *simp*  
**also note** *add.assoc[symmetric]*  
**also from** *bisim3[of loc]* **have**  $\tau Exec\text{-}mover\text{-}a\ P\ t\ e3\ h\ ([], loc, 0, None)\ ([v], loc, length\ (compE2\ e3), None)$   
**by**(*auto dest: bisim1Val2D1*)  
**hence**  $\tau Exec\text{-}mover\text{-}a\ P\ t\ (e1 \cdot compareAndSwap(D \cdot F, e2, e3))\ h\ ([[] @ [v, Null], loc, length\ (compE2\ e1) + length\ (compE2\ e2) + 0, None)\ ([v] @ [v, Null], loc, length\ (compE2\ e1) + length\ (compE2\ e2) + length\ (compE2\ e3), None)$   
**by**(*rule CAS- $\tau ExecrI3$* )  
**also** (*rtranclp-trans*)  
**have** *exec-move-a*  $P\ t\ (e1 \cdot compareAndSwap(D \cdot F, e2, e3))\ h\ ([v', v, Null], loc, length\ (compE2\ e1) + length\ (compE2\ e2) + length\ (compE2\ e3), None)\ \varepsilon$   
 $h\ ([v', v, Null], loc, length\ (compE2\ e1) + length\ (compE2\ e2) + length\ (compE2\ e3), [addr\text{-}of\text{-}sys\text{-}xcpt\ NullPointer])$   
**unfolding** *exec-move-def* **by**—(*rule exec-instr, auto simp add: is-Ref-def*)  
**moreover have**  $\tau move2\ (compP2\ P)\ h\ [v', v, Null]\ (e1 \cdot compareAndSwap(D \cdot F, e2, e3))\ (length\ (compE2\ e1) + length\ (compE2\ e2) + length\ (compE2\ e3))\ None \implies False$   
**by**(*simp add:  $\tau move2\text{-}iff$* )  
**moreover**  
**have**  $P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h' \vdash (THROW\ NullPointer, loc) \leftrightarrow ([v', v, Null], loc, length\ (compE2\ e1) + length\ (compE2\ e2) + length\ (compE2\ e3), [addr\text{-}of\text{-}sys\text{-}xcpt\ NullPointer])$   
**by**(*rule bisim1-bisims1.bisim1CASFail*)  
**ultimately show** *?thesis* **using**  $s\ \tau$  **by**(*auto simp add:  $\tau move1.simps$* ) *blast*  
**next**  
**case** (*Red1CASSucceed a v v'*)  
**hence** [*simp*]:  $e1' = addr\ a\ e' = true\ e2 = Val\ v$   
 $ta = \{ReadMem\ a\ (CField\ D\ F)\ v, WriteMem\ a\ (CField\ D\ F)\ v'\}\ xs' = xs\ e3 = Val\ v'$   
**and** *read: heap-read*  $h\ a\ (CField\ D\ F)\ v$   
**and** *write: heap-write*  $h\ a\ (CField\ D\ F)\ v'\ h'$  **by** *auto*  
**have**  $\tau: \neg \tau move1\ P\ h\ (CompareAndSwap\ (addr\ a)\ D\ F\ (Val\ v)\ (Val\ v'))$  **by**(*auto simp add:  $\tau move1.simps\ \tau moves1.simps$* )  
**from** *bisim1* **have**  $s: xcp = None\ xs = loc$   
**and**  $\tau Exec\text{-}mover\text{-}a\ P\ t\ e1\ h\ (stk, loc, pc, xcp)\ ([[] @ [Addr\ a], loc, length\ (compE2\ e1) + 0, None)$   
**by**(*auto dest: bisim1Val2D1*)  
**hence**  $\tau Exec\text{-}mover\text{-}a\ P\ t\ (e1 \cdot compareAndSwap(D \cdot F, e2, e3))\ h\ (stk, loc, pc, xcp)\ ([[] @ [Addr\ a], loc, length\ (compE2\ e1) + 0, None)$   
**by**—(*rule CAS- $\tau ExecrI1$* )  
**also from** *bisim2[of loc]*  
**have**  $\tau Exec\text{-}mover\text{-}a\ P\ t\ e2\ h\ ([], loc, 0, None)\ ([v], loc, length\ (compE2\ e2) + 0, None)$   
**by**(*auto dest: bisim1Val2D1*)  
**hence**  $\tau Exec\text{-}mover\text{-}a\ P\ t\ (e1 \cdot compareAndSwap(D \cdot F, e2, e3))\ h\ ([[] @ [Addr\ a], loc, length\ (compE2\ e1) + 0, None)\ ([v] @ [Addr\ a], loc, length\ (compE2\ e1) + (length\ (compE2\ e2) + 0), None)$   
**by**(*rule CAS- $\tau ExecrI2$* )  
**also** (*rtranclp-trans*) **have**  $[v] @ [Addr\ a] = [] @ [v, Addr\ a]$  **by** *simp*  
**also note** *add.assoc[symmetric]*  
**also from** *bisim3[of loc]* **have**  $\tau Exec\text{-}mover\text{-}a\ P\ t\ e3\ h\ ([], loc, 0, None)\ ([v], loc, length\ (compE2\ e3), None)$   
**by**(*auto dest: bisim1Val2D1*)  
**hence**  $\tau Exec\text{-}mover\text{-}a\ P\ t\ (e1 \cdot compareAndSwap(D \cdot F, e2, e3))\ h\ ([[] @ [v, Addr\ a], loc, length\ (compE2\ e1) + length\ (compE2\ e2) + 0, None)\ ([v] @ [v, Addr\ a], loc, length\ (compE2\ e1) + length\ (compE2\ e2) + length\ (compE2\ e3), None)$   
**by**(*rule CAS- $\tau ExecrI3$* )  
**also** (*rtranclp-trans*) **from** *read write*  
**have** *exec-move-a*  $P\ t\ (e1 \cdot compareAndSwap(D \cdot F, e2, e3))\ h\ ([v', v, Addr\ a], loc, length\ (compE2\ e1) + length\ (compE2\ e2) + length\ (compE2\ e3), None)$



$e1) + \text{length}(\text{compE2 } e2) + \text{length}(\text{compE2 } e3), \text{None})$   
 $\{ \text{ReadMem } a \text{ (CField } D \text{ F) } v, \text{WriteMem } a \text{ (CField } D \text{ F) } v' \}$   
 $h' ([\text{Bool True}], \text{loc}, \text{Suc}(\text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2) +$   
 $\text{length}(\text{compE2 } e3)), \text{None})$   
**unfolding** *exec-move-def* **by**—(*rule exec-instr, auto simp add: compP2-def is-Ref-def*)  
**moreover have**  $\tau \text{move2}(\text{compP2 } P) h [v', v, \text{Addr } a] (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) (\text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2) + \text{length}(\text{compE2 } e3)) \text{None} \implies \text{False}$   
**by**(*simp add:  $\tau \text{move2-iff}$* )  
**moreover**  
**have**  $P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), h' \vdash (\text{true}, \text{loc}) \leftrightarrow ([\text{Bool True}], \text{loc}, \text{length}(\text{compE2 } e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3))), \text{None})$   
**by**(*rule bisim1Val2*) *simp*  
**ultimately show** *?thesis using*  $s \tau$  **by**(*auto simp add: ta-upd-simps*) *blast*  
**next**  
**case** (*Red1CASFail*  $a v'' v v'$ )  
**hence** [*simp*]:  $e1' = \text{addr } a \ e' = \text{false } e2 = \text{Val } v \ h' = h$   
 $ta = \{ \text{ReadMem } a \text{ (CField } D \text{ F) } v'' \} \ xs' = xs \ e3 = \text{Val } v'$   
**and** *read: heap-read*  $h a \text{ (CField } D \text{ F) } v'' v \neq v''$  **by** *auto*  
**have**  $\tau: \neg \tau \text{move1 } P h (\text{CompareAndSwap}(\text{addr } a) D F (\text{Val } v) (\text{Val } v'))$  **by**(*auto simp add:  $\tau \text{move1.simps}$   $\tau \text{moves1.simps}$* )  
**from** *bisim1* **have**  $s: xcp = \text{None } xs = \text{loc}$   
**and**  $\tau \text{Exec-mover-a } P t e1 h (stk, \text{loc}, pc, xcp) ([\ ] @ [\text{Addr } a], \text{loc}, \text{length}(\text{compE2 } e1) + 0, \text{None})$   
**by**(*auto dest: bisim1Val2D1*)  
**hence**  $\tau \text{Exec-mover-a } P t (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) h (stk, \text{loc}, pc, xcp) ([\ ] @ [\text{Addr } a], \text{loc}, \text{length}(\text{compE2 } e1) + 0, \text{None})$   
**by**—(*rule CAS- $\tau \text{ExecrI1}$* )  
**also from** *bisim2[of loc]*  
**have**  $\tau \text{Exec-mover-a } P t e2 h ([\ ], \text{loc}, 0, \text{None}) ([v], \text{loc}, \text{length}(\text{compE2 } e2) + 0, \text{None})$   
**by**(*auto dest: bisim1Val2D1*)  
**hence**  $\tau \text{Exec-mover-a } P t (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) h ([\ ] @ [\text{Addr } a], \text{loc}, \text{length}(\text{compE2 } e1) + 0, \text{None}) ([v] @ [\text{Addr } a], \text{loc}, \text{length}(\text{compE2 } e1) + (\text{length}(\text{compE2 } e2) + 0), \text{None})$   
**by**(*rule CAS- $\tau \text{ExecrI2}$* )  
**also** (*rtranclp-trans*) **have**  $[v] @ [\text{Addr } a] = [\ ] @ [v, \text{Addr } a]$  **by** *simp*  
**also note** *add.assoc[symmetric]*  
**also from** *bisim3[of loc]* **have**  $\tau \text{Exec-mover-a } P t e3 h ([\ ], \text{loc}, 0, \text{None}) ([v'], \text{loc}, \text{length}(\text{compE2 } e3), \text{None})$   
**by**(*auto dest: bisim1Val2D1*)  
**hence**  $\tau \text{Exec-mover-a } P t (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) h ([\ ] @ [v, \text{Addr } a], \text{loc}, \text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2) + 0, \text{None}) ([v'] @ [v, \text{Addr } a], \text{loc}, \text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2) + \text{length}(\text{compE2 } e3), \text{None})$   
**by**(*rule CAS- $\tau \text{ExecrI3}$* )  
**also** (*rtranclp-trans*) **from** *read*  
**have** *exec-move-a*  $P t (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) h ([v', v, \text{Addr } a], \text{loc}, \text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2) + \text{length}(\text{compE2 } e3), \text{None})$   
 $\{ \text{ReadMem } a \text{ (CField } D \text{ F) } v'' \}$   
 $h ([\text{Bool False}], \text{loc}, \text{Suc}(\text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2) +$   
 $\text{length}(\text{compE2 } e3)), \text{None})$   
**unfolding** *exec-move-def* **by**—(*rule exec-instr, auto simp add: compP2-def is-Ref-def*)  
**moreover have**  $\tau \text{move2}(\text{compP2 } P) h [v', v, \text{Addr } a] (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) (\text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2) + \text{length}(\text{compE2 } e3)) \text{None} \implies \text{False}$   
**by**(*simp add:  $\tau \text{move2-iff}$* )  
**moreover**  
**have**  $P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), h \vdash (\text{false}, \text{loc}) \leftrightarrow ([\text{Bool False}], \text{loc}, \text{length}(\text{compE2 } e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3))), \text{None})$

```

    by(rule bisim1Val2) simp
    ultimately show ?thesis using s  $\tau$  by(auto simp add: ta-upd-simps)blast
next
  case (CAS1Throw a)
  hence [simp]:  $e1' = \text{Throw } a \text{ } ta = \varepsilon \text{ } e' = \text{Throw } a \text{ } h' = h \text{ } xs' = xs$  by auto
  have  $\tau: \tau \text{move1 } P \text{ } h \text{ } (\text{Throw } a \cdot \text{compareAndSwap}(D \cdot F, e2, e3))$  by(rule  $\tau \text{move1CASThrow1}$ )
  from bisim1 have  $xcp = \lfloor a \rfloor \vee xcp = \text{None}$  by(auto dest: bisim1-ThrowD)
  thus ?thesis
  proof
    assume [simp]:  $xcp = \lfloor a \rfloor$ 
    with bisim1 have  $P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, xcp)$ 
      by(auto intro: bisim1-bisims1.intros)
    thus ?thesis using  $\tau$  by(fastforce)
  next
    assume [simp]:  $xcp = \text{None}$ 
    with bisim1 obtain  $pc'$  where  $\tau \text{Exec-mover-a } P \text{ } t \text{ } e1 \text{ } h \text{ } (stk, loc, pc, \text{None}) ([\text{Addr } a], loc, pc', \lfloor a \rfloor)$ 
    and  $bisim': P, e1, h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], loc, pc', \lfloor a \rfloor)$ 
    and [simp]:  $xs = loc$ 
    by(auto dest: bisim1-Throw- $\tau \text{Exec-mover}$ )
    hence  $\tau \text{Exec-mover-a } P \text{ } t \text{ } (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \text{ } h \text{ } (stk, loc, pc, \text{None}) ([\text{Addr } a],$ 
     $loc, pc', \lfloor a \rfloor)$ 
    by-(rule CAS- $\tau \text{ExecrI1}$ )
    moreover from bisim'
    have  $P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], loc, pc', \lfloor a \rfloor)$ 
    by(auto intro: bisim1-bisims1.bisim1CASThrow1)
    ultimately show ?thesis using  $\tau$  by auto
  qed
next
  case (CAS1Throw2 v ad)
  note [simp] =  $\langle e1' = \text{Val } v \rangle \langle e2 = \text{Throw } ad \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw } ad \rangle \langle h' = h \rangle \langle xs' = xs \rangle$ 
  from bisim1 have  $s: xcp = \text{None } xs = loc$ 
    and  $\tau \text{Exec-mover-a } P \text{ } t \text{ } e1 \text{ } h \text{ } (stk, loc, pc, xcp) ([v], loc, \text{length } (compE2 \text{ } e1), \text{None})$ 
    by(auto dest: bisim1Val2D1)
  hence  $\tau \text{Exec-mover-a } P \text{ } t \text{ } (e1 \cdot \text{compareAndSwap}(D \cdot F, \text{Throw } ad, e3)) \text{ } h \text{ } (stk, loc, pc, xcp) ([v], loc,$ 
   $\text{length } (compE2 \text{ } e1), \text{None})$ 
  by-(rule CAS- $\tau \text{ExecrI1}$ )
  also have  $\tau \text{Exec-mover-a } P \text{ } t \text{ } (e1 \cdot \text{compareAndSwap}(D \cdot F, \text{Throw } ad, e3)) \text{ } h \text{ } ([v], loc, \text{length } (compE2$ 
   $e1), \text{None}) ([\text{Addr } ad, v], loc, \text{Suc } (\text{length } (compE2 \text{ } e1)), \lfloor ad \rfloor)$ 
  by(rule  $\tau \text{Execr2step}$ )(auto simp add: exec-move-def exec-meth-instr  $\tau \text{move2-iff } \tau \text{move1.simps}$ 
   $\tau \text{moves1.simps}$ )
  also (rtranclp-trans)
  have  $P, e1 \cdot \text{compareAndSwap}(D \cdot F, \text{Throw } ad, e3), h \vdash (\text{Throw } ad, loc) \leftrightarrow ([\text{Addr } ad] @ [v], loc,$ 
   $(\text{length } (compE2 \text{ } e1) + \text{length } (compE2 \text{ } (addr \text{ } ad))), \lfloor ad \rfloor)$ 
  by(rule bisim1CASThrow2[OF bisim1Throw2])
  moreover have  $\tau \text{move1 } P \text{ } h \text{ } (e1' \cdot \text{compareAndSwap}(D \cdot F, \text{Throw } ad, e3))$  by(auto intro:  $\tau \text{move1CASThrow2}$ )
  ultimately show ?thesis using s by auto
next
  case (CAS1Throw3 v v' ad)
  note [simp] =  $\langle e1' = \text{Val } v \rangle \langle e2 = \text{Val } v' \rangle \langle e3 = \text{Throw } ad \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw } ad \rangle \langle h' = h \rangle$ 
   $\langle xs' = xs \rangle$ 
  from bisim1 have  $s: xcp = \text{None } xs = loc$ 
    and  $\tau \text{Exec-mover-a } P \text{ } t \text{ } e1 \text{ } h \text{ } (stk, loc, pc, xcp) ([v], loc, \text{length } (compE2 \text{ } e1), \text{None})$ 
    by(auto dest: bisim1Val2D1)

```

hence  $\tau \text{Exec-mover-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, \text{Throw } ad)) \ h \ (stk, loc, pc, xcp) \ ([v], loc, \text{length} \ (\text{compE2 } e1), \text{None})$   
 by—(rule  $CAS\text{-}\tau \text{ExecrI1}$ )  
 also from  $\text{bisim2}[of \ loc] \ \text{have } \tau \text{Exec-mover-a } P \ t \ e2 \ h \ ([], loc, 0, \text{None}) \ ([v'], loc, \text{length} \ (\text{compE2 } e2), \text{None})$   
 by(auto dest:  $\text{bisim1Val2D1}$ )  
 from  $CAS\text{-}\tau \text{ExecrI2}[OF \ \text{this}, of \ e1 \ D \ F \ e3 \ v]$   
 have  $\tau \text{Exec-mover-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, \text{Throw } ad)) \ h \ ([v], loc, \text{length} \ (\text{compE2 } e1), \text{None}) \ ([v', v], loc, \text{length} \ (\text{compE2 } e1) + \text{length} \ (\text{compE2 } e2), \text{None})$  by simp  
 also (rtranclp-trans)  
 have  $\tau \text{Exec-mover-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, \text{Throw } ad)) \ h \ ([v', v], loc, \text{length} \ (\text{compE2 } e1) + \text{length} \ (\text{compE2 } e2), \text{None}) \ ([Addr \ ad, v', v], loc, \text{Suc} \ (\text{length} \ (\text{compE2 } e1) + \text{length} \ (\text{compE2 } e2)), [ad])$   
 by(rule  $\tau \text{Execr2step}$ )(auto simp add:  $\text{exec-move-def exec-meth-instr } \tau \text{move2-iff } \tau \text{move1.simps } \tau \text{moves1.simps}$ )  
 also (rtranclp-trans)  
 have  $P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, \text{Throw } ad), h \vdash (\text{Throw } ad, loc) \leftrightarrow ([Addr \ ad] @ [v', v], loc, (\text{length} \ (\text{compE2 } e1) + \text{length} \ (\text{compE2 } e2) + \text{length} \ (\text{compE2} \ (\text{addr } ad))), [ad])$   
 by(rule  $\text{bisim1CASThrow3}[OF \ \text{bisim1Throw2}]$ )  
 moreover have  $\tau \text{move1 } P \ h \ (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', \text{Throw } ad))$  by(auto intro:  $\tau \text{move1CASThrow3}$ )  
 ultimately show ?thesis using s by auto  
 qed  
 next  
 case (bisim1CAS2 e2 n e2' xs stk loc pc xcp e1 e3 D F v1)  
 note  $IH2 = \text{bisim1CAS2.IH}(2)$   
 note  $IH3 = \text{bisim1CAS2.IH}(6)$   
 note  $\text{bisim2} = \langle P, e2, h \vdash (e2', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$   
 note  $\text{bisim1} = \langle \bigwedge xs. P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$   
 note  $\text{bisim3} = \langle \bigwedge xs. P, e3, h \vdash (e3, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$   
 note  $bsok = \langle bsok \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ n \rangle$   
 from  $\langle \text{True}, P, t \vdash 1 \ \langle \text{Val } v1 \cdot \text{compareAndSwap}(D \cdot F, e2', e3), (h, xs) \rangle -ta\rightarrow \langle e', (h', xs') \rangle \rangle$  show ?case  
 proof cases  
 case (CAS1Red2 E')  
 note  $[simp] = \langle e' = \text{Val } v1 \cdot \text{compareAndSwap}(D \cdot F, E', e3) \rangle$   
 and  $red = \langle \text{True}, P, t \vdash 1 \ \langle e2', (h, xs) \rangle -ta\rightarrow \langle E', (h', xs') \rangle \rangle$   
 from red have  $\tau: \tau \text{move1 } P \ h \ (\text{Val } v1 \cdot \text{compareAndSwap}(D \cdot F, e2', e3)) = \tau \text{move1 } P \ h \ e2'$  by(auto simp add:  $\tau \text{move1.simps } \tau \text{moves1.simps}$ )  
 from  $IH2[OF \ red] \ bsok$  obtain  $pc'' \ stk'' \ loc'' \ xcp''$   
 where  $\text{bisim}' : P, e2, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$   
 and  $\text{exec}' : ?exec \ ta \ e2 \ e2' \ E' \ h \ stk \ loc \ pc \ xcp \ h' \ pc'' \ stk'' \ loc'' \ xcp''$  by auto  
 have  $?exec \ ta \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ (\text{Val } v1 \cdot \text{compareAndSwap}(D \cdot F, e2', e3)) \ (\text{Val } v1 \cdot \text{compareAndSwap}(D \cdot F, E', e3)) \ h \ (stk @ [v1]) \ loc \ (\text{length} \ (\text{compE2 } e1) + pc) \ xcp \ h' \ (\text{length} \ (\text{compE2 } e1) + pc'') \ (stk'' @ [v1]) \ loc'' \ xcp''$   
 proof(cases  $\tau \text{move1 } P \ h \ (\text{Val } v1 \cdot \text{compareAndSwap}(D \cdot F, e2', e3))$ )  
 case True  
 with  $\text{exec}' \ \tau$  have  $[simp]: h = h'$  and  $e: \text{sim-move } e2' \ E' \ P \ t \ e2 \ h \ (stk, loc, pc, xcp) \ (stk'', loc'', pc'', xcp'')$  by auto  
 from e have  $\text{sim-move} \ (\text{Val } v1 \cdot \text{compareAndSwap}(D \cdot F, e2', e3)) \ (\text{Val } v1 \cdot \text{compareAndSwap}(D \cdot F, E', e3)) \ P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ h \ (stk @ [v1], loc, \text{length} \ (\text{compE2 } e1) + pc, xcp) \ (stk'' @ [v1], loc'', \text{length} \ (\text{compE2 } e1) + pc'', xcp'')$   
 by(fastforce dest:  $CAS\text{-}\tau \text{ExecrI2 } CAS\text{-}\tau \text{ExecrI2 } \text{simp del: compE2.simps compEs2.simps}$ )  
 with True show ?thesis by auto  
 next

**case** *False*  
**with** *exec'*  $\tau$  **obtain** *pc' stk' loc' xcp'*  
**where** *e*:  $\tau \text{Exec-mover-a } P \ t \ e2 \ h \ (stk, loc, pc, xcp) \ (stk', loc', pc', xcp')$   
**and** *e'*:  $\text{exec-move-a } P \ t \ e2 \ h \ (stk', loc', pc', xcp') \ (\text{extTA2JVM } (\text{compP2 } P) \ ta) \ h' \ (stk'', loc'', pc'', xcp'')$   
**and**  $\tau'$ :  $\neg \tau \text{move2 } (\text{compP2 } P) \ h \ stk' \ e2 \ pc' \ xcp'$   
**and** *call*:  $\text{call1 } e2' = \text{None} \vee \text{no-call2 } e2 \ pc \vee pc' = pc \wedge stk' = stk \wedge loc' = loc \wedge xcp' = xcp$   
**by** *auto*  
**from** *e* **have**  $\tau \text{Exec-mover-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ h \ (stk \ @ \ [v1], loc, \text{length } (\text{compE2 } e1) + pc, xcp) \ (stk' \ @ \ [v1], loc', \text{length } (\text{compE2 } e1) + pc', xcp')$  **by** (*rule CAS- $\tau \text{ExecrI2}$* )  
**moreover from** *e'* **have**  $\text{exec-move-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ h \ (stk' \ @ \ [v1], loc', \text{length } (\text{compE2 } e1) + pc', xcp') \ (\text{extTA2JVM } (\text{compP2 } P) \ ta) \ h' \ (stk'' \ @ \ [v1], loc'', \text{length } (\text{compE2 } e1) + pc'', xcp'')$   
**by** (*rule exec-move-CASI2*)  
**moreover from** *e'* **have**  $pc' < \text{length } (\text{compE2 } e2)$  **by** (*auto elim: exec-meth.cases*)  
**with**  $\tau' \ e'$  **have**  $\neg \tau \text{move2 } (\text{compP2 } P) \ h \ (stk' \ @ \ [v1]) \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ (\text{length } (\text{compE2 } e1) + pc') \ xcp'$   
**by** (*auto simp add:  $\tau \text{instr-stk-drop-exec-move } \tau \text{move2-iff}$* )  
**moreover from** *red* **have**  $\text{call1 } (\text{Val } v1 \cdot \text{compareAndSwap}(D \cdot F, e2', e3)) = \text{call1 } e2'$  **by** *auto*  
**moreover have**  $\text{no-call2 } e2 \ pc \implies \text{no-call2 } (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ (\text{length } (\text{compE2 } e1) + pc)$   
**by** (*auto simp add: no-call2-def*)  
**ultimately show** *?thesis* **using** *False call* **by** (*auto simp del: split-paired-Ex call1.simps calls1.simps*)

**qed**  
**moreover from** *bisim'*  
**have**  $P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), h' \vdash (\text{Val } v1 \cdot \text{compareAndSwap}(D \cdot F, E', e3), xs') \leftrightarrow ((stk'' \ @ \ [v1]), loc'', \text{length } (\text{compE2 } e1) + pc'', xcp'')$   
**by** (*rule bisim1-bisims1.bisim1CAS2*)  
**ultimately show** *?thesis*  
**apply** (*auto simp del: split-paired-Ex call1.simps calls1.simps split: if-split-asm split del: if-split*)  
**apply** (*blast intro:  $\tau \text{Exec-mover-trans}$* )  
**done**

**next**  
**case** (*CAS1Red3 E' v'*)  
**note** [*simp*] =  $\langle e2' = \text{Val } v' \rangle \langle e' = \text{Val } v1 \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', E') \rangle$   
**and** *red* =  $\langle \text{True}, P, t \vdash 1 \ \langle e3, (h, xs) \rangle - ta \rightarrow \langle E', (h', xs') \rangle \rangle$   
**from** *red* **have**  $\tau: \tau \text{move1 } P \ h \ (\text{Val } v1 \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', e3)) = \tau \text{move1 } P \ h \ e3$   
**by** (*auto simp add:  $\tau \text{move1.simps } \tau \text{moves1.simps}$* )  
**from** *bisim2* **have** *s*:  $xcp = \text{None} \ xs = loc$   
**and** *exec1*:  $\tau \text{Exec-mover-a } P \ t \ e2 \ h \ (stk, loc, pc, xcp) \ ([v'], xs, \text{length } (\text{compE2 } e2), \text{None})$   
**by** (*auto dest: bisim1Val2D1*)  
**hence**  $\tau \text{Exec-mover-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ h \ (stk \ @ \ [v1], loc, \text{length } (\text{compE2 } e1) + pc, xcp) \ ([v'] \ @ \ [v1], xs, \text{length } (\text{compE2 } e1) + \text{length } (\text{compE2 } e2), \text{None})$   
**by** (*rule CAS- $\tau \text{ExecrI2}$* )  
**moreover from** *IH3[OF red] bsok* **obtain** *pc'' stk'' loc'' xcp''*  
**where** *bisim'*:  $P, e3, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$   
**and** *exec'*:  $? \text{exec } ta \ e3 \ e3 \ E' \ h \ [] \ xs \ 0 \ \text{None} \ h' \ pc'' \ stk'' \ loc'' \ xcp''$  **by** *auto*  
**have**  $? \text{exec } ta \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ (\text{Val } v1 \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', e3)) \ (\text{Val } v1 \ [ \text{Val } v'] := E') \ h \ ([] \ @ \ [v', v1]) \ xs \ (\text{length } (\text{compE2 } e1) + \text{length } (\text{compE2 } e2) + 0) \ \text{None} \ h' \ (\text{length } (\text{compE2 } e1) + \text{length } (\text{compE2 } e2) + pc'') \ (stk'' \ @ \ [v', v1]) \ loc'' \ xcp''$   
**proof** (*cases  $\tau \text{move1 } P \ h \ (\text{Val } v1 \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', e3))$* )  
**case** *True*  
**with** *exec'  $\tau$*  **have** [*simp*]:  $h = h'$

**and**  $e$ : *sim-move*  $e3\ E'\ P\ t\ e3\ h\ (\[],\ xs,\ 0,\ None)\ (stk'',\ loc'',\ pc'',\ xcp'')$  **by** *auto*  
**from**  $e$  **have** *sim-move*  $(\text{Val } v1 \cdot \text{compareAndSwap}(D \cdot F,\ \text{Val } v',\ e3))\ (\text{Val } v1 \cdot \text{compareAndSwap}(D \cdot F,\ \text{Val } v',\ E'))\ P\ t\ (e1 \cdot \text{compareAndSwap}(D \cdot F,\ e2,\ e3))\ h\ (\[]\ @\ [v',\ v1],\ xs,\ \text{length}\ (\text{compE2}\ e1) + \text{length}\ (\text{compE2}\ e2) + 0,\ None)\ (stk''\ @\ [v',\ v1],\ loc'',\ \text{length}\ (\text{compE2}\ e1) + \text{length}\ (\text{compE2}\ e2) + pc'',\ xcp'')$   
**by**(*fastforce dest: CAS- $\tau$ ExecI3 CAS- $\tau$ ExecrI3 simp del: compE2.simps compEs2.simps*)  
**with** *True show ?thesis by auto*  
**next**  
**case** *False*  
**with** *exec'  $\tau$  obtain  $pc'\ stk'\ loc'\ xcp'$*   
**where**  $e$ :  $\tau$ Exec-mover-a  $P\ t\ e3\ h\ (\[],\ xs,\ 0,\ None)\ (stk',\ loc',\ pc',\ xcp')$   
**and**  $e'$ : *exec-move-a*  $P\ t\ e3\ h\ (stk',\ loc',\ pc',\ xcp')\ (\text{extTA2JVM}\ (\text{compP2}\ P)\ ta)\ h'\ (stk'',\ loc'',\ pc'',\ xcp'')$   
**and**  $\tau'$ :  $\neg \tau \text{move2}\ (\text{compP2}\ P)\ h\ stk'\ e3\ pc'\ xcp'$   
**and** *call*:  $\text{call1}\ e3 = None \vee \text{no-call2}\ e3\ 0 \vee pc' = 0 \wedge stk' = [] \wedge loc' = xs \wedge xcp' = None$  **by** *auto*  
**from**  $e$  **have**  $\tau$ Exec-mover-a  $P\ t\ (e1 \cdot \text{compareAndSwap}(D \cdot F,\ e2,\ e3))\ h\ (\[]\ @\ [v',\ v1],\ xs,\ \text{length}\ (\text{compE2}\ e1) + \text{length}\ (\text{compE2}\ e2) + 0,\ None)\ (stk'\ @\ [v',\ v1],\ loc',\ \text{length}\ (\text{compE2}\ e1) + \text{length}\ (\text{compE2}\ e2) + pc',\ xcp')$  **by**(*rule CAS- $\tau$ ExecrI3*)  
**moreover from**  $e'$  **have** *exec-move-a*  $P\ t\ (e1 \cdot \text{compareAndSwap}(D \cdot F,\ e2,\ e3))\ h\ (stk'\ @\ [v',\ v1],\ loc',\ \text{length}\ (\text{compE2}\ e1) + \text{length}\ (\text{compE2}\ e2) + pc',\ xcp')\ (\text{extTA2JVM}\ (\text{compP2}\ P)\ ta)\ h'\ (stk''\ @\ [v',\ v1],\ loc'',\ \text{length}\ (\text{compE2}\ e1) + \text{length}\ (\text{compE2}\ e2) + pc'',\ xcp'')$   
**by**(*rule exec-move-CASI3*)  
**moreover from**  $e'\ \tau'$  **have**  $\neg \tau \text{move2}\ (\text{compP2}\ P)\ h\ (stk'\ @\ [v',\ v1])\ (e1 \cdot \text{compareAndSwap}(D \cdot F,\ e2,\ e3))\ (\text{length}\ (\text{compE2}\ e1) + \text{length}\ (\text{compE2}\ e2) + pc')\ xcp'$   
**by**(*auto simp add:  $\tau$ instr-stk-drop-exec-move  $\tau$ move2-iff*)  
**moreover from** *red* **have**  $\text{call1}\ (\text{Val } v1 \cdot \text{compareAndSwap}(D \cdot F,\ \text{Val } v',\ e3)) = \text{call1}\ e3$  **by** *auto*  
**moreover have**  $\text{no-call2}\ e3\ 0 \implies \text{no-call2}\ (e1 \cdot \text{compareAndSwap}(D \cdot F,\ e2,\ e3))\ (\text{length}\ (\text{compE2}\ e1) + \text{length}\ (\text{compE2}\ e2))$   
**by**(*auto simp add: no-call2-def*)  
**ultimately show** *?thesis using False call by*(*auto simp del: split-paired-Ex call1.simps calls1.simps*)  
*blast*  
**qed**  
**moreover from** *bisim'*  
**have**  $P, e1 \cdot \text{compareAndSwap}(D \cdot F,\ e2,\ e3), h' \vdash (\text{Val } v1 \cdot \text{compareAndSwap}(D \cdot F,\ \text{Val } v',\ E'), xs') \leftrightarrow ((stk''\ @\ [v',\ v1]),\ loc'',\ \text{length}\ (\text{compE2}\ e1) + \text{length}\ (\text{compE2}\ e2) + pc'',\ xcp'')$   
**by**(*rule bisim1-bisims1.bisim1CAS3*)  
**moreover from** *bisim2* **have**  $pc \neq \text{length}\ (\text{compE2}\ e2) \longrightarrow \text{no-call2}\ (e1 \cdot \text{compareAndSwap}(D \cdot F,\ e2,\ e3))\ (\text{length}\ (\text{compE2}\ e1) + pc)$   
**by**(*auto simp add: no-call2-def dest: bisim-Val-pc-not-Invoke bisim1-pc-length-compE2*)  
**ultimately show** *?thesis using  $\tau$  exec1 s*  
**apply**(*auto simp del: split-paired-Ex call1.simps calls1.simps split: if-split-asm split del: if-split*)  
**apply**(*blast intro:  $\tau$ Exec-mover-trans|fastforce elim!:  $\tau$ Exec-mover-trans simp del: split-paired-Ex call1.simps calls1.simps*)  
**done**  
**next**  
**case** (*CAS1Null*  $v\ v'$ )  
**note** [*simp*] =  $\langle v1 = \text{Null} \rangle\ \langle e' = \text{THROW NullPointer} \rangle\ \langle e2' = \text{Val } v \rangle\ \langle xs' = xs \rangle\ \langle ta = \varepsilon \rangle\ \langle h' = h \rangle\ \langle e3 = \text{Val } v' \rangle$   
**have**  $\tau$ :  $\neg \tau \text{move1}\ P\ h\ (\text{CompareAndSwap}\ \text{null}\ D\ F\ (\text{Val } v)\ (\text{Val } v'))$  **by**(*auto simp add:  $\tau$ move1.simps  $\tau$ moves1.simps*)  
**from** *bisim2* **have**  $s$ :  $xcp = None\ xs = loc$   
**and**  $\tau$ Exec-mover-a  $P\ t\ e2\ h\ (stk,\ loc,\ pc,\ xcp)\ ([v],\ loc,\ \text{length}\ (\text{compE2}\ e2),\ None)$   
**by**(*auto dest: bisim1Val2D1*)

**hence**  $\tau \text{Exec-mover-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ h \ (stk \ @ \ [Null], loc, length \ (compE2 \ e1) + pc, xcp) \ ([v] \ @ \ [Null], loc, length \ (compE2 \ e1) + length \ (compE2 \ e2), None)$   
**by**—(rule *CAS- $\tau$ ExecrI2*)  
**hence**  $\tau \text{Exec-mover-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ h \ (stk \ @ \ [Null], loc, length \ (compE2 \ e1) + pc, xcp) \ ([v] \ @ \ [v, Null], loc, length \ (compE2 \ e1) + length \ (compE2 \ e2) + 0, None)$  **by** *simp*  
**also from** *bisim3[of loc]* **have**  $\tau \text{Exec-mover-a } P \ t \ e3 \ h \ ([v], loc, 0, None) \ ([v], loc, length \ (compE2 \ e3), None)$   
**by**(auto *dest: bisim1Val2D1*)  
**hence**  $\tau \text{Exec-mover-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ h \ ([v] \ @ \ [v, Null], loc, length \ (compE2 \ e1) + length \ (compE2 \ e2) + 0, None) \ ([v] \ @ \ [v, Null], loc, length \ (compE2 \ e1) + length \ (compE2 \ e2) + length \ (compE2 \ e3), None)$   
**by**(rule *CAS- $\tau$ ExecrI3*)  
**also** (*rtranclp-trans*)  
**have**  $\text{exec-move-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ h \ ([v', v, Null], loc, length \ (compE2 \ e1) + length \ (compE2 \ e2) + length \ (compE2 \ e3), None) \ \varepsilon$   
 $h \ ([v', v, Null], loc, length \ (compE2 \ e1) + length \ (compE2 \ e2) + length \ (compE2 \ e3), \lfloor \text{addr-of-sys-xcpt } NullPointer \rfloor)$   
**unfolding** *exec-move-def* **by**—(rule *exec-instr, auto simp add: is-Ref-def*)  
**moreover have**  $\tau \text{move2} \ (compP2 \ P) \ h \ [v', v, Null] \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ (length \ (compE2 \ e1) + length \ (compE2 \ e2) + length \ (compE2 \ e3)) \ None \implies False$   
**by**(*simp add:  $\tau \text{move2-iff}$* )  
**moreover**  
**have**  $P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), h' \vdash (THROW \ NullPointer, loc) \leftrightarrow ([v', v, Null], loc, length \ (compE2 \ e1) + length \ (compE2 \ e2) + length \ (compE2 \ e3), \lfloor \text{addr-of-sys-xcpt } NullPointer \rfloor)$   
**by**(rule *bisim1-bisims1.bisim1CASFail*)  
**ultimately show** *?thesis* **using**  $s \ \tau$  **by** *auto blast*  
**next**  
**case** (*Red1CASSucceed a v v'*)  
**hence** [*simp*]:  $v1 = \text{Addr } a \ e' = \text{true } e2' = \text{Val } v$   
 $ta = \{ \text{ReadMem } a \ (CField \ D \ F) \ v, \text{WriteMem } a \ (CField \ D \ F) \ v' \} \ xs' = xs \ e3 = \text{Val } v'$   
**and** *read: heap-read h a (CField D F) v*  
**and** *write: heap-write h a (CField D F) v' h' by auto*  
**have**  $\tau: \neg \tau \text{move1 } P \ h \ (\text{CompareAndSwap} \ (\text{addr } a) \ D \ F \ (\text{Val } v) \ (\text{Val } v'))$  **by**(auto *simp add:  $\tau \text{move1.simps} \ \tau \text{moves1.simps}$* )  
**from** *bisim2* **have**  $s: xcp = None \ xs = loc$   
**and**  $\tau \text{Exec-mover-a } P \ t \ e2 \ h \ (stk, loc, pc, xcp) \ ([v], loc, length \ (compE2 \ e2), None)$   
**by**(auto *dest: bisim1Val2D1*)  
**hence**  $\tau \text{Exec-mover-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ h \ (stk \ @ \ [\text{Addr } a], loc, length \ (compE2 \ e1) + pc, xcp) \ ([v] \ @ \ [\text{Addr } a], loc, length \ (compE2 \ e1) + length \ (compE2 \ e2), None)$   
**by**—(rule *CAS- $\tau$ ExecrI2*)  
**hence**  $\tau \text{Exec-mover-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ h \ (stk \ @ \ [\text{Addr } a], loc, length \ (compE2 \ e1) + pc, xcp) \ ([v] \ @ \ [v, \text{Addr } a], loc, length \ (compE2 \ e1) + length \ (compE2 \ e2) + 0, None)$   
**by** *simp*  
**also from** *bisim3[of loc]* **have**  $\tau \text{Exec-mover-a } P \ t \ e3 \ h \ ([v], loc, 0, None) \ ([v], loc, length \ (compE2 \ e3), None)$   
**by**(auto *dest: bisim1Val2D1*)  
**hence**  $\tau \text{Exec-mover-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ h \ ([v] \ @ \ [v, \text{Addr } a], loc, length \ (compE2 \ e1) + length \ (compE2 \ e2) + 0, None) \ ([v] \ @ \ [v, \text{Addr } a], loc, length \ (compE2 \ e1) + length \ (compE2 \ e2) + length \ (compE2 \ e3), None)$   
**by**(rule *CAS- $\tau$ ExecrI3*)  
**also** (*rtranclp-trans*) **from** *read write*  
**have**  $\text{exec-move-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ h \ ([v', v, \text{Addr } a], loc, length \ (compE2 \ e1) + length \ (compE2 \ e2) + length \ (compE2 \ e3), None)$   
 $\{ \text{ReadMem } a \ (CField \ D \ F) \ v, \text{WriteMem } a \ (CField \ D \ F) \ v' \}$

$h' ([\text{Bool True}], \text{loc}, \text{Suc} (\text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2) + \text{length} (\text{compE2 } e3)), \text{None})$   
**unfolding** *exec-move-def* **by**–(*rule exec-instr, auto simp add: compP2-def is-Ref-def*)  
**moreover have**  $\tau \text{move2} (\text{compP2 } P) h [v', v, \text{Addr } a] (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) (\text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2) + \text{length} (\text{compE2 } e3)) \text{None} \implies \text{False}$   
**by**(*simp add:  $\tau \text{move2-iff}$* )  
**moreover**  
**have**  $P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), h' \vdash (\text{true}, \text{loc}) \leftrightarrow ([\text{Bool True}], \text{loc}, \text{length} (\text{compE2 } (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3))), \text{None})$   
**by**(*rule bisim1Val2*) *simp*  
**ultimately show** *?thesis* **using**  $s \tau$  **by**(*auto simp add: ta-upd-simps*) *blast*  
**next**  
**case** (*Red1CASFail a v'' v v'*)  
**hence** [*simp*]:  $v1 = \text{Addr } a \ e' = \text{false} \ e2' = \text{Val } v \ h' = h$   
 $ta = \{ \text{ReadMem } a \ (C\text{Field } D \ F) \ v'' \} \ xs' = xs \ e3 = \text{Val } v'$   
**and** *read: heap-read h a (CField D F) v'' v  $\neq$  v'' by auto*  
**have**  $\tau: \neg \tau \text{move1 } P h (\text{CompareAndSwap} (\text{addr } a) \ D \ F \ (\text{Val } v) \ (\text{Val } v'))$  **by**(*auto simp add:  $\tau \text{move1.simps}$   $\tau \text{moves1.simps}$* )  
**from** *bisim2* **have**  $s: xcp = \text{None} \ xs = \text{loc}$   
**and**  $\tau \text{Exec-mover-a } P \ t \ e2 \ h \ (stk, \text{loc}, pc, xcp) ([v], \text{loc}, \text{length} (\text{compE2 } e2), \text{None})$   
**by**(*auto dest: bisim1Val2D1*)  
**hence**  $\tau \text{Exec-mover-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ h \ (stk \ @ \ [\text{Addr } a], \text{loc}, \text{length} (\text{compE2 } e1) + pc, xcp) ([v] \ @ \ [\text{Addr } a], \text{loc}, \text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2), \text{None})$   
**by**–(*rule CAS- $\tau \text{ExecrI2}$* )  
**hence**  $\tau \text{Exec-mover-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ h \ (stk \ @ \ [\text{Addr } a], \text{loc}, \text{length} (\text{compE2 } e1) + pc, xcp) ([] \ @ \ [v, \text{Addr } a], \text{loc}, \text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2) + 0, \text{None})$   
**by** *simp*  
**also from** *bisim3[of loc]* **have**  $\tau \text{Exec-mover-a } P \ t \ e3 \ h \ ([], \text{loc}, 0, \text{None}) ([v'], \text{loc}, \text{length} (\text{compE2 } e3), \text{None})$   
**by**(*auto dest: bisim1Val2D1*)  
**hence**  $\tau \text{Exec-mover-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ h \ ([] \ @ \ [v, \text{Addr } a], \text{loc}, \text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2) + 0, \text{None}) ([v'] \ @ \ [v, \text{Addr } a], \text{loc}, \text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2) + \text{length} (\text{compE2 } e3), \text{None})$   
**by**(*rule CAS- $\tau \text{ExecrI3}$* )  
**also** (*rtranclp-trans*) **from** *read*  
**have**  $\text{exec-move-a } P \ t \ (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ h \ ([v', v, \text{Addr } a], \text{loc}, \text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2) + \text{length} (\text{compE2 } e3), \text{None})$   
 $\{ \text{ReadMem } a \ (C\text{Field } D \ F) \ v'' \}$   
 $h' ([\text{Bool False}], \text{loc}, \text{Suc} (\text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2) + \text{length} (\text{compE2 } e3)), \text{None})$   
**unfolding** *exec-move-def* **by**–(*rule exec-instr, auto simp add: compP2-def is-Ref-def*)  
**moreover have**  $\tau \text{move2} (\text{compP2 } P) h [v', v, \text{Addr } a] (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) (\text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2) + \text{length} (\text{compE2 } e3)) \text{None} \implies \text{False}$   
**by**(*simp add:  $\tau \text{move2-iff}$* )  
**moreover**  
**have**  $P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), h' \vdash (\text{false}, \text{loc}) \leftrightarrow ([\text{Bool False}], \text{loc}, \text{length} (\text{compE2 } (e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3))), \text{None})$   
**by**(*rule bisim1Val2*) *simp*  
**ultimately show** *?thesis* **using**  $s \tau$  **by**(*auto simp add: ta-upd-simps*) *blast*  
**next**  
**case** (*CAS1Throw2 ad*)  
**note** [*simp*] =  $\langle e2' = \text{Throw } ad \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw } ad \rangle \langle h' = h \rangle \langle xs' = xs \rangle$   
**have**  $\tau: \tau \text{move1 } P \ h \ (\text{Val } v1 \cdot \text{compareAndSwap}(D \cdot F, \text{Throw } ad, e3))$  **by**(*rule  $\tau \text{move1CASThrow2}$* )  
**from** *bisim2* **have**  $xcp = [ad] \vee xcp = \text{None}$  **by**(*auto dest: bisim1-ThrowD*)

```

thus ?thesis
proof
  assume [simp]: xcp = [ad]
  with bisim2
  have P, e1.compareAndSwap(D·F, e2, e3), h ⊢ (Throw ad, xs) ↔ (stk @ [v1], loc, length (compE2
e1) + pc, xcp)
    by(auto intro: bisim1-bisims1.intros)
  thus ?thesis using τ by(fastforce)
next
  assume [simp]: xcp = None
  with bisim2 obtain pc' where τExec-mover-a P t e2 h (stk, loc, pc, None) ([Addr ad], loc, pc',
[ad])
    and bisim': P, e2, h ⊢ (Throw ad, xs) ↔ ([Addr ad], loc, pc', [ad])
    and [simp]: xs = loc
    by(auto dest: bisim1-Throw-τExec-mover)
  hence τExec-mover-a P t (e1.compareAndSwap(D·F, e2, e3)) h (stk @ [v1], loc, length (compE2
e1) + pc, None) ([Addr ad] @ [v1], loc, length (compE2 e1) + pc', [ad])
    by-(rule CAS-τExecrI2)
  moreover from bisim'
  have P, e1.compareAndSwap(D·F, e2, e3), h ⊢ (Throw ad, xs) ↔ ([Addr ad] @ [v1], loc, length
(compE2 e1) + pc', [ad])
    by(rule bisim1-bisims1.bisim1CASThrow2)
  ultimately show ?thesis using τ by auto
qed
next
  case (CAS1Throw3 v' ad)
  note [simp] = ⟨e2' = Val v'⟩ ⟨e3 = Throw ad⟩ ⟨ta = ε⟩ ⟨e' = Throw ad⟩ ⟨h' = h⟩ ⟨xs' = xs⟩
  from bisim2 have s: xcp = None xs = loc
    and τExec-mover-a P t e2 h (stk, loc, pc, xcp) ([v], loc, length (compE2 e2), None)
    by(auto dest: bisim1Val2D1)
  hence τExec-mover-a P t (e1.compareAndSwap(D·F, e2, Throw ad)) h (stk @ [v1], loc, length
(compE2 e1) + pc, xcp) ([v] @ [v1], loc, length (compE2 e1) + length (compE2 e2), None)
    by-(rule CAS-τExecrI2)
  also have τExec-mover-a P t (e1.compareAndSwap(D·F, e2, Throw ad)) h ([v] @ [v1], loc, length
(compE2 e1) + length (compE2 e2), None) ([Addr ad, v', v1], loc, Suc (length (compE2 e1) + length
(compE2 e2)), [ad])
    by(rule τExecr2step)(auto simp add: exec-move-def exec-meth-instr τmove2-iff τmove1.simps
τmoves1.simps)
  also (rtranclp-trans)
  have P, e1.compareAndSwap(D·F, e2, Throw ad), h ⊢ (Throw ad, loc) ↔ ([Addr ad] @ [v', v1],
loc, (length (compE2 e1) + length (compE2 e2) + length (compE2 (addr ad))), [ad])
    by(rule bisim1CASThrow3[OF bisim1Throw2])
  moreover have τmove1 P h (CompareAndSwap (Val v1) D F (Val v') (Throw ad)) by(auto intro:
τmove1CASThrow3)
  ultimately show ?thesis using s by auto
qed auto
next
  case (bisim1CAS3 e3 n e3' xs stk loc pc xcp e1 e2 D F v v')
  note IH3 = bisim1CAS3.IH(2)
  note bisim3 = ⟨P, e3, h ⊢ (e3', xs) ↔ (stk, loc, pc, xcp)⟩
  note bsok = ⟨bsok (e1.compareAndSwap(D·F, e2, e3)) n⟩
  from ⟨True, P, t ⊢ 1 ⟨Val v·compareAndSwap(D·F, Val v', e3'), (h, xs)⟩ -ta→ ⟨e', (h', xs')⟩⟩ show
?case
proof cases

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case (CAS1Red3 E')
note [simp] = ⟨e' = Val v·compareAndSwap(D·F, Val v', E')⟩
  and red = ⟨True, P, t ⊢ 1 ⟨e3', (h, xs)⟩ -ta→ ⟨E', (h', xs')⟩⟩
  from red have τ: τmove1 P h (Val v·compareAndSwap(D·F, Val v', e3')) = τmove1 P h e3'
by(auto simp add: τmove1.simps τmoves1.simps)
  from IH3[OF red] bsok obtain pc'' stk'' loc'' xcp''
  where bisim': P, e3, h' ⊢ (E', xs') ↔ (stk'', loc'', pc'', xcp'')
  and exec': ?exec ta e3 e3' E' h stk loc pc xcp h' pc'' stk'' loc'' xcp'' by auto
  have no-call2 e3 pc ⇒ no-call2 (e1·compareAndSwap(D·F, e2, e3)) (length (compE2 e1) +
length (compE2 e2) + pc)
  by(auto simp add: no-call2-def)
  hence ?exec ta (e1·compareAndSwap(D·F, e2, e3)) (Val v·compareAndSwap(D·F, Val v', e3'))
(Val v·compareAndSwap(D·F, Val v', E')) h (stk @ [v', v]) loc (length (compE2 e1) + length (compE2
e2) + pc) xcp h' (length (compE2 e1) + length (compE2 e2) + pc'') (stk'' @ [v', v]) loc'' xcp''
  using exec' τ
  apply(cases τmove1 P h (Val v·compareAndSwap(D·F, Val v', e3')))
  apply(auto)
  apply(blast intro: CAS-τExecrI3 CAS-τExecI3 exec-move-CASI3)
  apply(blast intro: CAS-τExecrI3 CAS-τExecI3 exec-move-CASI3)
  apply(rule exI conjI CAS-τExecrI3 exec-move-CASI3|assumption)+
  apply(fastforce simp add: τinstr-stk-drop-exec-move τmove2-iff split: if-split-asm)
  apply(rule exI conjI CAS-τExecrI3 exec-move-CASI3|assumption)+
  apply(fastforce simp add: τinstr-stk-drop-exec-move τmove2-iff split: if-split-asm)
  apply(rule exI conjI CAS-τExecrI3 exec-move-CASI3 rtranclp.rtrancl-refl|assumption)+
  apply(fastforce simp add: τinstr-stk-drop-exec-move τmove2-iff split: if-split-asm)+
  done
moreover from bisim'
  have P, e1·compareAndSwap(D·F, e2, e3), h' ⊢ (Val v·compareAndSwap(D·F, Val v', E'), xs') ↔
(stk''@[v', v], loc'', length (compE2 e1) + length (compE2 e2) + pc'', xcp'')
  by(rule bisim1-bisims1.bisim1CAS3)
  ultimately show ?thesis using τ by auto blast+
next
case (CAS1Null v'')
note [simp] = ⟨v = Null⟩ ⟨e' = THROW NullPointer⟩ ⟨xs' = xs⟩ ⟨ta = ε⟩ ⟨h' = h⟩ ⟨e3' = Val v''⟩
  have τ: ¬ τmove1 P h (CompareAndSwap null D F (Val v') (Val v'')) by(auto simp add:
τmove1.simps τmoves1.simps)
  from bisim3 have s: xcp = None xs = loc
  and τExec-mover-a P t e3 h (stk, loc, pc, xcp) ([v''], loc, length (compE2 e3), None)
  by(auto dest: bisim1Val2D1)
  hence τExec-mover-a P t (e1·compareAndSwap(D·F, e2, e3)) h (stk @ [v', Null], loc, length
(compE2 e1) + length (compE2 e2) + pc, xcp) ([v''] @ [v', Null], loc, length (compE2 e1) + length
(compE2 e2) + length (compE2 e3), None)
  by-(rule CAS-τExecrI3)
moreover
  have exec-move-a P t (e1·compareAndSwap(D·F, e2, e3)) h ([v'', v', Null], loc, length (compE2
e1) + length (compE2 e2) + length (compE2 e3), None) ε
  h ([v'', v', Null], loc, length (compE2 e1) + length (compE2 e2) + length
(compE2 e3), [addr-of-sys-xcpt NullPointer])
  unfolding exec-move-def by-(rule exec-instr, auto simp add: is-Ref-def)
  moreover have τmove2 (compP2 P) h [v'', v', Null] (e1·compareAndSwap(D·F, e2, e3)) (length
(compE2 e1) + length (compE2 e2) + length (compE2 e3)) None ⇒ False
  by(simp add: τmove2-iff)
moreover
  have P, e1·compareAndSwap(D·F, e2, e3), h' ⊢ (THROW NullPointer, loc) ↔ ([v'', v', Null], loc,

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length (compE2 e1) + length (compE2 e2) + length (compE2 e3), [addr-of-sys-xcpt NullPointer])
  by(rule bisim1-bisims1.bisim1CASFail)
  ultimately show ?thesis using s  $\tau$  by auto blast
next
case (Red1CASSucceed a v'')
hence [simp]: v = Addr a e' = true e3' = Val v''
  ta = {ReadMem a (CField D F) v', WriteMem a (CField D F) v''} xs' = xs
  and read: heap-read h a (CField D F) v'
  and write: heap-write h a (CField D F) v'' h' by auto
  have  $\tau$ :  $\neg \tau \text{move1 } P \ h \ (CompareAndSwap \ (\text{addr } a) \ D \ F \ (Val \ v') \ (Val \ v''))$  by(auto simp add:
 $\tau \text{move1.simps } \tau \text{moves1.simps}$ )
  from bisim3 have s: xcp = None xs = loc
  and exec1:  $\tau \text{Exec-mover-a } P \ t \ e3 \ h \ (stk, loc, pc, xcp) \ ([v''], loc, length \ (compE2 \ e3), None)$ 
  by(auto dest: bisim1Val2D1)
  hence  $\tau \text{Exec-mover-a } P \ t \ (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) \ h \ (stk @ [v', Addr a], loc, length \ (compE2 \ e1) + length \ (compE2 \ e2) + pc, xcp) \ ([v''] @ [v', Addr a], loc, length \ (compE2 \ e1) + length \ (compE2 \ e2) + length \ (compE2 \ e3), None)$ 
  by-(rule CAS- $\tau \text{ExecrI3}$ )
  moreover from read write
  have exec-move-a  $P \ t \ (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) \ h \ ([v'', v', Addr a], loc, length \ (compE2 \ e1) + length \ (compE2 \ e2) + length \ (compE2 \ e3), None)$ 
    {ReadMem a (CField D F) v', WriteMem a (CField D F) v''}
    h' ([Bool True], loc, Suc (length (compE2 e1) + length (compE2 e2) +
length (compE2 e3)), None)
  unfolding exec-move-def by-(rule exec-instr, auto simp add: compP2-def is-Ref-def)
  moreover have  $\tau \text{move2} \ (compP2 \ P) \ h \ [v'', v', Addr a] \ (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) \ (length \ (compE2 \ e1) + length \ (compE2 \ e2) + length \ (compE2 \ e3)) \ None \implies False$ 
  by(simp add:  $\tau \text{move2-iff}$ )
  moreover
  have  $P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h' \vdash (true, loc) \leftrightarrow ([Bool \ True], loc, length \ (compE2 \ (e1 \cdot compareAndSwap(D \cdot F, e2, e3))), None)$ 
  by(rule bisim1Val2) simp
  ultimately show ?thesis using s  $\tau$  by(auto simp add: ta-upd-simps ac-simps) blast
next
case (Red1CASFail a v'' v''')
hence [simp]: v = Addr a e' = false e3' = Val v''' h' = h
  ta = {ReadMem a (CField D F) v''} xs' = xs
  and read: heap-read h a (CField D F) v'' v'  $\neq v''$  by auto
  have  $\tau$ :  $\neg \tau \text{move1 } P \ h \ (CompareAndSwap \ (\text{addr } a) \ D \ F \ (Val \ v') \ (Val \ v'''))$  by(auto simp add:
 $\tau \text{move1.simps } \tau \text{moves1.simps}$ )
  from bisim3 have s: xcp = None xs = loc
  and exec1:  $\tau \text{Exec-mover-a } P \ t \ e3 \ h \ (stk, loc, pc, xcp) \ ([v'''], loc, length \ (compE2 \ e3), None)$ 
  by(auto dest: bisim1Val2D1)
  hence  $\tau \text{Exec-mover-a } P \ t \ (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) \ h \ (stk @ [v', Addr a], loc, length \ (compE2 \ e1) + length \ (compE2 \ e2) + pc, xcp) \ ([v'''] @ [v', Addr a], loc, length \ (compE2 \ e1) + length \ (compE2 \ e2) + length \ (compE2 \ e3), None)$ 
  by-(rule CAS- $\tau \text{ExecrI3}$ )
  moreover from read
  have exec-move-a  $P \ t \ (e1 \cdot compareAndSwap(D \cdot F, e2, e3)) \ h \ ([v''', v', Addr a], loc, length \ (compE2 \ e1) + length \ (compE2 \ e2) + length \ (compE2 \ e3), None)$ 
    {ReadMem a (CField D F) v''}
    h' ([Bool False], loc, Suc (length (compE2 e1) + length (compE2 e2) +
length (compE2 e3)), None)
  unfolding exec-move-def by-(rule exec-instr, auto simp add: compP2-def is-Ref-def)

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moreover have  $\tau move2$  ( $compP2$   $P$ )  $h$  [ $v'''$ ,  $v'$ ,  $Addr$   $a$ ] ( $e1 \cdot compareAndSwap(D \cdot F, e2, e3)$ )
( $length$  ( $compE2$   $e1$ ) +  $length$  ( $compE2$   $e2$ ) +  $length$  ( $compE2$   $e3$ ))  $None \implies False$ 
by( $simp$   $add: \tau move2\text{-}iff$ )
moreover
have  $P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h' \vdash (false, loc) \leftrightarrow ([Bool\ False], loc, length$  ( $compE2$ 
( $e1 \cdot compareAndSwap(D \cdot F, e2, e3)$ )),  $None$ )
by( $rule$   $bisim1Val2$ )  $simp$ 
ultimately show  $?thesis$  using  $s \tau$  by( $auto$   $simp$   $add: ta\text{-}upd\text{-}simps\ ac\text{-}simps$ )  $blast$ 
next
case ( $CAS1Throw3$   $A$ )
note [ $simp$ ] =  $\langle e3' = Throw\ A \rangle \langle ta = \varepsilon \rangle \langle e' = Throw\ A \rangle \langle h' = h \rangle \langle xs' = xs \rangle$ 
have  $\tau: \tau move1\ P\ h$  ( $CompareAndSwap$  ( $Val\ v$ )  $D\ F$  ( $Val\ v'$ ) ( $Throw\ A$ )) by( $rule\ \tau move1CASThrow3$ )
from  $bisim3$  have  $xcp = \lfloor A \rfloor \vee xcp = None$  by( $auto\ dest: bisim1\ ThrowD$ )
thus  $?thesis$ 
proof
assume [ $simp$ ]:  $xcp = \lfloor A \rfloor$ 
with  $bisim3$ 
have  $P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h \vdash (Throw\ A, xs) \leftrightarrow (stk @ [v', v], loc, length$ 
( $compE2$   $e1$ ) +  $length$  ( $compE2$   $e2$ ) +  $pc, xcp$ )
by( $auto\ intro: bisim1\ bisims1.intros$ )
thus  $?thesis$  using  $\tau$  by( $fastforce$ )
next
assume [ $simp$ ]:  $xcp = None$ 
with  $bisim3$  obtain  $pc'$  where  $\tau Exec\text{-}mover\text{-}a\ P\ t\ e3\ h$  ( $stk, loc, pc, None$ ) ( $[Addr\ A], loc, pc',$ 
 $\lfloor A \rfloor$ )
and  $bisim': P, e3, h \vdash (Throw\ A, xs) \leftrightarrow ([Addr\ A], loc, pc', \lfloor A \rfloor)$ 
and [ $simp$ ]:  $xs = loc$ 
by( $auto\ dest: bisim1\ Throw\ \tau Exec\text{-}mover$ )
hence  $\tau Exec\text{-}mover\text{-}a\ P\ t$  ( $e1 \cdot compareAndSwap(D \cdot F, e2, e3)$ )  $h$  ( $stk @ [v', v], loc, length$  ( $compE2$ 
 $e1$ ) +  $length$  ( $compE2$   $e2$ ) +  $pc, None$ ) ( $[Addr\ A] @ [v', v], loc, length$  ( $compE2$   $e1$ ) +  $length$  ( $compE2$ 
 $e2$ ) +  $pc', \lfloor A \rfloor$ )
by-( $rule\ CAS\text{-}\tau ExecrI3$ )
moreover from  $bisim'$ 
have  $P, e1 \cdot compareAndSwap(D \cdot F, e2, e3), h \vdash (Throw\ A, xs) \leftrightarrow ([Addr\ A] @ [v', v], loc, length$ 
( $compE2$   $e1$ ) +  $length$  ( $compE2$   $e2$ ) +  $pc', \lfloor A \rfloor$ )
by( $rule\ bisim1\ bisims1.bisim1CASThrow3$ )
ultimately show  $?thesis$  using  $\tau$  by  $auto$ 
qed
qed  $auto$ 
next
case  $bisim1CASThrow1$  thus  $?case$  by  $auto$ 
next
case  $bisim1CASThrow2$  thus  $?case$  by  $auto$ 
next
case  $bisim1CASThrow3$  thus  $?case$  by  $auto$ 
next
case  $bisim1CASFail$  thus  $?case$  by  $auto$ 
next
case ( $bisim1CallParams\ ps\ n\ ps'\ xs\ stk\ loc\ pc\ xcp\ obj\ M'\ v$ )
note  $IHparam = bisim1CallParams.IH(2)$ 
note  $bisim1 = \langle \bigwedge xs. P, obj, h \vdash (obj, xs) \leftrightarrow ([], xs, 0, None) \rangle$ 
note  $bisim2 = \langle P, ps, h \vdash (ps', xs) [\leftrightarrow] (stk, loc, pc, xcp) \rangle$ 
note  $red = \langle True, P, t \vdash 1 \langle Val\ v \cdot M'(ps'), (h, xs) \rangle \text{-}ta \rightarrow \langle e', (h', xs') \rangle \rangle$ 
note  $bsok = \langle bsok\ (obj \cdot M'(ps))\ n \rangle$ 

```

```

from bisim2  $\langle ps \neq [] \rangle$  have  $ps' : ps' \neq []$  by(auto dest: bisims1-lengthD)
from red show ?case
proof cases
  case (Call1Params es')
    note [simp] =  $\langle e' = \text{Val } v \cdot M'(es') \rangle$ 
    and red =  $\langle \text{True}, P, t \vdash 1 \langle ps', (h, xs) \rangle [-ta \rightarrow] \langle es', (h', xs') \rangle \rangle$ 
    from red have  $\tau : \tau \text{move1 } P \ h \ (\text{Val } v \cdot M'(ps')) = \tau \text{moves1 } P \ h \ ps'$  by(auto simp add: \tau move1.simps \tau moves1.simps)
    from IHparam[OF red] bsok obtain  $pc'' \ stk'' \ loc'' \ xcp''$ 
    where bisim':  $P, ps, h' \vdash (es', xs') [\leftrightarrow] (stk'', loc'', pc'', xcp'')$ 
    and exec':  $?execs \ ta \ ps \ ps' \ es' \ h \ stk \ loc \ pc \ xcp \ h' \ pc'' \ stk'' \ loc'' \ xcp''$  by auto
    have  $?exec \ ta \ (obj \cdot M'(ps)) \ (\text{Val } v \cdot M'(ps')) \ (\text{Val } v \cdot M'(es')) \ h \ (stk \ @ \ [v]) \ loc \ (\text{length} \ (\text{compE2} \ obj) + pc) \ xcp \ h' \ (\text{length} \ (\text{compE2} \ obj) + pc'') \ (stk'' \ @ \ [v]) \ loc'' \ xcp''$ 
    proof(cases \tau move1 P h (Val v \cdot M'(ps')))
      case True
        with exec' \tau show ?thesis by (auto intro: Call-\tau ExecrI2 Call-\tau ExecI2)
      next
        case False
          with exec' \tau obtain  $pc' \ stk' \ loc' \ xcp'$ 
          where e:  $\tau \text{Exec-movesr-a } P \ t \ ps \ h \ (stk, loc, pc, xcp) \ (stk', loc', pc', xcp')$ 
          and e':  $\text{exec-moves-a } P \ t \ ps \ h \ (stk', loc', pc', xcp') \ (\text{extTA2JVM} \ (\text{compP2 } P) \ ta) \ h' \ (stk'', loc'', pc'', xcp'')$ 
          and  $\tau' : \neg \tau \text{moves2} \ (\text{compP2 } P) \ h \ stk' \ ps \ pc' \ xcp'$ 
          and call:  $(\text{calls1 } ps' = \text{None} \vee \text{no-calls2 } ps \ pc \vee pc' = pc \wedge stk' = stk \wedge loc' = loc \wedge xcp' = xcp)$  by auto
          from e have  $\tau \text{Exec-mover-a } P \ t \ (obj \cdot M'(ps)) \ h \ (stk \ @ \ [v], loc, \text{length} \ (\text{compE2} \ obj) + pc, xcp) \ (stk' \ @ \ [v], loc', \text{length} \ (\text{compE2} \ obj) + pc', xcp')$  by(rule Call-\tau ExecrI2)
          moreover from e' have  $\text{exec-move-a } P \ t \ (obj \cdot M'(ps)) \ h \ (stk' \ @ \ [v], loc', \text{length} \ (\text{compE2} \ obj) + pc', xcp') \ (\text{extTA2JVM} \ (\text{compP2 } P) \ ta) \ h' \ (stk'' \ @ \ [v], loc'', \text{length} \ (\text{compE2} \ obj) + pc'', xcp'')$ 
          by(rule exec-move-CallI2)
          moreover from  $\tau' \ e'$  have  $\tau \text{move2} \ (\text{compP2 } P) \ h \ (stk' \ @ \ [v]) \ (obj \cdot M'(ps)) \ (\text{length} \ (\text{compE2} \ obj) + pc') \ xcp' \implies \text{False}$ 
          by(auto simp add: \tau move2-iff \tau moves2-iff \tau instr-stk-drop-exec-moves split: if-split-asm)
          moreover from red have  $\text{call1} \ (\text{Val } v \cdot M'(ps')) = \text{calls1 } ps'$  by(auto simp add: is-vals-conv)
          moreover from red have  $\text{no-calls2 } ps \ pc \implies \text{no-call2} \ (obj \cdot M'(ps)) \ (\text{length} \ (\text{compE2} \ obj) + pc) \vee pc = \text{length} \ (\text{compEs2 } ps)$ 
          by(auto simp add: no-call2-def no-calls2-def)
          ultimately show ?thesis using False call e
          by(auto simp del: split-paired-Ex call1.simps calls1.simps) blast+
        qed
        moreover from bisim'
          have  $P, obj \cdot M'(ps), h' \vdash (\text{Val } v \cdot M'(es'), xs') \leftrightarrow ((stk'' \ @ \ [v]), loc'', \text{length} \ (\text{compE2} \ obj) + pc'', xcp'')$ 
          by(rule bisim1-bisims1.bisim1CallParams)
          ultimately show ?thesis using  $\tau$ 
          by(auto simp del: call1.simps calls1.simps split: if-split-asm split del: if-split) blast+
      next
        case (Red1CallNull vs)
          note [simp] =  $\langle h' = h \rangle \langle xs' = xs \rangle \langle ta = \varepsilon \rangle \langle v = \text{Null} \rangle \langle ps' = \text{map Val } vs \rangle \langle e' = \text{THROW NullPointer} \rangle$ 
          from bisim2 have length:  $\text{length } ps = \text{length } vs$  by(auto dest: bisims1-lengthD)
          have  $xs = loc \wedge xcp = \text{None} \wedge \tau \text{Exec-movesr-a } P \ t \ ps \ h \ (stk, loc, pc, xcp) \ (\text{rev } vs, loc, \text{length} \ (\text{compEs2 } ps), \text{None})$ 
          proof(cases pc < length (compEs2 ps))

```

```

case True
  with bisim2 show ?thesis by(auto dest: bisims1-Val-τExec-moves)
next
  case False
  with bisim2 have pc = length (compEs2 ps)
    by(auto dest: bisims1-pc-length-compEs2)
  with bisim2 show ?thesis by(auto dest: bisims1-Val-length-compEs2D)
qed
hence s: xs = loc xcp = None
  and  $\tau\text{Exec-movesr-a } P \ t \ ps \ h \ (stk, loc, pc, xcp) \ (rev \ vs, loc, length \ (compEs2 \ ps), None) \ \mathbf{by} \ auto$ 
  hence  $\tau\text{Exec-mover-a } P \ t \ (obj.M'(ps)) \ h \ (stk \ @ \ [Null], loc, length \ (compE2 \ obj) + pc, xcp) \ (rev \ vs \ @ \ [Null], loc, length \ (compE2 \ obj) + length \ (compEs2 \ ps), None)$ 
  by  $-(rule \ Call\text{-}\tau\text{ExecrI2})$ 
  also {
    from length have exec-move-a  $P \ t \ (obj.M'(ps)) \ h \ (rev \ vs \ @ \ [Null], loc, length \ (compE2 \ obj) + length \ (compEs2 \ ps), None) \ \varepsilon \ h \ (rev \ vs \ @ \ [Null], loc, length \ (compE2 \ obj) + length \ (compEs2 \ ps), [addr\text{-}of\text{-}sys\text{-}xcpt \ NullPointer])$ 
    unfolding exec-move-def by(cases ps)(auto intro: exec-instr)
    moreover have  $\tau\text{move2 } P \ h \ (rev \ vs \ @ \ [Null]) \ (obj.M'(ps)) \ (length \ (compE2 \ obj) + length \ (compEs2 \ ps)) \ None$ 
    using length by(simp add: τmove2-iff)
    ultimately have  $\tau\text{Exec-movet-a } P \ t \ (obj.M'(ps)) \ h \ (rev \ vs \ @ \ [Null], loc, length \ (compE2 \ obj) + length \ (compEs2 \ ps), None) \ (rev \ vs \ @ \ [Null], loc, length \ (compE2 \ obj) + length \ (compEs2 \ ps), [addr\text{-}of\text{-}sys\text{-}xcpt \ NullPointer])$ 
    by(auto intro: τexec-moveI) }
  also have  $\tau\text{move1 } P \ h \ (null.M'(\text{map } Val \ vs))$ 
  by(auto simp add: τmove1.simps τmoves1.simps map-eq-append-conv)
  moreover
  from length have  $P, obj.M'(ps), h \vdash (THROW \ NullPointer, loc) \leftrightarrow ((rev \ vs \ @ \ [Null]), loc, length \ (compE2 \ obj) + length \ (compEs2 \ ps), [addr\text{-}of\text{-}sys\text{-}xcpt \ NullPointer])$ 
  by  $-(rule \ bisim1CallThrow, simp)$ 
  ultimately show ?thesis using s by auto
next
  case (Call1ThrowParams vs a es')
  note [simp] =  $\langle ta = \varepsilon \rangle \langle e' = Throw \ a \rangle \langle ps' = \text{map } Val \ vs \ @ \ Throw \ a \ \# \ es' \rangle \langle h' = h \rangle \langle xs' = xs \rangle$ 
  have  $\tau: \tau\text{move1 } P \ h \ (Val \ v.M'(\text{map } Val \ vs \ @ \ Throw \ a \ \# \ es')) \ \mathbf{by} \ (rule \ \tau\text{move1CallThrowParams})$ 
  from bisim2 have [simp]: xs = loc and xcp = [a] ∨ xcp = None by(auto dest: bisims1-ThrowD)
  from  $\langle xcp = [a] \vee xcp = None \rangle$  show ?thesis
proof
  assume [simp]: xcp = [a]
  with bisim2
  have  $P, obj.M'(ps), h \vdash (Throw \ a, loc) \leftrightarrow (stk \ @ \ [v], loc, length \ (compE2 \ obj) + pc, [a])$ 
  by  $-(rule \ bisim1CallThrowParams, auto)$ 
  thus ?thesis using  $\tau$  by(fastforce)
next
  assume [simp]: xcp = None
  with bisim2 obtain pc'
    where exec: τExec-movesr-a  $P \ t \ ps \ h \ (stk, loc, pc, None) \ (Addr \ a \ \# \ rev \ vs, loc, pc', [a])$ 
    and bisim':  $P, ps, h \vdash (\text{map } Val \ vs \ @ \ Throw \ a \ \# \ es', loc) \ [\leftrightarrow] \ (Addr \ a \ \# \ rev \ vs, loc, pc', [a])$ 
    by(auto dest: bisims1-Throw-τExec-movesr)
  from bisim'
  have  $P, obj.M'(ps), h \vdash (Throw \ a, loc) \leftrightarrow ((Addr \ a \ \# \ rev \ vs) \ @ \ [v], loc, length \ (compE2 \ obj) + pc', [a])$ 
  by(rule bisim1CallThrowParams)

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    with Call-τExecrI2[OF exec, of obj M' v] τ
    show ?thesis by auto
  qed
next
  case (Red1CallExternal a Ta Ts Tr D vs va H')
  hence [simp]: v = Addr a ps' = map Val vs e' = extRet2J (addr a·M'(map Val vs)) va H' = h'
  xs' = xs
    and Ta: typeof-addr h a = [Ta]
    and iee: P ⊢ class-type-of Ta sees M': Ts→Tr = Native in D
    and redex: P, t ⊢ ⟨a·M'(vs), h⟩ -ta→ext ⟨va, h'⟩ by auto
  from bisim2 have [simp]: xs = loc by (auto dest: bisims-Val-loc-eq-xcp-None)
  moreover from bisim2 have length ps = length ps'
    by (rule bisims1-lengthD)
  hence τ: τmove1 P h (addr a·M'(map Val vs) :: 'addr expr1) = τmove2 (compP2 P) h (rev vs @
  [Addr a]) (obj·M'(ps)) (length (compE2 obj) + length (compEs2 ps)) None
    using Ta iee by (auto simp add: τmove1.simps τmoves1.simps map-eq-append-conv τmove2-iff
  compP2-def)
  obtain s: xcp = None xs = loc
    and τExec-movesr-a P t ps h (stk, loc, pc, xcp) (rev vs, loc, length (compEs2 ps), None)
  proof (cases pc < length (compEs2 ps))
  case True
    with bisim2 show ?thesis by (auto dest: bisims1-Val-τExec-moves intro: that)
  next
  case False
    with bisim2 have pc = length (compEs2 ps) by (auto dest: bisims1-pc-length-compEs2)
    with bisim2 show ?thesis by -(rule that, auto dest!: bisims1-pc-length-compEs2D)
  qed
  from Call-τExecrI2[OF this(3), of obj M' v]
  have τExec-mover-a P t (obj·M'(ps)) h (stk @ [Addr a], loc, length (compE2 obj) + pc, xcp) (rev
  vs @ [Addr a], loc, length (compE2 obj) + length (compEs2 ps), None) by simp
  moreover from bisim2 have pc ≤ length (compEs2 ps) by (rule bisims1-pc-length-compEs2)
  hence no-call2 (obj·M'(ps)) (length (compE2 obj) + pc) ∨ pc = length (compEs2 ps)
    using bisim2 by (auto simp add: no-call2-def neq-Nil-conv dest: bisims-Val-pc-not-Invoke)
  moreover {
    assume pc = length (compEs2 ps)
    with ⟨τExec-movesr-a P t ps h (stk, loc, pc, xcp) (rev vs, loc, length (compEs2 ps), None)⟩
    have stk = rev vs xcp = None by auto }
  moreover
  let ?ret = extRet2JVM (length ps) h' (rev vs @ [Addr a]) loc undefined undefined (length (compE2
  obj) + length (compEs2 ps)) [] va
  let ?stk' = fst (hd (snd (snd ?ret)))
  let ?xcp' = fst ?ret
  let ?pc' = snd (snd (snd (snd (hd (snd (snd ?ret))))))
  from bisim2 have [simp]: length ps = length vs by (auto dest: bisims1-lengthD)
  from redex have redex': (ta, va, h') ∈ red-external-aggr (compP2 P) t a M' vs h
    by -(rule red-external-imp-red-external-aggr, simp add: compP2-def)
  with Ta iee
  have exec-move-a P t (obj·M'(ps)) h (rev vs @ [Addr a], loc, length (compE2 obj) + length (compEs2
  ps), None) (extTA2JVM (compP2 P) ta) h' (?stk', loc, ?pc', ?xcp')
    unfolding exec-move-def
    by -(rule exec-instr, cases va, (force simp add: compP2-def is-Ref-def simp del: split-paired-Ex
  intro: external-WT'.intros)+)
  moreover have P, obj·M'(ps), h' ⊢ (extRet2J1 (addr a·M'(map Val vs)) va, loc) ↔ (?stk', loc,
  ?pc', ?xcp')

```

```

proof(cases va)
  case (RetVal v)
    have  $P, \text{obj} \cdot M'(ps), h' \vdash (\text{Val } v, \text{loc}) \leftrightarrow ([v], \text{loc}, \text{length}(\text{compE2}(\text{obj} \cdot M'(ps))), \text{None})$ 
      by(rule bisim1Val2) simp
    thus ?thesis unfolding RetVal by simp
  next
    case (RetExc ad) thus ?thesis by(auto intro: bisim1CallThrow)
  next
    case RetStaySame
    from bisims1-map-Val-append[OF bisims1Nil, of ps vs P h' loc]
    have  $P, ps, h' \vdash (\text{map Val vs}, \text{loc}) [\leftrightarrow] (\text{rev vs}, \text{loc}, \text{length}(\text{compEs2 ps}), \text{None})$  by simp
    hence  $P, \text{obj} \cdot M'(ps), h' \vdash (\text{addr } a \cdot M'(\text{map Val vs}), \text{loc}) \leftrightarrow (\text{rev vs} @ [\text{Addr } a], \text{loc}, \text{length}(\text{compE2}(\text{obj}) + \text{length}(\text{compEs2 ps}), \text{None}))$ 
      by(rule bisim1-bisims1.bisim1CallParams)
    thus ?thesis using RetStaySame by simp
  qed
moreover from redex Ta iec
have  $\tau \text{move1 } P h (\text{addr } a \cdot M'(\text{map Val vs}) :: \text{'addr expr1}) \implies ta = \varepsilon \wedge h' = h$ 
by(fastforce simp add:  $\tau \text{move1.simps } \tau \text{moves1.simps map-eq-append-conv } \tau \text{external'-def } \tau \text{external-def}$ 
dest:  $\tau \text{external'-red-external-TA-empty } \tau \text{external'-red-external-heap-unchanged sees-method-fun}$ )
ultimately show ?thesis using  $\tau$ 
  apply(cases  $\tau \text{move1 } P h (\text{addr } a \cdot M'(\text{map Val vs}) :: \text{'addr expr1})$ )
  apply(auto simp del: split-paired-Ex simp add: compP2-def)
  apply(blast intro: rtranclp.rtrancl-into-rtrancl rtranclp-into-tranclp1  $\tau \text{exec-moveI}$ ) +
  done
qed(insert ps', auto)
next
  case bisim1CallThrowObj thus ?case by fastforce
next
  case bisim1CallThrowParams thus ?case by auto
next
  case bisim1CallThrow thus ?case by fastforce
next
  case (bisim1BlockSome1 e n V Ty v xs e')
  from  $\langle \text{True}, P, t \vdash 1 \ \langle \{ V: \text{Ty} = [v]; e \}, (h, xs) \rangle - ta \rightarrow \langle e', (h', xs') \rangle \rangle$  show ?case
proof(cases)
  case Block1Some
  note [simp] =  $\langle ta = \varepsilon \rangle \langle e' = \{ V: \text{Ty} = \text{None}; e \} \rangle \langle h' = h \rangle \langle xs' = xs[V := v] \rangle$ 
    and  $\text{len} = \langle V < \text{length } xs \rangle$ 
  from len have  $\text{exec}: \tau \text{Exec-movet-a } P t \ \{ V: \text{Ty} = [v]; e \} \ h \ (\[], xs, 0, \text{None}) \ (\[], xs[V := v], \text{Suc}(\text{Suc } 0), \text{None})$ 
    by-(rule  $\tau \text{Exec2step}$ , auto intro:  $\text{exec-instr simp add: exec-move-def } \tau \text{move2-iff}$ )
  moreover have  $P, \{ V: \text{Ty} = [v]; e \}, h \vdash (\{ V: \text{Ty} = \text{None}; e \}, xs[V := v]) \leftrightarrow (\[], xs[V := v], \text{Suc}(\text{Suc } 0), \text{None})$ 
    by(rule bisim1BlockSome4)(rule bisim1-refl)
  moreover have  $\tau \text{move1 } P h \ \{ V: \text{Ty} = [v]; e \}$  by(auto intro:  $\tau \text{move1BlockSome}$ )
  ultimately show ?thesis by auto
  qed
next
  case (bisim1BlockSome2 e n V Ty v xs)
  from  $\langle \text{True}, P, t \vdash 1 \ \langle \{ V: \text{Ty} = [v]; e \}, (h, xs) \rangle - ta \rightarrow \langle e', (h', xs') \rangle \rangle$  show ?case
proof(cases)
  case Block1Some
  note [simp] =  $\langle ta = \varepsilon \rangle \langle e' = \{ V: \text{Ty} = \text{None}; e \} \rangle \langle h' = h \rangle \langle xs' = xs[V := v] \rangle$ 

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    and len = ⟨V < length xs⟩
    from len have exec:  $\tau\text{Exec-move1-a } P \ t \ \{V:\text{Ty}=[v]; e\} \ h \ ([v], xs, \text{Suc } 0, \text{None}) \ ([], xs[V := v], \text{Suc } (\text{Suc } 0), \text{None})$ 
    by- (rule  $\tau\text{Exec1step}$ , auto intro:  $\text{exec-instr } \tau\text{move2BlockSome2 simp: exec-move-def}$ )
    moreover have  $P, \{V:\text{Ty}=[v]; e\}, h \vdash (\{V:\text{Ty}=\text{None}; e\}, xs[V := v]) \leftrightarrow ([], xs[V := v], \text{Suc } (\text{Suc } 0), \text{None})$ 
    by (rule  $\text{bisim1BlockSome4}$ ) (rule  $\text{bisim1-refl}$ )
    moreover have  $\tau\text{move1 } P \ h \ \{V:\text{Ty}=[v]; e\}$  by (auto intro:  $\tau\text{move1BlockSome}$ )
    ultimately show ?thesis by auto
  qed
next
case (bisim1BlockSome4 E n e xs stk loc pc xcp V Ty v)
note IH = bisim1BlockSome4.IH(2)
note bisim = ⟨P, E, h ⊢ (e, xs) ↔ (stk, loc, pc, xcp)⟩
note bsok = bsok {V:Ty=[v]; E} n
hence [simp]: n = V by simp
from ⟨True, P, t ⊢ 1 ⟨{V:Ty=None; e}, (h, xs)⟩ -ta→ ⟨e', (h', xs')⟩⟩ show ?case
proof cases
  case (Block1Red E')
  note [simp] = ⟨e' = {V:Ty=None; E'}⟩
  and red = ⟨True, P, t ⊢ 1 ⟨e, (h, xs)⟩ -ta→ ⟨E', (h', xs')⟩⟩
  from red have  $\tau: \tau\text{move1 } P \ h \ \{V:\text{Ty}=\text{None}; e\} = \tau\text{move1 } P \ h \ e$  by (auto simp add:  $\tau\text{move1.simps}$   $\tau\text{moves1.simps}$ )
  from IH[OF red] bsok obtain pc'' stk'' loc'' xcp''
  where bisim':  $P, E, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$ 
  and exec':  $?exec \ ta \ E \ e \ E' \ h \ stk \ loc \ pc \ xcp \ h' \ pc'' \ stk'' \ loc'' \ xcp''$  by auto
  have no-call2 E pc ⇒ no-call2 ({V:Ty=[v]; E}) (Suc (Suc pc)) by (auto simp add: no-call2-def)
  hence  $?exec \ ta \ \{V:\text{Ty}=[v]; E\} \ \{V:\text{Ty}=\text{None}; e\} \ \{V:\text{Ty}=\text{None}; E'\} \ h \ stk \ loc \ (\text{Suc } (\text{Suc } pc)) \ xcp \ h' \ (\text{Suc } (\text{Suc } pc'')) \ stk'' \ loc'' \ xcp''$ 
  using exec'  $\tau$ 
  by (cases  $\tau\text{move1 } P \ h \ \{V:\text{Ty}=\text{None}; e\}$ ) (auto, (blast intro:  $\text{exec-move-BlockSomeI Block-}\tau\text{ExecrI-Some Block-}\tau\text{ExecI-Some}$ )+)
  with bisim'  $\tau$  show ?thesis by auto (blast intro: bisim1-bisims1.bisim1BlockSome4)+
next
case (Red1Block u)
note [simp] = ⟨e = Val u⟩ ⟨ta = ε⟩ ⟨e' = Val u⟩ ⟨h' = h⟩ ⟨xs' = xs⟩
have  $\tau\text{move1 } P \ h \ \{V:\text{Ty}=\text{None}; \text{Val } u\}$  by (rule  $\tau\text{move1BlockRed}$ )
moreover from bisim have [simp]:  $xcp = \text{None} \ loc = xs$ 
and exec:  $\tau\text{Exec-mover-a } P \ t \ E \ h \ (stk, loc, pc, xcp) \ ([u], loc, \text{length } (\text{compE2 } E), \text{None})$  by (auto dest: bisim1Val2D1)
moreover
have  $P, \{V:\text{Ty}=[v]; E\}, h \vdash (\text{Val } u, xs) \leftrightarrow ([u], xs, \text{length } (\text{compE2 } \{V:\text{Ty}=[v]; E\}), \text{None})$ 
by (rule bisim1Val2) simp
ultimately show ?thesis by (fastforce elim!: Block- $\tau\text{ExecrI-Some}$ )
next
case (Block1Throw a)
note [simp] = ⟨e = Throw a⟩ ⟨ta = ε⟩ ⟨e' = Throw a⟩ ⟨h' = h⟩ ⟨xs' = xs⟩
have  $\tau: \tau\text{move1 } P \ h \ \{V:\text{Ty}=\text{None}; e\}$  by (auto intro:  $\tau\text{move1BlockThrow}$ )
from bisim have  $xcp = [a] \vee xcp = \text{None}$  by (auto dest: bisim1-ThrowD)
thus ?thesis
proof
  assume [simp]:  $xcp = [a]$ 
  with bisim have  $P, \{V:\text{Ty}=[v]; E\}, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, \text{Suc } (\text{Suc } pc), xcp)$ 
  by (auto intro: bisim1BlockThrowSome)

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    thus ?thesis using  $\tau$  by (fastforce)
next
  assume [simp]:  $xcp = None$ 
  with bisim obtain  $pc'$ 
    where  $\tau Exec\text{-}mover\text{-}a\ P\ t\ E\ h\ (stk, loc, pc, None)\ ([Addr\ a], loc, pc', [a])$ 
    and  $bisim': P, E, h \vdash (Throw\ a, xs) \leftrightarrow ([Addr\ a], loc, pc', [a])$  and [simp]:  $xs = loc$ 
    by (auto dest: bisim1-Throw- $\tau Exec\text{-}mover$ )
    hence  $\tau Exec\text{-}mover\text{-}a\ P\ t\ \{V:Ty=[v]; E\}\ h\ (stk, loc, Suc\ (Suc\ pc), None)\ ([Addr\ a], loc, Suc\ (Suc\ pc'), [a])$ 
    by (auto intro: Block- $\tau ExecrI\text{-}Some$ )
    moreover from bisim' have  $P, \{V:Ty=[v]; E\}, h \vdash (Throw\ a, xs) \leftrightarrow ([Addr\ a], loc, Suc\ (Suc\ pc'), [a])$ 
    by (auto intro: bisim1-bisims1.bisim1BlockThrowSome)
    ultimately show ?thesis using  $\tau$  by auto
  qed
qed
next
  case bisim1BlockThrowSome thus ?case by auto
next
  case (bisim1BlockNone  $E\ n\ e\ xs\ stk\ loc\ pc\ xcp\ V\ Ty$ )
  note  $IH = bisim1BlockNone.IH(2)$ 
  note  $bisim = \langle P, E, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
  note  $bsok = \langle bsok\ \{V:Ty=None; E\}\ n \rangle$ 
  hence [simp]:  $n = V$  by simp
  from  $\langle True, P, t \vdash 1\ \langle \{V:Ty=None; e\}, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$  show ?case
  proof cases
    case (Block1Red  $E'$ )
    note [simp] =  $\langle e' = \{V:Ty=None; E'\} \rangle$ 
    and  $red = \langle True, P, t \vdash 1\ \langle e, (h, xs) \rangle -ta \rightarrow \langle E', (h', xs') \rangle \rangle$ 
    from red have  $\tau: \tau move1\ P\ h\ \{V:Ty=None; e\} = \tau move1\ P\ h\ e$  by (auto simp add:  $\tau move1.simps$   $\tau moves1.simps$ )
    moreover have  $call1\ (\{V:Ty=None; e\}) = call1\ e$  by auto
    moreover from  $IH[OF\ red]\ bsok$ 
    obtain  $pc''\ stk''\ loc''\ xcp''$  where  $bisim: P, E, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$ 
    and redo:  $?exec\ ta\ E\ e\ E'\ h\ stk\ loc\ pc\ xcp\ h'\ pc''\ stk''\ loc''\ xcp''$  by auto
    from bisim
    have  $P, \{V:Ty=None; E\}, h' \vdash (\{V:Ty=None; E'\}, xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$ 
    by (rule bisim1-bisims1.bisim1BlockNone)
    moreover {
      assume no-call2  $E\ pc$ 
      hence no-call2  $\{V:Ty=None; E\}\ pc$  by (auto simp add: no-call2-def) }
    ultimately show ?thesis using redo
    by (auto simp add: exec-move-BlockNone simp del: call1.simps calls1.simps split: if-split-asm split del: if-split) (blast intro: Block- $\tau ExecrI\text{-}None\ Block- $\tau ExectI\text{-}None$$ ) +
  next
    case (Red1Block  $u$ )
    note [simp] =  $\langle e = Val\ u \rangle \langle ta = \varepsilon \rangle \langle e' = Val\ u \rangle \langle h' = h \rangle \langle xs' = xs \rangle$ 
    have  $\tau move1\ P\ h\ \{V:Ty=None; Val\ u\}$  by (rule  $\tau move1BlockRed$ )
    moreover from bisim have [simp]:  $xcp = None\ loc = xs$ 
    and exec:  $\tau Exec\text{-}mover\text{-}a\ P\ t\ E\ h\ (stk, loc, pc, xcp)\ ([u], loc, length\ (compE2\ E), None)$  by (auto dest: bisim1Val2D1)
    moreover
    have  $P, \{V:Ty=None; E\}, h \vdash (Val\ u, xs) \leftrightarrow ([u], xs, length\ (compE2\ \{V:Ty=None; E\}), None)$ 
    by (rule bisim1Val2) simp

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```

ultimately show ?thesis by(fastforce intro: Block- $\tau$ ExecrI-None)
next
case (Block1Throw a)
note [simp] =  $\langle e = \text{Throw } a \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw } a \rangle \langle h' = h \rangle \langle xs' = xs \rangle$ 
have  $\tau: \tau\text{move1 } P \ h \ \{V: \text{Ty} = \text{None}; e\}$  by(auto intro:  $\tau\text{move1BlockThrow}$ )
from bisim have  $xcp = \lfloor a \rfloor \vee xcp = \text{None}$  by(auto dest: bisim1-ThrowD)
thus ?thesis
proof
  assume [simp]:  $xcp = \lfloor a \rfloor$ 
  with bisim have  $P, \{V: \text{Ty} = \text{None}; E\}, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, xcp)$ 
  by(auto intro: bisim1BlockThrowNone)
  thus ?thesis using  $\tau$  by(fastforce)
next
assume [simp]:  $xcp = \text{None}$ 
with bisim obtain  $pc'$ 
  where  $\tau\text{Exec-mover-a } P \ t \ E \ h \ (stk, loc, pc, \text{None}) \ ([\text{Addr } a], loc, pc', \lfloor a \rfloor)$ 
  and  $\text{bisim}': P, E, h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], loc, pc', \lfloor a \rfloor)$  and [simp]:  $xs = loc$ 
  by(auto dest: bisim1-Throw- $\tau\text{Exec-mover}$ )
  hence  $\tau\text{Exec-mover-a } P \ t \ \{V: \text{Ty} = \text{None}; E\} \ h \ (stk, loc, pc, \text{None}) \ ([\text{Addr } a], loc, pc', \lfloor a \rfloor)$ 
  by(auto intro: Block- $\tau\text{ExecrI-None}$ )
  moreover from bisim' have  $P, \{V: \text{Ty} = \text{None}; E\}, h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], loc, pc', \lfloor a \rfloor)$ 
  by(auto intro: bisim1-bisims1.bisim1BlockThrowNone)
  ultimately show ?thesis using  $\tau$  by auto
qed
qed
next
case bisim1BlockThrowNone thus ?case by auto
next
case (bisim1Sync1  $e1 \ n \ e1' \ xs \ stk \ loc \ pc \ xcp \ e2 \ V$ )
note  $IH = \text{bisim1Sync1.IH}(2)$ 
note  $\text{bisim1} = \langle P, e1, h \vdash (e1', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
note  $\text{bisim2} = \langle \bigwedge xs. P, e2, h \vdash (e2, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$ 
note  $\text{red} = \langle \text{True}, P, t \vdash 1 \ \langle \text{sync}_V(e1') \ e2, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$ 
note  $\text{bsok} = \langle \text{bsok}(\text{sync}_V(e1') \ e2) \ n \rangle$ 
hence [simp]:  $n = V$  by simp
from red show ?case
proof cases
  case (Synchronized1Red1  $E1'$ )
  note [simp] =  $\langle e' = \text{sync}_V(E1') \ e2 \rangle$ 
  and  $\text{red} = \langle \text{True}, P, t \vdash 1 \ \langle e1', (h, xs) \rangle -ta \rightarrow \langle E1', (h', xs') \rangle \rangle$ 
  from red have  $\tau: \tau\text{move1 } P \ h \ (\text{sync}_V(e1') \ e2) = \tau\text{move1 } P \ h \ e1'$  by(auto simp add:  $\tau\text{move1.simps}$ 
 $\tau\text{moves1.simps}$ )
  moreover have  $\text{call1}(\text{sync}_V(e1') \ e2) = \text{call1 } e1'$  by auto
  moreover from  $IH[OF \text{red}] \text{bsok}$ 
  obtain  $pc'' \ stk'' \ loc'' \ xcp''$  where  $\text{bisim}: P, e1, h' \vdash (E1', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$ 
  and  $\text{redo}: ?\text{exec } ta \ e1 \ e1' \ E1' \ h \ stk \ loc \ pc \ xcp \ h' \ pc'' \ stk'' \ loc'' \ xcp''$  by auto
  from bisim
  have  $P, \text{sync}_V(e1') \ e2, h' \vdash (\text{sync}_V(E1') \ e2, xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$ 
  by(rule bisim1-bisims1.bisim1Sync1)
  moreover {
    assume no-call2  $e1 \ pc$ 
    hence no-call2  $(\text{sync}_V(e1') \ e2) \ pc$  by(auto simp add: no-call2-def) }
  ultimately show ?thesis using redo
  by(auto simp del: call1.simps calls1.simps split: if-split-asm split del: if-split)(blast intro:

```

$\text{Sync-}\tau\text{ExecrI } \text{Sync-}\tau\text{ExecI } \text{exec-move-SyncI1}) +$   
**next**  
**case** *Synchronized1Null*  
**note**  $[simp] = \langle e1' = \text{null} \rangle \langle e' = \text{THROW NullPointer} \rangle \langle ta = \varepsilon \rangle \langle h' = h \rangle \langle xs' = xs[V := \text{Null}] \rangle$   
**and**  $V = \langle V < \text{length } xs \rangle$   
**from** *bisim1* **have**  $[simp]: xcp = \text{None } xs = \text{loc}$   
**and**  $\text{exec}: \tau\text{Exec-mover-a } P \ t \ e1 \ h \ (stk, \text{loc}, pc, xcp) \ ([\text{Null}], \text{loc}, \text{length } (\text{compE2 } e1), \text{None})$   
**by**  $(\text{auto dest: bisim1Val2D1})$   
**from** *Sync-}\tau\text{ExecrI}[OF \text{exec}]*  
**have**  $\tau\text{Exec-mover-a } P \ t \ (\text{sync}_V \ (e1) \ e2) \ h \ (stk, \text{loc}, pc, xcp) \ ([\text{Null}], \text{loc}, \text{length } (\text{compE2 } e1),$   
 $\text{None}) \text{ by } simp$   
**also from** *V*  
**have**  $\tau\text{Exec-mover-a } P \ t \ (\text{sync}_V \ (e1) \ e2) \ h \ ([\text{Null}], \text{loc}, \text{length } (\text{compE2 } e1), \text{None}) \ ([\text{Null}], \text{loc}[V$   
 $:= \text{Null}], \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e1))), \text{None})$   
**by**  $-(\text{rule } \tau\text{Execr2step}, \text{auto intro: exec-instr } \tau\text{move2-}\tau\text{moves2.intros simp add: exec-move-def})$   
**also**  $(\text{rtranclp-trans})$   
**have**  $\text{exec-move-a } P \ t \ (\text{sync}_V \ (e1) \ e2) \ h \ ([\text{Null}], \text{loc}[V := \text{Null}], \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e1))),$   
 $\text{None}) \ \varepsilon$   
 $h \ ([\text{Null}], \text{loc}[V := \text{Null}], \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e1))), [\text{addr-of-sys-xcpt}$   
 $\text{NullPointer}])$   
**unfolding** *exec-move-def* **by**  $(\text{rule exec-instr}) \text{ auto}$   
**moreover have**  $\neg \tau\text{move2 } (\text{compP2 } P) \ h \ [\text{Null}] \ (\text{sync}_V \ (e1) \ e2) \ (\text{Suc } (\text{Suc } (\text{length } (\text{compE2}$   
 $e1)))) \text{ None}$   
**by**  $(simp \text{ add: } \tau\text{move2-iff})$   
**moreover**  
**have**  $P, \text{sync}_V \ (e1) \ e2, h \vdash (\text{THROW NullPointer}, \text{loc}[V := \text{Null}]) \leftrightarrow ([\text{Null}], (\text{loc}[V := \text{Null}]),$   
 $\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e1))), [\text{addr-of-sys-xcpt NullPointer}])$   
**by**  $(\text{rule bisim1SyncI1})$   
**moreover have**  $\neg \tau\text{move1 } P \ h \ (\text{sync}_V \ (\text{null}) \ e2) \text{ by } (\text{auto simp add: } \tau\text{move1.simps } \tau\text{moves1.simps})$   
**ultimately show** *?thesis* **by** *auto blast*  
**next**  
**case** *Lock1Synchronized a*  
**note**  $[simp] = \langle e1' = \text{addr } a \rangle \langle ta = \{\text{Lock} \rightarrow a, \text{SyncLock } a\} \rangle \langle e' = \text{insync}_V \ (a) \ e2 \rangle \langle h' = h \rangle \langle xs'$   
 $= xs[V := \text{Addr } a] \rangle$   
**and**  $V = \langle V < \text{length } xs \rangle$   
**from** *bisim1* **have**  $[simp]: xcp = \text{None } xs = \text{loc}$   
**and**  $\text{exec}: \tau\text{Exec-mover-a } P \ t \ e1 \ h \ (stk, \text{loc}, pc, xcp) \ ([\text{Addr } a], \text{loc}, \text{length } (\text{compE2 } e1), \text{None})$   
**by**  $(\text{auto dest: bisim1Val2D1})$   
**from** *Sync-}\tau\text{ExecrI}[OF \text{exec}]*  
**have**  $\tau\text{Exec-mover-a } P \ t \ (\text{sync}_V \ (e1) \ e2) \ h \ (stk, \text{loc}, pc, xcp) \ ([\text{Addr } a], \text{loc}, \text{length } (\text{compE2 } e1),$   
 $\text{None}) \text{ by } simp$   
**also from** *V*  
**have**  $\tau\text{Exec-mover-a } P \ t \ (\text{sync}_V \ (e1) \ e2) \ h \ ([\text{Addr } a], \text{loc}, \text{length } (\text{compE2 } e1), \text{None}) \ ([\text{Addr } a],$   
 $\text{loc}[V := \text{Addr } a], \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e1))), \text{None})$   
**by**  $-(\text{rule } \tau\text{Execr2step}, \text{auto intro: exec-instr } \tau\text{move2-}\tau\text{moves2.intros simp add: exec-move-def})$   
**also**  $(\text{rtranclp-trans})$   
**have**  $\text{exec-move-a } P \ t \ (\text{sync}_V \ (e1) \ e2) \ h \ ([\text{Addr } a], \text{loc}[V := \text{Addr } a], \text{Suc } (\text{Suc } (\text{length } (\text{compE2}$   
 $e1))), \text{None})$   
 $(\{\text{Lock} \rightarrow a, \text{SyncLock } a\})$   
 $h \ ([], \text{loc}[V := \text{Addr } a], \text{Suc } (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e1))))) \text{ None})$   
**unfolding** *exec-move-def* **by**  $-(\text{rule exec-instr}, \text{auto simp add: is-Ref-def})$   
**moreover have**  $\neg \tau\text{move2 } (\text{compP2 } P) \ h \ [\text{Addr } a] \ (\text{sync}_V \ (e1) \ e2) \ (\text{Suc } (\text{Suc } (\text{length } (\text{compE2}$   
 $e1)))) \text{ None}$   
**by**  $(simp \text{ add: } \tau\text{move2-iff})$

```

moreover
  from bisim1Sync4[OF bisim1-refl, of P h V e1 e2 a loc[V := Addr a]]
  have P, syncV (e1) e2, h ⊢ (insyncV (a) e2, loc[V := Addr a]) ↔ ([], loc[V := Addr a], Suc (Suc (Suc (length (compE2 e1))))), None) by simp
  moreover have ¬ τmove1 P h (syncV (addr a) e2) by(auto simp add: τmove1.simps τmoves1.simps)
  ultimately show ?thesis by(auto simp add: eval-nat-numeral ta-upd-simps) blast
next
  case (Synchronized1Throw1 a)
  note [simp] = ⟨e1' = Throw a⟩ ⟨ta = ε⟩ ⟨e' = Throw a⟩ ⟨h' = h⟩ ⟨xs' = xs⟩
  have τ: τmove1 P h (syncV (Throw a) e2) by(rule τmove1SyncThrow)
  from bisim1 have xcp = [a] ∨ xcp = None by(auto dest: bisim1-ThrowD)
  thus ?thesis
proof
  assume [simp]: xcp = [a]
  with bisim1
  have P, syncV (e1) e2, h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])
    by(auto intro: bisim1SyncThrow)
  thus ?thesis using τ by(fastforce)
next
  assume [simp]: xcp = None
  with bisim1 obtain pc'
    where τExec-mover-a P t e1 h (stk, loc, pc, None) ([Addr a], loc, pc', [a])
    and bisim': P, e1, h ⊢ (Throw a, xs) ↔ ([Addr a], loc, pc', [a]) and [simp]: xs = loc
    by(auto dest: bisim1-Throw-τExec-mover)
  hence τExec-mover-a P t (syncV (e1) e2) h (stk, loc, pc, None) ([Addr a], loc, pc', [a])
    by-(rule Sync-τExecrI)
  moreover from bisim'
  have P, syncV (e1) e2, h ⊢ (Throw a, xs) ↔ ([Addr a], loc, pc', [a])
    by -(rule bisim1-bisims1.bisim1SyncThrow, auto)
  ultimately show ?thesis using τ by fastforce
qed
qed
next
  case (bisim1Sync2 e1 n e2 V v xs)
  note bisim1 = ⟨∧xs. P, e1, h ⊢ (e1, xs) ↔ ([], xs, 0, None)⟩
  note bisim2 = ⟨∧xs. P, e2, h ⊢ (e2, xs) ↔ ([], xs, 0, None)⟩
  from ⟨True, P, t ⊢ 1 ⟨syncV (Val v) e2, (h, xs)⟩ -ta→ ⟨e', (h', xs')⟩⟩ show ?case
proof cases
  case (Lock1Synchronized a)
  note [simp] = ⟨v = Addr a⟩ ⟨ta = {Lock→a, SyncLock a}⟩ ⟨e' = insyncV (a) e2⟩
    ⟨h' = h⟩ ⟨xs' = xs[V := Addr a]⟩
    and V = ⟨V < length xs⟩
  from V
  have τExec-mover-a P t (syncV (e1) e2) h ([Addr a, Addr a], xs, Suc (length (compE2 e1)), None)
    ([Addr a], xs[V := Addr a], Suc (Suc (length (compE2 e1))), None)
    by -(rule τExecr1step, auto intro: exec-instr simp add: τmove2-iff exec-move-def)
  moreover
  have exec-move-a P t (syncV (e1) e2) h ([Addr a], xs[V := Addr a], Suc (Suc (length (compE2 e1))), None)
    ({Lock→a, SyncLock a})
    (h ([], xs[V := Addr a], Suc (Suc (Suc (length (compE2 e1))))), None)
    unfolding exec-move-def by -(rule exec-instr, auto simp add: is-Ref-def)
  moreover have ¬ τmove2 (compP2 P) h [Addr a] (syncV (e1) e2) (Suc (Suc (length (compE2 e1)))) None

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  by(simp add:  $\tau$ move2-iff)
  moreover
  from bisim1Sync4[OF bisim1-refl, of  $P$   $h$   $V$   $e1$   $e2$   $a$   $xs[V := Addr\ a]$ ]
  have  $P, sync_V (e1) e2, h \vdash (insync_V (a) e2, xs[V := Addr\ a]) \leftrightarrow ([], xs[V := Addr\ a], Suc (Suc (Suc (length (compE2\ e1)))))$ , None) by simp
  moreover have  $\neg \tau move1\ P\ h\ (sync_V (addr\ a) e2)$  by(auto simp add:  $\tau move1.simps\ \tau moves1.simps$ )
  ultimately show ?thesis by(auto simp add: eval-nat-numeral ta-upd-simps) blast
next
case Synchronized1Null
note [simp] =  $\langle v = Null \rangle \langle e' = THROW\ NullPointer \rangle \langle ta = \varepsilon \rangle \langle h' = h \rangle \langle xs' = xs[V := Null] \rangle$ 
  and  $V = \langle V < length\ xs \rangle$ 
from  $V$ 
  have  $\tau Exec-mover-a\ P\ t\ (sync_V (e1) e2)\ h\ ([Null, Null], xs, Suc (length (compE2\ e1)), None)$ 
  ( $[Null], xs[V := Null], Suc (Suc (length (compE2\ e1))), None$ )
  by  $-(rule\ \tau Execr1step, auto\ intro: exec-instr\ simp\ add: exec-move-def\ \tau move2-iff)$ 
  also have  $exec-move-a\ P\ t\ (sync_V (e1) e2)\ h\ ([Null], xs[V := Null], Suc (Suc (length (compE2\ e1))), None) \varepsilon$ 
     $h\ ([Null], xs[V := Null], Suc (Suc (length (compE2\ e1))), [addr-of-sys-xcpt\ NullPointer])$ 
  unfolding exec-move-def by(rule exec-instr) auto
  moreover have  $\neg \tau move2\ (compP2\ P)\ h\ [Null]\ (sync_V (e1) e2)\ (Suc (Suc (length (compE2\ e1))))$  None
    by(simp add:  $\tau move2-iff$ )
  moreover
  have  $P, sync_V (e1) e2, h \vdash (THROW\ NullPointer, xs[V := Null]) \leftrightarrow ([Null], (xs[V := Null]), Suc (Suc (length (compE2\ e1))))$ ,  $[addr-of-sys-xcpt\ NullPointer]$ 
    by(rule bisim1Sync11)
  moreover have  $\neg \tau move1\ P\ h\ (sync_V (null) e2)$  by(auto simp add:  $\tau move1.simps\ \tau moves1.simps$ )
  ultimately show ?thesis by(auto simp add: eval-nat-numeral) blast
qed auto
next
case (bisim1Sync3  $e1\ n\ e2\ V\ v\ xs$ )
note bisim1 =  $\langle \bigwedge xs. P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, None) \rangle$ 
note bisim2 =  $\langle \bigwedge xs. P, e2, h \vdash (e2, xs) \leftrightarrow ([], xs, 0, None) \rangle$ 
from  $\langle True, P, t \vdash 1\ \langle sync_V (Val\ v) e2, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$  show ?case
proof cases
  case (Lock1Synchronized  $a$ )
  note [simp] =  $\langle v = Addr\ a \rangle \langle ta = \{\!\!| Lock \!\!\rightarrow a, SyncLock\ a \}\!\!| \rangle \langle e' = insync_V (a) e2 \rangle \langle h' = h \rangle \langle xs' = xs[V := Addr\ a] \rangle$ 
  and  $V = \langle V < length\ xs \rangle$ 
  have  $exec-move-a\ P\ t\ (sync_V (e1) e2)\ h\ ([Addr\ a], xs[V := Addr\ a], Suc (Suc (length (compE2\ e1))), None)$ 
    ( $\{\!\!| Lock \!\!\rightarrow a, SyncLock\ a \}\!\!|$ )
     $h\ ([], xs[V := Addr\ a], Suc (Suc (Suc (length (compE2\ e1)))))$ , None)
  unfolding exec-move-def by  $-(rule\ exec-instr, auto\ simp\ add: is-Ref-def)$ 
  moreover have  $\neg \tau move2\ (compP2\ P)\ h\ [Addr\ a]\ (sync_V (e1) e2)\ (Suc (Suc (length (compE2\ e1))))$  None
    by(simp add:  $\tau move2-iff$ )
  moreover
  from bisim1Sync4[OF bisim1-refl, of  $P$   $h$   $V$   $e1$   $e2$   $a$   $xs[V := Addr\ a]$ ]
  have  $P, sync_V (e1) e2, h \vdash (insync_V (a) e2, xs[V := Addr\ a]) \leftrightarrow ([], xs[V := Addr\ a], Suc (Suc (Suc (length (compE2\ e1)))))$ , None) by simp
  moreover have  $\neg \tau move1\ P\ h\ (sync_V (addr\ a) e2)$  by(auto simp add:  $\tau move1.simps\ \tau moves1.simps$ )
  ultimately show ?thesis by(auto simp add: eval-nat-numeral ta-upd-simps) blast

```

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next
  case Synchronized1Null
  note [simp] = ⟨v = Null⟩ ⟨e' = THROW NullPointer⟩ ⟨ta = ε⟩ ⟨h' = h⟩ ⟨xs' = xs[V := Null]⟩
    and V = ⟨V < length xs⟩
  have exec-move-a P t (sync_V (e1) e2) h ([Null], xs[V := Null], Suc (Suc (length (compE2 e1))),
    None) ε
    h ([Null], xs[V := Null], Suc (Suc (length (compE2 e1))), [addr-of-sys-xcpt
    NullPointer])
    unfolding exec-move-def by(rule exec-instr) auto
    moreover have ¬ τmove2 (compP2 P) h [Null] (sync_V (e1) e2) (Suc (Suc (length (compE2
    e1)))) None
      by(simp add: τmove2-iff)
    moreover
      have P, sync_V (e1) e2, h ⊢ (THROW NullPointer, xs[V := Null]) ↔ ([Null], (xs[V := Null]), Suc
      (Suc (length (compE2 e1))), [addr-of-sys-xcpt NullPointer])
        by(rule bisim1Sync11)
      moreover have ¬ τmove1 P h (sync_V (null) e2) by(auto simp add: τmove1.simps τmoves1.simps)
      ultimately show ?thesis by(auto simp add: eval-nat-numeral) blast
    qed auto
next
  case (bisim1Sync4 e2 n e2' xs stk loc pc xcp e1 V a)
  note IH = bisim1Sync4.IH(2)
  note bisim2 = ⟨P, e2, h ⊢ (e2', xs) ↔ (stk, loc, pc, xcp)⟩
  note bisim1 = ⟨∧xs. P, e1, h ⊢ (e1, xs) ↔ ([], xs, 0, None)⟩
  note bsok = ⟨bsok (sync_V (e1) e2) n⟩
  note red = ⟨True, P, t ⊢ 1 ⟨insync_V (a) e2', (h, xs)⟩ -ta→ ⟨e', (h', xs')⟩⟩
  from red show ?case
  proof cases
    case (Synchronized1Red2 E')
    note [simp] = ⟨e' = insync_V (a) E'⟩
    and red = ⟨True, P, t ⊢ 1 ⟨e2', (h, xs)⟩ -ta→ ⟨E', (h', xs')⟩⟩
    from red have τ: τmove1 P h (insync_V (a) e2') = τmove1 P h e2' by(auto simp add: τmove1.simps
    τmoves1.simps)
    from IH[OF red] bsok obtain pc'' stk'' loc'' xcp''
      where bisim': P, e2, h' ⊢ (E', xs') ↔ (stk'', loc'', pc'', xcp'')
      and exec': ?exec ta e2 e2' E' h stk loc pc xcp h' pc'' stk'' loc'' xcp'' by auto
    have no-call2 e2 pc ⇒ no-call2 (sync_V(e1) e2) (Suc (Suc (Suc (length (compE2 e1) + pc))))
      by(auto simp add: no-call2-def)
    hence ?exec ta (sync_V (e1) e2) (insync_V (a) e2') (insync_V (a) E') h stk loc (Suc (Suc (Suc
    (length (compE2 e1) + pc)))) xcp h' (Suc (Suc (Suc (length (compE2 e1) + pc'')))) stk'' loc'' xcp''
      using exec' τ
      by(cases τmove1 P h (insync_V (a) e2'))(auto, (blast intro: exec-move-SyncI2 Insync-τExecrI
      Insync-τExecI)+)
    moreover from bisim'
      have P, sync_V (e1) e2, h' ⊢ (insync_V (a) E', xs') ↔ (stk'', loc'', (Suc (Suc (Suc (length (compE2
      e1) + pc'')))), xcp'')
        by(rule bisim1-bisims1.bisim1Sync4)
      ultimately show ?thesis using τ by auto blast+
  next
    case (Unlock1Synchronized a' v)
    note [simp] = ⟨e2' = Val v⟩ ⟨e' = Val v⟩ ⟨ta = {Unlock→a', SyncUnlock a'}⟩ ⟨h' = h⟩ ⟨xs' = xs⟩
      and V = ⟨V < length xs⟩ and xsV = ⟨xs ! V = Addr a'⟩
    from bisim2 have [simp]: xcp = None xs = loc
      and exec: τExec-mover-a P t e2 h (stk, loc, pc, xcp) ([v], loc, length (compE2 e2), None)

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  by(auto dest: bisim1Val2D1)
  let ?pc1 = (Suc (Suc (Suc (length (compE2 e1) + length (compE2 e2)))))
  note Insync- $\tau$ ExecrI[OF exec, of V e1]
  also from V xsV have  $\tau$ Exec-mover-a P t (syncV (e1) e2) h ([v], loc, ?pc1, None) ([Addr a', v],
  loc, Suc ?pc1, None)
  by -(rule  $\tau$ Execr1step, auto simp add: exec-move-def intro: exec-instr  $\tau$ move2- $\tau$ moves2.intros)
  also (rtranclp-trans)
  have exec-move-a P t (syncV (e1) e2) h ([Addr a', v], loc, Suc ?pc1, None) ( $\llbracket$ Unlock $\rightarrow$ a', Syn-
  cUnlock a' $\rrbracket$ ) h ([v], loc, Suc (Suc ?pc1), None)
  unfolding exec-move-def by(rule exec-instr)(auto simp add: is-Ref-def)
  moreover have  $\neg \tau$ move2 (compP2 P) h [Addr a', v] (syncV (e1) e2) (Suc ?pc1) None by(simp
  add:  $\tau$ move2-iff)
  moreover
  from bisim1Sync6[of P h V e1 e2 v xs]
  have P, syncV (e1) e2, h  $\vdash$  (Val v, xs)  $\leftrightarrow$  ([v], xs, Suc (Suc ?pc1), None)
  by(auto simp add: eval-nat-numeral)
  moreover have  $\neg \tau$ move1 P h (insyncV (a) e2^) by(auto simp add:  $\tau$ move1.simps  $\tau$ moves1.simps)
  ultimately show ?thesis by(auto simp add: ta-upd-simps) blast
next
case (Unlock1SynchronizedNull v)
note [simp] =  $\langle e2' = \text{Val } v \rangle \langle e' = \text{THROW NullPointer} \rangle \langle ta = \varepsilon \rangle \langle h' = h \rangle \langle xs' = xs \rangle$ 
and V =  $\langle V < \text{length } xs \rangle$  and xsV =  $\langle xs ! V = \text{Null} \rangle$ 
from bisim2 have [simp]: xcp = None xs = loc
and exec:  $\tau$ Exec-mover-a P t e2 h (stk, loc, pc, xcp) ([v], loc, length (compE2 e2), None)
by(auto dest: bisim1Val2D1)
let ?pc1 = (Suc (Suc (Suc (length (compE2 e1) + length (compE2 e2)))))
note Insync- $\tau$ ExecrI[OF exec, of V e1]
also from V xsV have  $\tau$ Exec-mover-a P t (syncV (e1) e2) h ([v], loc, ?pc1, None) ([Null, v], loc,
Suc ?pc1, None)
by -(rule  $\tau$ Execr1step, auto intro: exec-instr  $\tau$ move2- $\tau$ moves2.intros simp add: exec-move-def)
also (rtranclp-trans)
have exec-move-a P t (syncV (e1) e2) h ([Null, v], loc, Suc ?pc1, None)  $\varepsilon$  h ([Null, v], loc, Suc
?pc1, [addr-of-sys-xcpt NullPointer])
unfolding exec-move-def by(rule exec-instr)(auto simp add: is-Ref-def)
moreover have  $\neg \tau$ move2 (compP2 P) h [Null, v] (syncV (e1) e2) (Suc ?pc1) None by(simp
add:  $\tau$ move2-iff)
moreover
from bisim1Sync12[of P h V e1 e2 addr-of-sys-xcpt NullPointer xs]
have P, syncV (e1) e2, h  $\vdash$  (THROW NullPointer, xs)  $\leftrightarrow$  ([Null, v], xs, Suc ?pc1, [addr-of-sys-xcpt
NullPointer])
by(auto simp add: eval-nat-numeral)
moreover have  $\neg \tau$ move1 P h (insyncV (a) e2^) by(auto simp add:  $\tau$ move1.simps  $\tau$ moves1.simps)
ultimately show ?thesis by auto blast
next
case (Unlock1SynchronizedFail a' v)
note [simp] =  $\langle e2' = \text{Val } v \rangle \langle e' = \text{THROW IllegalMonitorState} \rangle \langle ta = \llbracket \text{UnlockFail} \rightarrow a' \rrbracket \rangle \langle xs' =$ 
xs $\rangle \langle h' = h \rangle$ 
and V =  $\langle V < \text{length } xs \rangle$  and xsV =  $\langle xs ! V = \text{Addr } a' \rangle$ 
from bisim2 have [simp]: xcp = None xs = loc
and exec:  $\tau$ Exec-mover-a P t e2 h (stk, loc, pc, xcp) ([v], loc, length (compE2 e2), None)
by(auto dest: bisim1Val2D1)
let ?pc1 = (Suc (Suc (Suc (length (compE2 e1) + length (compE2 e2)))))
note Insync- $\tau$ ExecrI[OF exec, of V e1]
also from V xsV have  $\tau$ Exec-mover-a P t (syncV (e1) e2) h ([v], loc, ?pc1, None) ([Addr a', v],

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loc, Suc ?pc1, None)
  by  $\neg$ (rule  $\tau Execr1step, auto$  intro: exec-instr  $\tau move2\text{-}\tau moves2.intros$  simp add: exec-move-def)
  also (rtrancpl-trans)
  have exec-move-a  $P\ t\ (sync_V\ (e1)\ e2)\ h\ ([Addr\ a',\ v],\ loc,\ Suc\ ?pc1,\ None)\ \{\!\!\{UnlockFail \rightarrow a'\}\!\!\}\ h$ 
  ([Addr  $a',\ v$ ], loc, Suc  $?pc1$ , [addr-of-sys-xcpt IllegalMonitorState])
    unfolding exec-move-def by (rule exec-instr)(auto simp add: is-Ref-def)
  moreover have  $\neg\ \tau move2\ (compP2\ P)\ h\ [Addr\ a',\ v]\ (sync_V\ (e1)\ e2)\ (Suc\ ?pc1)\ None$  by (simp
  add:  $\tau move2\text{-}iff$ )
  moreover
  from bisim1Sync12[of  $P\ h\ V\ e1\ e2\ addr\text{-}of\text{-}sys\text{-}xcpt\ IllegalMonitorState\ xs\ Addr\ a'\ v$ ]
  have  $P, sync_V\ (e1)\ e2, h \vdash (THROW\ IllegalMonitorState, xs) \leftrightarrow ([Addr\ a',\ v], xs, Suc\ ?pc1, [addr\text{-}of\text{-}sys\text{-}xcpt$ 
  IllegalMonitorState])
    by (auto simp add: eval-nat-numeral)
  moreover have  $\neg\ \tau move1\ P\ h\ (insync_V\ (a)\ Val\ v)$  by (auto simp add:  $\tau move1.simps\ \tau moves1.simps$ )
  ultimately show ?thesis by (auto simp add: ta-upd-simps) blast
next
case (Synchronized1Throw2  $a'\ ad$ )
note [simp] =  $\langle e2' = Throw\ ad \rangle\ \langle ta = \{\!\!\{Unlock \rightarrow a',\ SyncUnlock\ a'\}\!\!\}\ \langle e' = Throw\ ad \rangle$ 
 $\langle h' = h \rangle\ \langle xs' = xs \rangle$  and  $xsV = \langle xs!\ V = Addr\ a' \rangle$  and  $V = \langle V < length\ xs \rangle$ 
let  $?pc = 6 + length\ (compE2\ e1) + length\ (compE2\ e2)$ 
let  $?stk = Addr\ ad\ \# \ drop\ (size\ stk - 0)\ stk$ 
from bisim2 have [simp]:  $xs = loc$  by (auto dest: bisim1-ThrowD)
from bisim2
have  $\tau Exec\text{-}movet\text{-}a\ P\ t\ (sync_V\ (e1)\ e2)\ h\ (stk,\ loc,\ Suc\ (Suc\ (length\ (compE2\ e1) + pc)))$ ,
xcpt ([Addr  $ad$ ], loc,  $?pc$ , None)
  by (auto intro: bisim1-insync-Throw-exec)
also from  $xsV\ V$ 
have  $\tau Exec\text{-}movet\text{-}a\ P\ t\ (sync_V\ (e1)\ e2)\ h\ ([Addr\ ad],\ loc,\ ?pc,\ None)\ ([Addr\ a',\ Addr\ ad],\ loc,$ 
  Suc  $?pc$ , None)
  by  $\neg$ (rule  $\tau Execr1step, auto$  intro: exec-instr  $\tau move2Sync7$  simp add: exec-move-def)
  also (trancpl-trans)
  have exec-move-a  $P\ t\ (sync_V\ (e1)\ e2)\ h\ ([Addr\ a',\ Addr\ ad],\ loc,\ Suc\ ?pc,\ None)\ (\{\!\!\{Unlock \rightarrow a',$ 
  SyncUnlock  $a'\}\!\!\})\ h\ ([Addr\ ad],\ loc,\ Suc\ (Suc\ ?pc),\ None)$ 
    unfolding exec-move-def by (rule exec-instr)(auto simp add: is-Ref-def)
  moreover have  $\neg\ \tau move2\ (compP2\ P)\ h\ [Addr\ a',\ Addr\ ad]\ (sync_V\ (e1)\ e2)\ (Suc\ ?pc)\ None$ 
  by (simp add:  $\tau move2\text{-}iff$ )
  moreover
  hence  $P, sync_V\ (e1)\ e2, h \vdash (Throw\ ad,\ loc) \leftrightarrow ([Addr\ ad],\ loc,\ 8 + length\ (compE2\ e1) + length$ 
  (compE2  $e2$ ), None)
    by (auto intro: bisim1Sync9)
  moreover have  $\neg\ \tau move1\ P\ h\ (insync_V\ (a)\ Throw\ ad)$  by (auto simp add:  $\tau move1.simps\ \tau moves1.simps$ )
  ultimately show ?thesis by (auto simp add: add.assoc ta-upd-simps)(blast intro: trancpl-into-rtrancpl)
next
case (Synchronized1Throw2Fail  $a'\ ad$ )
note [simp] =  $\langle e2' = Throw\ ad \rangle\ \langle ta = \{\!\!\{UnlockFail \rightarrow a'\}\!\!\}\ \langle e' = THROW\ IllegalMonitorState \rangle\ \langle h' = h \rangle$ 
 $\langle xs' = xs \rangle$ 
  and  $xsV = \langle xs!\ V = Addr\ a' \rangle$  and  $V = \langle V < length\ xs \rangle$ 
let  $?pc = 6 + length\ (compE2\ e1) + length\ (compE2\ e2)$ 
let  $?stk = Addr\ ad\ \# \ drop\ (size\ stk - 0)\ stk$ 
from bisim2 have [simp]:  $xs = loc$  by (auto dest: bisim1-ThrowD)
from bisim2
have  $\tau Exec\text{-}movet\text{-}a\ P\ t\ (sync_V\ (e1)\ e2)\ h\ (stk,\ loc,\ Suc\ (Suc\ (length\ (compE2\ e1) + pc)))$ ,
xcpt ([Addr  $ad$ ], loc,  $?pc$ , None)

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by(auto intro: bisim1-insync-Throw-exec)  
 also from  $xsV\ V$   
 have  $\tau Exec\text{-}movet\text{-}a\ P\ t\ (sync_V\ (e1)\ e2)\ h\ ([Addr\ ad],\ loc,\ ?pc,\ None)\ ([Addr\ a',\ Addr\ ad],\ loc,\ Suc\ ?pc,\ None)$   
 by  $-(rule\ \tau Exec1step, auto\ intro: exec\text{-}instr\ \tau move2Sync7\ simp\ add: exec\text{-}move\text{-}def)$   
 also (trancpl-trans)  
 have  $exec\text{-}move\text{-}a\ P\ t\ (sync_V\ (e1)\ e2)\ h\ ([Addr\ a',\ Addr\ ad],\ loc,\ Suc\ ?pc,\ None)\ \{\!|UnlockFail \rightarrow a'\!\}$   
 $h\ ([Addr\ a',\ Addr\ ad],\ loc,\ Suc\ ?pc,\ [addr\text{-}of\text{-}sys\text{-}xcpt\ IllegalMonitorState])$   
 unfolding  $exec\text{-}move\text{-}def$  by(rule  $exec\text{-}instr$ )(auto simp add:  $is\text{-}Ref\text{-}def$ )  
 moreover have  $\neg \tau move2\ (compP2\ P)\ h\ [Addr\ a',\ Addr\ ad]\ (sync_V\ (e1)\ e2)\ (Suc\ ?pc)\ None$   
 by(simp add:  $\tau move2\text{-}iff$ )  
 moreover  
 hence  $P,\ sync_V\ (e1)\ e2,\ h \vdash (THROW\ IllegalMonitorState,\ loc) \leftrightarrow ([Addr\ a',\ Addr\ ad],\ loc,\ 7 + length\ (compE2\ e1) + length\ (compE2\ e2),\ [addr\text{-}of\text{-}sys\text{-}xcpt\ IllegalMonitorState])$   
 by(auto intro: bisim1Sync14)  
 moreover have  $\neg \tau move1\ P\ h\ (insync_V\ (a)\ e2')$  by(auto simp add:  $\tau move1.simps\ \tau moves1.simps$ )  
 ultimately show  $?thesis$  by(auto simp add:  $add.assoc\ ta\text{-}upd\text{-}simps$ )(blast intro:  $trancpl\text{-}into\text{-}rtrancpl$ )  
 next  
 case (Synchronized1Throw2Null ad)  
 note  $[simp] = \langle e2' = Throw\ ad \rangle \langle ta = \varepsilon \rangle \langle e' = THROW\ NullPointer \rangle \langle h' = h \rangle \langle xs' = xs \rangle$   
 and  $xsV = \langle xs ! V = Null \rangle$  and  $V = \langle V < length\ xs \rangle$   
 let  $?pc = 6 + length\ (compE2\ e1) + length\ (compE2\ e2)$   
 let  $?stk = Addr\ ad \# drop\ (size\ stk - 0)\ stk$   
 from bisim2 have  $[simp]: xs = loc$  by(auto dest: bisim1-ThrowD)  
 from bisim2  
 have  $\tau Exec\text{-}movet\text{-}a\ P\ t\ (sync_V\ (e1)\ e2)\ h\ (stk,\ loc,\ Suc\ (Suc\ (length\ (compE2\ e1) + pc))),$   
 $xcpt\ ([Addr\ ad],\ loc,\ ?pc,\ None)$   
 by(auto intro: bisim1-insync-Throw-exec)  
 also from  $xsV\ V$   
 have  $\tau Exec\text{-}movet\text{-}a\ P\ t\ (sync_V\ (e1)\ e2)\ h\ ([Addr\ ad],\ loc,\ ?pc,\ None)\ ([Null,\ Addr\ ad],\ loc,\ Suc\ ?pc,\ None)$   
 by  $-(rule\ \tau Exec1step, auto\ intro: exec\text{-}instr\ simp\ add: exec\text{-}move\text{-}def\ \tau move2\text{-}iff)$   
 also (trancpl-trans)  
 have  $exec\text{-}move\text{-}a\ P\ t\ (sync_V\ (e1)\ e2)\ h\ ([Null,\ Addr\ ad],\ loc,\ Suc\ ?pc,\ None)\ \varepsilon\ h\ ([Null,\ Addr\ ad],\ loc,\ Suc\ ?pc,\ [addr\text{-}of\text{-}sys\text{-}xcpt\ NullPointer])$   
 unfolding  $exec\text{-}move\text{-}def$  by(rule  $exec\text{-}instr$ )(auto simp add:  $is\text{-}Ref\text{-}def$ )  
 moreover have  $\neg \tau move2\ (compP2\ P)\ h\ [Null,\ Addr\ ad]\ (sync_V\ (e1)\ e2)\ (Suc\ ?pc)\ None$  by(simp add:  $\tau move2\text{-}iff$ )  
 moreover  
 hence  $P,\ sync_V\ (e1)\ e2,\ h \vdash (THROW\ NullPointer,\ loc) \leftrightarrow ([Null,\ Addr\ ad],\ loc,\ 7 + length\ (compE2\ e1) + length\ (compE2\ e2),\ [addr\text{-}of\text{-}sys\text{-}xcpt\ NullPointer])$   
 by(auto intro: bisim1Sync14)  
 moreover have  $\neg \tau move1\ P\ h\ (insync_V\ (a)\ e2')$  by(auto simp add:  $\tau move1.simps\ \tau moves1.simps$ )  
 ultimately show  $?thesis$  by(auto simp add:  $add.assoc$ )(blast intro:  $trancpl\text{-}into\text{-}rtrancpl$ )  
 qed  
 next  
 case (bisim1Sync5 e1 n e2 V a v xs)  
 note  $bisim2 = \langle \bigwedge xs.\ P, e2, h \vdash (e2, xs) \leftrightarrow ([],\ xs,\ 0,\ None) \rangle$   
 note  $bisim1 = \langle \bigwedge xs.\ P, e1, h \vdash (e1, xs) \leftrightarrow ([],\ xs,\ 0,\ None) \rangle$   
 from  $\langle True, P, t \vdash 1\ \langle insync_V\ (a)\ Val\ v, (h, xs) \rangle \text{-}ta \rightarrow \langle e', (h', xs') \rangle$  show  $?case$   
 proof cases  
 case (Unlock1Synchronized a')  
 note  $[simp] = \langle e' = Val\ v \rangle \langle ta = \{\!|Unlock \rightarrow a',\ SyncUnlock\ a'\!\} \rangle \langle h' = h \rangle \langle xs' = xs \rangle$   
 and  $V = \langle V < length\ xs \rangle$  and  $xsV = \langle xs ! V = Addr\ a' \rangle$

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    let ?pc1 = 4 + length (compE2 e1) + length (compE2 e2)
    have exec-move-a P t (sync_V (e1) e2) h ([Addr a', v], xs, ?pc1, None)  $\Downarrow$  Unlock  $\rightarrow$  a', SyncUnlock
    a'  $\Downarrow$  h ([v], xs, Suc ?pc1, None)
    unfolding exec-move-def by(rule exec-instr)(auto simp add: is-Ref-def)
    moreover have  $\neg \tau\text{move2}$  (compP2 P) h [Addr a', v] (sync_V (e1) e2) ?pc1 None by(simp add:
     $\tau\text{move2}\text{-iff}$ )
    moreover
    from bisim1Sync6[of P h V e1 e2 v xs]
    have P, sync_V (e1) e2, h  $\vdash$  (Val v, xs)  $\leftrightarrow$  ([v], xs, Suc ?pc1, None)
    by(auto simp add: eval-nat-numeral)
    moreover have  $\neg \tau\text{move1}$  P h (insync_V (a) Val v) by(auto simp add:  $\tau\text{move1.simps}$   $\tau\text{moves1.simps}$ )
    ultimately show ?thesis using xsV by(auto simp add: eval-nat-numeral ta-upd-simps) blast
  next
  case Unlock1SynchronizedNull
  note [simp] =  $\langle e' = \text{THROW NullPointer} \rangle \langle ta = \varepsilon \rangle \langle h' = h \rangle \langle xs' = xs \rangle$ 
  and V =  $\langle V < \text{length } xs \rangle$  and xsV =  $\langle xs ! V = \text{Null} \rangle$ 
  let ?pc1 = 4 + length (compE2 e1) + length (compE2 e2)
  have exec-move-a P t (sync_V (e1) e2) h ([Null, v], xs, ?pc1, None)  $\varepsilon$  h ([Null, v], xs, ?pc1,
  [addr-of-sys-xcpt NullPointer])
  unfolding exec-move-def by(rule exec-instr)(auto simp add: is-Ref-def)
  moreover have  $\neg \tau\text{move2}$  (compP2 P) h [Null, v] (sync_V (e1) e2) ?pc1 None by(simp add:
   $\tau\text{move2}\text{-iff}$ )
  moreover
  from bisim1Sync12[of P h V e1 e2 addr-of-sys-xcpt NullPointer xs Null v]
  have P, sync_V (e1) e2, h  $\vdash$  (THROW NullPointer, xs)  $\leftrightarrow$  ([Null, v], xs, ?pc1, [addr-of-sys-xcpt Null-
  Pointer])
  by(auto simp add: eval-nat-numeral)
  moreover have  $\neg \tau\text{move1}$  P h (insync_V (a) Val v) by(auto simp add:  $\tau\text{move1.simps}$   $\tau\text{moves1.simps}$ )
  ultimately show ?thesis using xsV by(auto simp add: eval-nat-numeral) blast
  next
  case (Unlock1SynchronizedFail a')
  note [simp] =  $\langle e' = \text{THROW IllegalMonitorState} \rangle \langle ta = \Downarrow \text{UnlockFail} \rightarrow a' \Downarrow \rangle \langle xs' = xs \rangle \langle h' = h \rangle$ 
  and V =  $\langle V < \text{length } xs \rangle$  and xsV =  $\langle xs ! V = \text{Addr } a' \rangle$ 
  let ?pc1 = 4 + length (compE2 e1) + length (compE2 e2)
  have exec-move-a P t (sync_V (e1) e2) h ([Addr a', v], xs, ?pc1, None)  $\Downarrow$  UnlockFail  $\rightarrow$  a' h ([Addr
  a', v], xs, ?pc1, [addr-of-sys-xcpt IllegalMonitorState])
  unfolding exec-move-def by(rule exec-instr)(auto simp add: is-Ref-def)
  moreover have  $\neg \tau\text{move2}$  (compP2 P) h [Addr a', v] (sync_V (e1) e2) ?pc1 None by(simp add:
   $\tau\text{move2}\text{-iff}$ )
  moreover
  from bisim1Sync12[of P h V e1 e2 addr-of-sys-xcpt IllegalMonitorState xs Addr a' v]
  have P, sync_V (e1) e2, h  $\vdash$  (THROW IllegalMonitorState, xs)  $\leftrightarrow$  ([Addr a', v], xs, ?pc1, [addr-of-sys-xcpt
  IllegalMonitorState])
  by(auto simp add: eval-nat-numeral)
  moreover have  $\neg \tau\text{move1}$  P h (insync_V (a) Val v) by(auto simp add:  $\tau\text{move1.simps}$   $\tau\text{moves1.simps}$ )
  ultimately show ?thesis using xsV by(auto simp add: eval-nat-numeral ta-upd-simps) blast
qed auto
next
case bisim1Sync6 thus ?case by auto
next
case (bisim1Sync7 e1 n e2 V a ad xs)
note bisim2 =  $\langle \bigwedge xs. P, e2, h \vdash (e2, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$ 
note bisim1 =  $\langle \bigwedge xs. P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$ 
from  $\langle \text{True}, P, t \vdash 1 \rangle \langle \text{insync}_V (a) \text{Throw ad}, (h, xs) \rangle \neg ta \rightarrow \langle e', (h', xs') \rangle$  show ?case

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**proof cases**

**case** (*Synchronized1Throw2 a'*)  
**note**  $[simp] = \langle ta = \llbracket Unlock \rightarrow a' \rrbracket, SyncUnlock\ a' \rrbracket \rangle \langle e' = Throw\ ad \rangle \langle h' = h \rangle \langle xs' = xs \rangle$   
**and**  $xsV = \langle xs ! V = Addr\ a' \rangle$  **and**  $V = \langle V < length\ xs \rangle$   
**let**  $?pc = 6 + length\ (compE2\ e1) + length\ (compE2\ e2)$   
**from**  $xsV\ V$   
**have**  $\tau Exec\text{-}mover\text{-}a\ P\ t\ (sync_V\ (e1)\ e2)\ h\ ([Addr\ ad], xs, ?pc, None)\ ([Addr\ a', Addr\ ad], xs, Suc\ ?pc, None)$   
**by**  $-(rule\ \tau Execr1step, auto\ intro: exec\text{-}instr\ simp\ add: exec\text{-}move\text{-}def\ \tau move2\text{-}iff)$   
**moreover have**  $exec\text{-}move\text{-}a\ P\ t\ (sync_V\ (e1)\ e2)\ h\ ([Addr\ a', Addr\ ad], xs, Suc\ ?pc, None)\ \llbracket Unlock \rightarrow a', SyncUnlock\ a' \rrbracket\ h\ ([Addr\ ad], xs, Suc\ (Suc\ ?pc), None)$   
**unfolding**  $exec\text{-}move\text{-}def$  **by**  $(rule\ exec\text{-}instr)(auto\ simp\ add: is\text{-}Ref\text{-}def)$   
**moreover have**  $\neg\ \tau move2\ (compP2\ P)\ h\ [Addr\ a', Addr\ ad]\ (sync_V\ (e1)\ e2)\ (Suc\ ?pc)\ None$   
**by**  $(simp\ add: \tau move2\text{-}iff)$   
**moreover**  
**have**  $P, sync_V\ (e1)\ e2, h \vdash (Throw\ ad, xs) \leftrightarrow ([Addr\ ad], xs, 8 + length\ (compE2\ e1) + length\ (compE2\ e2), None)$   
**by**  $(auto\ intro: bisim1Sync9)$   
**moreover have**  $\neg\ \tau move1\ P\ h\ (insync_V\ (a)\ Throw\ ad)$  **by**  $(auto\ simp\ add: \tau move1.simps\ \tau moves1.simps)$   
**ultimately show**  $?thesis$  **by**  $(auto\ simp\ add: add.assoc\ eval\text{-}nat\text{-}numeral\ ta\text{-}upd\text{-}simps)$  **blast**  
**next**  
**case** (*Synchronized1Throw2Fail a'*)  
**note**  $[simp] = \langle ta = \llbracket UnlockFail \rightarrow a' \rrbracket \rangle \langle e' = THROW\ IllegalMonitorState \rangle \langle h' = h \rangle \langle xs' = xs \rangle$   
**and**  $xsV = \langle xs ! V = Addr\ a' \rangle$  **and**  $V = \langle V < length\ xs \rangle$   
**let**  $?pc = 6 + length\ (compE2\ e1) + length\ (compE2\ e2)$   
**from**  $xsV\ V$   
**have**  $\tau Exec\text{-}mover\text{-}a\ P\ t\ (sync_V\ (e1)\ e2)\ h\ ([Addr\ ad], xs, ?pc, None)\ ([Addr\ a', Addr\ ad], xs, Suc\ ?pc, None)$   
**by**  $-(rule\ \tau Execr1step, auto\ intro: exec\text{-}instr\ simp\ add: exec\text{-}move\text{-}def\ \tau move2\text{-}iff)$   
**moreover have**  $exec\text{-}move\text{-}a\ P\ t\ (sync_V\ (e1)\ e2)\ h\ ([Addr\ a', Addr\ ad], xs, Suc\ ?pc, None)\ \llbracket UnlockFail \rightarrow a' \rrbracket\ h\ ([Addr\ a', Addr\ ad], xs, Suc\ ?pc, [addr\text{-}of\text{-}sys\text{-}xcpt\ IllegalMonitorState])$   
**unfolding**  $exec\text{-}move\text{-}def$  **by**  $(rule\ exec\text{-}instr)(auto\ simp\ add: is\text{-}Ref\text{-}def)$   
**moreover have**  $\neg\ \tau move2\ (compP2\ P)\ h\ [Addr\ a', Addr\ ad]\ (sync_V\ (e1)\ e2)\ (Suc\ ?pc)\ None$   
**by**  $(simp\ add: \tau move2\text{-}iff)$   
**moreover**  
**have**  $P, sync_V\ (e1)\ e2, h \vdash (THROW\ IllegalMonitorState, xs) \leftrightarrow ([Addr\ a', Addr\ ad], xs, 7 + length\ (compE2\ e1) + length\ (compE2\ e2), [addr\text{-}of\text{-}sys\text{-}xcpt\ IllegalMonitorState])$   
**by**  $(auto\ intro: bisim1Sync14)$   
**moreover have**  $\neg\ \tau move1\ P\ h\ (insync_V\ (a)\ Throw\ ad)$  **by**  $(auto\ simp\ add: \tau move1.simps\ \tau moves1.simps)$   
**ultimately show**  $?thesis$  **by**  $(auto\ simp\ add: add.assoc\ ta\text{-}upd\text{-}simps)$  **blast**  
**next**  
**case** *Synchronized1Throw2Null*  
**note**  $[simp] = \langle ta = \varepsilon \rangle \langle e' = THROW\ NullPointer \rangle \langle h' = h \rangle \langle xs' = xs \rangle$   
**and**  $xsV = \langle xs ! V = Null \rangle$  **and**  $V = \langle V < length\ xs \rangle$   
**let**  $?pc = 6 + length\ (compE2\ e1) + length\ (compE2\ e2)$   
**from**  $xsV\ V$   
**have**  $\tau Exec\text{-}mover\text{-}a\ P\ t\ (sync_V\ (e1)\ e2)\ h\ ([Addr\ ad], xs, ?pc, None)\ ([Null, Addr\ ad], xs, Suc\ ?pc, None)$   
**by**  $-(rule\ \tau Execr1step, auto\ intro: exec\text{-}instr\ simp\ add: exec\text{-}move\text{-}def\ \tau move2\text{-}iff)$   
**moreover have**  $exec\text{-}move\text{-}a\ P\ t\ (sync_V\ (e1)\ e2)\ h\ ([Null, Addr\ ad], xs, Suc\ ?pc, None)\ \varepsilon\ h\ ([Null, Addr\ ad], xs, Suc\ ?pc, [addr\text{-}of\text{-}sys\text{-}xcpt\ NullPointer])$   
**unfolding**  $exec\text{-}move\text{-}def$  **by**  $(rule\ exec\text{-}instr)(auto\ simp\ add: is\text{-}Ref\text{-}def)$

**moreover have**  $\neg \tau \text{move2} (\text{compP2 } P) h \text{ [Null, Addr ad]} (\text{sync}_V (e1) e2) (\text{Suc } ?pc) \text{ None}$  **by**(simp add:  $\tau \text{move2-iff}$ )

**moreover**

**have**  $P, \text{sync}_V (e1) e2, h \vdash (\text{THROW NullPointer}, xs) \leftrightarrow ([\text{Null}, \text{Addr ad}], xs, 7 + \text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2), [\text{addr-of-sys-xcpt NullPointer}])$

**by**(auto intro: bisim1Sync14)

**moreover have**  $\neg \tau \text{move1 } P h (\text{insync}_V (a) \text{ Throw ad})$  **by**(auto simp add:  $\tau \text{move1.simps}$   $\tau \text{moves1.simps}$ )

**ultimately show**  $?thesis$  **by**(auto simp add: add.assoc) blast

**qed auto**

**next**

**case** (bisim1Sync8 e1 n e2 V a ad xs)

**note**  $\text{bisim2} = \langle \bigwedge xs. P, e2, h \vdash (e2, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$

**note**  $\text{bisim1} = \langle \bigwedge xs. P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$

**from**  $\langle \text{True}, P, t \vdash 1 \langle \text{insync}_V (a) \text{ Throw ad}, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$  **show**  $?case$

**proof cases**

**case** (Synchronized1Throw2 a')

**note**  $[\text{simp}] = \langle ta = \langle \text{Unlock} \rightarrow a', \text{SyncUnlock } a' \rangle \langle e' = \text{Throw ad} \rangle \langle h' = h \rangle \langle xs' = xs \rangle$

**and**  $xsV = \langle xs ! V = \text{Addr } a' \rangle$  **and**  $V = \langle V < \text{length } xs \rangle$

**let**  $?pc = 7 + \text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2)$

**have**  $\text{exec-move-a } P t (\text{sync}_V (e1) e2) h ([\text{Addr } a', \text{Addr ad}], xs, ?pc, \text{None}) \langle \text{Unlock} \rightarrow a', \text{SyncUnlock } a' \rangle h ([\text{Addr ad}], xs, \text{Suc } ?pc, \text{None})$

**unfolding**  $\text{exec-move-def}$  **by**(rule  $\text{exec-instr}$ )(auto simp add: is-Ref-def)

**moreover have**  $\neg \tau \text{move2} (\text{compP2 } P) h [\text{Addr } a', \text{Addr ad}] (\text{sync}_V (e1) e2) ?pc \text{ None}$  **by**(simp add:  $\tau \text{move2-iff}$ )

**moreover**

**have**  $P, \text{sync}_V (e1) e2, h \vdash (\text{Throw ad}, xs) \leftrightarrow ([\text{Addr ad}], xs, 8 + \text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2), \text{None})$

**by**(auto intro: bisim1Sync9)

**moreover have**  $\neg \tau \text{move1 } P h (\text{insync}_V (a) \text{ Throw ad})$  **by**(auto simp add:  $\tau \text{move1.simps}$   $\tau \text{moves1.simps}$ )

**ultimately show**  $?thesis$  **using**  $xsV$  **by**(auto simp add: add.assoc eval-nat-numeral ta-upd-simps) blast

**next**

**case** (Synchronized1Throw2Fail a')

**note**  $[\text{simp}] = \langle ta = \langle \text{UnlockFail} \rightarrow a' \rangle \langle e' = \text{THROW IllegalMonitorState} \rangle \langle h' = h \rangle \langle xs' = xs \rangle$

**and**  $xsV = \langle xs ! V = \text{Addr } a' \rangle$  **and**  $V = \langle V < \text{length } xs \rangle$

**let**  $?pc = 7 + \text{length} (\text{compE2 } e1) + \text{length} (\text{compE2 } e2)$

**have**  $\text{exec-move-a } P t (\text{sync}_V (e1) e2) h ([\text{Addr } a', \text{Addr ad}], xs, ?pc, \text{None}) \langle \text{UnlockFail} \rightarrow a' \rangle h ([\text{Addr } a', \text{Addr ad}], xs, ?pc, [\text{addr-of-sys-xcpt IllegalMonitorState}])$

**unfolding**  $\text{exec-move-def}$  **by**(rule  $\text{exec-instr}$ )(auto simp add: is-Ref-def)

**moreover have**  $\neg \tau \text{move2} (\text{compP2 } P) h [\text{Addr } a', \text{Addr ad}] (\text{sync}_V (e1) e2) ?pc \text{ None}$  **by**(simp add:  $\tau \text{move2-iff}$ )

**moreover**

**have**  $P, \text{sync}_V (e1) e2, h \vdash (\text{THROW IllegalMonitorState}, xs) \leftrightarrow ([\text{Addr } a', \text{Addr ad}], xs, ?pc, [\text{addr-of-sys-xcpt IllegalMonitorState}])$

**by**(auto intro: bisim1Sync14)

**moreover have**  $\neg \tau \text{move1 } P h (\text{insync}_V (a) \text{ Throw ad})$  **by**(auto simp add:  $\tau \text{move1.simps}$   $\tau \text{moves1.simps}$ )

**ultimately show**  $?thesis$  **using**  $xsV$  **by**(auto simp add: add.assoc ta-upd-simps) blast

**next**

**case** Synchronized1Throw2Null

**note**  $[\text{simp}] = \langle ta = \varepsilon \rangle \langle e' = \text{THROW NullPointer} \rangle \langle h' = h \rangle \langle xs' = xs \rangle$

**and**  $xsV = \langle xs ! V = \text{Null} \rangle$  **and**  $V = \langle V < \text{length } xs \rangle$

```

let ?pc = 7 + length (compE2 e1) + length (compE2 e2)
have exec-move-a P t (syncV (e1) e2) h ([Null, Addr ad], xs, ?pc, None) ε h ([Null, Addr ad], xs,
?pc, [addr-of-sys-xcpt NullPointer])
  unfolding exec-move-def by(rule exec-instr)(auto simp add: is-Ref-def)
  moreover have ¬ τmove2 (compP2 P) h [Null, Addr ad] (syncV (e1) e2) ?pc None by(simp
add: τmove2-iff)
  moreover
  have P, syncV (e1) e2, h ⊢ (THROW NullPointer, xs) ↔ ([Null, Addr ad], xs, ?pc, [addr-of-sys-xcpt
NullPointer])
    by(auto intro: bisim1Sync14)
  moreover have ¬ τmove1 P h (insyncV (a) Throw ad) by(auto simp add: τmove1.simps
τmoves1.simps)
  ultimately show ?thesis using xsV by(auto simp add: add.assoc) blast
qed auto
next
case bisim1Sync9 thus ?case by auto
next
case bisim1Sync10 thus ?case by auto
next
case bisim1Sync11 thus ?case by auto
next
case bisim1Sync12 thus ?case by auto
next
case bisim1Sync14 thus ?case by auto
next
case bisim1SyncThrow thus ?case by auto
next
case bisim1InSync thus ?case by simp
next
case (bisim1Seq1 e1 n e1' xs stk loc pc xcp e2)
note IH = bisim1Seq1.IH(2)
note bisim1 = ⟨P, e1, h ⊢ (e1', xs) ↔ (stk, loc, pc, xcp)⟩
note bisim2 = ⟨ $\bigwedge$ xs. P, e2, h ⊢ (e2, xs) ↔ ([], xs, 0, None)⟩
note red = ⟨True, P, t ⊢ 1 ⟨e1';; e2, (h, xs)⟩ -ta→ ⟨e', (h', xs')⟩⟩
note bsok = ⟨bsok (e1;; e2) n⟩
from red show ?case
proof cases
case (Seq1Red E')
note [simp] = ⟨e' = E';; e2⟩
and red = ⟨True, P, t ⊢ 1 ⟨e1', (h, xs)⟩ -ta→ ⟨E', (h', xs')⟩⟩
from red have τ: τmove1 P h (e1';; e2) = τmove1 P h e1' by(auto simp add: τmove1.simps
τmoves1.simps)
moreover have call1 (e1';; e2) = call1 e1' by auto
moreover from IH[OF red] bsok
obtain pc'' stk'' loc'' xcp'' where bisim: P, e1, h' ⊢ (E', xs') ↔ (stk'', loc'', pc'', xcp'')
and redo: ?exec ta e1 e1' E' h stk loc pc xcp h' pc'' stk'' loc'' xcp'' by auto
from bisim have P, e1;; e2, h' ⊢ (E';; e2, xs') ↔ (stk'', loc'', pc'', xcp'')
by(rule bisim1-bisims1.bisim1Seq1)
moreover {
  assume no-call2 e1 pc
  hence no-call2 (e1;; e2) pc by(auto simp add: no-call2-def) }
ultimately show ?thesis using redo
by(auto simp del: call1.simps calls1.simps split: if-split-asm split del: if-split)(blast intro:
Seq-τExecrI1 Seq-τExecI1 exec-move-SeqI1)+

```

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next
  case (Red1Seq v)
  note [simp] = ⟨e1' = Val v⟩ ⟨ta = ε⟩ ⟨h' = h⟩ ⟨xs' = xs⟩ ⟨e' = e2⟩
  from bisim1 have s: xcp = None xs = loc
    and τExec-mover-a P t e1 h (stk, loc, pc, xcp) ([v], loc, length (compE2 e1), None)
    by(auto dest: bisim1Val2D1)
  hence τExec-mover-a P t (e1;; e2) h (stk, loc, pc, xcp) ([v], loc, length (compE2 e1), None)
    by-(rule Seq-τExecrI1)
  moreover have exec-move-a P t (e1;; e2) h ([v], loc, length (compE2 e1), None) ε h ([], loc, Suc
    (length (compE2 e1)), None)
    unfolding exec-move-def by(rule exec-instr, auto)
  moreover have τmove2 (compP2 P) h [v] (e1;;e2) (length (compE2 e1)) None by(simp add:
    τmove2-iff)
  ultimately have τExec-mover-a P t (e1;; e2) h (stk, loc, pc, xcp) ([], loc, Suc (length (compE2
    e1)), None)
    by(auto intro: rtrancp.rtrancI-into-rtrancI τexec-moveI simp add: compP2-def)
  moreover from bisim1-refl
  have P, e1;; e2, h ⊢ (e2, xs) ↔ ([], loc, Suc (length (compE2 e1) + 0), None)
    unfolding s by(rule bisim1Seq2)
  moreover have τmove1 P h (Val v;; e2) by(rule τmove1SeqRed)
  ultimately show ?thesis by(auto)
next
  case (Seq1Throw a)
  note [simp] = ⟨e1' = Throw a⟩ ⟨ta = ε⟩ ⟨e' = Throw a⟩ ⟨h' = h⟩ ⟨xs' = xs⟩
  have τ: τmove1 P h (Throw a;; e2) by(rule τmove1SeqThrow)
  from bisim1 have xcp = [a] ∨ xcp = None by(auto dest: bisim1-ThrowD)
  thus ?thesis
proof
  assume [simp]: xcp = [a]
  with bisim1 have P, e1;; e2, h ⊢ (Throw a, xs) ↔ (stk, loc, pc, xcp)
    by(auto intro: bisim1SeqThrow1)
  thus ?thesis using τ by(fastforce)
next
  assume [simp]: xcp = None
  with bisim1 obtain pc'
    where τExec-mover-a P t e1 h (stk, loc, pc, None) ([Addr a], loc, pc', [a])
    and bisim': P, e1, h ⊢ (Throw a, xs) ↔ ([Addr a], loc, pc', [a]) and [simp]: xs = loc
    by(auto dest: bisim1-Throw-τExec-mover)
  hence τExec-mover-a P t (e1;;e2) h (stk, loc, pc, None) ([Addr a], loc, pc', [a])
    by-(rule Seq-τExecrI1)
  moreover from bisim'
  have P, e1;;e2, h ⊢ (Throw a, xs) ↔ ([Addr a], loc, pc', [a])
    by(rule bisim1SeqThrow1)
  ultimately show ?thesis using τ by auto
qed
qed
next
  case bisim1SeqThrow1 thus ?case by fastforce
next
  case (bisim1Seq2 e2 n e2' xs stk loc pc xcp e1)
  note IH = bisim1Seq2.IH(2)
  note bisim2 = ⟨P, e2, h ⊢ (e2', xs) ↔ (stk, loc, pc, xcp)⟩
  note bisim1 = ⟨∧xs. P, e1, h ⊢ (e1, xs) ↔ ([], xs, 0, None)⟩
  note red = ⟨True, P, t ⊢ 1 ⟨e2', (h, xs)⟩ -ta→ ⟨e', (h', xs')⟩⟩

```

**note**  $bsok = \langle bsok (e1;; e2) n \rangle$   
**from**  $IH[OF \text{ red}] \text{ bsok}$  **obtain**  $pc'' \text{ stk}'' \text{ loc}'' \text{ xcp}''$   
**where**  $bisim': P, e2, h' \vdash (e', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$   
**and**  $exec': ?exec \text{ ta } e2 \text{ e2}' e' h \text{ stk } loc \text{ pc } xcp \text{ h}' pc'' \text{ stk}'' \text{ loc}'' \text{ xcp}''$  **by**  $auto$   
**have**  $no\text{-call2 } e2 \text{ pc} \implies no\text{-call2 } (e1;; e2) (Suc (\text{length } (compE2 \text{ e1}) + pc))$   
**by**  $(auto \text{ simp add: no-call2-def})$   
**hence**  $?exec \text{ ta } (e1;; e2) \text{ e2}' e' h \text{ stk } loc (Suc (\text{length } (compE2 \text{ e1}) + pc)) \text{ xcp } h' (Suc (\text{length } (compE2 \text{ e1}) + pc'')) \text{ stk}'' \text{ loc}'' \text{ xcp}''$   
**using**  $exec' \text{ by } (cases \tau \text{ move1 } P \text{ h } e2') (auto, (blast \text{ intro: Seq-}\tau \text{ ExecrI2 Seq-}\tau \text{ ExectI2 exec-move-SeqI2})+)$   
**moreover from**  $bisim'$   
**have**  $P, e1;; e2, h' \vdash (e', xs') \leftrightarrow (stk'', loc'', Suc (\text{length } (compE2 \text{ e1}) + pc''), xcp'')$   
**by**  $(rule \text{ bisim1-bisims1.bisim1Seq2})$   
**ultimately show**  $?case \text{ by } (auto \text{ split: if-split-asm}) \text{ blast+}$   
**next**  
**case**  $(bisim1Cond1 \text{ E } n \text{ e } xs \text{ stk } loc \text{ pc } xcp \text{ e1 } e2)$   
**note**  $IH = bisim1Cond1.IH(2)$   
**note**  $bisim = \langle P, E, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$   
**note**  $bisim1 = \langle \bigwedge xs. P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, None) \rangle$   
**note**  $bisim2 = \langle \bigwedge xs. P, e2, h \vdash (e2, xs) \leftrightarrow ([], xs, 0, None) \rangle$   
**note**  $bsok = \langle bsok (if (E) \text{ e1 } else \text{ e2}) n \rangle$   
**from**  $\langle True, P, t \vdash 1 \langle if (e) \text{ e1 } else \text{ e2}, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$  **show**  $?case$   
**proof cases**  
**case**  $(Cond1Red \text{ b'})$   
**note**  $[simp] = \langle e' = if (b') \text{ e1 } else \text{ e2} \rangle$   
**and**  $red = \langle True, P, t \vdash 1 \langle e, (h, xs) \rangle -ta \rightarrow \langle b', (h', xs') \rangle \rangle$   
**from**  $red$  **have**  $\tau \text{ move1 } P \text{ h } (if (e) \text{ e1 } else \text{ e2}) = \tau \text{ move1 } P \text{ h } e$  **by**  $(auto \text{ simp add: } \tau \text{ move1.simps } \tau \text{ moves1.simps})$   
**moreover have**  $call1 (if (e) \text{ e1 } else \text{ e2}) = call1 \text{ e}$  **by**  $auto$   
**moreover from**  $IH[OF \text{ red}] \text{ bsok}$   
**obtain**  $pc'' \text{ stk}'' \text{ loc}'' \text{ xcp}''$  **where**  $bisim: P, E, h' \vdash (b', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$   
**and**  $redo: ?exec \text{ ta } E \text{ e } b' h \text{ stk } loc \text{ pc } xcp \text{ h}' pc'' \text{ stk}'' \text{ loc}'' \text{ xcp}''$  **by**  $auto$   
**from**  $bisim$   
**have**  $P, if (E) \text{ e1 } else \text{ e2}, h' \vdash (if (b') \text{ e1 } else \text{ e2}, xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$   
**by**  $(rule \text{ bisim1-bisims1.bisim1Cond1})$   
**moreover {**  
**assume**  $no\text{-call2 } E \text{ pc}$   
**hence**  $no\text{-call2 } (if (E) \text{ e1 } else \text{ e2}) \text{ pc}$  **by**  $(auto \text{ simp add: no-call2-def})$  **}**  
**ultimately show**  $?thesis$  **using**  $redo$   
**by**  $(auto \text{ simp del: call1.simps calls1.simps split: if-split-asm split del: if-split}) (blast \text{ intro: Cond-}\tau \text{ ExecrI1 Cond-}\tau \text{ ExectI1 exec-move-CondI1})+$   
**next**  
**case**  $Red1CondT$   
**note**  $[simp] = \langle e = true \rangle \langle e' = e1 \rangle \langle ta = \varepsilon \rangle \langle h' = h \rangle \langle xs' = xs \rangle$   
**from**  $bisim$  **have**  $s: xcp = None \text{ xs} = loc$   
**and**  $\tau \text{ Exec-mover-a } P \text{ t } E \text{ h } (stk, loc, pc, xcp) ([Bool \text{ True}], loc, \text{length } (compE2 \text{ E}), None)$   
**by**  $(auto \text{ dest: bisim1Val2D1})$   
**hence**  $\tau \text{ Exec-mover-a } P \text{ t } (if (E) \text{ e1 } else \text{ e2}) \text{ h } (stk, loc, pc, xcp) ([Bool \text{ True}], loc, \text{length } (compE2 \text{ E}), None)$   
**by**  $-(rule \text{ Cond-}\tau \text{ ExecrI1})$   
**moreover have**  $exec\text{-move-a } P \text{ t } (if (E) \text{ e1 } else \text{ e2}) \text{ h } ([Bool \text{ True}], loc, \text{length } (compE2 \text{ E}), None)$   
 $\varepsilon \text{ h } ([], loc, Suc (\text{length } (compE2 \text{ E})), None)$   
**unfolding**  $exec\text{-move-def}$  **by**  $(rule \text{ exec-instr, auto})$   
**moreover have**  $\tau \text{ move2 } (compP2 \text{ P}) \text{ h } [Bool \text{ True}] (if (E) \text{ e1 } else \text{ e2}) (\text{length } (compE2 \text{ E})) \text{ None}$   
**by**  $(simp \text{ add: } \tau \text{ move2-iff})$

**ultimately have**  $\tau_{Exec\text{-}move\text{-}a} P \ t \ (if \ (E) \ e1 \ else \ e2) \ h \ (stk, loc, pc, xcp) \ ([], loc, Suc \ (length \ (compE2 \ E)), None)$   
**by**(*auto intro: rtrancpl-into-trancpl1*  $\tau_{exec\text{-}moveI}$  *simp add: compP2-def*)  
**moreover have**  $\tau_{move1} P \ h \ (if \ (true) \ e1 \ else \ e2) \ \mathbf{by}(\text{rule } \tau_{move1}CondRed)$   
**moreover**  
**from** *bisim1-refl*  
**have**  $P, if \ (E) \ e1 \ else \ e2, h \vdash (e1, xs) \leftrightarrow ([], loc, Suc \ (length \ (compE2 \ E) + 0), None)$   
**unfolding**  $s \ \mathbf{by}(\text{rule } bisim1CondThen)$   
**ultimately show** *?thesis* **by** (*fastforce*)  
**next**  
**case** *Red1CondF*  
**note**  $[simp] = \langle e = false \rangle \langle e' = e2 \rangle \langle ta = \varepsilon \rangle \langle h' = h \rangle \langle xs' = xs \rangle$   
**from** *bisim* **have**  $s: xcp = None \ xs = loc$   
**and**  $\tau_{Exec\text{-}mover\text{-}a} P \ t \ E \ h \ (stk, loc, pc, xcp) \ ([Bool \ False], loc, length \ (compE2 \ E), None)$   
**by**(*auto dest: bisim1Val2D1*)  
**hence**  $\tau_{Exec\text{-}mover\text{-}a} P \ t \ (if \ (E) \ e1 \ else \ e2) \ h \ (stk, loc, pc, xcp) \ ([Bool \ False], loc, length \ (compE2 \ E), None)$   
**by**-(*rule Cond- $\tau_{ExecrI1}$* )  
**moreover have**  $exec\text{-}move\text{-}a \ P \ t \ (if \ (E) \ e1 \ else \ e2) \ h \ ([Bool \ False], loc, length \ (compE2 \ E), None)$   
 $\varepsilon \ h \ ([], loc, Suc \ (Suc \ (length \ (compE2 \ E) + length \ (compE2 \ e1))), None)$   
**unfolding** *exec-move-def* **by**(*rule exec-instr*)(*auto*)  
**moreover have**  $\tau_{move2} \ (compP2 \ P) \ h \ [Bool \ False] \ (if \ (E) \ e1 \ else \ e2) \ (length \ (compE2 \ E)) \ None$   
**by**(*rule*  $\tau_{move2}CondRed$ )  
**ultimately have**  $\tau_{Exec\text{-}move\text{-}a} P \ t \ (if \ (E) \ e1 \ else \ e2) \ h \ (stk, loc, pc, xcp) \ ([], loc, Suc \ (Suc \ (length \ (compE2 \ E) + length \ (compE2 \ e1))), None)$   
**by**(*auto intro: rtrancpl-into-trancpl1*  $\tau_{exec\text{-}moveI}$  *simp add: compP2-def*)  
**moreover have**  $\tau_{move1} P \ h \ (if \ (false) \ e1 \ else \ e2) \ \mathbf{by}(\text{rule } \tau_{move1}CondRed)$   
**moreover**  
**from** *bisim1-refl*  
**have**  $P, if \ (E) \ e1 \ else \ e2, h \vdash (e2, loc) \leftrightarrow ([], loc, (Suc \ (Suc \ (length \ (compE2 \ E) + length \ (compE2 \ e1) + 0))), None)$   
**unfolding**  $s \ \mathbf{by}(\text{rule } bisim1CondElse)$   
**ultimately show** *?thesis* **using**  $s \ \mathbf{by} \ \text{auto}(\text{blast intro: trancpl-into-rtrancpl})$   
**next**  
**case** (*Cond1Throw a*)  
**note**  $[simp] = \langle e = Throw \ a \rangle \langle ta = \varepsilon \rangle \langle e' = Throw \ a \rangle \langle h' = h \rangle \langle xs' = xs \rangle$   
**have**  $\tau: \tau_{move1} P \ h \ (if \ (Throw \ a) \ e1 \ else \ e2) \ \mathbf{by}(\text{rule } \tau_{move1}CondThrow)$   
**from** *bisim* **have**  $xcp = [a] \vee xcp = None \ \mathbf{by}(\text{auto dest: bisim1-ThrowD})$   
**thus** *?thesis*  
**proof**  
**assume**  $[simp]: xcp = [a]$   
**with** *bisim*  
**have**  $P, if \ (E) \ e1 \ else \ e2, h \vdash (Throw \ a, xs) \leftrightarrow (stk, loc, pc, [a])$   
**by**(*auto intro: bisim1-bisims1.bisim1CondThrow*)  
**thus** *?thesis* **using**  $\tau \ \mathbf{by}(\text{fastforce})$   
**next**  
**assume**  $[simp]: xcp = None$   
**with** *bisim* **obtain**  $pc'$   
**where**  $\tau_{Exec\text{-}mover\text{-}a} P \ t \ E \ h \ (stk, loc, pc, None) \ ([Addr \ a], loc, pc', [a])$   
**and**  $bisim': P, E, h \vdash (Throw \ a, xs) \leftrightarrow ([Addr \ a], loc, pc', [a])$  **and**  $[simp]: xs = loc$   
**by**(*auto dest: bisim1-Throw- $\tau_{Exec\text{-}mover}$* )  
**hence**  $\tau_{Exec\text{-}mover\text{-}a} P \ t \ (if \ (E) \ e1 \ else \ e2) \ h \ (stk, loc, pc, None) \ ([Addr \ a], loc, pc', [a])$   
**by**-(*rule Cond- $\tau_{ExecrI1}$* )  
**moreover from** *bisim'*



```

have  $P$ , if  $(E)$   $e1$  else  $e2$ ,  $h \vdash (Throw\ a,\ xs) \leftrightarrow ([Addr\ a],\ loc,\ pc',\ [a])$ 
  by-(rule bisim1CondThrow, auto)
ultimately show ?thesis using  $\tau$  by auto
qed
qed
next
case (bisim1CondThen  $e1\ n\ e1'\ xs\ stk\ loc\ pc\ xcp\ e\ e2$ )
note  $IH = bisim1CondThen.IH(2)$ 
note  $bisim1 = \langle P, e1, h \vdash (e1', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
note  $bisim = \langle \bigwedge xs. P, e, h \vdash (e, xs) \leftrightarrow ([], xs, 0, None) \rangle$ 
note  $bisim2 = \langle \bigwedge xs. P, e2, h \vdash (e2, xs) \leftrightarrow ([], xs, 0, None) \rangle$ 
note  $bsok = \langle bsok\ (if\ (e)\ e1\ else\ e2)\ n \rangle$ 
from  $IH[OF\ \langle True, P, t \vdash 1\ \langle e1', (h, xs) \rangle - ta \rightarrow \langle e', (h', xs') \rangle \rangle]\ bsok$  obtain  $pc''\ stk''\ loc''\ xcp''$ 
  where  $bisim': P, e1, h' \vdash (e', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$ 
  and  $exec': ?exec\ ta\ e1\ e1'\ e'\ h\ stk\ loc\ pc\ xcp\ h'\ pc''\ stk''\ loc''\ xcp''$  by auto
have  $no-call2\ e1\ pc \implies no-call2\ (if\ (e)\ e1\ else\ e2)\ (Suc\ (length\ (compE2\ e) + pc))$ 
  by(auto simp add: no-call2-def)
hence  $?exec\ ta\ (if\ (e)\ e1\ else\ e2)\ e1'\ e'\ h\ stk\ loc\ (Suc\ (length\ (compE2\ e) + pc))\ xcp\ h'\ (Suc\ (length\ (compE2\ e) + pc''))\ stk''\ loc''\ xcp''$ 
  using  $exec'$  by(cases  $\tau\ move1\ P\ h\ e1'$ )(auto, (blast intro: Cond- $\tau$ ExecrI2 Cond- $\tau$ ExectI2 exec-move-CondI2))+
moreover from  $bisim'$ 
have  $P, if\ (e)\ e1\ else\ e2, h' \vdash (e', xs') \leftrightarrow (stk'', loc'', Suc\ (length\ (compE2\ e) + pc''), xcp'')$ 
  by(rule bisim1-bisims1.bisim1CondThen)
ultimately show ?case
  by(auto split: if-split-asm) blast+
next
case (bisim1CondElse  $e2\ n\ e2'\ xs\ stk\ loc\ pc\ xcp\ e\ e1$ )
note  $IH = bisim1CondElse.IH(2)$ 
note  $bisim2 = \langle P, e2, h \vdash (e2', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
note  $bisim = \langle \bigwedge xs. P, e, h \vdash (e, xs) \leftrightarrow ([], xs, 0, None) \rangle$ 
note  $bisim1 = \langle \bigwedge xs. P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, None) \rangle$ 
from  $IH[OF\ \langle True, P, t \vdash 1\ \langle e2', (h, xs) \rangle - ta \rightarrow \langle e', (h', xs') \rangle \rangle]\ \langle bsok\ (if\ (e)\ e1\ else\ e2)\ n \rangle$ 
obtain  $pc''\ stk''\ loc''\ xcp''$ 
  where  $bisim': P, e2, h' \vdash (e', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$ 
  and  $exec': ?exec\ ta\ e2\ e2'\ e'\ h\ stk\ loc\ pc\ xcp\ h'\ pc''\ stk''\ loc''\ xcp''$  by auto
have  $no-call2\ e2\ pc \implies no-call2\ (if\ (e)\ e1\ else\ e2)\ (Suc\ (Suc\ (length\ (compE2\ e) + length\ (compE2\ e1) + pc)))$ 
  by(auto simp add: no-call2-def)
hence  $?exec\ ta\ (if\ (e)\ e1\ else\ e2)\ e2'\ e'\ h\ stk\ loc\ (Suc\ (Suc\ (length\ (compE2\ e) + length\ (compE2\ e1) + pc)))\ xcp\ h'\ (Suc\ (Suc\ (length\ (compE2\ e) + length\ (compE2\ e1) + pc'')))\ stk''\ loc''\ xcp''$ 
  using  $exec'$  by(cases  $\tau\ move1\ P\ h\ e2'$ )(auto, (blast intro: Cond- $\tau$ ExecrI3 Cond- $\tau$ ExectI3 exec-move-CondI3))+
moreover from  $bisim'$ 
have  $P, if\ (e)\ e1\ else\ e2, h' \vdash (e', xs') \leftrightarrow (stk'', loc'', Suc\ (Suc\ (length\ (compE2\ e) + length\ (compE2\ e1) + pc''))\ xcp'')$ 
  by(rule bisim1-bisims1.bisim1CondElse)
ultimately show ?case
  by(auto split: if-split-asm) blast+
next
case bisim1CondThrow thus ?case by auto
next
case (bisim1While1  $c\ n\ e\ xs$ )
note  $bisim1 = \langle \bigwedge xs. P, c, h \vdash (c, xs) \leftrightarrow ([], xs, 0, None) \rangle$ 
note  $bisim2 = \langle \bigwedge xs. P, e, h \vdash (e, xs) \leftrightarrow ([], xs, 0, None) \rangle$ 
from  $\langle True, P, t \vdash 1\ \langle while\ (c)\ e, (h, xs) \rangle - ta \rightarrow \langle e', (h', xs') \rangle \rangle$  show ?case

```

**proof cases**  
**case** *Red1While*  
**note**  $[simp] = \langle ta = \varepsilon \rangle \langle e' = \text{if } (c) (e;; \text{while } (c) e) \text{ else unit} \rangle \langle h' = h \rangle \langle xs' = xs \rangle$   
**have**  $\tau\text{move1 } P \ h \ (\text{while } (c) e) \text{ by}(\text{rule } \tau\text{move1WhileRed})$   
**moreover**  
**have**  $P, h \vdash (\text{if } (c) (e;; \text{while } (c) e) \text{ else unit}, xs) \leftrightarrow ([], xs, 0, \text{None})$   
**by**(rule *bisim1-bisims1.bisim1While3[OF bisim1-refl]*)  
**moreover have**  $\text{sim12-size } (\text{while } (c) e) > \text{sim12-size } e' \text{ by}(simp)$   
**ultimately show** *?thesis* **by** *auto*  
**qed**  
**next**  
**case** (*bisim1While3 c n c' xs stk loc pc xcp e*)  
**note**  $IH = \text{bisim1While3.IH}(2)$   
**note**  $\text{bisim1} = \langle P, c, h \vdash (c', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$   
**note**  $\text{bisim2} = \langle \bigwedge xs. P, e, h \vdash (e, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$   
**note**  $\text{bsok} = \langle \text{bsok } (\text{while } (c) e) \ n \rangle$   
**from**  $\langle \text{True}, P, t \vdash 1 \ \langle \text{if } (c') (e;; \text{while } (c) e) \text{ else unit}, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$  **show** *?case*  
**proof cases**  
**case** (*Cond1Red b'*)  
**note**  $[simp] = \langle e' = \text{if } (b') (e;; \text{while } (c) e) \text{ else unit} \rangle$   
**and**  $\text{red} = \langle \text{True}, P, t \vdash 1 \ \langle c', (h, xs) \rangle -ta \rightarrow \langle b', (h', xs') \rangle \rangle$   
**from red have**  $\tau\text{move1 } P \ h \ (\text{if } (c') (e;; \text{while } (c) e) \text{ else unit}) = \tau\text{move1 } P \ h \ c' \text{ by}(\text{auto simp add: } \tau\text{move1.simps } \tau\text{moves1.simps})$   
**moreover from red have**  $\text{call1 } (\text{if } (c') (e;; \text{while } (c) e) \text{ else unit}) = \text{call1 } c' \text{ by auto}$   
**moreover from**  $IH[OF \text{red}] \text{ bsok}$   
**obtain**  $pc'' \ stk'' \ loc'' \ xcp''$  **where**  $\text{bisim}: P, c, h' \vdash (b', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$   
**and**  $\text{redo}: ?\text{exec } ta \ c \ c' \ b' \ h \ stk \ loc \ pc \ xcp \ h' \ pc'' \ stk'' \ loc'' \ xcp'' \text{ by auto}$   
**from** *bisim*  
**have**  $P, \text{while } (c) \ e, h' \vdash (\text{if } (b') (e;; \text{while } (c) e) \text{ else unit}, xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$   
**by**(rule *bisim1-bisims1.bisim1While3*)  
**moreover** {  
**assume** *no-call2 c pc*  
**hence** *no-call2* (*while* (*c*) *e*) *pc* **by**(*auto simp add: no-call2-def*) }  
**ultimately show** *?thesis* **using** *redo*  
**by**(*auto simp del: call1.simps calls1.simps split: if-split-asm split del: if-split*)(*blast intro: While- $\tau$ ExecrI1 While- $\tau$ ExectI1 exec-move-WhileI1*) +  
**next**  
**case** *Red1CondT*  
**note**  $[simp] = \langle c' = \text{true} \rangle \langle e' = e \rangle \langle \text{while } (c) e \rangle \langle ta = \varepsilon \rangle \langle h' = h \rangle \langle xs' = xs \rangle$   
**from** *bisim1* **have**  $s: xcp = \text{None } xs = loc$   
**and**  $\tau\text{Exec-mover-a } P \ t \ c \ h \ (stk, loc, pc, xcp) \ ([\text{Bool True}], loc, \text{length } (\text{compE2 } c), \text{None})$   
**by**(*auto dest: bisim1Val2D1*)  
**hence**  $\tau\text{Exec-mover-a } P \ t \ (\text{while } (c) e) \ h \ (stk, loc, pc, xcp) \ ([\text{Bool True}], loc, \text{length } (\text{compE2 } c), \text{None})$   
**by**-(rule *While- $\tau$ ExecrI1*)  
**moreover have**  $\text{exec-move-a } P \ t \ (\text{while } (c) e) \ h \ ([\text{Bool True}], loc, \text{length } (\text{compE2 } c), \text{None}) \varepsilon \ h$   
 $([], loc, \text{Suc } (\text{length } (\text{compE2 } c)), \text{None})$   
**unfolding** *exec-move-def* **by**(rule *exec-instr, auto*)  
**moreover have**  $\tau\text{move2 } (\text{compP2 } P) \ h \ [\text{Bool True}] \ (\text{while } (c) e) \ (\text{length } (\text{compE2 } c)) \ \text{None} \text{ by}(simp \text{ add: } \tau\text{move2-iff})$   
**ultimately have**  $\tau\text{Exec-movet-a } P \ t \ (\text{while } (c) e) \ h \ (stk, loc, pc, xcp) \ ([], loc, \text{Suc } (\text{length } (\text{compE2 } c)), \text{None})$   
**by**(*auto intro: rtranclp-into-tranclp1  $\tau$ exec-moveI simp add: compP2-def*)  
**moreover have**  $\tau\text{move1 } P \ h \ (\text{if } (c') (e;; \text{while } (c) e) \text{ else unit}) \text{ by}(\text{auto simp add: } \tau\text{move1.simps})$

```

τmoves1.simps)
  moreover from bisim1-refl
  have P, while (c) e, h ⊢ (e;; while (c) e, xs) ↔ ([], loc, Suc (length (compE2 c) + 0), None)
    unfolding s by(rule bisim1While4)
  ultimately show ?thesis by (fastforce)
next
case Red1CondF
note [simp] = ⟨c' = false⟩ ⟨e' = unit⟩ ⟨ta = ε⟩ ⟨h' = h⟩ ⟨xs' = xs⟩
from bisim1 have s: xcp = None xs = loc
  and τExec-mover-a P t c h (stk, loc, pc, xcp) ([Bool False], loc, length (compE2 c), None)
  by(auto dest: bisim1Val2D1)
hence τExec-mover-a P t (while (c) e) h (stk, loc, pc, xcp) ([Bool False], loc, length (compE2 c),
None)
  by-(rule While-τExecrI1)
moreover have exec-move-a P t (while (c) e) h ([Bool False], loc, length (compE2 c), None) ∈ h
([], loc, Suc (Suc (Suc (length (compE2 c) + length (compE2 e))))), None)
  by(auto intro!: exec-instr simp add: exec-move-def)
moreover have τmove2 (compP2 P) h [Bool False] (while (c) e) (length (compE2 c)) None
by(simp add: τmove2-iff)
ultimately have τExec-mover-a P t (while (c) e) h (stk, loc, pc, xcp) ([], loc, Suc (Suc (Suc
(length (compE2 c) + length (compE2 e))))), None)
  by(auto intro: rtranclp.rtrancl-into-rtrancl τexec-moveI simp add: compP2-def)
moreover have τmove1 P h (if (false) (e;;while (c) e) else unit) by(rule τmove1CondRed)
moreover have P, while (c) e, h ⊢ (unit, xs) ↔ ([], loc, (Suc (Suc (Suc (length (compE2 c) +
length (compE2 e))))), None)
  unfolding s by(rule bisim1While7)
ultimately show ?thesis using s by auto
next
case (Cond1Throw a)
note [simp] = ⟨c' = Throw a⟩ ⟨ta = ε⟩ ⟨e' = Throw a⟩ ⟨h' = h⟩ ⟨xs' = xs⟩
have τ: τmove1 P h (if (c') (e;; while (c) e) else unit) by(auto intro: τmove1CondThrow)
from bisim1 have xcp = [a] ∨ xcp = None by(auto dest: bisim1-ThrowD)
thus ?thesis
proof
  assume [simp]: xcp = [a]
  with bisim1
  have P, while (c) e, h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])
    by(auto intro: bisim1-bisims1.bisim1WhileThrow1)
  thus ?thesis using τ by(fastforce)
next
  assume [simp]: xcp = None
  with bisim1 obtain pc'
  where τExec-mover-a P t c h (stk, loc, pc, None) ([Addr a], loc, pc', [a])
  and bisim': P, c, h ⊢ (Throw a, xs) ↔ ([Addr a], loc, pc', [a]) and [simp]: xs = loc
  by(auto dest: bisim1-Throw-τExec-mover)
hence τExec-mover-a P t (while (c) e) h (stk, loc, pc, None) ([Addr a], loc, pc', [a])
  by-(rule While-τExecrI1)
moreover from bisim'
have P, while (c) e, h ⊢ (Throw a, xs) ↔ ([Addr a], loc, pc', [a])
  by-(rule bisim1WhileThrow1, auto)
ultimately show ?thesis using τ by auto
qed
qed
next

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case (bisim1While4 E n e xs stk loc pc xcp c)
note IH = bisim1While4.IH(2)
note bisim2 =  $\langle P, E, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
note bisim1 =  $\langle \bigwedge xs. P, c, h \vdash (c, xs) \leftrightarrow ([], xs, 0, None) \rangle$ 
note bsok =  $\langle bsok (while (c) E) n \rangle$ 
from  $\langle True, P, t \vdash 1 \langle e;; while (c) E, (h, xs) \rangle -ta\rightarrow \langle e', (h', xs') \rangle \rangle$  show ?case
proof cases
  case (Seq1Red E')
    note [simp] =  $\langle e' = E'; while (c) E \rangle$ 
    and red =  $\langle True, P, t \vdash 1 \langle e, (h, xs) \rangle -ta\rightarrow \langle E', (h', xs') \rangle \rangle$ 
    from red have  $\tau: \tau move1 P h (e;; while (c) E) = \tau move1 P h e$  by (auto simp add:  $\tau move1.simps$ 
 $\tau moves1.simps$ )
    with IH[OF red] bsok obtain pc'' stk'' loc'' xcp''
      where bisim:  $P, E, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$ 
      and exec':  $?exec ta E e E' h stk loc pc xcp h' pc'' stk'' loc'' xcp''$  by auto
      have  $?exec ta (while (c) E) (e;; while (c) E) (E'; while (c) E) h stk loc (Suc (length (compE2 c) + pc))$ 
 $xcp h' (Suc (length (compE2 c) + pc'')) stk'' loc'' xcp''$ 
      proof (cases  $\tau move1 P h (e;; while (c) E)$ )
        case True
          with exec' show ?thesis using  $\tau$  by (fastforce intro: While- $\tau ExecrI2$  While- $\tau ExecI2$ )
        next
          case False
            with exec'  $\tau$  obtain pc' stk' loc' xcp'
              where e:  $\tau Exec-mover-a P t E h (stk, loc, pc, xcp) (stk', loc', pc', xcp')$ 
              and e':  $exec-move-a P t E h (stk', loc', pc', xcp') (extTA2JVM (compP2 P) ta) h' (stk'', loc'',$ 
 $pc'', xcp'')$ 
              and  $\tau'$ :  $\neg \tau move2 (compP2 P) h stk' E pc' xcp'$ 
              and call:  $(call1 e = None \vee no-call2 E pc \vee pc' = pc \wedge stk' = stk \wedge loc' = loc \wedge xcp' = xcp)$ 
by auto
              from e have  $\tau Exec-mover-a P t (while (c) E) h (stk, loc, Suc (length (compE2 c) + pc), xcp)$ 
 $(stk', loc', Suc (length (compE2 c) + pc'), xcp')$  by (rule While- $\tau ExecrI2$ )
              moreover
                from e' have  $exec-move-a P t (while (c) E) h (stk', loc', Suc (length (compE2 c) + pc'), xcp')$ 
 $(extTA2JVM (compP2 P) ta) h' (stk'', loc'', Suc (length (compE2 c) + pc''), xcp'')$ 
                by (rule exec-move-WhileI2)
              moreover from  $\tau' e'$  have  $\neg \tau move2 (compP2 P) h stk' (while (c) E) (Suc (length (compE2 c)$ 
 $+ pc')) xcp'$ 
              by (auto simp add:  $\tau move2-iff$ )
              moreover have  $call1 (e;; while (c) E) = call1 e$  by simp
              moreover have  $no-call2 E pc \implies no-call2 (while (c) E) (Suc (length (compE2 c) + pc))$ 
by (auto simp add: no-call2-def)
              ultimately show ?thesis using False call by (auto simp del: split-paired-Ex call1.simps calls1.simps)
            qed
            with bisim  $\tau$  show ?thesis by auto (blast intro: bisim1-bisims1.bisim1While4)+
          next
            case (Red1Seq v)
              note [simp] =  $\langle e = Val v \rangle \langle ta = \varepsilon \rangle \langle e' = while (c) E \rangle \langle h' = h \rangle \langle xs' = xs \rangle$ 
              from bisim2 have s:  $xcp = None$   $xs = loc$ 
                and  $\tau Exec-mover-a P t E h (stk, loc, pc, xcp) ([v], loc, length (compE2 E), None)$ 
by (auto dest: bisim1Val2D1)
              hence  $\tau Exec-mover-a P t (while (c) E) h (stk, loc, Suc (length (compE2 c) + pc), xcp) ([v], loc,$ 
 $Suc (length (compE2 c) + length (compE2 E)), None)$ 
by—(rule While- $\tau ExecrI2$ )
              moreover

```

**have** *exec-move-a*  $P \ t \ (while \ (c) \ E) \ h \ ([v], \ loc, \ Suc \ (length \ (compE2 \ c) + length \ (compE2 \ E)), \ None) \ \varepsilon \ h \ ([], \ loc, \ Suc \ (Suc \ (length \ (compE2 \ c) + length \ (compE2 \ E))), \ None)$   
**unfolding** *exec-move-def* **by**(*rule exec-instr*, *auto*)  
**moreover have**  $\tau move2 \ (compP2 \ P) \ h \ [v] \ (while \ (c) \ E) \ (Suc \ (length \ (compE2 \ c) + length \ (compE2 \ E))) \ None$  **by**(*simp add:  $\tau move2$ -iff*)  
**ultimately have**  $\tau Exec-movet-a \ P \ t \ (while \ (c) \ E) \ h \ (stk, \ loc, \ Suc \ (length \ (compE2 \ c) + pc), \ xcp) \ ([], \ loc, \ Suc \ (Suc \ (length \ (compE2 \ c) + length \ (compE2 \ E))), \ None)$   
**by**(*auto intro: rtrancplp-into-trancplp1  $\tau exec-moveI$  simp add: compP2-def*)  
**moreover**  
**have**  $P, \ while \ (c) \ E, \ h \vdash (while \ (c) \ E, \ xs) \leftrightarrow ([], \ xs, \ (Suc \ (Suc \ (length \ (compE2 \ c) + length \ (compE2 \ E))))), \ None)$   
**unfolding**  $s$  **by**(*rule bisim1While6*)  
**moreover have**  $\tau move1 \ P \ h \ (e;; \ while \ (c) \ E)$  **by**(*auto intro:  $\tau move1SeqRed$* )  
**ultimately show** *?thesis* **using**  $s$  **by**(*auto*)(*blast intro: trancplp-into-rtrancplp*)  
**next**  
**case** (*Seq1Throw a*)  
**note** [*simp*] =  $\langle e = Throw \ a \rangle \langle ta = \varepsilon \rangle \langle e' = Throw \ a \rangle \langle h' = h \rangle \langle xs' = xs \rangle$   
**have**  $\tau: \tau move1 \ P \ h \ (e;; \ while \ (c) \ E)$  **by**(*auto intro:  $\tau move1SeqThrow$* )  
**from** *bisim2* **have**  $xcp = [a] \vee xcp = None$  **by**(*auto dest: bisim1-ThrowD*)  
**thus** *?thesis*  
**proof**  
**assume** [*simp*]:  $xcp = [a]$   
**with** *bisim2*  
**have**  $P, \ while \ (c) \ E, \ h \vdash (Throw \ a, \ xs) \leftrightarrow (stk, \ loc, \ Suc \ (length \ (compE2 \ c) + pc), \ xcp)$   
**by**(*auto intro: bisim1WhileThrow2*)  
**thus** *?thesis* **using**  $\tau$  **by**(*fastforce*)  
**next**  
**assume** [*simp*]:  $xcp = None$   
**with** *bisim2* **obtain**  $pc'$   
**where**  $\tau Exec-mover-a \ P \ t \ E \ h \ (stk, \ loc, \ pc, \ None) \ ([Addr \ a], \ loc, \ pc', \ [a])$   
**and** *bisim'*:  $P, \ E, \ h \vdash (Throw \ a, \ xs) \leftrightarrow ([Addr \ a], \ loc, \ pc', \ [a])$  **and** [*simp*]:  $xs = loc$   
**by**(*auto dest: bisim1-Throw- $\tau Exec-mover$* )  
**hence**  $\tau Exec-mover-a \ P \ t \ (while \ (c) \ E) \ h \ (stk, \ loc, \ Suc \ (length \ (compE2 \ c) + pc), \ None) \ ([Addr \ a], \ loc, \ Suc \ (length \ (compE2 \ c) + pc'), \ [a])$   
**by**-(*rule While- $\tau ExecrI2$* )  
**moreover from** *bisim'*  
**have**  $P, \ while \ (c) \ E, \ h \vdash (Throw \ a, \ xs) \leftrightarrow ([Addr \ a], \ loc, \ Suc \ (length \ (compE2 \ c) + pc'), \ [a])$   
**by**-(*rule bisim1WhileThrow2, auto*)  
**ultimately show** *?thesis* **using**  $\tau$  **by** *auto*  
**qed**  
**qed**  
**next**  
**case** (*bisim1While6 c n e xs*)  
**note** *bisim1* =  $\langle \bigwedge xs. P, c, h \vdash (c, \ xs) \leftrightarrow ([], \ xs, \ 0, \ None) \rangle$   
**note** *bisim2* =  $\langle \bigwedge xs. P, e, h \vdash (e, \ xs) \leftrightarrow ([], \ xs, \ 0, \ None) \rangle$   
**from**  $\langle True, P, t \vdash 1 \ \langle while \ (c) \ e, (h, \ xs) \rangle -ta \rightarrow \langle e', (h', \ xs') \rangle \rangle$  **show** *?case*  
**proof** *cases*  
**case** *Red1While*  
**note** [*simp*] =  $\langle ta = \varepsilon \rangle \langle e' = if \ (c) \ (e;; \ while \ (c) \ e) \ else \ unit \rangle \langle h' = h \rangle \langle xs' = xs \rangle$   
**have**  $\tau move1 \ P \ h \ (while \ (c) \ e)$  **by**(*rule  $\tau move1WhileRed$* )  
**moreover**  
**have**  $P, while \ (c) \ e, h \vdash (if \ (c) \ (e;; \ while \ (c) \ e) \ else \ unit, \ xs) \leftrightarrow ([], \ xs, \ 0, \ None)$   
**by**(*rule bisim1-bisims1.bisim1While3[OF bisim1-refl]*)  
**moreover have**  $\tau Exec-movet-a \ P \ t \ (while \ (c) \ e) \ h \ ([], \ xs, \ Suc \ (Suc \ (length \ (compE2 \ c) + length \ (compE2 \ E))))$

```

(compE2 e))), None) ([], xs, 0, None)
  by(rule  $\tau$ Exec1step)(auto simp add: exec-move-def  $\tau$ move2-iff intro: exec-instr)
  ultimately show ?thesis by(fastforce)
qed
next
  case bisim1While7 thus ?case by fastforce
next
  case bisim1WhileThrow1 thus ?case by auto
next
  case bisim1WhileThrow2 thus ?case by auto
next
  case (bisim1Throw1 E n e xs stk loc pc xcp)
  note IH = bisim1Throw1.IH(2)
  note bisim =  $\langle P, E, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
  note red =  $\langle \text{True}, P, t \vdash 1 \langle \text{throw } e, (h, xs) \rangle -ta\rightarrow \langle e', (h', xs') \rangle \rangle$ 
  note bsok =  $\langle \text{bsok } (\text{throw } E) \ n \rangle$ 
  from red show ?case
  proof cases
    case (Throw1Red E')
    note [simp] =  $\langle e' = \text{throw } E' \rangle$ 
    and red =  $\langle \text{True}, P, t \vdash 1 \langle e, (h, xs) \rangle -ta\rightarrow \langle E', (h', xs') \rangle \rangle$ 
    from red have  $\tau\text{move1 } P \ h \ (\text{throw } e) = \tau\text{move1 } P \ h \ e$  by(auto simp add:  $\tau\text{move1.simps}$ 
 $\tau\text{moves1.simps}$ )
    moreover have  $\text{call1 } (\text{throw } e) = \text{call1 } e$  by auto
    moreover from IH[OF red] bsok
    obtain  $pc'' \ stk'' \ loc'' \ xcp''$  where bisim:  $P, E, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$ 
    and redo:  $?exec \ ta \ E \ e \ E' \ h \ stk \ loc \ pc \ xcp \ h' \ pc'' \ stk'' \ loc'' \ xcp''$  by auto
    from bisim
    have  $P, \text{throw } E, h' \vdash (\text{throw } E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$ 
    by(rule bisim1-bisims1.bisim1Throw1)
    moreover {
      assume no-call2 E pc
      hence no-call2 (throw E) pc by(auto simp add: no-call2-def) }
    ultimately show ?thesis using redo
    by(auto simp del: call1.simps calls1.simps split: if-split-asm split del: if-split)(blast intro:
    Throw- $\tau$ ExecrI Throw- $\tau$ ExecI exec-move-ThrowI)+
  next
    case Red1ThrowNull
    note [simp] =  $\langle e = \text{null} \rangle \langle ta = \varepsilon \rangle \langle e' = \text{THROW NullPointer} \rangle \langle h' = h \rangle \langle xs' = xs \rangle$ 
    from bisim have s:  $xcp = \text{None} \ xs = \text{loc}$ 
    and  $\tau\text{Exec-mover-a } P \ t \ E \ h \ (stk, loc, pc, xcp) \ ([\text{Null}], \text{loc}, \text{length } (\text{compE2 } E), \text{None})$ 
    by(auto dest: bisim1Val2D1)
    hence  $\tau\text{Exec-mover-a } P \ t \ (\text{throw } E) \ h \ (stk, loc, pc, xcp) \ ([\text{Null}], \text{loc}, \text{length } (\text{compE2 } E), \text{None})$ 
    by-(rule Throw- $\tau$ ExecrI)
    also have  $\tau\text{Exec-movet-a } P \ t \ (\text{throw } E) \ h \ ([\text{Null}], \text{loc}, \text{length } (\text{compE2 } E), \text{None}) \ ([\text{Null}], \text{loc},$ 
 $\text{length } (\text{compE2 } E), \lfloor \text{addr-of-sys-xcpt NullPointer} \rfloor)$ 
    by(rule  $\tau$ Exec1step)(auto intro: exec-instr  $\tau\text{move2-}\tau\text{moves2.intros}$  simp add: exec-move-def)
    also have  $P, \text{throw } E, h \vdash (\text{THROW NullPointer}, xs) \leftrightarrow ([\text{Null}], \text{loc}, \text{length } (\text{compE2 } E),$ 
 $\lfloor \text{addr-of-sys-xcpt NullPointer} \rfloor)$ 
    unfolding s by(rule bisim1ThrowNull)
    moreover have  $\tau\text{move1 } P \ h \ (\text{throw } e)$  by(auto intro:  $\tau\text{move1ThrowNull}$ )
    ultimately show ?thesis by auto
  next
    case (Throw1Throw a)

```

```

note [simp] = ⟨e = Throw a⟩ ⟨ta = ε⟩ ⟨e' = Throw a⟩ ⟨h' = h⟩ ⟨xs' = xs⟩
have τ: τmove1 P h (throw (Throw a)) by(rule τmove1ThrowThrow)
from bisim have xcp = [a] ∨ xcp = None by(auto dest: bisim1-ThrowD)
thus ?thesis
proof
  assume xcp = [a]
  with bisim show ?thesis using τ by(fastforce intro: bisim1ThrowThrow)
next
  assume [simp]: xcp = None
  from bisim obtain pc'
    where τExec-mover-a P t E h (stk, loc, pc, None) ([Addr a], loc, pc', [a])
    and bisim: P, E, h ⊢ (Throw a, xs) ↔ ([Addr a], loc, pc', [a]) and s: xs = loc
    by(auto dest: bisim1-Throw-τExec-mover)
  hence τExec-mover-a P t (throw E) h (stk, loc, pc, None) ([Addr a], loc, pc', [a])
    by -(rule Throw-τExecrI)
  moreover from bisim have P, throw E, h ⊢ (Throw a, xs) ↔ ([Addr a], loc, pc', [a])
    by(rule bisim1ThrowThrow)
  ultimately show ?thesis using τ by auto
qed
qed
next
  case bisim1Throw2 thus ?case by auto
next
  case bisim1ThrowNull thus ?case by auto
next
  case bisim1ThrowThrow thus ?case by auto
next
  case (bisim1Try E n e xs stk loc pc xcp e2 C' V)
  note IH = bisim1Try.IH(2)
  note bisim1 = ⟨P, E, h ⊢ (e, xs) ↔ (stk, loc, pc, xcp)⟩
  note bisim2 = ⟨∧xs. P, e2, h ⊢ (e2, xs) ↔ ([], xs, 0, None)⟩
  note red = ⟨True, P, t ⊢ 1 ⟨try e catch(C' V) e2, (h, xs)⟩ -ta→ ⟨e', (h', xs')⟩⟩
  note bsok = ⟨bsok (try E catch(C' V) e2) n⟩
  from red show ?case
proof cases
  case (Try1Red E')
  note [simp] = ⟨e' = try E' catch(C' V) e2⟩
  and red = ⟨True, P, t ⊢ 1 ⟨e, (h, xs)⟩ -ta→ ⟨E', (h', xs')⟩⟩
  from red have τmove1 P h (try e catch(C' V) e2) = τmove1 P h e by(auto simp add: τmove1.simps
τmoves1.simps)
  moreover have call1 (try e catch(C' V) e2) = call1 e by auto
  moreover from IH[OF red] bsok
  obtain pc'' stk'' loc'' xcp'' where bisim: P, E, h' ⊢ (E', xs') ↔ (stk'', loc'', pc'', xcp'')
  and redo: ?exec ta E e E' h stk loc pc xcp h' pc'' stk'' loc'' xcp'' by auto
  from bisim
  have P, try E catch(C' V) e2, h' ⊢ (try E' catch(C' V) e2, xs') ↔ (stk'', loc'', pc'', xcp'')
    by(rule bisim1-bisims1.bisim1Try)
  moreover {
    assume no-call2 E pc
    hence no-call2 (try E catch(C' V) e2) pc by(auto simp add: no-call2-def) }
  ultimately show ?thesis using redo
    by(auto simp del: call1.simps calls1.simps split: if-split-asm split del: if-split)(blast intro:
Try-τExecrI1 Try-τExectI1 exec-move-TryI1)+
next

```

```

case (Red1Try v)
note [simp] =  $\langle e = \text{Val } v \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Val } v \rangle \langle h' = h \rangle \langle xs' = xs \rangle$ 
have  $\tau$ :  $\tau\text{move1 } P \ h \ (try \ \text{Val } v \ catch(C' \ V) \ e2)$  by(rule  $\tau\text{move1TryRed}$ )
from bisim1 have s: xcp = None xs = loc
  and  $\tau\text{Exec-mover-a } P \ t \ E \ h \ (stk, loc, pc, xcp) \ ([v], loc, length \ (compE2 \ E), None)$ 
  by(auto dest: bisim1Val2D1)
hence  $\tau\text{Exec-mover-a } P \ t \ (try \ E \ catch(C' \ V) \ e2) \ h \ (stk, loc, pc, xcp) \ ([v], loc, length \ (compE2 \ E), None)$ 
  by-(rule Try- $\tau\text{ExecrI1}$ )
also have  $\tau\text{Exec-mover-a } P \ t \ (try \ E \ catch(C' \ V) \ e2) \ h \ ([v], loc, length \ (compE2 \ E), None) \ ([v], loc, length \ (compE2 \ (try \ E \ catch(C' \ V) \ e2)), None)$ 
  by(rule  $\tau\text{Execr1step}$ )(auto intro: exec-instr simp add: exec-move-def  $\tau\text{move2-iff}$ )
also (rtranclp-trans)
have P, try E catch(C' V) e2,  $h \vdash (\text{Val } v, xs) \leftrightarrow ([v], xs, length \ (compE2 \ (try \ E \ catch(C' \ V) \ e2)), None)$ 
  by(rule bisim1Val2) simp
ultimately show ?thesis using s  $\tau$  by(auto)
next
case (Red1TryCatch a D)
hence [simp]:  $e = \text{Throw } a \ ta = \varepsilon \ e' = \{V:\text{Class } C'=\text{None}; e2\} \ h' = h \ xs' = xs[V := \text{Addr } a]$ 
  and ha: typeof-addr h a =  $\lfloor \text{Class-type } D \rfloor$  and sub:  $P \vdash D \preceq^* C'$ 
  and  $V: V < length \ xs$  by auto
from bisim1 have [simp]: xs = loc and xcp: xcp =  $\lfloor a \rfloor \vee xcp = \text{None}$ 
  by(auto dest: bisim1-ThrowD)
from xcp have  $\tau\text{Exec-mover-a } P \ t \ (try \ E \ catch(C' \ V) \ e2) \ h \ (stk, loc, pc, xcp) \ ([\text{Addr } a], loc, \text{Suc} \ (length \ (compE2 \ E)), None)$ 
proof
  assume [simp]: xcp =  $\lfloor a \rfloor$ 
  with bisim1 have match-ex-table (compP2 P) (cname-of h a) pc (compxE2 E 0 0) = None
    by(auto dest: bisim1-xcp-Some-not-caught[where pc'=0] simp add: compP2-def)
  moreover from bisim1 have pc < length (compE2 E)
    by(auto dest: bisim1-ThrowD)
  ultimately show ?thesis using ha sub unfolding  $\langle xcp = \lfloor a \rfloor \rangle$ 
    by-(rule  $\tau\text{Execr1step}[\text{unfolded exec-move-def, OF exec-catch[where } d=0, \text{simplified}]$ ],
      auto simp add:  $\tau\text{move2-iff matches-ex-entry-def compP2-def match-ex-table-append-not-pcs cname-of-def}$ )
next
assume [simp]: xcp = None
with bisim1 obtain pc' where  $\tau\text{Exec-mover-a } P \ t \ E \ h \ (stk, loc, pc, None) \ ([\text{Addr } a], loc, pc', \lfloor a \rfloor)$ 
  and bisim': P, E, h  $\vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], loc, pc', \lfloor a \rfloor)$  and s: xs = loc
    by(auto dest: bisim1-Throw- $\tau\text{Exec-mover}$ )
hence  $\tau\text{Exec-mover-a } P \ t \ (try \ E \ catch(C' \ V) \ e2) \ h \ (stk, loc, pc, None) \ ([\text{Addr } a], loc, pc', \lfloor a \rfloor)$ 
  by-(rule Try- $\tau\text{ExecrI1}$ )
also from bisim' have match-ex-table (compP2 P) (cname-of h a) pc' (compxE2 E 0 0) = None
  by(auto dest: bisim1-xcp-Some-not-caught[where pc'=0] simp add: compP2-def)
with ha sub bisim1-ThrowD[OF bisim']
have  $\tau\text{Exec-mover-a } P \ t \ (try \ E \ catch(C' \ V) \ e2) \ h \ ([\text{Addr } a], loc, pc', \lfloor a \rfloor) \ ([\text{Addr } a], loc, \text{Suc} \ (length \ (compE2 \ E)), None)$ 
  by-(rule  $\tau\text{Execr1step}[\text{unfolded exec-move-def, OF exec-catch[where } d=0, \text{simplified}]$ ], auto simp add:  $\tau\text{move2-iff matches-ex-entry-def compP2-def match-ex-table-append-not-pcs cname-of-def}$ )
  finally (rtranclp-trans) show ?thesis by simp
qed
also let ?pc' =  $\text{Suc} \ (length \ (compE2 \ E))$  from V

```



**have**  $\text{exec}: \tau \text{Exec-mover-a } P \ t \ (\text{try } E \ \text{catch}(C' \ V) \ e2) \ h \ ([\text{Addr } a], \text{loc}, \ ?pc', \text{None}) \ ([], \text{loc}[V := \text{Addr } a], \text{Suc } ?pc', \text{None})$   
**by**—( $\text{rule } \tau \text{Exec1step}[\text{unfolded exec-move-def}, \text{OF exec-instr}], \text{auto simp add: nth-append intro: } \tau \text{move2-}\tau \text{moves2.intros}$ )  
**also** ( $\text{rtrancpl-trancpl-trancpl}$ )  
**have**  $\text{bisim}': P, \text{try } E \ \text{catch}(C' \ V) \ e2, h \vdash (\{V:\text{Class } C'=\text{None}; e2\}, xs[V := \text{Addr } a]) \leftrightarrow ([], \text{loc}[V := \text{Addr } a], \text{Suc } ?pc', \text{None})$   
**unfolding**  $\langle xs = \text{loc} \rangle$  **by**( $\text{rule bisim1TryCatch2}[\text{OF bisim1-refl, simplified}]$ )  
**moreover have**  $\tau \text{move1 } P \ h \ (\text{try } \text{Throw } a \ \text{catch}(C' \ V) \ e2) \ \text{by}(\text{rule } \tau \text{move1TryThrow})$   
**ultimately show**  $?thesis$  **by**( $\text{auto}$ )( $\text{blast intro: trancpl-into-rtrancpl}$ )  
**next**  
**case** ( $\text{Red1TryFail } a \ D$ )  
**hence**  $[\text{simp}]: e = \text{Throw } a \ \text{ta} = \varepsilon \ e' = \text{Throw } a \ h' = h \ xs' = xs$   
**and**  $ha: \text{typeof-addr } h \ a = \lfloor \text{Class-type } D \rfloor$  **and**  $\text{sub}: \neg P \vdash D \preceq^* C'$  **by**  $\text{auto}$   
**have**  $\tau: \tau \text{move1 } P \ h \ (\text{try } \text{Throw } a \ \text{catch}(C' \ V) \ e2) \ \text{by}(\text{rule } \tau \text{move1TryThrow})$   
**from**  $\text{bisim1}$  **have**  $[\text{simp}]: xs = \text{loc}$  **and**  $xcp = \lfloor a \rfloor \vee xcp = \text{None}$  **by**( $\text{auto dest: bisim1-ThrowD}$ )  
**from**  $\text{bisim1}$  **have**  $pc: pc \leq \text{length } (\text{compE2 } E) \ \text{by}(\text{rule bisim1-pc-length-compE2})$   
**from**  $\langle xcp = \lfloor a \rfloor \vee xcp = \text{None} \rangle$  **show**  $?thesis$   
**proof**  
**assume**  $[\text{simp}]: xcp = \lfloor a \rfloor$   
**with**  $\text{bisim1 } ha \ \text{sub}$   
**have**  $P, \text{try } E \ \text{catch}(C' \ V) \ e2, h \vdash (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, pc, \lfloor a \rfloor)$   
**by**( $\text{auto intro: bisim1TryFail}$ )  
**thus**  $?thesis$  **using**  $\tau$  **by**( $\text{fastforce}$ )  
**next**  
**assume**  $[\text{simp}]: xcp = \text{None}$   
**with**  $\text{bisim1}$  **obtain**  $pc'$   
**where**  $\tau \text{Exec-mover-a } P \ t \ E \ h \ (\text{stk}, \text{loc}, pc, \text{None}) \ ([\text{Addr } a], \text{loc}, pc', \lfloor a \rfloor)$   
**and**  $\text{bisim}': P, E, h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], \text{loc}, pc', \lfloor a \rfloor)$   
**by**( $\text{auto dest: bisim1-Throw-}\tau \text{Exec-mover}$ )  
**hence**  $\tau \text{Exec-mover-a } P \ t \ (\text{try } E \ \text{catch}(C' \ V) \ e2) \ h \ (\text{stk}, \text{loc}, pc, \text{None}) \ ([\text{Addr } a], \text{loc}, pc', \lfloor a \rfloor)$   
**by**—( $\text{rule Try-}\tau \text{ExecrI1}$ )  
**moreover from**  $\text{bisim}' \ ha \ \text{sub}$   
**have**  $P, \text{try } E \ \text{catch}(C' \ V) \ e2, h \vdash (\text{Throw } a, xs) \leftrightarrow ([\text{Addr } a], \text{loc}, pc', \lfloor a \rfloor)$   
**by**( $\text{auto intro: bisim1TryFail}$ )  
**ultimately show**  $?thesis$  **using**  $\tau$  **by**  $\text{auto}$   
**qed**  
**qed**  
**next**  
**case** ( $\text{bisim1TryCatch1 } e \ n \ a \ xs \ \text{stk} \ \text{loc} \ pc \ D \ C' \ e2 \ V$ )  
**note**  $\text{bisim1} = \langle P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, pc, \lfloor a \rfloor) \rangle$   
**note**  $\text{bisim2} = \langle \bigwedge xs. P, e2, h \vdash (e2, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$   
**note**  $\text{IH2} = \text{bisim1TryCatch1.IH}(6)$   
**note**  $ha = \langle \text{typeof-addr } h \ a = \lfloor \text{Class-type } D \rfloor \rangle$   
**note**  $\text{sub} = \langle P \vdash D \preceq^* C' \rangle$   
**note**  $\text{red} = \langle \text{True}, P, t \vdash 1 \ \langle \{V:\text{Class } C'=\text{None}; e2\}, (h, xs[V := \text{Addr } a]) \rangle - \text{ta} \rightarrow \langle e', (h', xs') \rangle \rangle$   
**note**  $\text{bsok} = \langle \text{bsok } (\text{try } e \ \text{catch}(C' \ V) \ e2) \ n \rangle$   
**from**  $\text{bisim1}$  **have**  $[\text{simp}]: xs = \text{loc}$  **by**( $\text{auto dest: bisim1-ThrowD}$ )  
**from**  $\text{red}$  **show**  $?case$   
**proof cases**  
**case** ( $\text{Block1Red } E'$ )  
**note**  $[\text{simp}] = \langle e' = \{V:\text{Class } C'=\text{None}; E'\} \rangle$   
**and**  $\text{red} = \langle \text{True}, P, t \vdash 1 \ \langle e2, (h, xs[V := \text{Addr } a]) \rangle - \text{ta} \rightarrow \langle E', (h', xs') \rangle \rangle$   
**from**  $\text{red}$  **have**  $\tau: \tau \text{move1 } P \ h \ \{V:\text{Class } C'=\text{None}; e2\} = \tau \text{move1 } P \ h \ e2$  **by**( $\text{auto simp add:}$

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τmove1.simps τmoves1.simps)
  have exec: τExec-mover-a P t (try e catch(C' V) e2) h ([Addr a], xs, Suc (length (compE2 e) + 0), None) ([], xs[V := Addr a], Suc (Suc (length (compE2 e) + 0)), None)
    by -(rule τExecr1step, auto simp add: exec-move-def τmove2-iff intro: exec-instr)
  moreover from IH2[OF red] bsok obtain pc'' stk'' loc'' xcp''
    where bisim': P, e2, h' ⊢ (E', xs') ↔ (stk'', loc'', pc'', xcp'')
    and exec': ?exec ta e2 e2 E' h [] (xs[V := Addr a]) 0 None h' pc'' stk'' loc'' xcp'' by auto
  have ?exec ta (try e catch(C' V) e2) {V:Class C'=None; e2} {V:Class C'=None; E'} h [] (xs[V := Addr a]) (Suc (Suc (length (compE2 e)))) None h' (Suc (Suc (length (compE2 e) + pc''))) stk'' loc'' xcp''
  proof(cases τmove1 P h {V:Class C'=None; e2})
    case True with τ exec' show ?thesis
      by(fastforce dest: Try-τExecrI2 Try-τExecI2 simp del: compE2.simps compEs2.simps)
    next
      case False
      with τ exec' obtain pc' stk' loc' xcp'
        where e: τExec-mover-a P t e2 h ([], xs[V := Addr a], 0, None) (stk', loc', pc', xcp')
        and e': exec-move-a P t e2 h (stk', loc', pc', xcp') (extTA2JVM (compP2 P) ta) h' (stk'', loc'', pc'', xcp'')
        and τ': ¬ τmove2 (compP2 P) h stk' e2 pc' xcp'
        and call: call1 e2 = None ∨ no-call2 e2 0 ∨ pc' = 0 ∧ stk' = [] ∧ loc' = xs[V := Addr a] ∧ xcp' = None by auto
      from e have τExec-mover-a P t (try e catch(C' V) e2) h ([], xs[V := Addr a], Suc (Suc (length (compE2 e) + 0)), None) (stk', loc', Suc (Suc (length (compE2 e) + pc')), xcp')
        by(rule Try-τExecrI2)
      moreover from e'
        have exec-move-a P t (try e catch(C' V) e2) h (stk', loc', Suc (Suc (length (compE2 e) + pc')), xcp') (extTA2JVM (compP2 P) ta) h' (stk'', loc'', Suc (Suc (length (compE2 e) + pc'')), xcp'')
          by(rule exec-move-TryI2)
        moreover from τ' have τmove2 (compP2 P) h stk' (try e catch(C' V) e2) (Suc (Suc (length (compE2 e) + pc'))) xcp' ⇒ False
          by(simp add: τmove2-iff)
        moreover have call1 {V:Class C'=None; e2} = call1 e2 by simp
        moreover have no-call2 e2 0 ⇒ no-call2 (try e catch(C' V) e2) (Suc (Suc (length (compE2 e)))) by auto
      by(auto simp add: no-call2-def)
      ultimately show ?thesis using False call by(auto simp del: split-paired-Ex call1.simps calls1.simps)
    blast
  qed
  moreover from bisim'
    have P, try e catch(C' V) e2, h' ⊢ ({V:Class C'=None; E'}, xs') ↔ (stk'', loc'', Suc (Suc (length (compE2 e) + pc'')), xcp'')
      by(rule bisim1TryCatch2)
    moreover have no-call2 (try e catch(C' V) e2) (Suc (length (compE2 e))) by(simp add: no-call2-def)
  ultimately show ?thesis using τ
    by auto(blast intro: rtranclp-trans rtranclp-tranclp-tranclp)+
  next
    case (Red1Block u)
    note [simp] = ⟨e2 = Val u⟩ ⟨ta = ε⟩ ⟨e' = Val u⟩ ⟨h' = h⟩ ⟨xs' = xs[V := Addr a]⟩
    have τExec-mover-a P t (try e catch(C' V) Val u) h ([Addr a], xs, Suc (length (compE2 e) + 0), None) ([], xs[V := Addr a], Suc (Suc (length (compE2 e) + 0)), None)
      by -(rule τExecr1step, auto simp add: exec-move-def τmove2-iff intro: exec-instr)
    also have τExec-mover-a P t (try e catch(C' V) Val u) h ([], xs[V := Addr a], Suc (Suc (length

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$(\text{compE2 } e) + 0)), \text{None}) ([u], xs[V := \text{Addr } a], \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e) + 1)), \text{None})$   
**by**  $-(\text{rule Try-}\tau\text{ExecrI2}[OF \tau\text{Execr1step}[\text{unfolded exec-move-def}, OF \text{exec-instr}], \text{auto simp add: } \tau\text{move2-iff})$   
**also**  $(\text{rtrancplp-trans})$   
**have**  $P, \text{try } e \text{ catch}(C' V) \text{ Val } u, h \vdash (\text{Val } u, xs[V := \text{Addr } a]) \leftrightarrow ([u], xs[V := \text{Addr } a], \text{length } (\text{compE2 } (\text{try } e \text{ catch}(C' V) \text{ Val } u)), \text{None})$   
**by** $(\text{rule bisim1Val2}) \text{ simp}$   
**moreover have**  $\tau\text{move1 } P h \{V:\text{Class } C'=\text{None}; \text{Val } u\}$  **by** $(\text{rule } \tau\text{move1BlockRed})$   
**ultimately show**  $?thesis$  **by** $(\text{auto})$   
**next**  
**case**  $(\text{Block1Throw } a')$   
**note**  $[simp] = \langle e2 = \text{Throw } a' \rangle \langle h' = h \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw } a' \rangle \langle xs' = xs[V := \text{Addr } a] \rangle$   
**have**  $\tau\text{move1 } P h \{V:\text{Class } C'=\text{None}; \text{Throw } a'\}$  **by** $(\text{rule } \tau\text{move1BlockThrow})$   
**moreover have**  $\tau\text{Exec-mover-a } P t (\text{try } e \text{ catch}(C' V) e2) h ([\text{Addr } a], \text{loc}, \text{Suc } (\text{length } (\text{compE2 } e)), \text{None})$   
 $([\text{Addr } a'], xs', \text{Suc } (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e))))), [a']$   
**by** $(\text{rule } \tau\text{Execr3step})(\text{auto simp add: exec-move-def exec-meth-instr } \tau\text{move2-iff})$   
**moreover have**  $P, \text{try } e \text{ catch}(C' V) \text{ Throw } a', h \vdash (\text{Throw } a', xs') \leftrightarrow ([\text{Addr } a'], xs', \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e) + \text{length } (\text{compE2 } (\text{addr } a')))), [a'])$   
**by** $(\text{rule bisim1TryCatchThrow})(\text{rule bisim1Throw2})$   
**ultimately show**  $?thesis$  **by**  $\text{auto}$   
**qed**  
**next**  
**case**  $(\text{bisim1TryCatch2 } e2 n e2' xs \text{ stk loc pc xcp } e C' V)$   
**note**  $\text{bisim2} = \langle P, e2, h \vdash (e2', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \rangle$   
**note**  $\text{bisim1} = \langle \bigwedge xs. P, e, h \vdash (e, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$   
**note**  $\text{IH2} = \text{bisim1TryCatch2.IH}(2)$   
**note**  $\text{red} = \langle \text{True}, P, t \vdash 1 \langle \{V:\text{Class } C'=\text{None}; e2'\rangle, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \rangle$   
**note**  $\text{bsok} = \langle \text{bsok } (\text{try } e \text{ catch}(C' V) e2) n \rangle$   
**from red show**  $?case$   
**proof cases**  
**case**  $(\text{Block1Red } E')$   
**note**  $[simp] = \langle e' = \{V:\text{Class } C'=\text{None}; E'\} \rangle$   
**and**  $\text{red} = \langle \text{True}, P, t \vdash 1 \langle e2', (h, xs) \rangle -ta \rightarrow \langle E', (h', xs') \rangle \rangle$   
**from red have**  $\tau: \tau\text{move1 } P h \{V:\text{Class } C'=\text{None}; e2'\} = \tau\text{move1 } P h e2'$  **by** $(\text{auto simp add: } \tau\text{move1.simps } \tau\text{moves1.simps})$   
**from IH2** $[OF \text{red}] \text{ bsok}$  **obtain**  $pc'' \text{ stk'' loc'' xcp''}$   
**where**  $\text{bisim}' : P, e2, h' \vdash (E', xs') \leftrightarrow (\text{stk'', loc'', pc'', xcp''})$   
**and**  $\text{exec}' : ?\text{exec } ta e2 e2' E' h \text{ stk loc pc xcp } h' pc'' \text{ stk'' loc'' xcp''}$  **by**  $\text{auto}$   
**have**  $?\text{exec } ta (\text{try } e \text{ catch}(C' V) e2) \{V:\text{Class } C'=\text{None}; e2'\} \{V:\text{Class } C'=\text{None}; E'\} h \text{ stk loc } (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e) + \text{pc}))) \text{ xcp } h' (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e) + \text{pc''}))) \text{ stk'' loc'' xcp''}$   
**proof**  $(\text{cases } \tau\text{move1 } P h \{V:\text{Class } C'=\text{None}; e2'\})$   
**case**  $\text{True}$  **with**  $\tau \text{ exec}'$  **show**  $?thesis$  **by** $(\text{auto intro: Try-}\tau\text{ExecrI2 Try-}\tau\text{ExecrI2})$   
**next**  
**case**  $\text{False}$   
**with**  $\tau \text{ exec}'$  **obtain**  $pc' \text{ stk' loc' xcp'}$   
**where**  $e: \tau\text{Exec-mover-a } P t e2 h (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) (\text{stk}', \text{loc}', \text{pc}', \text{xcp}')$   
**and**  $e': \text{exec-move-a } P t e2 h (\text{stk}', \text{loc}', \text{pc}', \text{xcp}') (\text{extTA2JVM } (\text{compP2 } P) ta) h' (\text{stk'', loc'', pc'', xcp''})$   
**and**  $\tau': \neg \tau\text{move2 } (\text{compP2 } P) h \text{ stk' } e2 \text{ pc' xcp'}$   
**and**  $\text{call: call1 } e2' = \text{None} \vee \text{no-call2 } e2 \text{ pc} \vee \text{pc}' = \text{pc} \wedge \text{stk}' = \text{stk} \wedge \text{loc}' = \text{loc} \wedge \text{xcp}' = \text{xcp}$   
**by**  $\text{auto}$   
**from**  $e$  **have**  $\tau\text{Exec-mover-a } P t (\text{try } e \text{ catch}(C' V) e2) h (\text{stk}, \text{loc}, \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e) + \text{pc})), \text{xcp}) (\text{stk}', \text{loc}', \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e) + \text{pc'})), \text{xcp'})$

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    by(rule Try- $\tau$ ExecrI2)
  moreover from e'
  have exec-move-a P t (try e catch(C' V) e2) h (stk', loc', Suc (Suc (length (compE2 e) + pc')),
  xcp') (extTA2JVM (compP2 P) ta) h' (stk'', loc'', Suc (Suc (length (compE2 e) + pc'')), xcp'')
    by(rule exec-move-TryI2)
  moreover from  $\tau'$  have  $\tau$ move2 (compP2 P) h stk' (try e catch(C' V) e2) (Suc (Suc (length
  (compE2 e) + pc'))) xcp'  $\implies$  False
    by(simp add:  $\tau$ move2-iff)
  moreover have call1 {V:Class C'=None; e2'} = call1 e2' by simp
  moreover have no-call2 e2 pc  $\implies$  no-call2 (try e catch(C' V) e2) (Suc (Suc (length (compE2
  e) + pc)))
    by(auto simp add: no-call2-def)
  ultimately show ?thesis using False call by(auto simp del: split-paired-Ex call1.simps calls1.simps)
  qed
  moreover from bisim'
  have P, try e catch(C' V) e2, h'  $\vdash$  ({V:Class C'=None; E'}, xs')  $\leftrightarrow$  (stk'', loc'', Suc (Suc (length
  (compE2 e) + pc'')), xcp'')
    by(rule bisim1-bisims1.bisim1TryCatch2)
  ultimately show ?thesis using  $\tau$  by auto blast+
next
case (Red1Block u)
note [simp] =  $\langle e2' = \text{Val } u \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Val } u \rangle \langle h' = h \rangle \langle xs' = xs \rangle$ 
from bisim2 have s: xcp = None xs = loc
  and  $\tau$ Exec-mover-a P t e2 h (stk, loc, pc, xcp) ([u], loc, length (compE2 e2), None)
  by(auto dest: bisim1Val2D1)
hence  $\tau$ Exec-mover-a P t (try e catch(C' V) e2) h (stk, loc, Suc (Suc (length (compE2 e) + pc)),
xcp) ([u], loc, Suc (Suc (length (compE2 e) + length (compE2 e2))), None)
  by  $\neg$ (rule Try- $\tau$ ExecrI2)
moreover
have P, try e catch(C' V) e2, h  $\vdash$  (Val u, xs)  $\leftrightarrow$  ([u], xs, length (compE2 (try e catch(C' V) e2)),
None)
  by(rule bisim1Val2) simp
moreover have  $\tau$ move1 P h {V:Class C'=None; Val u} by(rule  $\tau$ move1BlockRed)
ultimately show ?thesis using s by auto
next
case (Block1Throw a)
note [simp] =  $\langle e2' = \text{Throw } a \rangle \langle ta = \varepsilon \rangle \langle e' = \text{Throw } a \rangle \langle h' = h \rangle \langle xs' = xs \rangle$ 
have  $\tau$ :  $\tau$ move1 P h {V:Class C'=None; e2'} by(auto simp add:  $\tau$ move1.simps  $\tau$ moves1.simps)
from bisim2 have xcp = [a]  $\vee$  xcp = None by(auto dest: bisim1-ThrowD)
thus ?thesis
proof
  assume [simp]: xcp = [a]
  with bisim2
  have P, try e catch(C' V) e2, h  $\vdash$  (Throw a, xs)  $\leftrightarrow$  (stk, loc, Suc (Suc (length (compE2 e) +
  pc)), xcp)
    by(auto intro: bisim1TryCatchThrow)
  thus ?thesis using  $\tau$  by(fastforce)
next
  assume [simp]: xcp = None
  with bisim2 obtain pc'
  where  $\tau$ Exec-mover-a P t e2 h (stk, loc, pc, None) ([Addr a], loc, pc', [a])
  and bisim': P, e2, h  $\vdash$  (Throw a, xs)  $\leftrightarrow$  ([Addr a], loc, pc', [a]) and [simp]: xs = loc
  by(auto dest: bisim1-Throw- $\tau$ Exec-mover)
  hence  $\tau$ Exec-mover-a P t (try e catch(C' V) e2) h (stk, loc, Suc (Suc (length (compE2 e) +

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pc)), None) ([Addr a], loc, Suc (Suc (length (compE2 e) + pc')), [a])
  by-(rule Try- $\tau$ ExecrI2)
  moreover from bisim'
  have P, try e catch (C' V) e2, h  $\vdash$  (Throw a, xs)  $\leftrightarrow$  ([Addr a], loc, Suc (Suc (length (compE2 e)
+ pc')), [a])
  by(rule bisim1TryCatchThrow)
  ultimately show ?thesis using  $\tau$  by auto
qed
qed
next
case bisim1TryFail thus ?case by auto
next
case bisim1TryCatchThrow thus ?case by auto
next
case bisims1Nil thus ?case by(auto elim!: reds1.cases)
next
case (bisims1List1 E n e xs stk loc pc xcp es)
note IH1 = bisims1List1.IH(2)
note IH2 = bisims1List1.IH(4)
note bisim1 =  $\langle P, E, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
note bisim2 =  $\langle \bigwedge xs. P, es, h \vdash (es, xs) [\leftrightarrow] ([], xs, 0, None) \rangle$ 
note bsok =  $\langle bsoks (E \# es) n \rangle$ 
from  $\langle True, P, t \vdash 1 \langle e \# es, (h, xs) \rangle [-ta \rightarrow] \langle es', (h', xs') \rangle \rangle$  show ?case
proof cases
case (List1Red1 E')
note [simp] =  $\langle es' = E' \# es \rangle$ 
and red =  $\langle True, P, t \vdash 1 \langle e, (h, xs) \rangle [-ta \rightarrow] \langle E', (h', xs') \rangle \rangle$ 
from red have  $\tau: \tau moves1 P h (e \# es) = \tau move1 P h e$  by(auto simp add:  $\tau move1.simps$ 
 $\tau moves1.simps$ )
moreover from IH1[OF red] bsok
obtain pc'' stk'' loc'' xcp'' where bisim:  $P, E, h' \vdash (E', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$ 
and redo: ?exec ta E e E' h stk loc pc xcp h' pc'' stk'' loc'' xcp'' by auto
from bisim
have  $P, E \# es, h' \vdash (E' \# es, xs') [\leftrightarrow] (stk'', loc'', pc'', xcp'')$ 
by(rule bisim1-bisims1.bisims1List1)
moreover {
assume no-call2 E pc
hence no-calls2 (E # es) pc  $\vee$  pc = length (compE2 E) by(auto simp add: no-call2-def
no-calls2-def) }
moreover from red have calls1 (e # es) = call1 e by auto
ultimately show ?thesis using redo
apply(auto simp add: exec-move-def exec-moves-def simp del: call1.simps calls1.simps split:
if-split-asm split del: if-split)
apply(blast intro:  $\tau Exec-mover-\tau Exec-movesr \tau Exec-movet-\tau Exec-movest$  intro!: bisim1-bisims1.bisims1List1
elim:  $\tau moves2.cases$ ) +
done
next
case (List1Red2 ES' v)
note [simp] =  $\langle es' = Val v \# ES' \rangle \langle e = Val v \rangle$ 
and red =  $\langle True, P, t \vdash 1 \langle es, (h, xs) \rangle [-ta \rightarrow] \langle ES', (h', xs') \rangle \rangle$ 
from bisim1 have s: xs = loc xcp = None
and exec1:  $\tau Exec-mover-a P t E h (stk, loc, pc, xcp) ([v], loc, length (compE2 E), None)$ 
by(auto dest: bisim1Val2D1)
hence  $\tau Exec-movesr-a P t (E \# es) h (stk, loc, pc, xcp) ([v], loc, length (compE2 E), None)$ 

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    by  $\neg$ (rule  $\tau$ Exec-mover- $\tau$ Exec-movesr)
  moreover from IH2[OF red] bsok obtain  $pc''\ stk''\ loc''\ xcp''$ 
    where  $bisim'$ :  $P, es, h' \vdash (ES', xs') [\leftrightarrow] (stk'', loc'', pc'', xcp'')$ 
    and  $exec'$ :  $?execs\ ta\ es\ es\ ES'\ h\ []\ xs\ 0\ None\ h'\ pc''\ stk''\ loc''\ xcp''$  by auto
  have  $\tau$ :  $\tau moves1\ P\ h\ (Val\ v\ \# es) = \tau moves1\ P\ h\ es$  by (auto simp add:  $\tau move1.simps\ \tau moves1.simps$ )
  have  $?execs\ ta\ (E\ \# es)\ (Val\ v\ \# es)\ (Val\ v\ \# ES')\ h\ [v]\ xs\ (length\ (compE2\ E))\ None\ h'\ (length\ (compE2\ E) + pc'')\ (stk''\ @\ [v])\ loc''\ xcp''$ 
  proof (cases  $\tau moves1\ P\ h\ (Val\ v\ \# es)$ )
    case True with  $\tau\ exec'$  show ?thesis
      using append- $\tau$ Exec-movesr[of  $[v]\ [E] - P\ t\ es\ h\ []\ xs\ 0\ None\ stk''\ loc''\ pc''\ xcp''$ ]
      append- $\tau$ Exec-movest[of  $[v]\ [E] - P\ t\ es\ h\ []\ xs\ 0\ None\ stk''\ loc''\ pc''\ xcp''$ ] by auto
    next
      case False with  $\tau\ exec'$  obtain  $pc'\ stk'\ loc'\ xcp'$ 
        where  $e$ :  $\tau$ Exec-movesr-a  $P\ t\ es\ h\ ([], xs, 0, None)\ (stk', loc', pc', xcp')$ 
        and  $e'$ :  $exec-moves-a\ P\ t\ es\ h\ (stk', loc', pc', xcp')\ (extTA2JVM\ (compP2\ P)\ ta)\ h'\ (stk'', loc'', pc'', xcp'')$ 
        and  $\tau'$ :  $\neg\ \tau moves2\ (compP2\ P)\ h\ stk'\ es\ pc'\ xcp'$ 
        and  $call$ :  $calls1\ es = None \vee no-calls2\ es\ 0 \vee pc' = 0 \wedge stk' = [] \wedge loc' = xs \wedge xcp' = None$ 
      by auto
      from append- $\tau$ Exec-movesr[OF -  $e$ , where  $vs=[v]$  and  $es'=[E]$ ]
      have  $\tau$ Exec-movesr-a  $P\ t\ (E\ \# es)\ h\ ([v], xs, length\ (compE2\ E), None)\ (stk'\ @\ [v], loc', length\ (compE2\ E) + pc', xcp')$  by simp
      moreover from append-exec-moves[OF -  $e'$ , of  $[v]\ [E]$ ]
      have  $exec-moves-a\ P\ t\ (E\ \# es)\ h\ (stk'\ @\ [v], loc', length\ (compE2\ E) + pc', xcp')\ (extTA2JVM\ (compP2\ P)\ ta)\ h'\ (stk''\ @\ [v], loc'', length\ (compE2\ E) + pc'', xcp'')$ 
      by simp
      moreover from  $\tau'\ e'$ 
      have  $\tau moves2\ (compP2\ P)\ h\ (stk'\ @\ [v])\ (E\ \# es)\ (length\ (compE2\ E) + pc')\ xcp' \implies False$ 
      by (auto simp add:  $\tau moves2\ iff\ \tau instr-stk-drop-exec-moves$ )
      moreover have  $calls1\ (Val\ v\ \# es) = calls1\ es$  by simp
      moreover have  $no-calls2\ es\ 0 \implies no-calls2\ (E\ \# es)\ (length\ (compE2\ E))$ 
      by (auto simp add: no-calls2-def)
      ultimately show ?thesis using False call by (auto simp del: split-paired-Ex call1.simps calls1.simps)
  blast
qed
moreover from  $bisim'$ 
have  $P, E\ \# es, h' \vdash (Val\ v\ \# ES', xs') [\leftrightarrow] (stk''\ @\ [v], loc'', length\ (compE2\ E) + pc'', xcp'')$ 
  by (rule bisim1-bisims1.bisims1List2)
moreover from  $bisim1$  have  $pc \neq length\ (compE2\ E) \longrightarrow no-calls2\ (E\ \# es)\ pc$ 
  by (auto simp add: no-calls2-def dest: bisim-Val-pc-not-Invoke bisim1-pc-length-compE2)
ultimately show ?thesis using  $\tau\ exec1\ s$ 
  apply (auto simp del: split-paired-Ex call1.simps calls1.simps split: if-split-asm split del: if-split)
  apply (blast intro:  $\tau$ Exec-movesr-trans|fastforce elim!:  $\tau$ Exec-movesr-trans simp del: split-paired-Ex call1.simps calls1.simps)+
  done
qed
next
case (bisims1List2  $ES\ n\ es\ xs\ stk\ loc\ pc\ xcp\ e\ v$ )
note IH2 = bisims1List2.IH(2)
note  $bisim1 = \langle \bigwedge xs. P, e, h \vdash (e, xs) \leftrightarrow ([], xs, 0, None) \rangle$ 
note  $bisim2 = \langle P, ES, h \vdash (es, xs) [\leftrightarrow] (stk, loc, pc, xcp) \rangle$ 
note  $bsok = \langle bsoks\ (e\ \# ES)\ n \rangle$ 
from  $\langle True, P, t \vdash 1\ \langle Val\ v\ \# es, (h, xs) \rangle [-ta\rightarrow] \langle es', (h', xs') \rangle \rangle$  show ?case
proof cases

```

```

case (List1Red2 ES')
note [simp] = ⟨es' = Val v # ES'⟩
and red = ⟨True, P, t ⊢ 1 ⟨es, (h, xs)⟩ [-ta→] ⟨ES', (h', xs')⟩⟩
from IH2[OF red] bsok obtain pc'' stk'' loc'' xcp''
where bisim': P, ES, h' ⊢ (ES', xs') [↔] (stk'', loc'', pc'', xcp'')
and exec': ?execs ta ES es ES' h stk loc pc xcp h' pc'' stk'' loc'' xcp'' by auto
have τ: τmoves1 P h (Val v # es) = τmoves1 P h es by (auto simp add: τmove1.simps τmoves1.simps)
have ?execs ta (e # ES) (Val v # es) (Val v # ES') h (stk @ [v]) loc (length (compE2 e) + pc)
xcp h' (length (compE2 e) + pc'') (stk'' @ [v]) loc'' xcp''
proof (cases τmoves1 P h (Val v # es))
case True with τ exec' show ?thesis
using append-τExec-movesr[of [v] [e] - P t ES h stk]
append-τExec-movest[of [v] [e] - P t ES h stk] by auto
next
case False with τ exec' obtain pc' stk' loc' xcp'
where e: τExec-movesr-a P t ES h (stk, loc, pc, xcp) (stk', loc', pc', xcp')
and e': exec-moves-a P t ES h (stk', loc', pc', xcp') (extTA2JVM (compP2 P) ta) h' (stk'', loc'',
pc'', xcp'')
and τ': ¬ τmoves2 (compP2 P) h stk' ES pc' xcp'
and call: calls1 es = None ∨ no-calls2 ES pc ∨ pc' = pc ∧ stk' = stk ∧ loc' = loc ∧ xcp' = xcp
by auto
from append-τExec-movesr[OF - e, where vs=[v] and es' = [e]]
have τExec-movesr-a P t (e # ES) h (stk @ [v], loc, length (compE2 e) + pc, xcp) (stk' @ [v],
loc', length (compE2 e) + pc', xcp') by simp
moreover from append-exec-moves[OF - e', of [v] [e]]
have exec-moves-a P t (e # ES) h (stk' @ [v], loc', length (compE2 e) + pc', xcp') (extTA2JVM
(compP2 P) ta) h' (stk'' @ [v], loc'', length (compE2 e) + pc'', xcp'') by simp
moreover from τ' e'
have τmoves2 (compP2 P) h (stk' @ [v]) (e # ES) (length (compE2 e) + pc') xcp' ⇒ False
by (auto simp add: τmoves2-iff τinstr-stk-drop-exec-moves)
moreover have calls1 (Val v # es) = calls1 es by simp
moreover have no-calls2 ES pc ⇒ no-calls2 (e # ES) (length (compE2 e) + pc)
by (auto simp add: no-calls2-def)
ultimately show ?thesis using False call by (auto simp del: split-paired-Ex call1.simps calls1.simps)

qed
moreover from bisim'
have P, e # ES, h' ⊢ (Val v # ES', xs') [↔] (stk'' @ [v], loc'', length (compE2 e) + pc'', xcp'')
by (rule bisim1-bisims1.bisims1List2)
ultimately show ?thesis using τ by auto blast+
qed auto
qed
end

```

context J1-JVM-conf-read begin

lemma exec-1-simulates-Red1-τ:

```

assumes wf: wf-J1-prog P
and Red1: True, P, t ⊢ 1 ⟨(e, xs)/exs, h⟩ -ta→ ⟨(e', xs')/exs', h⟩
and bisim: bisim1-list1 t h (e, xs) exs xcp frs
and τ: τMove1 P h ((e, xs), exs)
shows ∃ xcp' frs'. (if sim12-size e' < sim12-size e then τExec-1-dr else τExec-1-dt) (compP2 P) t

```

$(xcp, h, frs) (xcp', h, frs') \wedge bisim1-list1\ t\ h\ (e', xs')\ exs'\ xcp'\ frs'$

**proof** –

**from**  $wf$  **have**  $wt: wf-jvm-prog_{compTP\ P}\ (compP2\ P)$  **by**  $(rule\ wt-compTP-compP2)$

**from**  $Red1$  **show**  $?thesis$

**proof**  $(cases)$

**case**  $(red1Red\ TA)$

**note**  $[simp] = \langle ta = extTA2J1\ P\ TA \rangle \langle exs' = exs \rangle$

**and**  $red = \langle True, P, t \vdash 1\ \langle e, (h, xs) \rangle - TA \rightarrow \langle e', (h, xs') \rangle \rangle$

**from**  $\tau\ red$  **have**  $\tau': \tau\ move1\ P\ h\ e$  **by**  $(auto\ elim: red1-cases)$

**from**  $bisim$  **show**  $?thesis$

**proof**  $(cases)$

**case**  $(bl1-Normal\ stk\ loc\ C\ M\ pc\ FRS\ Ts\ T\ body\ D)$

**hence**  $[simp]: frs = (stk, loc, C, M, pc) \# FRS$

**and**  $conf: compTP\ P \vdash t: (xcp, h, frs) \checkmark$

**and**  $sees: P \vdash C\ sees\ M: Ts \rightarrow T = \lfloor body \rfloor\ in\ D$

**and**  $bisim: P, blocks1\ 0\ (Class\ D \# Ts)\ body, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp)$

**and**  $bisims: list-all2\ (bisim1-fr\ P\ h)\ exs\ FRS$

**and**  $lenxs: max-vars\ e \leq length\ xs$

**by**  $auto$

**from**  $sees\ wf$  **have**  $bsok\ (blocks1\ 0\ (Class\ D \# Ts)\ body)\ 0$

**by**  $(auto\ dest!: sees-wf-mdecl\ WT1-expr-locks\ simp\ add: wf-J1-mdecl-def\ wf-mdecl-def\ bsok-def)$

**from**  $exec-instr-simulates-red1[OF\ wf\ bisim\ red\ this]\ \tau'$  **obtain**  $pc'\ stk'\ loc'\ xcp'$

**where**  $exec: (if\ sim12-size\ e' < sim12-size\ e\ then\ \tau Exec-mover-a\ else\ \tau Exec-movet-a)\ P\ t\ body\ h\ (stk, loc, pc, xcp)\ (stk', loc', pc', xcp')$

**and**  $b': P, blocks1\ 0\ (Class\ D \# Ts)\ body, h \vdash (e', xs') \leftrightarrow (stk', loc', pc', xcp')$

**by**  $(auto\ split: if-split-asm\ simp\ del: blocks1.simps)$

**from**  $exec\ sees$  **have**  $(if\ sim12-size\ e' < sim12-size\ e\ then\ \tau Exec-1r\ else\ \tau Exec-1t)\ (compP2\ P)\ t\ (xcp, h, frs)\ (xcp', h, (stk', loc', C, M, pc')) \# FRS$

**by**  $(auto\ intro: \tau Exec-mover-\tau Exec-1r\ \tau Exec-movet-\tau Exec-1t)$

**from**  $wt\ this\ conf$  **have**  $execd: (if\ sim12-size\ e' < sim12-size\ e\ then\ \tau Exec-1-dr\ else\ \tau Exec-1-dt)\ (compP2\ P)\ t\ (xcp, h, frs)\ (xcp', h, (stk', loc', C, M, pc')) \# FRS$

**by**  $(auto\ intro: \tau Exec-1r-\tau Exec-1-dr\ \tau Exec-1t-\tau Exec-1-dt)$

**moreover** **from**  $wt\ execd\ conf$

**have**  $compTP\ P \vdash t: (xcp', h, (stk', loc', C, M, pc')) \# FRS) \checkmark$

**by**  $(auto\ intro: \tau Exec-1-dr-preserves-correct-state\ \tau Exec-1-dt-preserves-correct-state\ split: if-split-asm)$

**hence**  $bisim1-list1\ t\ h\ (e', xs')\ exs\ xcp'\ ((stk', loc', C, M, pc')) \# FRS$

**using**  $sees\ b'$

**proof**

**from**  $red$  **have**  $max-vars\ e' \leq max-vars\ e$  **by**  $(rule\ red1-max-vars)$

**with**  $red1-preserves-len[OF\ red]\ lenxs$

**show**  $max-vars\ e' \leq length\ xs'$  **by**  $simp$

**qed**  $fact$

**hence**  $bisim1-list1\ t\ h\ (e', xs')\ exs'\ xcp'\ ((stk', loc', C, M, pc')) \# FRS$  **by**  $simp$

**ultimately** **show**  $?thesis$  **by**  $blast$

**qed**  $(insert\ red, auto\ elim: red1-cases)$

**next**

**case**  $(red1Call\ a'\ M'\ vs'\ U'\ Ts'\ T'\ body'\ D')$

**hence**  $[simp]: ta = \varepsilon$

**and**  $exs'\ [simp]: exs' = (e, xs) \# exs$

**and**  $e': e' = blocks1\ 0\ (Class\ D' \# Ts')\ body'$

**and**  $xs': xs' = Addr\ a' \# vs' @ replicate\ (max-vars\ body')\ undefined-value$

**and**  $ha': typeof-addr\ h\ a' = \lfloor U' \rfloor$

**and**  $call: call1\ e = \lfloor (a', M', vs') \rfloor$  **by**  $auto$

**note**  $sees' = \langle P \vdash class-type-of\ U'\ sees\ M': Ts' \rightarrow T' = \lfloor body' \rfloor\ in\ D' \rangle$



```

note  $lenvs'Ts' = \langle length\ vs' = length\ Ts \rangle$ 
from  $ha'$  sees-method-decl-above[ $OF\ sees'$ ]
have  $conf: P, h \vdash Addr\ a' : \leq ty\text{-}of\text{-}h\text{-}type\ U'$  by(auto simp add: conf-def)
note  $wt = wt\text{-}compTP\text{-}compP2[OF\ wf]$ 
from bisim show ?thesis
proof(cases)
  case (bl1-Normal stk loc C M pc FRS Ts T body D)
  hence [simp]:  $frs = (stk, loc, C, M, pc) \# FRS$ 
  and  $conf: compTP\ P \vdash t: (xcp, h, frs) \checkmark$ 
  and  $sees: P \vdash C\ sees\ M: Ts \rightarrow T = \lfloor body \rfloor$  in D
  and  $bisim: P, blocks1\ 0\ (Class\ D \# Ts)\ body, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp)$ 
  and  $bisims: list\text{-}all2\ (bisim1\text{-}fr\ P\ h)\ exs\ FRS$ 
  and  $lenxs: max\text{-}vars\ e \leq length\ xs$  by auto
  from call bisim have [simp]:  $xcp = None$  by(cases xcp, auto dest: bisim1-call-xcpNone)
  from bisim have  $b: P, blocks1\ 0\ (Class\ D \# Ts)\ body, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, None)$  by simp
  from bisim have  $lenloc: length\ xs = length\ loc$  by(rule bisim1-length-xs)
  from sees have  $sees'': compP2\ P \vdash C\ sees\ M: Ts \rightarrow T = \lfloor (max\text{-}stack\ body, max\text{-}vars\ body, compE2\ body\ @\ [Return], compxE2\ body\ 0\ 0) \rfloor$  in D
  unfolding compP2-def compMb2-def Let-def by(auto dest: sees-method-compP)
  from sees wf have  $\neg contains\text{-}insync\ (blocks1\ 0\ (Class\ D \# Ts)\ body)$ 
  by(auto dest!: sees-wf-mdecl WT1-expr-locks simp add: wf-J1-mdecl-def wf-mdecl-def contains-insync-conv)
  with bisim1-call- $\tau Exec\text{-}move[OF\ b\ call, of\ 0\ t]\ lenxs$  obtain  $pc'\ loc'\ stk'$ 
  where  $exec: \tau Exec\text{-}mover\text{-}a\ P\ t\ body\ h\ (stk, loc, pc, None)\ (rev\ vs' @ Addr\ a' \# stk', loc', pc', None)$ 
  and  $pc': pc' < length\ (compE2\ body)$  and  $ins: compE2\ body ! pc' = Invoke\ M'\ (length\ vs')$ 
  and  $bisim': P, blocks1\ 0\ (Class\ D \# Ts)\ body, h \vdash (e, xs) \leftrightarrow (rev\ vs' @ Addr\ a' \# stk', loc', pc', None)$ 
  by(auto simp add: blocks1-max-vars simp del: blocks1.simps)
  let  $?f = (rev\ vs' @ Addr\ a' \# stk', loc', C, M, pc')$ 
  from exec sees
  have  $exec1: \tau Exec\text{-}1r\ (compP2\ P)\ t\ (None, h, (stk, loc, C, M, pc) \# FRS)\ (None, h, ?f \# FRS)$ 
  by(rule  $\tau Exec\text{-}mover\text{-}\tau Exec\text{-}1r$ )
  with wt have  $\tau Exec\text{-}1dr\ (compP2\ P)\ t\ (None, h, (stk, loc, C, M, pc) \# FRS)\ (None, h, ?f \# FRS)$ 
  using conf
  by(simp)(rule  $\tau Exec\text{-}1r\text{-}\tau Exec\text{-}1dr$ )
  also with wt have  $conf': compTP\ P \vdash t: (None, h, ?f \# FRS) \checkmark$  using conf
  by simp (rule  $\tau Exec\text{-}1dr\text{-}preserves\text{-}correct\text{-}state$ )
  let  $?f' = (\square, Addr\ a' \# vs' @ (replicate\ (max\text{-}vars\ body')\ undefined\text{-}value), D', M', 0)$ 
  from  $pc'\ ins\ sees\ sees'\ ha'$ 
  have  $(\varepsilon, None, h, ?f' \# ?f \# FRS) \in exec\text{-}instr\ (instrs\text{-}of\ (compP2\ P)\ C\ M ! pc')\ (compP2\ P)\ t\ h\ (rev\ vs' @ Addr\ a' \# stk')\ loc'\ C\ M\ pc'\ FRS$ 
  by(auto simp add: compP2-def compMb2-def nth-append split-beta)
  hence  $exec\text{-}1\ (compP2\ P)\ t\ (None, h, ?f \# FRS)\ \varepsilon\ (None, h, ?f' \# ?f \# FRS)$ 
  using exec sees by(simp add: exec-1-iff)
  with  $conf'$  have  $execd: compP2\ P, t \vdash Normal\ (None, h, ?f \# FRS) \text{-}\varepsilon\text{-}jvmd \rightarrow Normal\ (None, h, ?f' \# ?f \# FRS)$ 
  by(simp add: welltyped-commute[ $OF\ wt$ ])
  hence check:  $check\ (compP2\ P)\ (None, h, ?f \# FRS)$  by(rule jvmd-NormalE)
  have  $\tau move2\ (compP2\ P)\ h\ (rev\ vs' @ Addr\ a' \# stk')\ body\ pc'\ None$  using  $pc'\ ins\ ha'\ sees'$ 
  by(auto simp add:  $\tau move2\text{-}iff\ compP2\text{-}def\ dest: sees\text{-}method\text{-}fun$ )
  with sees  $pc'\ ins$  have  $\tau Move2\ (compP2\ P)\ (None, h, (rev\ vs' @ Addr\ a' \# stk', loc', C, M, pc') \# FRS)$ 

```

**unfolding**  $\tau \text{Move2-compP2}[OF \text{ sees}]$  **by** (*auto simp add: compP2-def compMb2-def*)  
**with**  $\langle \text{exec-1 } (compP2 \ P) \ t \ (None, h, ?f \ \# \ FRS) \ \varepsilon \ (None, h, ?f' \ \# \ ?f \ \# \ FRS) \rangle$  **check**  
**have**  $\tau \text{Exec-1-dt } (compP2 \ P) \ t \ (None, h, ?f \ \# \ FRS) \ (None, h, ?f' \ \# \ ?f \ \# \ FRS)$  **by** *fastforce*  
**also from** *execd sees'' sees' ins ha' pc'* **have**  $compP2 \ P, h \vdash vs' [: \leq] \ Ts'$   
**by** (*auto simp add: check-def compP2-def split: if-split-asm elim!: jvmd-NormalE*)  
**hence**  $lenvs: \text{length } vs' = \text{length } Ts'$  **by** (*rule list-all2-lengthD*)  
**from** *wt execd conf'* **have**  $compTP \ P \vdash t: (None, h, ?f' \ \# \ ?f \ \# \ FRS)$   $\checkmark$   
**by** (*rule BV-correct-d-1*)  
**hence**  $bisim1\text{-list1 } t \ h \ (blocks1 \ 0 \ (Class \ D' \ \# \ Ts') \ body', xs') \ ((e, xs) \ \# \ exs) \ None \ (?f' \ \# \ ?f \ \# \ FRS)$   
**proof**  
**from** *sees'* **show**  $P \vdash D' \ \text{sees } M': Ts' \rightarrow T' = \lfloor body' \rfloor \text{ in } D'$  **by** (*rule sees-method-idemp*)  
**show**  $P, blocks1 \ 0 \ (Class \ D' \ \# \ Ts') \ body', h \vdash (blocks1 \ 0 \ (Class \ D' \ \# \ Ts') \ body', xs') \leftrightarrow$   
 $([], Addr \ a' \ \# \ vs' @ replicate \ (max\text{-vars } body') \ undefined\text{-value}, 0, None)$   
**unfolding**  $xs'$  **by** (*rule bisim1-refl*)  
**show**  $max\text{-vars } (blocks1 \ 0 \ (Class \ D' \ \# \ Ts') \ body') \leq \text{length } xs'$   
**unfolding**  $xs'$  **using**  $lenvs$  **by** (*simp add: blocks1-max-vars*)  
**from**  $lenxs$  **have**  $(max\text{-vars } e) \leq \text{length } xs$  **by** *simp*  
**with**  $sees \ bisim'$  **call** **have**  $bisim1\text{-fr } P \ h \ (e, xs) \ (rev \ vs' @ Addr \ a' \ \# \ stk', loc', C, M, pc')$   
**by** (*rule bisim1-fr.intros*)  
**thus**  $list\text{-all2 } (bisim1\text{-fr } P \ h) \ ((e, xs) \ \# \ exs)$   
 $((rev \ vs' @ Addr \ a' \ \# \ stk', loc', C, M, pc') \ \# \ FRS)$   
**using**  $bisims$  **by** *simp*  
**qed**  
**moreover** **have**  $ta\text{-bisim } wbisim1 \ ta \ \varepsilon$  **by** *simp*  
**ultimately show** *?thesis*  
**unfolding**  $\langle frs = (stk, loc, C, M, pc) \ \# \ FRS \rangle \ \langle xcp = None \rangle \ e' \ exs'$   
**by** *auto(blast intro: tranclp-into-rtranclp)*  
**next**  
**case** *bl1-finalVal*  
**with** *call* **show** *?thesis* **by** *simp*  
**next**  
**case** *bl1-finalThrow*  
**with** *call* **show** *?thesis* **by** *simp*  
**qed**  
**next**  
**case** (*red1Return E*)  
**note**  $[simp] = \langle exs = (E, xs') \ \# \ exs' \rangle \ \langle ta = \varepsilon \rangle \ \langle e' = inline\text{-call } e \ E \rangle$   
**note**  $wt = wt\text{-compTP-compP2}[OF \ wf]$   
**from**  $bisim$  **have**  $bisim: bisim1\text{-list1 } t \ h \ (e, xs) \ ((E, xs') \ \# \ exs') \ xcp \ frs$  **by** *simp*  
**thus** *?thesis*  
**proof cases**  
**case** (*bl1-Normal stk loc C M pc FRS Ts T body D*)  
**hence**  $[simp]: frs = (stk, loc, C, M, pc) \ \# \ FRS$   
**and**  $conf: compTP \ P \vdash t: (xcp, h, frs) \ \checkmark$   
**and**  $sees: P \vdash C \ \text{sees } M: Ts \rightarrow T = \lfloor body \rfloor \text{ in } D$   
**and**  $bisim: P, blocks1 \ 0 \ (Class \ D \ \# \ Ts) \ body, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp)$   
**and**  $bisims: list\text{-all2 } (bisim1\text{-fr } P \ h) \ ((E, xs') \ \# \ exs') \ FRS$   
**and**  $lenxs: max\text{-vars } e \leq \text{length } xs$  **by** *auto*  
**from**  $bisims$  **obtain**  $f \ FRS'$  **where**  $[simp]: FRS = f \ \# \ FRS'$  **by** (*fastforce simp add: list-all2-Cons1*)  
**from**  $bisims$  **have**  $bisim1\text{-fr } P \ h \ (E, xs') \ f$  **by** *simp*  
**then obtain**  $C0 \ M0 \ Ts0 \ T0 \ body0 \ D0 \ stk0 \ loc0 \ pc0 \ a' \ M' \ vs'$   
**where**  $[simp]: f = (stk0, loc0, C0, M0, pc0)$   
**and**  $sees0: P \vdash C0 \ \text{sees } M0: Ts0 \rightarrow T0 = \lfloor body0 \rfloor \text{ in } D0$

**and**  $\text{bisim0}: P, \text{blocks1 } 0 \text{ (Class } D0 \# Ts0) \text{ body0}, h \vdash (E, xs') \leftrightarrow (stk0, loc0, pc0, None)$   
**and**  $\text{lenxs0}: \text{max-vars } E \leq \text{length } xs'$   
**and**  $\text{call0}: \text{call1 } E = [(a', M', vs')]$   
**by** *cases auto*

**let**  $?ee = \text{inline-call } e \ E$

**from**  $\text{bisim0 call0 have } pc0: pc0 < \text{length (compE2 (blocks1 } 0 \text{ (Class } D0 \# Ts0) \text{ body0))}$   
**by**  $(\text{rule bisim1-call-pcD})$   
**hence**  $pc0: pc0 < \text{length (compE2 body0)}$  **by** *simp*  
**with**  $\text{sees-method-compP}[OF \text{ sees0, where } f = \lambda C \ M \ Ts \ T. \text{compMb2}]$   
 $\text{sees-method-compP}[OF \text{ sees, where } f = \lambda C \ M \ Ts \ T. \text{compMb2}] \text{ conf}$   
**obtain**  $ST \ LT \text{ where } \Phi: \text{compTP } P \ C0 \ M0 \ ! \ pc0 = [(ST, LT)]$   
**and**  $\text{conf'f}: \text{conf-f (compP } (\lambda C \ M \ Ts \ T. \text{compMb2}) \ P) \ h \ (ST, LT) \ (\text{compE2 body0} \ @ \ [\text{Return}])$   
 $(stk0, loc0, C0, M0, pc0)$   
**and**  $\text{ins}: (\text{compE2 body0} \ @ \ [\text{Return}]) \ ! \ pc0 = \text{Invoke } M \ (\text{length } Ts)$   
**by**  $(\text{simp add: correct-state-def})(\text{fastforce simp add: compP2-def compMb2-def dest: sees-method-fun})$   
**from**  $\text{bisim1-callD}[OF \text{ bisim0 call0, of } M \text{ length } Ts] \text{ ins } pc0$   
**have**  $[\text{simp}]: M' = M$  **by** *simp*

**from**  $\langle \text{final } e \rangle \text{ show } ?thesis$   
**proof**  $(\text{cases})$   
**fix**  $v$   
**assume**  $[\text{simp}]: e = \text{Val } v$   
**with**  $\text{bisim have } [\text{simp}]: xcp = \text{None}$  **by**  $(\text{auto dest: bisim-Val-loc-eq-xcp-None})$

**from**  $\text{bisim1Val2D1}[OF \text{ bisim}[\text{unfolded } \langle xcp = \text{None} \rangle \langle e = \text{Val } v \rangle]]$   
**have**  $\tau \text{Exec-mover-a } P \ t \text{ body } h \ (stk, loc, pc, \text{None}) \ ([v], loc, \text{length (compE2 body)}, \text{None})$   
**and**  $[\text{simp}]: xs = loc$  **by**  $(\text{auto simp del: blocks1.simps})$   
**with**  $\text{sees have } \tau \text{Exec-1r (compP2 } P) \ t \ (\text{None}, h, (stk, loc, C, M, pc) \ # \ \text{FRS}) \ (\text{None}, h, ([v],$   
 $loc, C, M, \text{length (compE2 body)}) \ # \ \text{FRS})$   
**by**  $-(\text{rule } \tau \text{Exec-mover-}\tau \text{Exec-1r})$   
**with**  $\text{conf wt have } \tau \text{Exec-1-dr (compP2 } P) \ t \ (\text{None}, h, (stk, loc, C, M, pc) \ # \ \text{FRS}) \ (\text{None}, h,$   
 $([v], loc, C, M, \text{length (compE2 body)}) \ # \ \text{FRS})$   
**by**  $(\text{simp})(\text{rule } \tau \text{Exec-1r-}\tau \text{Exec-1-dr})$   
**moreover with**  $\text{conf wt have conf': compTP } P \vdash t: (\text{None}, h, ([v], loc, C, M, \text{length (compE2}$   
 $\text{body})) \ # \ \text{FRS}) \ \checkmark$   
**by**  $(\text{simp})(\text{rule } \tau \text{Exec-1-dr-preserves-correct-state})$   
**from**  $\text{sees sees0}$   
**have**  $\text{exec}: \text{exec-1 (compP2 } P) \ t \ (\text{None}, h, ([v], loc, C, M, \text{length (compE2 body)}) \ # \ \text{FRS}) \ \varepsilon$   
 $(\text{None}, h, (v \ # \ \text{drop (Suc (length } Ts)) \ stk0, loc0, C0, M0, \text{Suc } pc0) \ # \ \text{FRS'})$   
**by**  $(\text{simp add: exec-1-iff compP2-def compMb2-def})$   
**moreover with**  $\text{conf' wt have compP2 } P, t \vdash \text{Normal } (\text{None}, h, ([v], loc, C, M, \text{length (compE2}$   
 $\text{body})) \ # \ \text{FRS}) \ -\varepsilon - \text{jvmd} \rightarrow \text{Normal } (\text{None}, h, (v \ # \ \text{drop (Suc (length } Ts)) \ stk0, loc0, C0, M0, \text{Suc}$   
 $pc0) \ # \ \text{FRS'})$   
**by**  $(\text{simp add: welltyped-commute})$   
**hence**  $\text{check (compP2 } P) \ (\text{None}, h, ([v], loc, C, M, \text{length (compE2 body)}) \ # \ \text{FRS})$   
**by**  $(\text{rule jvmd-NormalE})$   
**moreover have**  $\tau \text{Move2 (compP2 } P) \ (\text{None}, h, ([v], loc, C, M, \text{length (compE2 body)}) \ # \ \text{FRS})$   
**unfolding**  $\tau \text{Move2-compP2}[OF \text{ sees}]$  **by**  $(\text{auto})$   
**ultimately have**  $\tau \text{Exec-1-dt (compP2 } P) \ t \ (\text{None}, h, (stk, loc, C, M, pc) \ # \ \text{FRS}) \ (\text{None}, h,$   
 $(v \ # \ \text{drop (Suc (length } Ts)) \ stk0, loc0, C0, M0, \text{Suc } pc0) \ # \ \text{FRS'})$   
**by**  $-(\text{erule rtranclp-into-tranclp1, rule } \tau \text{exec-1-dI})$   
**moreover from**  $wt \text{ conf' exec}$

```

have compTP  $P \vdash t:(None, h, (v \# \text{drop} (\text{Suc} (\text{length } Ts)) \text{stk0}, \text{loc0}, C0, M0, \text{Suc } \text{pc0})) \#$ 
FRS')  $\checkmark$ 
  by(rule BV-correct-1)
  hence bisim1-list1  $t \ h \ (\text{?ee}, \text{xs}') \ \text{exs}' \ None \ ((v \# \text{drop} (\text{Suc} (\text{length } Ts)) \text{stk0}, \text{loc0}, C0, M0,$ 
 $\text{Suc } \text{pc0})) \# \text{FRS}'$ )
    using sees0
  proof
    from bisim1-inline-call-Val[OF bisim0 call0, of length Ts v] ins pc0
    show  $P, \text{blocks1 } 0 \ (\text{Class } D0 \# Ts0) \ \text{body0}, h \vdash (\text{?ee}, \text{xs}') \leftrightarrow (v \# \text{drop} (\text{Suc} (\text{length } Ts)) \text{stk0},$ 
 $\text{loc0}, \text{Suc } \text{pc0}, None)$ 
      by simp
    from lenxs0 max-vars-inline-call[of e E]
    show  $\text{max-vars} (\text{inline-call } e \ E) \leq \text{length } \text{xs}'$  by simp
    from bisims show list-all2 (bisim1-fr P h)  $\text{exs}' \ \text{FRS}'$  by simp
  qed
ultimately show ?thesis
  by  $-(\text{rule } \text{exI } \text{conjI} | \text{assumption} | \text{simp}) +$ 
next
fix ad
assume [simp]:  $e = \text{Throw } ad$ 

have  $\exists \text{stk}' \ \text{pc}'. \ \tau \text{Exec-mover-a } P \ t \ \text{body } h \ (\text{stk}, \text{loc}, \text{pc}, \text{pcp}) \ (\text{stk}', \text{loc}, \text{pc}', \lfloor ad \rfloor) \wedge$ 
 $P, \text{blocks1 } 0 \ (\text{Class } D \# Ts) \ \text{body}, h \vdash (\text{Throw } ad, \text{loc}) \leftrightarrow (\text{stk}', \text{loc}, \text{pc}', \lfloor ad \rfloor)$ 
proof(cases xcp)
  case [simp]: None
    from bisim1-Throw- $\tau \text{Exec-mover}$ [OF bisim[unfolded None  $\langle e = \text{Throw } ad \rangle$ ]] obtain pc'
      where  $\text{exec}: \tau \text{Exec-mover-a } P \ t \ \text{body } h \ (\text{stk}, \text{loc}, \text{pc}, None) \ ([\text{Addr } ad], \text{loc}, \text{pc}', \lfloor ad \rfloor)$ 
      and  $\text{bisim}': P, \text{blocks1 } 0 \ (\text{Class } D \# Ts) \ \text{body}, h \vdash (\text{Throw } ad, \text{xs}) \leftrightarrow ([\text{Addr } ad], \text{loc}, \text{pc}', \lfloor ad \rfloor)$ 
      and [simp]:  $\text{xs} = \text{loc}$  by(auto simp del: blocks1.simps)
    thus ?thesis by fastforce
  next
    case (Some a')
    with bisim have  $a' = ad \ \text{xs} = \text{loc}$  by(auto dest: bisim1-ThrowD)
    thus ?thesis using bisim Some by(auto)
  qed
then obtain  $\text{stk}' \ \text{pc}'$  where  $\text{exec}: \tau \text{Exec-mover-a } P \ t \ \text{body } h \ (\text{stk}, \text{loc}, \text{pc}, \text{pcp}) \ (\text{stk}', \text{loc}, \text{pc}',$ 
 $\lfloor ad \rfloor)$ 
and  $\text{bisim}': P, \text{blocks1 } 0 \ (\text{Class } D \# Ts) \ \text{body}, h \vdash (\text{Throw } ad, \text{loc}) \leftrightarrow (\text{stk}', \text{loc}, \text{pc}', \lfloor ad \rfloor)$  by
blast

with sees have  $\tau \text{Exec-1r} (\text{compP2 } P) \ t \ (\text{pcp}, h, (\text{stk}, \text{loc}, C, M, \text{pc}) \# \text{FRS}) \ (\lfloor ad \rfloor, h, (\text{stk}',$ 
 $\text{loc}, C, M, \text{pc}') \# \text{FRS})$ 
  by  $-(\text{rule } \tau \text{Exec-mover-} \tau \text{Exec-1r})$ 
with conf wt have  $\tau \text{Exec-1-dr} (\text{compP2 } P) \ t \ (\text{pcp}, h, (\text{stk}, \text{loc}, C, M, \text{pc}) \# \text{FRS}) \ (\lfloor ad \rfloor, h,$ 
 $(\text{stk}', \text{loc}, C, M, \text{pc}') \# \text{FRS})$ 
  by(simp)(rule  $\tau \text{Exec-1r-} \tau \text{Exec-1-dr}$ )
moreover with conf wt have  $\text{conf}': \text{compTP } P \vdash t: (\lfloor ad \rfloor, h, (\text{stk}', \text{loc}, C, M, \text{pc}') \# \text{FRS}) \checkmark$ 
  by(simp)(rule  $\tau \text{Exec-1-dr-preserves-correct-state}$ )
from bisim1-xcp-Some-not-caught[OF bisim', of  $\lambda C \ M \ Ts \ T. \ \text{compMb2 } 0 \ 0$ ] sees
have match:  $\text{match-ex-table} (\text{compP2 } P) \ (\text{cname-of } h \ ad) \ \text{pc}' \ (\text{ex-table-of } (\text{compP2 } P) \ C \ M) =$ 
None
  by(simp add: compP2-def compMb2-def)
hence  $\text{exec}: \text{exec-1} (\text{compP2 } P) \ t \ (\lfloor ad \rfloor, h, (\text{stk}', \text{loc}, C, M, \text{pc}') \# \text{FRS}) \ \varepsilon \ (\lfloor ad \rfloor, h, \text{FRS})$ 
by(simp add: exec-1-iff)
moreover

```

**with**  $\text{conf}' \text{ wt}$  **have**  $\text{compP2 } P, t \vdash \text{Normal } ([\text{ad}], h, (\text{stk}', \text{loc}, C, M, \text{pc}')) \# \text{FRS}$   $-\varepsilon\text{-jvmd} \rightarrow$   
 $\text{Normal } ([\text{ad}], h, \text{FRS})$   
**by** ( $\text{simp add: welltyped-commute}$ )  
**hence**  $\text{check } (\text{compP2 } P) ([\text{ad}], h, (\text{stk}', \text{loc}, C, M, \text{pc}')) \# \text{FRS}$  **by** ( $\text{rule jvmd-NormalE}$ )  
**moreover from**  $\text{bisim}'$  **have**  $\tau \text{Move2 } (\text{compP2 } P) ([\text{ad}], h, (\text{stk}', \text{loc}, C, M, \text{pc}')) \# \text{FRS}$   
**unfolding**  $\tau \text{Move2-compP2}[OF \text{ sees}]$  **by** ( $\text{auto dest: bisim1-pc-length-compE2}$ )  
**ultimately have**  $\tau \text{Exec-1-dt } (\text{compP2 } P) t (xcp, h, (\text{stk}, \text{loc}, C, M, \text{pc})) \# \text{FRS}$   $([\text{ad}], h,$   
 $\text{FRS})$   
**by**  $-(\text{erule rtrancpl-into-trancpl1}, \text{rule } \tau \text{exec-1-dI})$   
**moreover from**  $\text{wt conf}' \text{ exec}$   
**have**  $\text{compTP } P \vdash t: ([\text{ad}], h, (\text{stk0}, \text{loc0}, C0, M0, \text{pc0})) \# \text{FRS}'$   $\checkmark$   
**by** ( $\text{simp}$ ) ( $\text{rule BV-correct-1}$ )  
**hence**  $\text{bisim1-list1 } t h (\text{?ee}, \text{xs}') \text{ xs}' [\text{ad}] ((\text{stk0}, \text{loc0}, C0, M0, \text{pc0})) \# \text{FRS}'$   
**using**  $\text{sees0}$   
**proof**  
**from**  $\text{bisim1-inline-call-Throw}[OF \text{ bisim0 call0}] \text{ ins pc0}$   
**show**  $P, \text{blocks1 } 0 (\text{Class } D0 \# \text{Ts0}) \text{ body0}, h \vdash (\text{?ee}, \text{xs}') \leftrightarrow (\text{stk0}, \text{loc0}, \text{pc0}, [\text{ad}])$  **by**  $\text{simp}$   
**from**  $\text{lenxs0 max-vars-inline-call}[of e E]$   
**show**  $\text{max-vars } ?ee \leq \text{length xs}'$  **by**  $\text{simp}$   
**from**  $\text{bisims Cons}$  **show**  $\text{list-all2 } (\text{bisim1-fr } P h) \text{ xs}' \text{FRS}'$  **by**  $\text{simp}$   
**qed**  
**moreover from**  $\text{call0}$  **have**  $\text{sim12-size } (\text{inline-call } (\text{Throw ad}) E) > 0$  **by** ( $\text{cases } E$ )  $\text{simp-all}$   
**ultimately show**  $\text{?thesis}$   
**by**  $-(\text{rule exI conjI} | \text{assumption} | \text{simp}) +$   
**qed**  
**qed**  
**qed**  
**qed**

**lemma**  $\text{exec-1-simulates-Red1-not-}\tau$ :

**assumes**  $\text{wf: wf-J1-prog } P$   
**and**  $\text{Red1: True}, P, t \vdash 1 \langle (e, \text{xs}) / \text{xs}, h \rangle - \text{ta} \rightarrow \langle (e', \text{xs}') / \text{xs}', h' \rangle$   
**and**  $\text{bisim: bisim1-list1 } t h (e, \text{xs}) \text{ xs } xcp \text{ frs}$   
**and**  $\tau: \neg \tau \text{Move1 } P h ((e, \text{xs}), \text{xs})$   
**shows**  $\exists xcp' \text{ frs}'. \tau \text{Exec-1-dr } (\text{compP2 } P) t (xcp, h, \text{frs}) (xcp', h, \text{frs}') \wedge$   
 $(\exists ta' xcp'' \text{ frs}''. \text{exec-1-d } (\text{compP2 } P) t (\text{Normal } (xcp', h, \text{frs}')) ta' (\text{Normal } (xcp'', h', \text{frs}'')))$   
 $\wedge$   
 $\neg \tau \text{Move2 } (\text{compP2 } P) (xcp', h, \text{frs}') \wedge \text{ta-bisim } \text{wbisim1 } ta \text{ ta}' \wedge$   
 $\text{bisim1-list1 } t h' (e', \text{xs}') \text{ xs}' xcp'' \text{ frs}'' \wedge$   
 $(\text{call1 } e = \text{None} \vee$   
 $(\text{case frs of Nil} \Rightarrow \text{False} \mid (\text{stk}, \text{loc}, C, M, \text{pc}) \# \text{FRS} \Rightarrow \forall M' n. \text{instrs-of } (\text{compP2 } P) C$   
 $M ! \text{pc} \neq \text{Invoke } M' n) \vee$   
 $xcp' = xcp \wedge \text{frs}' = \text{frs})$   
**using**  $\text{Red1}$   
**proof** ( $\text{cases}$ )  
**case** ( $\text{red1Red TA}$ )  
**hence**  $[\text{simp}]: \text{ta} = \text{extTA2J1 } P \text{ TA } \text{xs}' = \text{xs}$   
**and**  $\text{red: True}, P, t \vdash 1 \langle e, (h, \text{xs}) \rangle - \text{TA} \rightarrow \langle e', (h', \text{xs}') \rangle$  **by**  $\text{simp-all}$   
**from**  $\text{red}$  **have**  $\text{hext: hext } h h'$  **by** ( $\text{auto dest: red1-hext-incr}$ )  
**from**  $\tau$  **have**  $\tau': \neg \tau \text{move1 } P h e$  **by** ( $\text{auto intro: } \tau \text{move1Block}$ )  
**note**  $\text{wt} = \text{wt-compTP-compP2}[OF \text{ wf}]$   
**from**  $\text{bisim}$  **show**  $\text{?thesis}$   
**proof** ( $\text{cases}$ )  
**case** ( $\text{bl1-Normal stk loc C M pc FRS Ts T body D}$ )

hence  $[simp]: frs = (stk, loc, C, M, pc) \# FRS$   
 and  $conf: compTP\ P \vdash t: (xcp, h, frs) \checkmark$   
 and  $sees: P \vdash C\ sees\ M: Ts \rightarrow T = [body]\ in\ D$   
 and  $bisim: P, blocks1\ 0\ (Class\ D \# Ts)\ body, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp)$   
 and  $bisims: list-all2\ (bisim1-fr\ P\ h)\ exs\ FRS$   
 and  $lenxs: max-vars\ e \leq length\ xs\ \mathbf{by}\ auto$   
**from**  $sees\ wf\ \mathbf{have}\ bsok\ (blocks1\ 0\ (Class\ D \# Ts)\ body)\ 0$   
**by**  $(auto\ dest!: sees-wf-mdecl\ WT1-expr-locks\ simp\ add: wf-J1-mdecl-def\ wf-mdecl-def\ bsok-def)$

**from**  $exec-instr-simulates-red1[OF\ wf\ bisim\ red\ this]\ \tau'$   
**obtain**  $pc'\ stk'\ loc'\ xcp'\ pc''\ stk''\ loc''\ xcp''$   
**where**  $exec1: \tau Exec-mover-a\ P\ t\ body\ h\ (stk, loc, pc, xcp)\ (stk', loc', pc', xcp')$   
**and**  $exec2: exec-move-a\ P\ t\ body\ h\ (stk', loc', pc', xcp')\ (extTA2JVM\ (compP2\ P)\ TA)\ h'\ (stk'', loc'', pc'', xcp'')$   
**and**  $\tau2: \neg \tau move2\ (compP2\ P)\ h\ stk'\ body\ pc'\ xcp'$   
**and**  $b': P, blocks1\ 0\ (Class\ D \# Ts)\ body, h' \vdash (e', xs') \leftrightarrow (stk'', loc'', pc'', xcp'')$   
**and**  $call: call1\ e = None \vee no-call2\ (blocks1\ 0\ (Class\ D \# Ts)\ body)\ pc \vee pc' = pc \wedge stk' = stk \wedge loc' = loc \wedge xcp' = xcp$   
**by**  $(fastforce\ simp\ add: exec-move-def\ simp\ del: blocks1.simps)$   
**from**  $exec2\ \mathbf{have}\ pc'body: pc' < length\ (compE2\ body)\ \mathbf{by}\ (auto)$   
**from**  $exec1\ sees\ \mathbf{have}\ exec1': \tau Exec-1r\ (compP2\ P)\ t\ (xcp, h, frs)\ (xcp', h, (stk', loc', C, M, pc')) \# FRS$   
**by**  $(auto\ intro: \tau Exec-mover-\tau Exec-1r)$   
**with**  $wt\ \mathbf{have}\ execd: \tau Exec-1-dr\ (compP2\ P)\ t\ (xcp, h, frs)\ (xcp', h, (stk', loc', C, M, pc')) \# FRS$   
**using**  $conf\ \mathbf{by}\ (rule\ \tau Exec-1r-\tau Exec-1-dr)$   
**moreover**  $\{ \mathbf{fix}\ a$   
**assume**  $[simp]: xcp' = [a]$   
**from**  $exec2\ sees-method-compP[OF\ sees, of\ \lambda C\ M\ Ts\ T.\ compMb2]\ pc'body$   
**have**  $match-ex-table\ (compP2\ P)\ (cname-of\ h\ a)\ pc'\ (ex-table-of\ (compP2\ P)\ C\ M) \neq None$   
**by**  $(auto\ simp\ add: exec-move-def\ compP2-def\ compMb2-def\ elim!: exec-meth.cases)\ }$   
**note**  $xt = this$   
**with**  $\tau2\ sees\ pc'body\ \mathbf{have}\ \tau2': \neg \tau Move2\ (compP2\ P)\ (xcp', h, (stk', loc', C, M, pc')) \# FRS$   
**unfolding**  $\tau Move2-compP2[OF\ sees]\ \mathbf{by}\ (auto\ simp\ add: compP2-def\ compMb2-def\ \tau move2-iff)$   
**moreover** **from**  $exec2\ sees$   
**have**  $exec2': exec-1\ (compP2\ P)\ t\ (xcp', h, (stk', loc', C, M, pc')) \# FRS\ (extTA2JVM\ (compP2\ P)\ TA)\ (xcp'', h', (stk'', loc'', C, M, pc'')) \# FRS$   
**by**  $(rule\ exec-move-exec-1)$   
**from**  $wt\ execd\ conf\ \mathbf{have}\ conf': compTP\ P \vdash t: (xcp', h, (stk', loc', C, M, pc')) \# FRS\ \checkmark$   
**by**  $(rule\ \tau Exec-1-dr-preserves-correct-state)$   
**with**  $exec2'\ wt$   
**have**  $exec-1-d\ (compP2\ P)\ t\ (Normal\ (xcp', h, (stk', loc', C, M, pc')) \# FRS)\ (extTA2JVM\ (compP2\ P)\ TA)\ (Normal\ (xcp'', h', (stk'', loc'', C, M, pc'')) \# FRS)$   
**by**  $(simp\ add: welltyped-commute)$   
**moreover**  
**from**  $\tau2\ sees\ pc'body\ xt\ \mathbf{have}\ \tau2': \neg \tau Move2\ (compP2\ P)\ (xcp', h, (stk', loc', C, M, pc')) \# FRS$   
**unfolding**  $\tau Move2-compP2[OF\ sees]\ \mathbf{by}\ (auto\ simp\ add: compP2-def\ compMb2-def\ \tau move2-iff)$   
**moreover** **from**  $wt\ conf'\ exec2'$   
**have**  $conf'': compTP\ P \vdash t: (xcp'', h', (stk'', loc'', C, M, pc'')) \# FRS\ \checkmark\ \mathbf{by}\ (rule\ BV-correct-1)$   
**hence**  $bisim1-list1\ t\ h'\ (e', xs')\ exs\ xcp''\ ((stk'', loc'', C, M, pc'')) \# FRS\ \mathbf{using}\ sees\ b'$   
**proof**  
**from**  $red1-preserves-len[OF\ red]\ red1-max-vars[OF\ red]\ lenxs$   
**show**  $max-vars\ e' \leq length\ xs'\ \mathbf{by}\ simp$

**from**  $bisims\ \mathbf{show}\ list-all2\ (bisim1-fr\ P\ h')\ exs\ FRS$

```

    by(rule List.list-all2-mono)(rule bisim1-fr-heap-mono[OF - heap])
  qed
  moreover from conf'' have hconf h' preallocated h' by(auto simp add: correct-state-def)
  with wf red
  have ta-bisim wbisim1 ta (extTA2JVM (compP2 P) TA)
    by(auto intro: ta-bisim-red-extTA2J1-extTA2JVM)
  moreover from call sees-method-compP[OF sees, of  $\lambda C M Ts T. compMb2$ ]
  have call1 e = None  $\vee$  (case frs of []  $\Rightarrow$  False | (stk, loc, C, M, pc) # FRS  $\Rightarrow \forall M' n. instrs-of$ 
    (compP2 P) C M ! pc  $\neq$  Invoke M' n)  $\vee$  xcp' = xcp  $\wedge$  (stk', loc', C, M, pc') # FRS = frs
    by(auto simp add: no-call2-def compP2-def compMb2-def)
  ultimately show ?thesis by -(rule exI conjI|assumption|simp)+
next
  case bl1-finalVal
  with red show ?thesis by auto
next
  case bl1-finalThrow
  with red show ?thesis by(auto elim: red1-cases)
qed
next
  case red1Call
  with  $\tau$  have False
    by(auto simp add: synthesized-call-def dest!:  $\tau move1-not-call1$  [where  $P=P$  and  $h=h$ ] dest: sees-method-fun)
  thus ?thesis ..
next
  case red1Return
  with  $\tau$  have False by auto
  thus ?thesis ..
qed
end
end

```

## 7.18 Correctness of Stage 2: From JVM to intermediate language

theory JVMJ1 imports

J1JVMBisim

begin

declare split-paired-Ex[simp del]

lemma rec-option-is-case-option: rec-option = case-option

apply (rule ext)+

apply (rename-tac y)

apply (case-tac y)

apply auto

done

context J1-JVM-heap-base begin

lemma assumes ha: typeof-addr h a = [Class-type D]

and subclsObj:  $P \vdash D \preceq^* Throwable$

**shows** *bisim1-xcp-τRed*:  
 $\llbracket P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor);$   
 $match\text{-}ex\text{-}table (compP f P) (cname\text{-}of h a) pc (compxE2 e 0 0) = None;$   
 $\exists n. n + max\text{-}vars e' \leq length xs \wedge \mathcal{B} e n \rrbracket$   
 $\implies \tau red1r P t h (e', xs) (Throw a, loc) \wedge P, e, h \vdash (Throw a, loc) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor)$

**and** *bisims1-xcp-τReds*:  
 $\llbracket P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, \lfloor a \rfloor);$   
 $match\text{-}ex\text{-}table (compP f P) (cname\text{-}of h a) pc (compxEs2 es 0 0) = None;$   
 $\exists n. n + max\text{-}varss es' \leq length xs \wedge \mathcal{B}s es n \rrbracket$   
 $\implies \exists vs es''. \tau reds1r P t h (es', xs) (map Val vs @ Throw a \# es'', loc) \wedge$   
 $P, es, h \vdash (map Val vs @ Throw a \# es'', loc) [\leftrightarrow] (stk, loc, pc, \lfloor a \rfloor)$

**proof**(*induct* (*e'*, *xs*) (*stk*, *loc*, *pc*,  $\lfloor a \rfloor :: 'addr$ ))  
**and** (*es'*, *xs*) (*stk*, *loc*, *pc*,  $\lfloor a \rfloor :: 'addr$ )  
*arbitrary*: *e' xs stk loc pc* **and** *es' xs stk loc pc* *rule*: *bisim1-bisims1.inducts*)  
**case** *bisim1NewThrow* **thus** ?*case*  
**by**(*fastforce* *intro*: *bisim1-bisims1.intros*)  
**next**  
**case** *bisim1NewArray* **thus** ?*case*  
**by**(*auto* *intro*: *rtranclp.rtrancl-into-rtrancl New1ArrayThrow bisim1-bisims1.intros* *dest*: *bisim1-ThrowD*  
*elim*!: *NewArray-τred1r-xt*)  
**next**  
**case** *bisim1NewArrayThrow* **thus** ?*case*  
**by**(*fastforce* *intro*: *bisim1-bisims1.intros*)  
**next**  
**case** *bisim1NewArrayFail* **thus** ?*case*  
**by**(*fastforce* *intro*: *bisim1-bisims1.intros*)  
**next**  
**case** *bisim1Cast* **thus** ?*case*  
**by**(*fastforce* *intro*: *rtranclp.rtrancl-into-rtrancl Cast1Throw bisim1-bisims1.intros* *dest*: *bisim1-ThrowD*  
*elim*!: *Cast-τred1r-xt*)  
**next**  
**case** *bisim1CastThrow* **thus** ?*case*  
**by**(*fastforce* *intro*: *bisim1-bisims1.intros*)  
**next**  
**case** *bisim1CastFail* **thus** ?*case*  
**by**(*fastforce* *intro*: *bisim1-bisims1.intros*)  
**next**  
**case** *bisim1InstanceOf* **thus** ?*case*  
**by**(*fastforce* *intro*: *rtranclp.rtrancl-into-rtrancl InstanceOf1Throw bisim1-bisims1.intros* *dest*: *bisim1-ThrowD*  
*elim*!: *InstanceOf-τred1r-xt*)  
**next**  
**case** *bisim1InstanceOfThrow* **thus** ?*case*  
**by**(*fastforce* *intro*: *bisim1-bisims1.intros*)  
**next**  
**case** *bisim1BinOp1* **thus** ?*case*  
**by**(*fastforce* *intro*: *rtranclp.rtrancl-into-rtrancl Bin1OpThrow1 bisim1-bisims1.intros* *simp* *add*:  
*match-ex-table-append* *dest*: *bisim1-ThrowD* *elim*!: *BinOp-τred1r-xt1*)  
**next**  
**case** *bisim1BinOp2* **thus** ?*case*  
**by**(*clarsimp* *simp* *add*: *match-ex-table-append-not-pcs compxE2-size-convs compxE2-stack-xlift-convs*  
*match-ex-table-shift-pc-None*)  
(*fastforce* *intro*: *rtranclp.rtrancl-into-rtrancl red1-reds1.intros bisim1BinOpThrow2* *elim*!: *BinOp-τred1r-xt2*)  
**next**



```

case bisim1BinOpThrow1 thus ?case
  by(auto simp add: match-ex-table-append intro: bisim1-bisims1.intros)
next
case (bisim1BinOpThrow2 e xs stk loc pc e1 bop)
note bisim =  $\langle P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$ 
hence xs = loc by(auto dest: bisim1-ThrowD)
with bisim show ?case
  by(auto intro: bisim1-bisims1.bisim1BinOpThrow2)
next
case bisim1BinOpThrow thus ?case
  by(fastforce intro: bisim1-bisims1.intros)
next
case bisim1LAss1 thus ?case
  by(fastforce intro: rtranclp.rtrancl-into-rtrancl LAss1Throw bisim1-bisims1.intros dest: bisim1-ThrowD
elim!: LAss- $\tau$ red1r)
next
case bisim1LAssThrow thus ?case
  by(fastforce intro: bisim1-bisims1.intros)
next
case bisim1AAcc1 thus ?case
  by(fastforce intro: rtranclp.rtrancl-into-rtrancl AAcc1Throw1 bisim1-bisims1.intros simp add: match-ex-table-append
dest: bisim1-ThrowD elim!: AAcc- $\tau$ red1r-xt1)
next
case bisim1AAcc2 thus ?case
  by(clarsimp simp add: match-ex-table-append-not-pcs compxE2-size-convs compxE2-stack-xlift-convs
match-ex-table-shift-pc-None)
  (fastforce intro: rtranclp.rtrancl-into-rtrancl red1-reds1.intros bisim1AAccThrow2 elim!: AAcc- $\tau$ red1r-xt2)
next
case bisim1AAccThrow1 thus ?case
  by(auto simp add: match-ex-table-append intro: bisim1-bisims1.intros)
next
case bisim1AAccThrow2 thus ?case
  by(auto simp add: match-ex-table-append intro: bisim1-bisims1.intros dest: bisim1-ThrowD)
next
case bisim1AAccFail thus ?case
  by(fastforce intro: bisim1-bisims1.intros)
next
case bisim1AAss1 thus ?case
  by(fastforce intro: rtranclp.rtrancl-into-rtrancl AAass1Throw1 bisim1-bisims1.intros simp add: match-ex-table-append
dest: bisim1-ThrowD elim!: AAass- $\tau$ red1r-xt1)
next
case bisim1AAss2 thus ?case
  by(clarsimp simp add: compxE2-size-convs compxE2-stack-xlift-convs)
  (fastforce simp add: match-ex-table-append intro: rtranclp.rtrancl-into-rtrancl red1-reds1.intros
bisim1AAssThrow2 elim!: AAass- $\tau$ red1r-xt2)
next
case bisim1AAss3 thus ?case
  by(clarsimp simp add: compxE2-size-convs compxE2-stack-xlift-convs)
  (fastforce simp add: match-ex-table-append intro: rtranclp.rtrancl-into-rtrancl red1-reds1.intros
bisim1AAssThrow3 elim!: AAass- $\tau$ red1r-xt3)
next
case bisim1AAssThrow1 thus ?case
  by(auto simp add: match-ex-table-append intro: bisim1-bisims1.intros)
next

```

```

case (bisim1AAssThrow2 e xs stk loc pc i e2)
note bisim =  $\langle P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor) \rangle$ 
hence xs = loc by(auto dest: bisim1-ThrowD)
with bisim show ?case
  by(auto intro: bisim1-bisims1.bisim1AAssThrow2)
next
case (bisim1AAssThrow3 e xs stk loc pc A i)
note bisim =  $\langle P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor) \rangle$ 
hence xs = loc by(auto dest: bisim1-ThrowD)
with bisim show ?case
  by(auto intro: bisim1-bisims1.bisim1AAssThrow3)
next
case bisim1AAssFail thus ?case
  by(fastforce intro: bisim1-bisims1.intros)
next
case bisim1ALength thus ?case
  by(fastforce intro: rtranclp.rtrancl-into-rtrancl ALength1Throw bisim1-bisims1.intros dest: bisim1-ThrowD
elim!: ALength-τred1r-xt)
next
case bisim1ALengthThrow thus ?case
  by(fastforce intro: bisim1-bisims1.intros)
next
case bisim1ALengthNull thus ?case
  by(fastforce intro: bisim1-bisims1.intros)
next
case bisim1FAcc thus ?case
  by(fastforce intro: rtranclp.rtrancl-into-rtrancl FAcc1Throw bisim1-bisims1.intros dest: bisim1-ThrowD
elim!: FAcc-τred1r-xt)
next
case bisim1FAccThrow thus ?case
  by(fastforce intro: bisim1-bisims1.intros)
next
case bisim1FAccNull thus ?case
  by(fastforce intro: bisim1-bisims1.intros)
next
case bisim1FAss1 thus ?case
  by(fastforce intro: rtranclp.rtrancl-into-rtrancl FAss1Throw1 bisim1-bisims1.intros simp add: match-ex-table-append
dest: bisim1-ThrowD elim!: FAss-τred1r-xt1)
next
case bisim1FAss2 thus ?case
  by(clarsimp simp add: match-ex-table-append-not-pcs compxE2-size-convs compxE2-stack-xlift-convs)
  (fastforce intro: rtranclp.rtrancl-into-rtrancl red1-reds1.intros bisim1FAssThrow2 elim!: FAss-τred1r-xt2)
next
case bisim1FAssThrow1 thus ?case
  by(auto simp add: match-ex-table-append intro: bisim1-bisims1.intros)
next
case (bisim1FAssThrow2 e2 xs stk loc pc e)
note bisim =  $\langle P, e2, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor) \rangle$ 
hence xs = loc by(auto dest: bisim1-ThrowD)
with bisim show ?case
  by(auto intro: bisim1-bisims1.bisim1FAssThrow2)
next
case bisim1FAssNull thus ?case
  by(fastforce intro: bisim1-bisims1.intros)

```

```

next
  case bisim1CAS1 thus ?case
    by(fastforce intro: rtrancpl.rtrancle-into-rtrancle CAS1Throw bisim1-bisims1.intros simp add: match-ex-table-append
      dest: bisim1-ThrowD elim!: CAS-τred1r-xt1)
next
  case bisim1CAS2 thus ?case
    by(clarsimp simp add: compxE2-size-convs compxE2-stack-xlift-convs)
    (fastforce simp add: match-ex-table-append intro: rtrancpl.rtrancle-into-rtrancle red1-reds1.intros
      bisim1CASThrow2 elim!: CAS-τred1r-xt2)
next
  case bisim1CAS3 thus ?case
    by(clarsimp simp add: compxE2-size-convs compxE2-stack-xlift-convs)
    (fastforce simp add: match-ex-table-append intro: rtrancpl.rtrancle-into-rtrancle red1-reds1.intros
      bisim1CASThrow3 elim!: CAS-τred1r-xt3)
next
  case bisim1CASThrow1 thus ?case
    by(auto simp add: match-ex-table-append intro: bisim1-bisims1.intros)
next
  case (bisim1CASThrow2 e xs stk loc pc i e2)
  note bisim =  $\langle P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$ 
  hence xs = loc by(auto dest: bisim1-ThrowD)
  with bisim show ?case
    by(auto intro: bisim1-bisims1.bisim1CASThrow2)
next
  case (bisim1CASThrow3 e xs stk loc pc A i)
  note bisim =  $\langle P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$ 
  hence xs = loc by(auto dest: bisim1-ThrowD)
  with bisim show ?case
    by(auto intro: bisim1-bisims1.bisim1CASThrow3)
next
  case bisim1CASFail thus ?case
    by(fastforce intro: bisim1-bisims1.intros)
next
  case bisim1Call1 thus ?case
    by(fastforce intro: rtrancpl.rtrancle-into-rtrancle Call1ThrowObj bisim1-bisims1.intros simp add:
      match-ex-table-append dest: bisim1-ThrowD elim!: Call-τred1r-obj)
next
  case bisim1CallParams thus ?case
    by(clarsimp simp add: match-ex-table-append-not-pcs compxE2-size-convs compxEs2-size-convs com-
      pxE2-stack-xlift-convs compxEs2-stack-xlift-convs match-ex-table-shift-pc-None)
    (fastforce intro: rtrancpl.rtrancle-into-rtrancle Call1ThrowParams[OF refl] bisim1CallThrowParams
      elim!: Call-τred1r-param)
next
  case bisim1CallThrowObj thus ?case
    by(auto simp add: match-ex-table-append intro: bisim1-bisims1.intros)
next
  case (bisim1CallThrowParams es vs es' xs stk loc pc obj M)
  note bisim =  $\langle P, es, h \vdash (\text{map Val vs } @ \text{ Throw } a \# es', xs) [\leftrightarrow] (stk, loc, pc, [a]) \rangle$ 
  hence xs = loc by(auto dest: bisims1-ThrowD)
  with bisim show ?case
    by(auto intro: bisim1-bisims1.bisim1CallThrowParams)
next
  case bisim1CallThrow thus ?case
    by(auto intro: bisim1-bisims1.intros)

```

next

case (bisim1BlockSome4 e e' xs stk loc pc V Ty v)  
 from  $\langle \exists n. n + \text{max-vars } \{V:Ty=None; e'\} \leq \text{length } xs \wedge \mathcal{B} \{V:Ty=\lfloor v \rfloor; e\} n \rangle$   
 have  $V: V < \text{length } xs$  by simp  
 from bisim1BlockSome4 have Red:  $\tau\text{red1r } P \text{ t h } (e', xs) (\text{Throw } a, \text{loc})$   
 and bisim:  $P, e, h \vdash (\text{Throw } a, \text{loc}) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \lfloor a \rfloor)$   
 by(auto simp add: match-ex-table-append-not-pcs compxE2-size-convs compxEs2-size-convs compxE2-stack-xlift-convs compxEs2-stack-xlift-convs match-ex-table-shift-pc-None intro!: exI[where x=Suc V] elim: meta-impE)  
 note len =  $\tau\text{red1r-preserves-len}[OF \text{Red}]$   
 from Red have  $\tau\text{red1r } P \text{ t h } (\{V:Ty=None; e'\}, xs) (\{V:Ty=None; \text{Throw } a\}, \text{loc})$  by(auto intro: Block-None- $\tau\text{red1r-xt}$ )  
 thus ?case using V len bisim  
 by(auto intro:  $\tau\text{move1BlockThrow Block1Throw bisim1BlockThrowSome elim!: rtranclp.rtrancl-into-rtrancl}$ )

next

case bisim1BlockThrowSome thus ?case  
 by(auto dest: bisim1-ThrowD intro: bisim1-bisims1.bisim1BlockThrowSome)

next

case (bisim1BlockNone e e' xs stk loc pc V Ty)  
 hence Red:  $\tau\text{red1r } P \text{ t h } (e', xs) (\text{Throw } a, \text{loc})$   
 and bisim:  $P, e, h \vdash (\text{Throw } a, \text{loc}) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \lfloor a \rfloor)$   
 by(auto elim: meta-impE intro!: exI[where x=Suc V])  
 from Red have len:  $\text{length } \text{loc} = \text{length } xs$  by(rule  $\tau\text{red1r-preserves-len}$ )  
 from  $\langle \exists n. n + \text{max-vars } \{V:Ty=None; e'\} \leq \text{length } xs \wedge \mathcal{B} \{V:Ty=None; e\} n \rangle$   
 have  $V: V < \text{length } xs$  by simp  
 from Red have  $\tau\text{red1r } P \text{ t h } (\{V:Ty=None; e'\}, xs) (\{V:Ty=None; \text{Throw } a\}, \text{loc})$  by(rule Block-None- $\tau\text{red1r-xt}$ )  
 thus ?case using V len bisim  
 by(auto intro:  $\tau\text{move1BlockThrow Block1Throw bisim1BlockThrowNone elim!: rtranclp.rtrancl-into-rtrancl}$ )

next

case bisim1BlockThrowNone thus ?case  
 by(auto dest: bisim1-ThrowD intro: bisim1-bisims1.bisim1BlockThrowNone)

next

case bisim1Sync1 thus ?case  
 by(fastforce intro: rtranclp.rtrancl-into-rtrancl Synchronized1Throw1 bisim1-bisims1.intros simp add: match-ex-table-append dest: bisim1-ThrowD elim!: Sync- $\tau\text{red1r-xt}$ )

next

case (bisim1Sync4 e2 e' xs stk loc pc V e1 a')  
 from  $\langle P, e2, h \vdash (e', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \lfloor a \rfloor) \rangle$   
 have  $\text{pc} < \text{length } (\text{compE2 } e2)$  by(auto dest!: bisim1-xcp-pcD)  
 with  $\langle \text{match-ex-table } (\text{compP } f P) (\text{cname-of } h a) (\text{Suc } (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e1) + \text{pc})))) (\text{compxE2 } (\text{sync}_V (e1) e2) 0 0) = \text{None} \rangle \text{subclsObj } ha$   
 have False by(simp add: match-ex-table-append matches-ex-entry-def split: if-split-asm)  
 thus ?case ..

next

case bisim1Sync10 thus ?case  
 by(fastforce intro: bisim1-bisims1.intros)

next

case bisim1Sync11 thus ?case  
 by(fastforce intro: bisim1-bisims1.intros)

next

case bisim1Sync12 thus ?case  
 by(fastforce intro: bisim1-bisims1.intros)

next

case bisim1Sync14 thus ?case

```

    by(fastforce intro: bisim1-bisims1.intros)
next
  case bisim1SyncThrow thus ?case
    by(auto simp add: match-ex-table-append intro: bisim1-bisims1.intros)
next
  case bisim1Seq1 thus ?case
    by(fastforce intro: rtrancpl.rtrancpl-into-rtrancpl Seq1Throw bisim1-bisims1.intros dest: bisim1-ThrowD
elim!: Seq- $\tau$ red1r-xt simp add: match-ex-table-append)
next
  case bisim1SeqThrow1 thus ?case
    by(auto simp add: match-ex-table-append intro: bisim1-bisims1.intros)
next
  case bisim1Seq2 thus ?case
    by(auto simp add: match-ex-table-append-not-pcs compxE2-size-convs compxE2-stack-xlift-convs
match-ex-table-shift-pc-None intro: bisim1-bisims1.bisim1Seq2)
next
  case bisim1Cond1 thus ?case
    by(fastforce intro: rtrancpl.rtrancpl-into-rtrancpl Cond1Throw bisim1-bisims1.intros dest: bisim1-ThrowD
elim!: Cond- $\tau$ red1r-xt simp add: match-ex-table-append)
next
  case bisim1CondThen thus ?case
    by(clarsimp simp add: match-ex-table-append)
    (auto simp add: match-ex-table-append-not-pcs compxE2-size-convs compxE2-stack-xlift-convs
match-ex-table-shift-pc-None intro: bisim1-bisims1.bisim1CondThen)
next
  case bisim1CondElse thus ?case
    by(clarsimp simp add: match-ex-table-append)
    (auto simp add: match-ex-table-append-not-pcs compxE2-size-convs compxE2-stack-xlift-convs
match-ex-table-shift-pc-None intro: bisim1-bisims1.bisim1CondElse)
next
  case bisim1CondThrow thus ?case
    by(auto simp add: match-ex-table-append intro: bisim1-bisims1.intros)
next
  case bisim1While3 thus ?case
    by(fastforce intro: rtrancpl.rtrancpl-into-rtrancpl Cond1Throw bisim1-bisims1.intros dest: bisim1-ThrowD
elim!: Cond- $\tau$ red1r-xt simp add: match-ex-table-append)
next
  case (bisim1While4 e e' xs stk loc pc c)
    hence  $\tau$ red1r P t h (e', xs) (Throw a, loc)  $\wedge$  P,e,h  $\vdash$  (Throw a, loc)  $\leftrightarrow$  (stk, loc, pc, [a])
    by(auto simp add: match-ex-table-append-not-pcs compxE2-size-convs compxEs2-size-convs com-
pxE2-stack-xlift-convs compxEs2-stack-xlift-convs match-ex-table-shift-pc-None)
    hence  $\tau$ red1r P t h (e';while (c) e, xs) (Throw a, loc)
    P,while (c) e,h  $\vdash$  (Throw a, loc)  $\leftrightarrow$  (stk, loc, Suc (length (compE2 c) + pc), [a])
    by(auto intro: rtrancpl.rtrancpl-into-rtrancpl Seq1Throw  $\tau$ move1SeqThrow bisim1WhileThrow2 elim!:
Seq- $\tau$ red1r-xt)
    thus ?case ..
next
  case bisim1WhileThrow1 thus ?case
    by(auto simp add: match-ex-table-append intro: bisim1-bisims1.intros)
next
  case bisim1WhileThrow2 thus ?case
    by(auto simp add: match-ex-table-append intro: bisim1-bisims1.intros dest: bisim1-ThrowD)
next
  case bisim1Throw1 thus ?case

```

**by**(*fastforce* *intro*: *rtrancpl.rtrancpl-into-rtrancpl Throw1Throw bisim1-bisims1.intros* *dest*: *bisim1-ThrowD*  
*elim!*: *Throw-τred1r-xt*)  
**next**  
**case** *bisim1Throw2* **thus** *?case*  
**by**(*fastforce* *intro*: *bisim1-bisims1.intros*)  
**next**  
**case** *bisim1ThrowNull* **thus** *?case*  
**by**(*fastforce* *intro*: *bisim1-bisims1.intros*)  
**next**  
**case** *bisim1ThrowThrow* **thus** *?case*  
**by**(*fastforce* *intro*: *bisim1-bisims1.intros*)  
**next**  
**case** (*bisim1Try* *e e' xs stk loc pc C V e2*)  
**hence** *red*:  $\tau\text{red1r } P \text{ t h } (e', xs) \text{ (Throw } a, \text{ loc)}$   
**and** *bisim*:  $P, e, h \vdash (\text{Throw } a, \text{ loc}) \leftrightarrow (stk, \text{ loc}, pc, \lfloor a \rfloor)$   
**by**(*auto simp add: match-ex-table-append*)  
**from** *red* **have** *Red*:  $\tau\text{red1r } P \text{ t h } (\text{try } e' \text{ catch}(C \text{ V}) e2, xs) \text{ (try Throw } a \text{ catch}(C \text{ V}) e2, \text{ loc})$   
**by**(*rule Try-τred1r-xt*)  
**from**  $\langle \text{match-ex-table } (\text{compP } f \text{ P}) \text{ (cname-of } h \text{ a)} \text{ pc } (\text{compxE2 } (\text{try } e \text{ catch}(C \text{ V}) e2) \text{ } 0 \text{ } 0) = \text{None} \rangle$   
**have**  $\neg \text{matches-ex-entry } (\text{compP } f \text{ P}) \text{ (cname-of } h \text{ a)} \text{ pc } (0, \text{length } (\text{compE2 } e), \lfloor C \rfloor, \text{Suc } (\text{length } (\text{compE2 } e)), 0)$   
**by**(*auto simp add: match-ex-table-append split: if-split-asm*)  
**moreover from**  $\langle P, e, h \vdash (e', xs) \leftrightarrow (stk, \text{ loc}, pc, \lfloor a \rfloor) \rangle$   
**have**  $pc < \text{length } (\text{compE2 } e)$  **by**(*auto dest: bisim1-xcp-pcD*)  
**ultimately have** *subcls*:  $\neg P \vdash D \preceq^* C$  **using** *ha* **by**(*simp add: matches-ex-entry-def cname-of-def*)  
**with** *ha* **have** *True*,  $P, t \vdash 1 \langle \text{try Throw } a \text{ catch}(C \text{ V}) e2, (h, \text{ loc}) \rangle \rightarrow \langle \text{Throw } a, (h, \text{ loc}) \rangle$   
**by**  $\neg(\text{rule Red1TryFail, auto})$   
**moreover from** *bisim* *ha* *subcls*  
**have**  $P, \text{try } e \text{ catch}(C \text{ V}) e2, h \vdash (\text{Throw } a, \text{ loc}) \leftrightarrow (stk, \text{ loc}, pc, \lfloor a \rfloor)$   
**by**(*rule bisim1TryFail*)  
**ultimately show** *?case* **using** *Red* **by**(*blast intro: rtrancpl.rtrancpl-into-rtrancpl τmove1TryThrow*)  
**next**  
**case** (*bisim1TryCatch2* *e2 e' xs stk loc pc e1 C V*)  
**hence**  $*$ :  $\tau\text{red1r } P \text{ t h } (e', xs) \text{ (Throw } a, \text{ loc}) \wedge P, e2, h \vdash (\text{Throw } a, \text{ loc}) \leftrightarrow (stk, \text{ loc}, pc, \lfloor a \rfloor)$   
**by**(*clarsimp simp add: match-ex-table-append matches-ex-entry-def split: if-split-asm*)  
*(auto simp add: match-ex-table-append compxE2-size-conv compxE2-stack-xlift-conv match-ex-table-shift-pc-N*  
*elim: meta-impE intro!: exI[where x=Suc V])*  
**moreover note**  $\tau\text{red1r-preserves-len}[OF *[\text{THEN conjunct1}]]$   
**moreover from**  $\langle \exists n. n + \text{max-vars } \{V: \text{Class } C = \text{None}; e'\} \leq \text{length } xs \wedge \mathcal{B} \text{ (try } e1 \text{ catch}(C \text{ V})$   
*e2) n*  $\rangle$   
**have**  $V < \text{length } xs$  **by** *simp*  
**ultimately show** *?case*  
**by**(*fastforce* *intro*: *rtrancpl.rtrancpl-into-rtrancpl Block1Throw τmove1BlockThrow bisim1TryCatchThrow*  
*elim!*: *Block-None-τred1r-xt*)  
**next**  
**case** (*bisim1TryFail* *e xs stk loc pc C'' C' V e2*)  
**note** *bisim* =  $\langle P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, \text{ loc}, pc, \lfloor a \rfloor) \rangle$   
**hence**  $xs = \text{loc}$  **by**(*auto dest: bisim1-ThrowD*)  
**with** *bisim*  $\langle \text{typeof-addr } h \text{ a} = \lfloor \text{Class-type } C'' \rfloor \rangle \langle \neg P \vdash C'' \preceq^* C' \rangle$   
**show** *?case* **by**(*auto intro: bisim1-bisims1.bisim1TryFail*)  
**next**  
**case** (*bisim1TryCatchThrow* *e2 xs stk loc pc e C' V*)  
**from**  $\langle P, e2, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, \text{ loc}, pc, \lfloor a \rfloor) \rangle$  **have**  $xs = \text{loc}$   
**by**(*auto dest: bisim1-ThrowD*)

**with**  $\langle P, e2, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$  **show**  $?case$   
**by**(*auto intro: bisim1-bisims1.bisim1TryCatchThrow*)

**next**  
**case** (*bisims1List1 e e' xs stk loc pc es*)  
**hence**  $\tau red1r P t h (e', xs) (\text{Throw } a, loc)$   
**and** *bisim:  $P, e, h \vdash (\text{Throw } a, loc) \leftrightarrow (stk, loc, pc, [a])$*  **by**(*auto simp add: match-ex-table-append*)  
**hence**  $\tau reds1r P t h (e' \# es, xs) (\text{map Val } [] @ \text{Throw } a \# es, loc)$  **by**(*auto intro:  $\tau red1r\text{-inj-}\tau reds1r$* )  
**moreover from** *bisim*  
**have**  $P, e \# es, h \vdash (\text{Throw } a \# es, loc) [\leftrightarrow] (stk, loc, pc, [a])$   
**by**(*rule bisim1-bisims1.bisims1List1*)  
**ultimately show**  $?case$  **by** *fastforce*

**next**  
**case** (*bisims1List2 es es' xs stk loc pc e v*)  
**hence**  $\exists vs es''. \tau reds1r P t h (es', xs) (\text{map Val } vs @ \text{Throw } a \# es'', loc) \wedge P, es, h \vdash (\text{map Val } vs @ \text{Throw } a \# es'', loc) [\leftrightarrow] (stk, loc, pc, [a])$   
**by**(*auto simp add: match-ex-table-append-not-pcs compxEs2-size-convs compxEs2-stack-xlift-convs match-ex-table-shift-pc-None*)  
**then obtain**  $vs es''$  **where** *red:  $\tau reds1r P t h (es', xs) (\text{map Val } vs @ \text{Throw } a \# es'', loc)$*   
**and** *bisim:  $P, es, h \vdash (\text{map Val } vs @ \text{Throw } a \# es'', loc) [\leftrightarrow] (stk, loc, pc, [a])$*  **by** *blast*  
**from** *red* **have**  $\tau reds1r P t h (\text{Val } v \# es', xs) (\text{map Val } (v \# vs) @ \text{Throw } a \# es'', loc)$   
**by**(*auto intro:  $\tau reds1r\text{-cons-}\tau reds1r$* )  
**moreover from** *bisim*  
**have**  $P, e \# es, h \vdash (\text{map Val } (v \# vs) @ \text{Throw } a \# es'', loc) [\leftrightarrow] (stk @ [v], loc, \text{length } (compE2 e) + pc, [a])$   
**by**(*auto intro: bisim1-bisims1.bisims1List2*)  
**ultimately show**  $?case$  **by** *fastforce*

**qed**

**primrec** *conf-xcp' :: 'm prog  $\Rightarrow$  'heap  $\Rightarrow$  'addr option  $\Rightarrow$  bool* **where**  
*conf-xcp' P h None = True*  
*| conf-xcp' P h [a] = ( $\exists D. \text{typeof-addr } h a = \lfloor \text{Class-type } D \rfloor \wedge P \vdash D \preceq^* \text{Throwable}$ )*

**lemma** *conf-xcp-conf-xcp'*:  
*conf-xcp P h xcp i  $\implies$  conf-xcp' P h xcp*  
**by**(*cases xcp*) *auto*

**lemma** *conf-xcp'-compP [simp]: conf-xcp' (compP f P) = conf-xcp' P*  
**by**(*clarsimp simp add: fun-eq-iff conf-xcp'-def rec-option-is-case-option*)

**end**

**context** *J1-heap-base* **begin**

**lemmas**  $\tau red1\text{-Val-simps [simp]} =$   
 $\tau red1r\text{-Val } \tau red1t\text{-Val } \tau reds1r\text{-map-Val } \tau reds1t\text{-map-Val}$

**end**

**context** *J1-JVM-conf-read* **begin**

**lemma** **assumes** *wf: wf-J1-prog P*  
**and** *hconf: hconf h preallocated h*  
**and** *tconf:  $P, h \vdash t \sqrt{t}$*   
**shows** *red1-simulates-exec-instr:*

$\llbracket P, E, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp);$   
 $exec-move-d P t E h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp');$   
 $n + max-vars e \leq length xs; bsok E n; P, h \vdash stk [: \leq] ST; conf-xcp' (compP2 P) h xcp \rrbracket$   
 $\implies \exists e'' xs''. P, E, h' \vdash (e'', xs'') \leftrightarrow (stk', loc', pc', xcp') \wedge$   
 $(if \tau move2 (compP2 P) h stk E pc xcp$   
 $then h' = h \wedge (if xcp' = None \longrightarrow pc < pc' then \tau red1r else \tau red1t) P t h (e, xs) (e'', xs'')$   
 $else \exists ta' e' xs'. \tau red1r P t h (e, xs) (e', xs') \wedge True, P, t \vdash 1 \langle e', (h, xs') \rangle \rightarrow [-ta'] \langle e'', (h', xs'') \rangle$   
 $\wedge ta-bisim wbisim1 (extTA2J1 P ta') ta \wedge \neg \tau move1 P h e' \wedge (call1 e = None \vee no-call2 E pc \vee e'$   
 $= e \wedge xs' = xs))$   
 $(is \llbracket -; ?exec E stk loc pc xcp stk' loc' pc' xcp'; -; -; - \rrbracket$   
 $\implies \exists e'' xs''. P, E, h' \vdash (e'', xs'') \leftrightarrow (stk', loc', pc', xcp') \wedge ?red e xs e'' xs'' E stk pc pc' xcp$   
 $xcp')$

**and** *reds1-simulates-exec-instr*:

$\llbracket P, Es, h \vdash (es, xs) [\leftrightarrow] (stk, loc, pc, xcp);$   
 $exec-moves-d P t Es h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp');$   
 $n + max-varss es \leq length xs; bsoks Es n; P, h \vdash stk [: \leq] ST; conf-xcp' (compP2 P) h xcp \rrbracket$   
 $\implies \exists es'' xs''. P, Es, h' \vdash (es'', xs'') [\leftrightarrow] (stk', loc', pc', xcp') \wedge$   
 $(if \tau moves2 (compP2 P) h stk Es pc xcp$   
 $then h' = h \wedge (if xcp' = None \longrightarrow pc < pc' then \tau reds1r else \tau reds1t) P t h (es, xs) (es'', xs'')$   
 $else \exists ta' es' xs'. \tau reds1r P t h (es, xs) (es', xs') \wedge True, P, t \vdash 1 \langle es', (h, xs') \rangle \rightarrow [-ta'] \langle es'', (h',$   
 $xs'') \rangle \wedge ta-bisim wbisim1 (extTA2J1 P ta') ta \wedge \neg \tau moves1 P h es' \wedge (calls1 es = None \vee no-calls2$   
 $Es pc \vee es' = es \wedge xs' = xs))$   
 $(is \llbracket -; ?execs Es stk loc pc xcp stk' loc' pc' xcp'; -; -; - \rrbracket$   
 $\implies \exists es'' xs''. P, Es, h' \vdash (es'', xs'') [\leftrightarrow] (stk', loc', pc', xcp') \wedge ?reds es xs es'' xs'' Es stk pc$   
 $pc' xcp xcp')$

**proof**(*induction*  $E n e xs stk loc pc xcp$  **and**  $Es n es xs stk loc pc xcp$

*arbitrary*:  $stk' loc' pc' xcp' ST$  **and**  $stk' loc' pc' xcp' ST$  *rule*: *bisim1-bisims1-inducts-split*)

**case** (*bisim1Val2*  $e n v xs$ )

**from**  $\langle ?exec e [v] xs (length (compE2 e)) None stk' loc' pc' xcp' \rangle$

**have** *False* **by**(*auto* *dest*: *exec-meth-length-compE2D simp add*: *exec-move-def*)

**thus** *?case ..*

**next**

**case** (*bisim1New*  $C' n xs$ )

**note** *exec* =  $\langle exec-move-d P t (new C') h ([], xs, 0, None) ta h' (stk', loc', pc', xcp') \rangle$

**have**  $\tau: \neg \tau move2 (compP2 P) h [] (new C') 0 None \neg \tau move1 P h (new C') \mathbf{by}$ (*auto* *simp add*: *tau move2-iff*)

**show** *?case*

**proof**(*cases* *allocate*  $h (Class-type C') = \{\}$ )

**case** *True*

**have**  $P, new C', h' \vdash (THROW OutOfMemory, xs) \leftrightarrow ([], xs, 0, [addr-of-sys-xcpt OutOfMemory])$

**by**(*rule* *bisim1NewThrow*)

**with** *exec*  $\tau$  *True* **show** *?thesis*

**by**(*fastforce* *intro*: *Red1NewFail elim!*: *exec-meth.cases simp add*: *exec-move-def*)

**next**

**case** *False*

**have**  $\bigwedge a h'. P, new C', h' \vdash (addr a, xs) \leftrightarrow ([Addr a], xs, length (compE2 (new C')), None)$

**by**(*rule* *bisim1Val2*) *auto*

**thus** *?thesis* **using** *exec False*  $\tau$

**apply**(*simp add*: *exec-move-def*)

**apply**(*erule* *exec-meth.cases*)

**apply** *simp-all*

**apply** *clarsimp*

**apply**(*auto* *intro!*: *Red1New exI simp add*: *ta-bisim-def*)



```

done
qed
next
case (bisim1NewThrow C' n xs)
from ⟨?exec (new C') [] xs 0 [addr-of-sys-xcpt OutOfMemory] stk' loc' pc' xcp'⟩
have False by(auto elim: exec-meth.cases simp add: exec-move-def)
thus ?case ..
next
case (bisim1NewArray e n e' xs stk loc pc xcp U)
note IH = bisim1NewArray.IH(2)
note exec = ⟨?exec (newA U[e]) stk loc pc xcp stk' loc' pc' xcp'⟩
note bisim = ⟨P,e,h ⊢ (e', xs) ↔ (stk, loc, pc, xcp)⟩
note len = ⟨n + max-vars (newA U[e]) ≤ length xs⟩
note bsok = ⟨bsok (newA U[e]) n⟩
from bisim have pc: pc ≤ length (compE2 e) by(rule bisim1-pc-length-compE2)
show ?case
proof(cases pc < length (compE2 e))
case True
with exec have exec': ?exec e stk loc pc xcp stk' loc' pc' xcp'
by(auto simp add: exec-move-newArray)
from True have τmove2 (compP2 P) h stk (newA U[e]) pc xcp = τmove2 (compP2 P) h stk e
pc xcp by(simp add: τmove2-iff)
moreover have no-call2 e pc ⇒ no-call2 (newA U[e]) pc by(simp add: no-call2-def)
ultimately show ?thesis using IH[OF exec' - - ⟨P,h ⊢ stk [:≤] ST⟩ ⟨conf-xcp' (compP2 P) h
xcp⟩] len bsok
by(fastforce intro: bisim1-bisims1.bisim1NewArray New1ArrayRed elim!: NewArray-τred1r-xt
NewArray-τred1t-xt)
next
case False
with pc have [simp]: pc = length (compE2 e) by simp
with bisim obtain v where stk: stk = [v] and xcp: xcp = None
by(auto dest: bisim1-pc-length-compE2D)
with bisim pc len bsok have red: τred1r P t h (e', xs) (Val v, loc)
by(auto intro: bisim1-Val-τred1r simp add: bsok-def)
hence τred1r P t h (newA U[e], xs) (newA U[Val v], loc) by(rule NewArray-τred1r-xt)
moreover have τ: ¬ τmove2 (compP2 P) h [v] (newA U[e]) pc None by(simp add: τmove2-iff)
moreover have ¬ τmove1 P h (newA U[Val v]) by auto
moreover from exec stk xcp obtain I
where [simp]: v = Intg I by(auto elim!: exec-meth.cases simp add: exec-move-def)
have ∃ ta' e''. P,newA U[e],h' ⊢ (e'',loc) ↔ (stk', loc', pc', xcp') ∧ True,P,t ⊢ 1 ⟨newA U[Val
v],(h, loc)⟩ -ta'→ ⟨e'',(h', loc)⟩ ∧ ta-bisim wbisim1 (extTA2J1 P ta') ta
proof(cases I < s 0)
case True with exec stk xcp show ?thesis
by(fastforce elim!: exec-meth.cases intro: bisim1NewArrayFail Red1NewArrayNegative simp add:
exec-move-def)
next
case False
show ?thesis
proof(cases allocate h (Array-type U (nat (sint I))) = {})
case True
with False exec stk xcp show ?thesis
by(fastforce elim!: exec-meth.cases intro: bisim1NewArrayFail Red1NewArrayFail simp add:
exec-move-def)
next

```

```

    case False
    have  $\wedge a h'. P, newA \ U[e], h' \vdash (addr \ a, \ loc) \leftrightarrow ([Addr \ a], \ loc, \ length \ (compE2 \ (newA \ U[e])),$ 
    None)
    by(rule bisim1Val2) simp
    with False  $\langle \neg \ I < s \ 0 \rangle \ exec \ stk \ xcp$  show ?thesis
    apply(simp add: exec-move-def)
    apply(erule exec-meth.cases)
    apply simp-all
    apply clarsimp
    apply(auto intro!: Red1NewArray exI simp add: ta-bisim-def)
    done
  qed
  qed
  moreover have no-call2 (newA U[e]) pc by(simp add: no-call2-def)
  ultimately show ?thesis using exec stk xcp by fastforce
  qed
next
  case (bisim1NewArrayThrow e n a xs stk loc pc U)
  note exec =  $\langle ?exec \ (newA \ U[e]) \ stk \ loc \ pc \ [a] \ stk' \ loc' \ pc' \ xcp' \rangle$ 
  note bisim =  $\langle P, e, h \vdash (Throw \ a, \ xs) \leftrightarrow (stk, \ loc, \ pc, \ [a]) \rangle$ 
  from bisim have pc:  $pc < length \ (compE2 \ e)$  by(auto dest: bisim1-ThrowD)
  from bisim have match-ex-table (compP2 P) (cname-of h a) (0 + pc) (compxE2 e 0 0) = None
  unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
  with exec pc have False by (auto elim!: exec-meth.cases simp add: exec-move-def)
  thus ?case ..
next
  case (bisim1NewArrayFail e n U a xs v)
  note exec =  $\langle ?exec \ (newA \ U[e]) \ [v] \ xs \ (length \ (compE2 \ e)) \ [a] \ stk' \ loc' \ pc' \ xcp' \rangle$ 
  hence False by(auto elim!: exec-meth.cases dest: match-ex-table-pc-length-compE2 simp add: exec-move-def)
  thus ?case ..
next
  case (bisim1Cast e n e' xs stk loc pc xcp U)
  note IH = bisim1Cast.IH(2)
  note exec =  $\langle ?exec \ (Cast \ U \ e) \ stk \ loc \ pc \ xcp \ stk' \ loc' \ pc' \ xcp' \rangle$ 
  note bisim =  $\langle P, e, h \vdash (e', \ xs) \leftrightarrow (stk, \ loc, \ pc, \ xcp) \rangle$ 
  note len =  $\langle n + max-vars \ (Cast \ U \ e') \leq length \ xs \rangle$ 
  note ST =  $\langle P, h \vdash stk \ [:\leq] \ ST \rangle$ 
  note bsok =  $\langle bsok \ (Cast \ U \ e) \ n \rangle$ 
  from bisim have pc:  $pc \leq length \ (compE2 \ e)$  by(rule bisim1-pc-length-compE2)
  show ?case
  proof(cases pc < length (compE2 e))
    case True
    with exec have exec':  $?exec \ e \ stk \ loc \ pc \ xcp \ stk' \ loc' \ pc' \ xcp' \text{ by } (auto \ simp \ add: \ exec-move-Cast)$ 
    from True have  $\tau move2 \ (compP2 \ P) \ h \ stk \ (Cast \ U \ e) \ pc \ xcp = \tau move2 \ (compP2 \ P) \ h \ stk \ e \ pc$ 
    xcp by(simp add:  $\tau move2$ -iff)
    moreover have no-call2 e pc  $\implies no-call2 \ (Cast \ U \ e) \ pc$  by(simp add: no-call2-def)
    ultimately show ?thesis using IH[OF exec' - - ST  $\langle conf-xcp' \ (compP2 \ P) \ h \ xcp \rangle$  len bsok
    by(fastforce intro: bisim1-bisims1.bisim1Cast Cast1Red elim!: Cast- $\tau red1r$ -xt Cast- $\tau red1t$ -xt)
  next
    case False
    with pc have [simp]:  $pc = length \ (compE2 \ e)$  by simp
    with bisim obtain v where stk:  $stk = [v]$  and xcp:  $xcp = None$ 
    by(auto dest: bisim1-pc-length-compE2D)
    with bisim pc len bsok have red:  $\tau red1r \ P \ t \ h \ (e', \ xs) \ (Val \ v, \ loc)$ 

```

by(auto intro: bisim1-Val- $\tau$ red1r simp add: bsok-def)  
 hence  $\tau$ red1r  $P \ t \ h \ (Cast \ U \ e', \ xs) \ (Cast \ U \ (Val \ v), \ loc)$  by(rule Cast- $\tau$ red1r-xt)  
 also from exec have [simp]:  $h' = h \ ta = \varepsilon$  by(auto simp add: exec-move-def elim!: exec-meth.cases  
 split: if-split-asm)  
 have  $\exists e''. P, Cast \ U \ e, h \vdash (e'', loc) \leftrightarrow (stk', loc', pc', xcp') \wedge True, P, t \vdash 1 \ \langle Cast \ U \ (Val \ v), (h, loc) \rangle$   
 $-\varepsilon \rightarrow \langle e'', (h, loc) \rangle$   
 proof(cases  $P \vdash the \ (typeof_h \ v) \leq U$ )  
 case False with exec stk xcp bsok ST show ?thesis  
 by(fastforce simp add: compP2-def exec-move-def exec-meth-instr list-all2-Cons1 conf-def intro:  
 bisim1CastFail Red1CastFail)  
 next  
 case True  
 have  $P, Cast \ U \ e, h \vdash (Val \ v, loc) \leftrightarrow ([v], loc, length \ (compE2 \ (Cast \ U \ e)), None)$   
 by(rule bisim1Val2) simp  
 with exec stk xcp ST True show ?thesis  
 by(fastforce simp add: compP2-def exec-move-def exec-meth-instr list-all2-Cons1 conf-def intro:  
 Red1Cast)  
 qed  
 then obtain  $e''$  where  $bisim': P, Cast \ U \ e, h \vdash (e'', loc) \leftrightarrow (stk', loc', pc', xcp')$   
 and  $red: True, P, t \vdash 1 \ \langle Cast \ U \ (Val \ v), (h, loc) \rangle -\varepsilon \rightarrow \langle e'', (h, loc) \rangle$  by blast  
 have  $\tau$ move1  $P \ h \ (Cast \ U \ (Val \ v))$  by(rule  $\tau$ move1CastRed)  
 with red have  $\tau$ red1t  $P \ t \ h \ (Cast \ U \ (Val \ v), loc) \ (e'', loc)$  by(auto intro:  $\tau$ red1t-1step)  
 also have  $\tau: \tau$ move2 (compP2  $P$ )  $h \ [v] \ (Cast \ U \ e) \ pc \ None$  by(simp add:  $\tau$ move2-iff)  
 moreover have no-call2 (Cast  $U \ e$ )  $pc$  by(simp add: no-call2-def)  
 ultimately show ?thesis using exec stk xcp bisim' by fastforce  
 qed  
 next  
 case (bisim1CastThrow  $e \ n \ a \ xs \ stk \ loc \ pc \ U$ )  
 note exec =  $\langle ?exec \ (Cast \ U \ e) \ stk \ loc \ pc \ [a] \ stk' \ loc' \ pc' \ xcp' \rangle$   
 note bisim =  $\langle P, e, h \vdash (Throw \ a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$   
 from bisim have  $pc: pc < length \ (compE2 \ e)$  by(auto dest: bisim1-ThrowD)  
 from bisim have match-ex-table (compP2  $P$ ) (cname-of  $h \ a$ ) ( $0 + pc$ ) (compE2  $e \ 0 \ 0$ ) = None  
 unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)  
 with exec pc have False by (auto elim!: exec-meth.cases simp add: exec-move-def)  
 thus ?case ..  
 next  
 case (bisim1CastFail  $e \ n \ U \ xs \ v$ )  
 note exec =  $\langle ?exec \ (Cast \ U \ e) \ [v] \ xs \ (length \ (compE2 \ e)) \ [addr-of-sys-xcpt \ ClassCast] \ stk' \ loc' \ pc' \ xcp' \rangle$   
 hence False by(auto elim!: exec-meth.cases dest: match-ex-table-pc-length-compE2 simp add: exec-move-def)  
 thus ?case ..  
 next  
 case (bisim1InstanceOf  $e \ n \ e' \ xs \ stk \ loc \ pc \ xcp \ U$ )  
 note IH = bisim1InstanceOf.IH(2)  
 note exec =  $\langle ?exec \ (e \ instanceof \ U) \ stk \ loc \ pc \ xcp \ stk' \ loc' \ pc' \ xcp' \rangle$   
 note bisim =  $\langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$   
 note len =  $\langle n + max-vars \ (e' \ instanceof \ U) \leq length \ xs \rangle$   
 note ST =  $\langle P, h \vdash stk \ [:\leq] \ ST \rangle$   
 note bsok =  $\langle bsok \ (e \ instanceof \ U) \ n \rangle$   
 from bisim have  $pc: pc \leq length \ (compE2 \ e)$  by(rule bisim1-pc-length-compE2)  
 show ?case  
 proof(cases  $pc < length \ (compE2 \ e)$ )  
 case True  
 with exec have exec':  $?exec \ e \ stk \ loc \ pc \ xcp \ stk' \ loc' \ pc' \ xcp'$  by(auto simp add: exec-move-InstanceOf)

```

from True have  $\tau\text{move2}$  (compP2 P) h stk (e instanceof U) pc xcp =  $\tau\text{move2}$  (compP2 P) h stk
e pc xcp
  by(simp add:  $\tau\text{move2}\text{-iff}$ )
  moreover have no-call2 e pc  $\implies$  no-call2 (e instanceof U) pc by(simp add: no-call2-def)
  ultimately show ?thesis using IH[OF exec' - - ST  $\langle \text{conf-xcp}' \text{ (compP2 P) h xcp} \rangle$  len bsok]
    by(fastforce intro: bisim1-bisims1.bisim1InstanceOf InstanceOf1Red elim!: InstanceOf- $\tau\text{red1r-xt}$ 
InstanceOf- $\tau\text{red1t-xt}$ )
  next
    case False
    with pc have [simp]: pc = length (compE2 e) by simp
    with bisim obtain v where stk: stk = [v] and xcp: xcp = None
      by(auto dest: bisim1-pc-length-compE2D)
    with bisim pc len bsok have red:  $\tau\text{red1r}$  P t h (e', xs) (Val v, loc)
      by(auto intro: bisim1-Val- $\tau\text{red1r}$  simp add: bsok-def)
    hence  $\tau\text{red1r}$  P t h (e' instanceof U, xs) ((Val v) instanceof U, loc) by(rule InstanceOf- $\tau\text{red1r-xt}$ )
    also let ?v = Bool (v  $\neq$  Null  $\wedge$  P  $\vdash$  the (typeofh v)  $\leq$  U)
    from exec ST stk xcp have [simp]: h' = h ta =  $\varepsilon$  xcp' = None loc' = loc stk' = [?v] pc' = Suc pc
      by(auto simp add: exec-move-def list-all2-Cons1 conf-def compP2-def elim!: exec-meth.cases split:
if-split-asm)
    have bisim': P, e instanceof U, h  $\vdash$  (Val ?v, loc)  $\leftrightarrow$  ([?v], loc, length (compE2 (e instanceof U)),
None)
      by(rule bisim1Val2) simp
    from exec stk xcp ST
    have red: True, P, t  $\vdash$  1  $\langle$  (Val v) instanceof U, (h, loc)  $\rangle \rightarrow \langle$  Val ?v, (h, loc)  $\rangle$ 
      by(auto simp add: compP2-def exec-move-def exec-meth-instr list-all2-Cons1 conf-def intro:
Red1InstanceOf)
    have  $\tau\text{move1}$  P h ((Val v) instanceof U) by(rule  $\tau\text{move1}$ InstanceOfRed)
    with red have  $\tau\text{red1t}$  P t h ((Val v) instanceof U, loc) (Val ?v, loc) by(auto intro:  $\tau\text{red1t-1step}$ )
    also have  $\tau$ :  $\tau\text{move2}$  (compP2 P) h [v] (e instanceof U) pc None by(simp add:  $\tau\text{move2}\text{-iff}$ )
    moreover have no-call2 (e instanceof U) pc by(simp add: no-call2-def)
    ultimately show ?thesis using exec stk xcp bisim' by(fastforce)
  qed
next
  case (bisim1InstanceOfThrow e n a xs stk loc pc U)
  note exec =  $\langle ?\text{exec} \text{ (e instanceof U) stk loc pc } \lfloor a \rfloor \text{ stk' loc' pc' xcp} \rangle$ 
  note bisim =  $\langle P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, \lfloor a \rfloor) \rangle$ 
  from bisim have pc: pc < length (compE2 e) by(auto dest: bisim1-ThrowD)
  from bisim have match-ex-table (compP2 P) (cname-of h a) (0 + pc) (compE2 e 0 0) = None
    unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
  with exec pc have False by (auto elim!: exec-meth.cases simp add: exec-move-def)
  thus ?case ..
next
  case (bisim1Val v n xs)
  from  $\langle ?\text{exec} \text{ (Val v) } \lfloor \rfloor \text{ xs } 0 \text{ None stk' loc' pc' xcp} \rangle$ 
  have stk' = [v] loc' = xs h' = h pc' = length (compE2 (Val v)) xcp' = None
    by(auto elim: exec-meth.cases simp add: exec-move-def)
  moreover have P, Val v, h  $\vdash$  (Val v, xs)  $\leftrightarrow$  ([v], xs, length (compE2 (Val v)), None)
    by(rule bisim1Val2) simp
  moreover have  $\tau\text{move2}$  (compP2 P) h  $\lfloor \rfloor$  (Val v) 0 None by(rule  $\tau\text{move2}$ Val)
  ultimately show ?case by(auto)
next
  case (bisim1Var V n xs)
  note exec =  $\langle ?\text{exec} \text{ (Var V) } \lfloor \rfloor \text{ xs } 0 \text{ None stk' loc' pc' xcp} \rangle$ 
  moreover note len =  $\langle n + \text{max-vars} \text{ (Var V)} \leq \text{length xs} \rangle$ 

```

**moreover have**  $\tau\text{move2}$  ( $\text{compP2 } P$ )  $h$  [] ( $\text{Var } V$ ) 0 None  $\tau\text{move1 } P$   $h$  ( $\text{Var } V$ )  
**by**( $\text{auto intro: } \tau\text{move1Var simp add: } \tau\text{move2-iff}$ )  
**moreover have**  $P, \text{Var } V, h \vdash (\text{Val } (xs ! V), xs) \leftrightarrow ([xs ! V], xs, \text{length } (\text{compE2 } (\text{Var } V)), \text{None})$   
**by**( $\text{rule bisim1Val2}$ ) *simp*  
**ultimately show**  $\text{?case by}(\text{fastforce elim!: exec-meth.cases intro: Red1Var r-into-rtrancpl simp add: exec-move-def})$   
**next**  
**case** ( $\text{bisim1BinOp1 } e1 \ n \ e1' \ xs \ stk \ loc \ pc \ xcp \ e2 \ bop$ )  
**note**  $IH1 = \text{bisim1BinOp1.IH}(2)$   
**note**  $IH2 = \text{bisim1BinOp1.IH}(4)$   
**note**  $\text{exec} = \langle ?\text{exec } (e1 \ll bop \gg e2) \ stk \ loc \ pc \ xcp \ stk' \ loc' \ pc' \ xcp' \rangle$   
**note**  $\text{bisim1} = \langle P, e1, h \vdash (e1', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$   
**note**  $\text{bisim2} = \langle P, e2, h \vdash (e2, loc) \leftrightarrow ([], loc, 0, \text{None}) \rangle$   
**note**  $\text{len} = \langle n + \text{max-vars } (e1' \ll bop \gg e2) \leq \text{length } xs \rangle$   
**note**  $ST = \langle P, h \vdash stk \leq ST \rangle$   
**note**  $\text{bsok} = \langle \text{bsok } (e1 \ll bop \gg e2) \ n \rangle$   
**from**  $\text{bisim1}$  **have**  $pc: pc \leq \text{length } (\text{compE2 } e1)$  **by**( $\text{rule bisim1-pc-length-compE2}$ )  
**show**  $\text{?case}$   
**proof**( $\text{cases } pc < \text{length } (\text{compE2 } e1)$ )  
**case** *True*  
**with**  $\text{exec}$  **have**  $\text{exec}': ?\text{exec } e1 \ stk \ loc \ pc \ xcp \ stk' \ loc' \ pc' \ xcp'$  **by**( $\text{auto simp add: exec-move-BinOp1}$ )  
**from** *True* **have**  $\tau: \tau\text{move2 } (\text{compP2 } P) \ h \ stk \ (e1 \ll bop \gg e2) \ pc \ xcp = \tau\text{move2 } (\text{compP2 } P) \ h \ stk \ e1 \ pc \ xcp$   
**by**( $\text{simp add: } \tau\text{move2-iff}$ )  
**with**  $IH1[OF \ \text{exec}' \ - \ ST \ \langle \text{conf-xcp}' \ (\text{compP2 } P) \ h \ xcp \rangle]$   $\text{bisim2 len bsok}$  **obtain**  $e'' \ xs''$   
**where**  $\text{bisim}': P, e1, h' \vdash (e'', xs'') \leftrightarrow (stk', loc', pc', xcp')$   
**and**  $\text{red}: ?\text{red } e1' \ xs \ e'' \ xs'' \ e1 \ stk \ pc \ pc' \ xcp \ xcp'$  **by** *auto*  
**from**  $\text{bisim}'$  **have**  $P, e1 \ll bop \gg e2, h' \vdash (e'' \ll bop \gg e2, xs'') \leftrightarrow (stk', loc', pc', xcp')$   
**by**( $\text{rule bisim1-bisims1.bisim1BinOp1}$ )  
**moreover from** *True* **have**  $\text{no-call2 } (e1 \ll bop \gg e2) \ pc = \text{no-call2 } e1 \ pc$  **by**( $\text{simp add: no-call2-def}$ )  
**ultimately show**  $\text{?thesis using red } \tau$   
**by**( $\text{fastforce intro: Bin1OpRed1 elim!: BinOp-}\tau\text{red1r-xt1 BinOp-}\tau\text{red1t-xt1}$ )  
**next**  
**case** *False*  
**with**  $pc$  **have**  $pc: pc = \text{length } (\text{compE2 } e1)$  **by** *auto*  
**with**  $\text{bisim1}$  **obtain**  $v$  **where**  $e1': \text{is-val } e1' \longrightarrow e1' = \text{Val } v$   
**and**  $stk: stk = [v]$  **and**  $xcp: xcp = \text{None}$  **and**  $\text{call}: \text{call1 } e1' = \text{None}$   
**by**( $\text{auto dest: bisim1-pc-length-compE2D}$ )  
**with**  $\text{bisim1 } pc \ \text{len} \ \text{bsok}$  **have**  $\text{rede1}': \tau\text{red1r } P \ t \ h \ (e1', xs) \ (\text{Val } v, loc)$   
**by**( $\text{auto intro: bisim1-Val-}\tau\text{red1r simp add: bsok-def}$ )  
**hence**  $\text{rede1}'': \tau\text{red1r } P \ t \ h \ (e1' \ll bop \gg e2, xs) \ (\text{Val } v \ll bop \gg e2, loc)$  **by**( $\text{rule BinOp-}\tau\text{red1r-xt1}$ )  
**moreover from**  $pc \ \text{exec} \ stk \ xcp$   
**have**  $\text{exec-meth-d } (\text{compP2 } P) \ (\text{compE2 } (e1 \ll bop \gg e2)) \ (\text{compxE2 } (e1 \ll bop \gg e2) \ 0 \ 0) \ t \ h \ ([v], loc, \text{length } (\text{compE2 } e1) + 0, \text{None}) \ ta \ h' \ (stk', loc', pc', xcp')$   
**by**( $\text{simp add: compxE2-size-convs compxE2-stack-xlift-convs exec-move-def}$ )  
**hence**  $\text{exec}': \text{exec-meth-d } (\text{compP2 } P) \ (\text{compE2 } e2) \ (\text{stack-xlift } (\text{length } [v]) \ (\text{compxE2 } e2 \ 0 \ 0)) \ t \ h \ ([v], loc, 0, \text{None}) \ ta \ h' \ (stk', loc', pc' - \text{length } (\text{compE2 } e1), xcp')$   
**and**  $pc': pc' \geq \text{length } (\text{compE2 } e1)$  **by**( $\text{safe dest!: BinOp-exec2D}$ ) *simp-all*  
**then obtain**  $PC'$  **where**  $PC': pc' = \text{length } (\text{compE2 } e1) + PC'$   
**by**  $-(\text{rule that}[\text{where } PC' \leq pc' - \text{length } (\text{compE2 } e1)], \text{simp})$   
**from**  $\text{exec}' \ \text{bisim2}$  **obtain**  $stk''$  **where**  $stk': stk' = stk'' @ [v]$   
**and**  $\text{exec}'': \text{exec-move-d } P \ t \ e2 \ h \ ([v], loc, 0, \text{None}) \ ta \ h' \ (stk'', loc', pc' - \text{length } (\text{compE2 } e1), xcp')$   
**by**( $\text{unfold exec-move-def})(\text{drule } (1) \ \text{exec-meth-stk-split, auto})$

```

with pc xcp have  $\tau$ :  $\tau\text{move2}$  (compP2 P) h [v] (e1 «bop» e2) (length (compE2 e1)) None =
 $\tau\text{move2}$  (compP2 P) h [] e2 0 None
  using  $\tau\text{instr-stk-drop-exec-move}$ [where stk=[] and vs = [v]]
  by(simp add:  $\tau\text{move2-iff}$   $\tau\text{instr-stk-drop-exec-move}$ )
from bisim1 have length xs = length loc by(rule bisim1-length-xs)
with IH2[OF exec'', of []] len bsok obtain e'' xs''
  where bisim':  $P, e2, h' \vdash (e'', xs'') \leftrightarrow (stk'', loc', pc' - \text{length}(\text{compE2 } e1), xcp')$ 
  and red:  $?red\ e2\ loc\ e''\ xs''\ e2\ []\ 0\ (pc' - \text{length}(\text{compE2 } e1))\ None\ xcp'$  by auto
from bisim'
  have  $P, e1\ \llbracket bop \rrbracket\ e2, h' \vdash (Val\ v\ \llbracket bop \rrbracket\ e'', xs'') \leftrightarrow (stk''\ @\ [v], loc', \text{length}(\text{compE2 } e1) + (pc' - \text{length}(\text{compE2 } e1)), xcp')$ 
  by(rule bisim1-bisims1.bisim1BinOp2)
  moreover from red  $\tau$ 
  have  $?red\ (Val\ v\ \llbracket bop \rrbracket\ e2)\ loc\ (Val\ v\ \llbracket bop \rrbracket\ e'')\ xs''\ (e1\ \llbracket bop \rrbracket\ e2)\ [v]\ (\text{length}(\text{compE2 } e1))\ pc'\ None\ xcp'$ 
  by(fastforce intro: Bin1OpRed2 elim!: BinOp- $\tau\text{red1r-xt2}$  BinOp- $\tau\text{red1t-xt2}$  simp add: no-call2-def)
  moreover have no-call2 (e1 «bop» e2) (length (compE2 e1)) by(simp add: no-call2-def)
  ultimately show  $?thesis$  using  $\tau\ stk'\ pc\ xcp\ pc'\ PC'\ bisim1\ bisim2\ e1'\ stk\ call$  by(fastforce elim!:
rtrancpl-trans)
qed
next
case (bisim1BinOp2 e2 n e2' xs stk loc pc xcp e1 bop v1)
note IH2 = bisim1BinOp2.IH(2)
note exec =  $\langle ?exec\ (e1\ \llbracket bop \rrbracket\ e2)\ (stk\ @\ [v1])\ loc\ (\text{length}(\text{compE2 } e1) + pc)\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$ 
note bisim1 =  $\langle P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, None) \rangle$ 
note bisim2 =  $\langle P, e2, h \vdash (e2', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
note len =  $\langle n + \text{max-vars}\ (Val\ v1\ \llbracket bop \rrbracket\ e2') \leq \text{length}\ xs \rangle$ 
note bsok =  $\langle bsok\ (e1\ \llbracket bop \rrbracket\ e2)\ n \rangle$ 
note ST =  $\langle P, h \vdash stk\ @\ [v1]\ [:\leq]\ ST \rangle$ 
then obtain ST2 T where  $ST = ST2\ @\ [T]\ P, h \vdash stk\ [:\leq]\ ST2\ P, h \vdash v1\ : \leq\ T$ 
  by(auto simp add: list-all2-append1 length-Suc-conv)
from bisim2 have pc:  $pc \leq \text{length}(\text{compE2 } e2)$  by(rule bisim1-pc-length-compE2)
show  $?case$ 
proof(cases  $pc < \text{length}(\text{compE2 } e2)$ )
  case True
    with exec have exec':  $exec\text{-meth-d}\ (compP2\ P)\ (compE2\ e2)\ (stack\text{-xlift}\ (\text{length}\ [v1])\ (compE2\ e2\ 0\ 0))\ t\ h\ (stk\ @\ [v1], loc, pc, xcp)\ ta\ h'\ (stk', loc', pc' - \text{length}(\text{compE2 } e1), xcp')$ 
    and pc':  $pc' \geq \text{length}(\text{compE2 } e1)$ 
    by(unfold exec-move-def)(safe dest!: BinOp-exec2D)
    from exec' bisim2 obtain stk'' where stk':  $stk' = stk''\ @\ [v1]$ 
    and exec'':  $exec\text{-move-d}\ P\ t\ e2\ h\ (stk, loc, pc, xcp)\ ta\ h'\ (stk'', loc', pc' - \text{length}(\text{compE2 } e1), xcp')$ 
    by -(drule (1) exec-meth-stk-split, auto simp add: exec-move-def)
    with True have  $\tau$ :  $\tau\text{move2}$  (compP2 P) h (stk @ [v1]) (e1 «bop» e2) (length (compE2 e1) + pc)
     $xcp = \tau\text{move2}$  (compP2 P) h stk e2 pc xcp
    by(auto simp add:  $\tau\text{move2-iff}$   $\tau\text{instr-stk-drop-exec-move}$ )
    from IH2[OF exec'' - -  $\langle P, h \vdash stk\ [:\leq]\ ST2 \rangle\ \langle conf\text{-xcp}'\ (compP2\ P)\ h\ xcp \rangle\ len\ bsok$  obtain e'' xs''
    where bisim':  $P, e2, h' \vdash (e'', xs'') \leftrightarrow (stk'', loc', pc' - \text{length}(\text{compE2 } e1), xcp')$ 
    and red:  $?red\ e2'\ xs\ e''\ xs''\ e2\ stk\ pc\ (pc' - \text{length}(\text{compE2 } e1))\ xcp\ xcp'$  by auto
    from bisim' have  $P, e1\ \llbracket bop \rrbracket\ e2, h' \vdash (Val\ v1\ \llbracket bop \rrbracket\ e'', xs'') \leftrightarrow (stk''\ @\ [v1], loc', \text{length}(\text{compE2 } e1) + (pc' - \text{length}(\text{compE2 } e1)), xcp')$ 
    by(rule bisim1-bisims1.bisim1BinOp2)
    with red  $\tau\ stk'\ pc'\ True$  show  $?thesis$ 

```

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  by(fastforce intro: BinOpRed2 elim!: BinOp- $\tau$ red1r-xt2 BinOp- $\tau$ red1t-xt2 split: if-split-asm simp
add: no-call2-def)
next
case False
with pc have [simp]: pc = length (compE2 e2) by simp
with bisim2 obtain v2 where e2': is-val e2'  $\longrightarrow$  e2' = Val v2
  and stk: stk = [v2] and xcp: xcp = None and call: call1 e2' = None
  by(auto dest: bisim1-pc-length-compE2D)
with bisim2 pc len bsok have red:  $\tau$ red1r P t h (e2', xs) (Val v2, loc)
  by(auto intro: bisim1-Val- $\tau$ red1r simp add: bsok-def)
hence red1:  $\tau$ red1r P t h (Val v1 «bop» e2', xs) (Val v1 «bop» Val v2, loc) by(rule BinOp- $\tau$ red1r-xt2)
show ?thesis
proof(cases the (binop bop v1 v2))
  case (Inl v)
  note red1
  also from exec xcp ST stk Inl
  have  $\tau$ red1r P t h (Val v1 «bop» Val v2, loc) (Val v, loc)
    by(force simp add: exec-move-def exec-meth-instr list-all2-Cons1 conf-def compP2-def dest:
binop-progress intro: r-into-rtrancpl Red1BinOp  $\tau$ move1BinOp)
  also have  $\tau$ :  $\tau$ move2 (compP2 P) h [v2, v1] (e1 «bop» e2) (length (compE2 e1) + length
(compE2 e2)) None
    by(simp add:  $\tau$ move2-iff)
  moreover have P, e1 «bop» e2, h  $\vdash$  (Val v, loc)  $\leftrightarrow$  ([v], loc, length (compE2 (e1 «bop» e2)),
None)
    by(rule bisim1Val2) simp
  ultimately show ?thesis using exec xcp stk call Inl by(auto simp add: exec-move-def exec-meth-instr)
next
case (Inr a)
note red1
also from exec xcp ST stk Inr
have  $\tau$ red1r P t h (Val v1 «bop» Val v2, loc) (Throw a, loc)
  by(force simp add: exec-move-def exec-meth-instr list-all2-Cons1 conf-def compP2-def dest:
binop-progress intro: r-into-rtrancpl Red1BinOpFail  $\tau$ move1BinOp)
  also have  $\tau$ :  $\tau$ move2 (compP2 P) h [v2, v1] (e1 «bop» e2) (length (compE2 e1) + length
(compE2 e2)) None
    by(simp add:  $\tau$ move2-iff)
  moreover
  have P, e1 «bop» e2, h  $\vdash$  (Throw a, loc)  $\leftrightarrow$  ([v2, v1], loc, length (compE2 e1) + length (compE2
e2), [a])
    by(rule bisim1BinOpThrow)
  ultimately show ?thesis using exec xcp stk call Inr by(auto simp add: exec-move-def exec-meth-instr)
qed
qed
next
case (bisim1BinOpThrow1 e1 n a xs stk loc pc e2 bop)
note exec = «?exec (e1 «bop» e2) stk loc pc [a] stk' loc' pc' xcp'»
note bisim1 = «P, e1, h  $\vdash$  (Throw a, xs)  $\leftrightarrow$  (stk, loc, pc, [a])»
from bisim1 have pc: pc < length (compE2 e1) by(auto dest: bisim1-ThrowD)
from bisim1 have match-ex-table (compP2 P) (cname-of h a) (0 + pc) (compE2 e1 0 0) = None
  unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
with exec pc have False by(auto elim!: exec-meth.cases simp add: match-ex-table-not-pcs-None
exec-move-def)
thus ?case ..
next

```

```

case (bisim1BinOpThrow2 e2 n a xs stk loc pc e1 bop v1)
note exec = ⟨?exec (e1 «bop» e2) (stk @ [v1]) loc (length (compE2 e1) + pc) [a] stk' loc' pc' xcp'⟩
note bisim2 = ⟨P, e2, h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
hence match-ex-table (compP2 P) (cname-of h a) (length (compE2 e1) + pc) (compE2 e2 (length
(compE2 e1)) 0) = None
  unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
with exec have False
  apply(auto elim!: exec-meth.cases simp add: match-ex-table-append-not-pcs exec-move-def)
  apply(auto simp only: compE2-size-conv compE2-stack-xlift-conv match-ex-table-stack-xlift-eq-Some-conv)
  done
thus ?case ..
next
case (bisim1BinOpThrow e1 n e2 bop a xs v1 v2)
note ⟨?exec (e1 «bop» e2) [v1, v2] xs (length (compE2 e1) + length (compE2 e2)) [a] stk' loc' pc'
xcp'⟩
hence False
by(auto elim!: exec-meth.cases simp add: match-ex-table-append-not-pcs compE2-size-conv exec-move-def
dest!: match-ex-table-shift-pcD match-ex-table-pc-length-compE2)
thus ?case ..
next
case (bisim1LAss1 e n e' xs stk loc pc xcp V)
note IH = bisim1LAss1.IH(2)
note exec = ⟨?exec (V := e) stk loc pc xcp stk' loc' pc' xcp'⟩
note bisim = ⟨P, e, h ⊢ (e', xs) ↔ (stk, loc, pc, xcp)⟩
note len = ⟨n + max-vars (V := e') ≤ length xs⟩
note bsok = ⟨bsok (V := e) n⟩
from bisim have pc: pc ≤ length (compE2 e) by(rule bisim1-pc-length-compE2)
show ?case
proof(cases pc < length (compE2 e))
  case True
    with exec have exec': ?exec e stk loc pc xcp stk' loc' pc' xcp' by(auto simp add: exec-move-LAss)
    from True have τmove2 (compP2 P) h stk (V := e) pc xcp = τmove2 (compP2 P) h stk e pc xcp
by(simp add: τmove2-iff)
    with IH[OF exec' - - ⟨P, h ⊢ stk [:≤] ST⟩ ⟨conf-xcp' (compP2 P) h xcp⟩] len bsok show ?thesis
    by(fastforce intro: bisim1-bisims1.bisim1LAss1 LAss1Red elim!: LAss-τred1r LAss-τred1t simp
add: no-call2-def)
  case False
    with pc have [simp]: pc = length (compE2 e) by simp
    with bisim obtain v where stk: stk = [v] and xcp: xcp = None
    by(auto dest: bisim1-pc-length-compE2D)
    with bisim pc len bsok have red: τred1r P t h (e', xs) (Val v, loc)
    by(auto intro: bisim1-Val-τred1r simp add: bsok-def)
    hence τred1r P t h (V := e', xs) (V := Val v, loc) by(rule LAss-τred1r)
    also have τmove1 P h (V := Val v) by(rule τmove1LAssRed)
    with exec stk xcp have τred1r P t h (V := Val v, loc) (unit, loc[V := v])
    by(auto intro!: r-into-rtranclp Red1LAss simp add: exec-move-def elim!: exec-meth.cases)
    also have τ: τmove2 (compP2 P) h [v] (V := e) pc None by(simp add: τmove2-iff)
    moreover have P, (V := e), h ⊢ (unit, loc[V := v]) ↔ ([], loc[V := v], Suc (length (compE2 e)),
None)
    by(rule bisim1LAss2)
    ultimately show ?thesis using exec stk xcp
    by(fastforce elim!: exec-meth.cases simp add: exec-move-def)
qed

```



```

next
  case (bisim1LAss2 e n V xs)
  note bisim =  $\langle P, e, h \vdash (e, xs) \leftrightarrow ([], xs, 0, None) \rangle$ 
  note exec =  $\langle ?exec (V := e) \ [] \ xs \ (Suc \ (length \ (compE2 \ e))) \ None \ stk' \ loc' \ pc' \ xcp' \rangle$ 
  hence  $stk' = [Unit] \ loc' = xs \ pc' = length \ (compE2 \ (V := e)) \ xcp' = None \ h' = h$ 
  by(auto elim!: exec-meth.cases simp add: exec-move-def)
  moreover have  $\tau move2 \ (compP2 \ P) \ h \ [] \ (V := e) \ (Suc \ (length \ (compE2 \ e))) \ None$  by(simp add:
 $\tau move2\text{-iff}$ )
  moreover have  $P, V := e, h' \vdash (unit, xs) \leftrightarrow ([Unit], xs, length \ (compE2 \ (V := e)), None)$ 
  by(rule bisim1Val2) simp
  ultimately show ?case by(auto)
next
  case (bisim1LAssThrow e n a xs stk loc pc V)
  note exec =  $\langle ?exec (V := e) \ stk \ loc \ pc \ [a] \ stk' \ loc' \ pc' \ xcp' \rangle$ 
  note bisim =  $\langle P, e, h \vdash (Throw \ a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$ 
  from bisim have pc:  $pc < length \ (compE2 \ e)$  by(auto dest: bisim1-ThrowD)
  from bisim have match-ex-table (compP2 P) (cname-of h a) (0 + pc) (compE2 e 0 0) = None
  unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
  with exec pc have False by (auto elim!: exec-meth.cases simp add: exec-move-def)
  thus ?case ..
next
  case (bisim1AAcc1 a n a' xs stk loc pc xcp i)
  note IH1 = bisim1AAcc1.IH(2)
  note IH2 = bisim1AAcc1.IH(4)
  note exec =  $\langle ?exec (a[i]) \ stk \ loc \ pc \ xcp \ stk' \ loc' \ pc' \ xcp' \rangle$ 
  note bisim1 =  $\langle P, a, h \vdash (a', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
  note bisim2 =  $\langle P, i, h \vdash (i, loc) \leftrightarrow ([], loc, 0, None) \rangle$ 
  note len =  $\langle n + max\text{-vars} \ (a'[i]) \leq length \ xs \rangle$ 
  note bsok =  $\langle bsok \ (a[i]) \ n \rangle$ 
  from bisim1 have pc:  $pc \leq length \ (compE2 \ a)$  by(rule bisim1-pc-length-compE2)
  show ?case
  proof(cases pc < length (compE2 a))
    case True
    with exec have exec':  $?exec \ a \ stk \ loc \ pc \ xcp \ stk' \ loc' \ pc' \ xcp'$  by(auto simp add: exec-move-AAcc1)
    from True have  $\tau$ :  $\tau move2 \ (compP2 \ P) \ h \ stk \ (a[i]) \ pc \ xcp = \tau move2 \ (compP2 \ P) \ h \ stk \ a \ pc \ xcp$ 
    by(auto intro:  $\tau move2\text{AAcc1}$ )
    with IH1[OF exec' - -  $\langle P, h \vdash stk \ [:\leq] \ ST \rangle \ \langle conf\text{-}xcp' \ (compP2 \ P) \ h \ xcp \rangle \ len \ bsok$ ] obtain  $e'' \ xs''$ 
    where bisim':  $P, a, h' \vdash (e'', xs'') \leftrightarrow (stk', loc', pc', xcp')$ 
    and red:  $?red \ a' \ xs \ e'' \ xs'' \ a \ stk \ pc \ pc' \ xcp \ xcp'$  by auto
    from bisim' have  $P, a[i], h' \vdash (e''[i], xs'') \leftrightarrow (stk', loc', pc', xcp')$  by(rule bisim1-bisims1.bisim1AAcc1)
    moreover from True have no-call2 (a[i]) pc = no-call2 a pc by(simp add: no-call2-def)
    ultimately show ?thesis using red  $\tau$  by(fastforce intro: AAacc1Red1 elim!: AAacc- $\tau red1r\text{-}xt1$ 
AAcc- $\tau red1t\text{-}xt1$ )
  next
    case False
    with pc have pc:  $pc = length \ (compE2 \ a)$  by auto
    with bisim1 obtain v where a':  $is\text{-val} \ a' \longrightarrow a' = Val \ v$ 
    and stk:  $stk = [v]$  and xcp:  $xcp = None$  and call:  $call1 \ a' = None$ 
    by(auto dest: bisim1-pc-length-compE2D)
    with bisim1 pc len bsok have red1':  $\tau red1r \ P \ t \ h \ (a', xs) \ (Val \ v, loc)$ 
    by(auto intro: bisim1-Val- $\tau red1r$  simp add: bsok-def)
    hence  $\tau red1r \ P \ t \ h \ (a'[i], xs) \ (Val \ v[i], loc)$  by(rule AAacc- $\tau red1r\text{-}xt1$ )
    moreover from pc exec stk xcp
    have exec':  $exec\text{-meth-d} \ (compP2 \ P) \ (compE2 \ a \ @ \ compE2 \ i \ @ \ [ALoad]) \ (compE2 \ a \ 0 \ 0 \ @ \ shift$ 

```

$(\text{length } (\text{compE2 } a)) (\text{stack-xlift } (\text{length } [v]) (\text{compxE2 } i \ 0 \ 0))) \ t \ h \ ([\ ] @ [v], \text{loc}, \text{length } (\text{compE2 } a) + 0, \text{None}) \ ta \ h' (stk', \text{loc}', pc', xcp')$   
**by**(simp add: compxE2-size-convs compxE2-stack-xlift-convs exec-move-def)  
**hence** exec-meth-d (compP2 P) (compE2 i @ [ALoad]) (stack-xlift (length [v]) (compxE2 i 0 0)) t h ([\ ] @ [v], loc, 0, None) ta h' (stk', loc', pc' - length (compE2 a), xcp')  
**by**(rule exec-meth-drop-xt) auto  
**hence** exec-meth-d (compP2 P) (compE2 i) (stack-xlift (length [v]) (compxE2 i 0 0)) t h ([\ ] @ [v], loc, 0, None) ta h' (stk', loc', pc' - length (compE2 a), xcp')  
**by**(rule exec-meth-take) simp  
**with** bisim2 **obtain** stk'' **where** stk': stk' = stk'' @ [v]  
**and** exec'': exec-move-d P t i h ([\ ], loc, 0, None) ta h' (stk'', loc', pc' - length (compE2 a), xcp')  
**unfolding** exec-move-def **by**(blast dest: exec-meth-stk-split)  
**with** pc xcp **have**  $\tau: \tau \text{move2 } (\text{compP2 } P) \ h \ ([\ ] @ [v]) \ (a[i]) \ (\text{length } (\text{compE2 } a)) \ \text{None} = \tau \text{move2 } (\text{compP2 } P) \ h \ [\ ] \ i \ 0 \ \text{None}$   
**using**  $\tau \text{instr-stk-drop-exec-move}$  **where** stk=[\ ] **and** vs=[v]  
**by**(auto simp add:  $\tau \text{move2-iff } \tau \text{instr-stk-drop-exec-move}$ )  
**from** bisim1 **have** length xs = length loc **by**(rule bisim1-length-xs)  
**with** IH2[OF exec''] len bsok **obtain** e'' xs''  
**where** bisim':  $P, i, h' \vdash (e'', xs'') \leftrightarrow (stk'', \text{loc}', pc' - \text{length } (\text{compE2 } a), xcp')$   
**and** red:  $?red \ i \ \text{loc} \ e'' \ xs'' \ i \ [\ ] \ 0 \ (pc' - \text{length } (\text{compE2 } a)) \ \text{None} \ xcp' \ \text{by}(\text{fastforce})$   
**from** bisim'  
**have**  $P, a[i], h' \vdash (\text{Val } v[e''], xs'') \leftrightarrow (stk'' @ [v], \text{loc}', \text{length } (\text{compE2 } a) + (pc' - \text{length } (\text{compE2 } a)), xcp')$   
**by**(rule bisim1-bisims1.bisim1AAcc2)  
**moreover from** red  $\tau$  **have**  $?red \ (\text{Val } v[i]) \ \text{loc} \ (\text{Val } v[e'']) \ xs'' \ (a[i]) \ [v] \ (\text{length } (\text{compE2 } a)) \ pc' \ \text{None} \ xcp'$   
**by**(fastforce intro: AAacc1Red2 elim!: AAacc- $\tau$ red1r-xt2 AAacc- $\tau$ red1t-xt2 split: if-split-asm simp add: no-call2-def)  
**moreover from** exec' **have**  $pc' \geq \text{length } (\text{compE2 } a)$   
**by**(rule exec-meth-drop-xt-pc) auto  
**moreover have** no-call2 (a[i]) pc **using** pc **by**(simp add: no-call2-def)  
**ultimately show** ?thesis **using**  $\tau$  stk' pc xcp stk call  
**by**(fastforce elim!: rtranclp-trans)+  
**qed**  
**next**  
**case** (bisim1AAcc2 i n i' xs stk loc pc xcp a v)  
**note** IH2 = bisim1AAcc2.IH(2)  
**note** exec =  $\langle ?exec \ (a[i]) \ (stk @ [v]) \ \text{loc} \ (\text{length } (\text{compE2 } a) + pc) \ xcp \ stk' \ \text{loc}' \ pc' \ xcp' \rangle$   
**note** bisim2 =  $\langle P, i, h \vdash (i', xs) \leftrightarrow (stk, \text{loc}, pc, xcp) \rangle$   
**note** len =  $\langle n + \text{max-vars } (\text{Val } v[i]) \leq \text{length } xs \rangle$   
**note** bsok =  $\langle bsok \ (a[i]) \ n \rangle$   
**from** bisim2 **have** pc:  $pc \leq \text{length } (\text{compE2 } i)$  **by**(rule bisim1-pc-length-compE2)  
**show** ?case  
**proof**(cases  $pc < \text{length } (\text{compE2 } i)$ )  
**case** True  
**from** exec **have** exec': exec-meth-d (compP2 P) (compE2 a @ compE2 i @ [ALoad]) (compxE2 a 0 0 @ shift (length (compE2 a)) (stack-xlift (length [v]) (compxE2 i 0 0))) t h (stk @ [v], loc, length (compE2 a) + pc, xcp) ta h' (stk', loc', pc', xcp')  
**by**(simp add: shift-compxE2 stack-xlift-compxE2 exec-move-def)  
**hence** exec-meth-d (compP2 P) (compE2 i @ [ALoad]) (stack-xlift (length [v]) (compxE2 i 0 0)) t h (stk @ [v], loc, pc, xcp) ta h' (stk', loc', pc' - length (compE2 a), xcp')  
**by**(rule exec-meth-drop-xt) auto  
**hence** exec-meth-d (compP2 P) (compE2 i) (stack-xlift (length [v]) (compxE2 i 0 0)) t h (stk @ [v], loc, pc, xcp) ta h' (stk', loc', pc' - length (compE2 a), xcp')

```

    using True by(rule exec-meth-take)
  with bisim2 obtain stk'' where stk': stk' = stk'' @ [v]
    and exec'': exec-move-d P t i h (stk, loc, pc, xcp) ta h' (stk'', loc', pc' - length (compE2 a), xcp')
    unfolding exec-move-def by(blast dest: exec-meth-stk-split)
  with True have  $\tau$ :  $\tau\text{move2}$  (compP2 P) h (stk @ [v]) (a[i]) (length (compE2 a) + pc) xcp =
 $\tau\text{move2}$  (compP2 P) h stk i pc xcp
    by(auto simp add:  $\tau\text{move2}$ -iff  $\tau\text{instr-stk-drop-exec-move}$ )
  moreover from  $\langle P, h \vdash \text{stk} @ [v] [\leq] ST \rangle$  obtain ST2
    where  $P, h \vdash \text{stk} [\leq] ST2$  by(auto simp add: list-all2-append1)
  from IH2[OF exec'' - - this  $\langle \text{conf-xcp}' \text{ (compP2 P) h xcp} \rangle$  len bsok] obtain e'' xs''
    where bisim':  $P, i, h' \vdash (e'', xs'') \leftrightarrow (stk'', loc', pc' - \text{length (compE2 a)}, xcp')$ 
    and red:  $?red\ i'\ xs\ e''\ xs''\ i\ stk\ pc\ (pc' - \text{length (compE2 a)})\ xcp\ xcp'$  by auto
  from bisim' have  $P, a[i], h' \vdash (Val\ v[e''], xs'') \leftrightarrow (stk'' @ [v], loc', \text{length (compE2 a)} + (pc' - \text{length (compE2 a)}), xcp')$ 
    by(rule bisim1-bisims1.bisim1AAcc2)
  moreover from exec' have  $pc' \geq \text{length (compE2 a)}$ 
    by(rule exec-meth-drop-xt-pc) auto
  moreover have no-call2 i pc  $\implies$  no-call2 (a[i]) (length (compE2 a) + pc)
    by(simp add: no-call2-def)
  ultimately show ?thesis using red  $\tau\ stk'$  True
    by(fastforce intro: AAcc1Red2 elim!: AAcc- $\tau\text{red1r-xt2}$  AAcc- $\tau\text{red1t-xt2}$  split: if-split-asm)
next
case False
  with pc have [simp]:  $pc = \text{length (compE2 i)}$  by simp
  with bisim2 obtain v2 where i': is-val i'  $\longrightarrow$  i' = Val v2
    and stk:  $stk = [v2]$  and xcp:  $xcp = \text{None}$  and call:  $\text{call1 } i' = \text{None}$ 
    by(auto dest: bisim1-pc-length-compE2D)
  with bisim2 pc len bsok have red:  $\tau\text{red1r } P\ t\ h\ (i', xs)\ (Val\ v2, loc)$ 
    by(auto intro: bisim1-Val- $\tau\text{red1r}$  simp add: bsok-def)
  hence  $\tau\text{red1r } P\ t\ h\ (Val\ v[i], xs)\ (Val\ v[Val\ v2], loc)$  by(rule AAcc- $\tau\text{red1r-xt2}$ )
  moreover have  $\tau$ :  $\neg \tau\text{move2}$  (compP2 P) h [v2, v] (a[i]) (length (compE2 a) + length (compE2
i)) None
    by(simp add:  $\tau\text{move2}$ -iff)
  moreover
    have  $\exists ta' e''. P, a[i], h' \vdash (e'', loc) \leftrightarrow (stk', loc', pc', xcp') \wedge \text{True}, P, t \vdash 1\ \langle Val\ v[Val\ v2], (h, loc) \rangle$ 
    -  $ta' \rightarrow \langle e'', (h', loc) \rangle \wedge ta\text{-bisim } w\text{bisim1 } (extTA2J1\ P\ ta')\ ta$ 
    proof(cases v = Null)
      case True with exec stk xcp show ?thesis
        by(fastforce elim!: exec-meth.cases simp add: exec-move-def intro: bisim1AAccFail Red1AAccNull)
      case False
        with exec xcp stk obtain U el A len I where [simp]:  $v = \text{Addr } A$  and  $hA$ :  $\text{typeof-addr } h\ A =$ 
 $[Array\text{-type } U\ len]$ 
        and [simp]:  $v2 = \text{Intg } I$  by(auto simp add: exec-move-def exec-meth-instr is-Ref-def conf-def
split: if-split-asm)
        show ?thesis
        proof(cases  $0 \leq s\ I \wedge \text{sint } I < \text{int len}$ )
          case True
            hence  $\neg I < s\ 0$  by auto
            moreover
              with exec xcp stk True hA obtain v3 where  $stk' = [v3]$  heap-read h A (ACell (nat (sint I)))
              v3
              by(auto simp add: exec-move-def exec-meth-instr is-Ref-def)
            moreover

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    have  $P, a[i], h' \vdash (\text{Val } v3, \text{loc}) \leftrightarrow ([v3], \text{loc}, \text{length } (\text{compE2 } (a[i])), \text{None})$ 
    by(rule bisim1Val2) simp
    ultimately show ?thesis using exec stk xcp True hA
    by(fastforce elim!: exec-meth.cases intro: Red1AAcc simp add: exec-move-def ta-upd-simps
    ta-bisim-def split: if-split-asm)
  next
    case False
    with exec stk xcp hA show ?thesis
    by(fastforce elim!: exec-meth.cases simp add: is-Ref-def exec-move-def intro: bisim1AAccFail
    Red1AAccBounds split: if-split-asm)
  qed
  qed
  ultimately show ?thesis using exec xcp stk call by fastforce
  qed
next
  case (bisim1AAccThrow1 A n a xs stk loc pc i)
  note exec =  $\langle ?\text{exec } (A[i]) \text{ stk loc pc } [a] \text{ stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$ 
  note bisim1 =  $\langle P, A, h \vdash (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a]) \rangle$ 
  from bisim1 have pc:  $\text{pc} < \text{length } (\text{compE2 } A)$  by(auto dest: bisim1-ThrowD)
  from bisim1 have match-ex-table (compP2 P) (cname-of h a) (0 + pc) (compxE2 A 0 0) = None
  unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
  with exec pc have False
  by(auto elim!: exec-meth.cases simp add: match-ex-table-not-pcs-None exec-move-def)
  thus ?case ..
next
  case (bisim1AAccThrow2 i n a xs stk loc pc A v)
  note exec =  $\langle ?\text{exec } (A[i]) (\text{stk} @ [v]) \text{ loc } (\text{length } (\text{compE2 } A) + \text{pc}) [a] \text{ stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$ 
  note bisim2 =  $\langle P, i, h \vdash (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a]) \rangle$ 
  hence match-ex-table (compP2 P) (cname-of h a) (length (compE2 A) + pc) (compxE2 i (length
  (compE2 A)) 0) = None
  unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
  with exec have False
  apply(auto elim!: exec-meth.cases simp add: match-ex-table-append-not-pcs exec-move-def)
  apply(auto simp only: compxE2-size-conv compxE2-stack-xlift-conv match-ex-table-stack-xlift-eq-Some-conv)
  done
  thus ?case ..
next
  case (bisim1AAccFail a n i ad xs v v')
  note  $\langle ?\text{exec } (a[i]) [v, v'] \text{ xs } (\text{length } (\text{compE2 } a) + \text{length } (\text{compE2 } i)) [ad] \text{ stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$ 
  hence False
  by(auto elim!: exec-meth.cases simp add: match-ex-table-append-not-pcs compxE2-size-conv exec-move-def
  dest!: match-ex-table-shift-pcD match-ex-table-pc-length-compE2)
  thus ?case ..
next
  case (bisim1AAss1 a n a' xs stk loc pc xcp i e)
  note IH1 = bisim1AAss1.IH(2)
  note IH2 = bisim1AAss1.IH(4)
  note exec =  $\langle ?\text{exec } (a[i] := e) \text{ stk loc pc xcp stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$ 
  note bisim1 =  $\langle P, a, h \vdash (a', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \rangle$ 
  note bisim2 =  $\langle P, i, h \vdash (i, \text{loc}) \leftrightarrow ([], \text{loc}, 0, \text{None}) \rangle$ 
  note len =  $\langle n + \text{max-vars } (a'[i] := e) \leq \text{length } xs \rangle$ 
  note bsok =  $\langle \text{bsok } (a[i] := e) \text{ n} \rangle$ 
  from bisim1 have pc:  $\text{pc} \leq \text{length } (\text{compE2 } a)$  by(rule bisim1-pc-length-compE2)
  show ?case

```

**proof**(cases pc < length (compE2 a))  
**case** True  
**with** exec **have** exec': ?exec a stk loc pc xcp stk' loc' pc' xcp' **by**(simp add: exec-move-AAAss1)  
**from** True **have**  $\tau$ :  $\tau\text{move2}$  (compP2 P) h stk (a[i] := e) pc xcp =  $\tau\text{move2}$  (compP2 P) h stk a pc xcp **by**(simp add:  $\tau\text{move2}$ -iff)  
**with** IH1[OF exec' - -  $\langle P, h \vdash \text{stk} [\leq] ST \rangle \langle \text{conf-xcp}' (\text{compP2 } P) h \text{ xcp} \rangle$  len bsok] **obtain** e'' xs''  
**where** bisim':  $P, a, h' \vdash (e'', xs'') \leftrightarrow (stk', loc', pc', xcp')$   
**and** red: ?red a' xs e'' xs'' a stk pc pc' xcp xcp' **by** auto  
**from** bisim' **have**  $P, a[i] := e, h' \vdash (e''[i] := e, xs'') \leftrightarrow (stk', loc', pc', xcp')$   
**by**(rule bisim1-bisims1.bisim1AAAss1)  
**moreover from** True **have** no-call2 (a[i] := e) pc = no-call2 a pc **by**(simp add: no-call2-def)  
**ultimately show** ?thesis **using** red  $\tau$  **by**(fastforce intro: AAAss1Red1 elim!: AAAss- $\tau\text{red1r-xt1}$  AAAss- $\tau\text{red1t-xt1}$ )  
**next**  
**case** False  
**with** pc **have** pc: pc = length (compE2 a) **by** auto  
**with** bisim1 **obtain** v **where** a': is-val a'  $\longrightarrow$  a' = Val v  
**and** stk: stk = [v] **and** xcp: xcp = None **and** call: call1 a' = None  
**by**(auto dest: bisim1-pc-length-compE2D)  
**with** bisim1 pc len bsok **have** rede1':  $\tau\text{red1r } P t h (a', xs) (Val v, loc)$   
**by**(auto intro: bisim1-Val- $\tau\text{red1r}$  simp add: bsok-def)  
**hence**  $\tau\text{red1r } P t h (a'[i] := e, xs) (Val v[i] := e, loc)$  **by**(rule AAAss- $\tau\text{red1r-xt1}$ )  
**moreover from** pc exec stk xcp  
**have** exec': exec-meth-d (compP2 P) (compE2 a @ compE2 i @ compE2 e @ [AStore, Push Unit])  
(compxE2 a 0 0 @ shift (length (compE2 a)) (stack-xlift (length [v]) (compxE2 i 0 0) @ shift (length (compE2 i)) (compxE2 e 0 (Suc (Suc 0))))) t h ([@ [v], loc, length (compE2 a) + 0, None) ta h'  
(stk', loc', pc', xcp')  
**by**(simp add: compxE2-size-convxs compxE2-stack-xlift-convxs exec-move-def)  
**hence** exec-meth-d (compP2 P) (compE2 i @ compE2 e @ [AStore, Push Unit]) (stack-xlift (length [v]) (compxE2 i 0 0) @ shift (length (compE2 i)) (compxE2 e 0 (Suc (Suc 0))))) t h ([@ [v], loc, 0, None) ta h' (stk', loc', pc' - length (compE2 a), xcp')  
**by**(rule exec-meth-drop-xt) auto  
**hence** exec-meth-d (compP2 P) (compE2 i) (stack-xlift (length [v]) (compxE2 i 0 0)) t h ([@ [v], loc, 0, None) ta h' (stk', loc', pc' - length (compE2 a), xcp')  
**by**(rule exec-meth-take-xt) simp  
**with** bisim2 **obtain** stk'' **where** stk': stk' = stk'' @ [v]  
**and** exec'': exec-move-d P t i h ([, loc, 0, None) ta h' (stk'', loc', pc' - length (compE2 a), xcp')  
**unfolding** exec-move-def **by**(blast dest: exec-meth-stk-split)  
**with** pc xcp **have**  $\tau$ :  $\tau\text{move2}$  (compP2 P) h [v] (a[i] := e) (length (compE2 a)) None =  $\tau\text{move2}$  (compP2 P) h [, i 0 None  
**using**  $\tau\text{instr-stk-drop-exec-move}$ [where stk=[ and vs=[v]]  
**by**(auto simp add:  $\tau\text{move2}$ -iff)  
**from** bisim1 **have** length xs = length loc **by**(rule bisim1-length-xs)  
**with** IH2[OF exec''] len bsok **obtain** e'' xs''  
**where** bisim':  $P, i, h' \vdash (e'', xs'') \leftrightarrow (stk'', loc', pc' - \text{length} (\text{compE2 } a), xcp')$   
**and** red: ?red i loc e'' xs'' i [, 0 (pc' - length (compE2 a)) None xcp' **by** fastforce  
**from** bisim'  
**have**  $P, a[i] := e, h' \vdash (Val v[e''] := e, xs'') \leftrightarrow (stk'' @ [v], loc', \text{length} (\text{compE2 } a) + (pc' - \text{length} (\text{compE2 } a)), xcp')$   
**by**(rule bisim1-bisims1.bisim1AAAss2)  
**moreover from** red  $\tau$  **have** ?red (Val v[i] := e) loc (Val v[e''] := e) xs'' (a[i] := e) [v] (length (compE2 a)) pc' None xcp'  
**by**(fastforce intro: AAAss1Red2 elim!: AAAss- $\tau\text{red1r-xt2}$  AAAss- $\tau\text{red1t-xt2}$  split: if-split-asm simp add: no-call2-def)

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moreover from exec' have  $pc' \geq \text{length}(\text{compE2 } a)$ 
  by(rule exec-meth-drop-xt-pc) auto
moreover have no-call2 ( $a[i] := e$ ) pc using pc by(simp add: no-call2-def)
ultimately show ?thesis using  $\tau \text{ stk}' pc \text{ xcp} \text{ stk}$  by(fastforce elim!: rtrancp-trans)
qed
next
case (bisim1AAss2 i n i' xs stk loc pc xcp a e v)
note IH2 = bisim1AAss2.IH(2)
note IH3 = bisim1AAss2.IH(6)
note exec =  $\langle ?exec(a[i] := e) (stk @ [v]) \text{ loc } (\text{length}(\text{compE2 } a) + pc) \text{ xcp } stk' \text{ loc}' pc' \text{ xcp}' \rangle$ 
note bisim2 =  $\langle P, i, h \vdash (i', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
note bisim3 =  $\langle P, e, h \vdash (e, loc) \leftrightarrow ([], loc, 0, None) \rangle$ 
note len =  $\langle n + \text{max-vars}(\text{Val } v[i'] := e) \leq \text{length } xs \rangle$ 
note bsok =  $\langle bsok(a[i] := e) n \rangle$ 
from bisim2 have pc:  $pc \leq \text{length}(\text{compE2 } i)$  by(rule bisim1-pc-length-compE2)
show ?case
proof(cases pc < length(compE2 i))
  case True
    from exec have exec': exec-meth-d (compP2 P) (compE2 a @ compE2 i @ compE2 e @ [AStore, Push Unit]) (compxE2 a 0 0 @ shift (length(compE2 a)) (stack-xlift (length [v]) (compxE2 i 0 0) @ shift (length(compE2 i)) (compxE2 e 0 (Suc (Suc 0))))) t h (stk @ [v], loc, length(compE2 a) + pc, xcp) ta h' (stk', loc', pc', xcp')
    by(simp add: shift-compxE2 stack-xlift-compxE2 ac-simps exec-move-def)
    hence exec-meth-d (compP2 P) (compE2 i @ compE2 e @ [AStore, Push Unit]) (stack-xlift (length [v]) (compxE2 i 0 0) @ shift (length(compE2 i)) (compxE2 e 0 (Suc (Suc 0))))) t h (stk @ [v], loc, pc, xcp) ta h' (stk', loc', pc' - length(compE2 a), xcp')
    by(rule exec-meth-drop-xt) auto
    hence exec-meth-d (compP2 P) (compE2 i) (stack-xlift (length [v]) (compxE2 i 0 0)) t h (stk @ [v], loc, pc, xcp) ta h' (stk', loc', pc' - length(compE2 a), xcp')
    using True by(rule exec-meth-take-xt)
    with bisim2 obtain stk'' where stk':  $stk' = stk'' @ [v]$ 
    and exec'': exec-move-d P t i h (stk, loc, pc, xcp) ta h' (stk'', loc', pc' - length(compE2 a), xcp')
    unfolding exec-move-def by(blast dest: exec-meth-stk-split)
    with True have  $\tau$ :  $\tau \text{ move2}(\text{compP2 } P) h (stk @ [v]) (a[i] := e) (\text{length}(\text{compE2 } a) + pc) \text{ xcp} = \tau \text{ move2}(\text{compP2 } P) h \text{ stk } i \text{ pc } \text{ xcp}$ 
    by(auto simp add: \tau move2-iff \tau instr-stk-drop-exec-move)
    moreover from  $\langle P, h \vdash stk @ [v] [: \leq] ST \rangle$  obtain ST2 where  $P, h \vdash stk [: \leq] ST2$  by(auto simp add: list-all2-append1)
    from IH2[OF exec'' - this  $\langle \text{conf-xcp}'(\text{compP2 } P) h \text{ xcp} \rangle$  len bsok] obtain e'' xs''
    where bisim':  $P, i, h' \vdash (e'', xs'') \leftrightarrow (stk'', loc', pc' - \text{length}(\text{compE2 } a), xcp')$ 
    and red:  $?red i' xs e'' xs'' i \text{ stk } pc (pc' - \text{length}(\text{compE2 } a)) \text{ xcp } xcp'$  by fastforce
    from bisim'
    have  $P, a[i] := e, h' \vdash (\text{Val } v[e''] := e, xs'') \leftrightarrow (stk'' @ [v], loc', \text{length}(\text{compE2 } a) + (pc' - \text{length}(\text{compE2 } a)), xcp')$ 
    by(rule bisim1-bisims1.bisim1AAss2)
    moreover from exec' have  $pc' \geq \text{length}(\text{compE2 } a)$ 
    by(rule exec-meth-drop-xt-pc) auto
    moreover have no-call2 i pc  $\implies \text{no-call2}(a[i] := e) (\text{length}(\text{compE2 } a) + pc)$  by(simp add: no-call2-def)
    ultimately show ?thesis using  $\text{red } \tau \text{ stk}' \text{ True}$ 
    by(fastforce intro: AAAss1Red2 elim!: AAAss-\tau red1r-xt2 AAAss-\tau red1t-xt2 split: if-split-asm)
  next
    case False
    with pc have [simp]:  $pc = \text{length}(\text{compE2 } i)$  by simp

```

**with** *bisim2* **obtain** *v2* **where** *i'*: *is-val i' → i' = Val v2*  
**and** *stk*: *stk = [v2]* **and** *xcp*: *xcp = None* **and** *call*: *call1 i' = None*  
**by**(*auto dest: bisim1-pc-length-compE2D*)  
**with** *bisim2* *pc len bsok* **have** *red*:  $\tau_{red1r} P t h (i', xs) (Val v2, loc)$   
**by**(*auto intro: bisim1-Val- $\tau_{red1r}$  simp add: bsok-def*)  
**hence**  $\tau_{red1r} P t h (Val v[i'] := e, xs) (Val v[Val v2] := e, loc)$  **by**(*rule AAss- $\tau_{red1r}$ -xt2*)  
**moreover from** *pc exec stk xcp*  
**have** *exec'*: *exec-meth-d (compP2 P) ((compE2 a @ compE2 i) @ compE2 e @ [AStore, Push Unit])*  
*((compE2 a 0 0 @ compE2 i (length (compE2 a)) (Suc 0)) @ shift (length (compE2 a @ compE2 i)) (stack-xlift (length [v2, v]) (compE2 e 0 0))) t h ([ @ [v2, v], loc, length (compE2 a @ compE2 i) + 0, None) ta h' (stk', loc', pc', xcp')*  
**by**(*simp add: compE2-size-conv compE2-stack-xlift-conv exec-move-def*)  
**hence** *exec-meth-d (compP2 P) (compE2 e @ [AStore, Push Unit]) (stack-xlift (length [v2, v]) (compE2 e 0 0)) t h ([ @ [v2, v], loc, 0, None) ta h' (stk', loc', pc' - length (compE2 a @ compE2 i), xcp')*  
**by**(*rule exec-meth-drop-xt*) *auto*  
**hence** *exec-meth-d (compP2 P) (compE2 e) (stack-xlift (length [v2, v]) (compE2 e 0 0)) t h ([ @ [v2, v], loc, 0, None) ta h' (stk', loc', pc' - length (compE2 a @ compE2 i), xcp')*  
**by**(*rule exec-meth-take*) *simp*  
**with** *bisim3* **obtain** *stk''* **where** *stk'*: *stk' = stk'' @ [v2, v]*  
**and** *exec''*: *exec-move-d P t e h ([, loc, 0, None) ta h' (stk'', loc', pc' - length (compE2 a @ compE2 i), xcp')*  
**unfolding** *exec-move-def* **by**(*blast dest: exec-meth-stk-split*)  
**with** *pc xcp* **have**  $\tau$ :  $\tau_{move2} (compP2 P) h [v2, v] (a[i] := e) (length (compE2 a) + length (compE2 i)) None = \tau_{move2} (compP2 P) h [] e 0 None$   
**using**  $\tau_{instr-stk-drop-exec-move}$  **where** *stk* =  $[]$  **and** *vs* =  $[v2, v]$  **by**(*simp add:  $\tau_{move2}$ -iff*)  
**from** *bisim2* **have** *length xs = length loc* **by**(*rule bisim1-length-xs*)  
**with** *IH3[OF exec'', of [] len bsok]* **obtain** *e'' xs''*  
**where** *bisim'*:  $P, e, h' \vdash (e'', xs'') \leftrightarrow (stk'', loc', pc' - length (compE2 a) - length (compE2 i), xcp')$   
**and** *red*:  $?red e loc e'' xs'' e [] 0 (pc' - length (compE2 a) - length (compE2 i)) None xcp'$   
**by** *auto (fastforce simp only: length-append diff-diff-left)*  
**from** *bisim'*  
**have**  $P, a[i] := e, h' \vdash (Val v[Val v2] := e'', xs'') \leftrightarrow (stk'' @ [v2, v], loc', length (compE2 a) + length (compE2 i) + (pc' - length (compE2 a) - length (compE2 i)), xcp')$   
**by**(*rule bisim1-bisims1.bisim1AAss3*)  
**moreover from** *red  $\tau$*   
**have**  $?red (Val v[Val v2] := e) loc (Val v[Val v2] := e'') xs'' (a[i] := e) [v2, v] (length (compE2 a) + length (compE2 i)) pc' None xcp'$   
**by**(*fastforce intro: AAss1Red3 elim!: AAss- $\tau_{red1r}$ -xt3 AAss- $\tau_{red1t}$ -xt3 split: if-split-asm simp add: no-call2-def*)  
**moreover from** *exec' have pc' ≥ length (compE2 a @ compE2 i)*  
**by**(*rule exec-meth-drop-xt-pc*) *auto*  
**moreover have** *no-call2 (a[i] := e) (length (compE2 a) + pc)* **by**(*simp add: no-call2-def*)  
**ultimately show** *?thesis* **using**  $\tau$  *stk' pc xcp stk* **by**(*fastforce elim!: rtranclp-trans*)  
**qed**  
**next**  
**case** (*bisim1AAss3 e n e' xs stk loc pc xcp a i v v'*)  
**note** *IH3 = bisim1AAss3.IH(2)*  
**note** *exec = ⟨?exec (a[i] := e) (stk @ [v', v]) loc (length (compE2 a) + length (compE2 i) + pc) xcp stk' loc' pc' xcp'⟩*  
**note** *bisim3 = ⟨P, e, h' ⊢ (e', xs) ↔ (stk, loc, pc, xcp)⟩*  
**note** *len = ⟨n + max-vars (Val v[Val v'] := e') ≤ length xs⟩*  
**note** *bsok = ⟨bsok (a[i] := e) n⟩*

**from**  $\langle P, h \vdash \text{stk} @ [v', v] [:\leq] ST \rangle$  **obtain**  $T \ T' \ ST'$   
**where**  $[simp]: ST = ST' @ [T', T]$   
**and**  $wv: P, h \vdash v : \leq T$  **and**  $wv': P, h \vdash v' : \leq T'$  **and**  $ST': P, h \vdash \text{stk} [:\leq] ST'$   
**by**(*auto simp add: list-all2-Cons1 list-all2-append1*)  
**from** *bisim3* **have**  $pc: pc \leq \text{length} (\text{compE2 } e)$  **by**(*rule bisim1-pc-length-compE2*)  
**show** *?case*  
**proof**(*cases pc < length (compE2 e)*)  
**case** *True*  
**from** *exec* **have** *exec'*: *exec-meth-d* (*compP2 P*) ((*compE2 a @ compE2 i*) @ *compE2 e @ [AStore, Push Unit]*) ((*compxE2 a 0 0 @ compxE2 i (length (compE2 a)) (Suc 0)*) @ *shift (length (compE2 a @ compE2 i)) (stack-xlift (length [v', v]) (compxE2 e 0 0))*) *t h* (*stk @ [v', v], loc, length (compE2 a @ compE2 i) + pc, xcp*) *ta h'* (*stk', loc', pc', xcp'*)  
**by**(*simp add: shift-compxE2 stack-xlift-compxE2 exec-move-def*)  
**hence** *exec-meth-d* (*compP2 P*) (*compE2 e @ [AStore, Push Unit]*) (*stack-xlift (length [v', v]) (compxE2 e 0 0)*) *t h* (*stk @ [v', v], loc, pc, xcp*) *ta h'* (*stk', loc', pc' - length (compE2 a @ compE2 i), xcp'*)  
**by**(*rule exec-meth-drop-xt*) *auto*  
**hence** *exec-meth-d* (*compP2 P*) (*compE2 e*) (*stack-xlift (length [v', v]) (compxE2 e 0 0)*) *t h* (*stk @ [v', v], loc, pc, xcp*) *ta h'* (*stk', loc', pc' - length (compE2 a @ compE2 i), xcp'*)  
**using** *True* **by**(*rule exec-meth-take*)  
**with** *bisim3* **obtain** *stk''* **where** *stk'*: *stk' = stk'' @ [v', v]*  
**and** *exec''*: *exec-move-d P t e h* (*stk, loc, pc, xcp*) *ta h'* (*stk'', loc', pc' - length (compE2 a @ compE2 i), xcp'*)  
**unfolding** *exec-move-def* **by**(*blast dest: exec-meth-stk-split*)  
**with** *True* **have**  $\tau: \tau \text{move2} (\text{compP2 } P) h (\text{stk} @ [v', v]) (a[i] := e) (\text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i) + pc) xcp = \tau \text{move2} (\text{compP2 } P) h \text{stk } e \text{pc } xcp$   
**by**(*auto simp add: \tau move2-iff \tau instr-stk-drop-exec-move*)  
**moreover from** *IH3[OF exec'' - - ST' \conf-xcp' (compP2 P) h xcp]* *len bsok* **obtain** *e'' xs''*  
**where** *bisim'*:  $P, e, h' \vdash (e'', xs'') \leftrightarrow (\text{stk}'', \text{loc}', \text{pc}' - \text{length} (\text{compE2 } a) - \text{length} (\text{compE2 } i), xcp')$   
**and** *red*:  $?red \ e' \ xs \ e'' \ xs'' \ e \ \text{stk} \ pc \ (\text{pc}' - \text{length} (\text{compE2 } a) - \text{length} (\text{compE2 } i)) \ xcp \ xcp'$   
**by** *auto(fastforce simp only: length-append diff-diff-left)*  
**from** *bisim'*  
**have**  $P, a[i] := e, h' \vdash (\text{Val } v \mid \text{Val } v' := e'', xs'') \leftrightarrow (\text{stk}'' @ [v', v], \text{loc}', \text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i) + (\text{pc}' - \text{length} (\text{compE2 } a) - \text{length} (\text{compE2 } i)), xcp')$   
**by**(*rule bisim1-bisims1.bisim1AAss3*)  
**moreover from** *exec'* **have**  $pc' \geq \text{length} (\text{compE2 } a @ \text{compE2 } i)$   
**by**(*rule exec-meth-drop-xt-pc*) *auto*  
**moreover have**  $\text{no-call2 } e \text{pc} \implies \text{no-call2 } (a[i] := e) (\text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i) + pc)$   
**by**(*simp add: no-call2-def*)  
**ultimately show** *?thesis* **using** *red \tau stk' True*  
**by**(*fastforce intro: AAAss1Red3 elim!: AAAss-\tau red1r-xt3 AAAss-\tau red1t-xt3 split: if-split-asm*)  
**next**  
**case** *False*  
**with** *pc* **have**  $[simp]: pc = \text{length} (\text{compE2 } e)$  **by** *simp*  
**with** *bisim3* **obtain** *v2* **where** *stk*:  $\text{stk} = [v2]$  **and** *xcp*:  $xcp = \text{None}$   
**by**(*auto dest: bisim1-pc-length-compE2D*)  
**with** *bisim3 pc len bsok* **have** *red*:  $\tau \text{red1r } P \ t \ h \ (e', xs) \ (\text{Val } v2, \text{loc})$   
**by**(*auto intro: bisim1-Val-\tau red1r simp add: bsok-def*)  
**hence**  $\tau \text{red1r } P \ t \ h \ (\text{Val } v \mid \text{Val } v' := e', xs) \ (\text{Val } v \mid \text{Val } v' := \text{Val } v2, \text{loc})$  **by**(*rule AAAss-\tau red1r-xt3*)  
**moreover have**  $\tau: \neg \tau \text{move2} (\text{compP2 } P) h [v2, v', v] (a[i] := e) (\text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i) + \text{length} (\text{compE2 } e)) \ \text{None}$   
**by**(*simp add: \tau move2-iff*)



```

moreover
  have  $\exists ta' e''. P, a[i] := e, h' \vdash (e'', loc) \leftrightarrow (stk', loc', pc', xcp') \wedge True, P, t \vdash 1 \langle Val v [Val v'] := Val v2, (h, loc) \rangle - ta' \rightarrow \langle e'', (h', loc) \rangle \wedge ta\text{-bisim } w\text{bisim1 } (extTA2J1 P ta') ta$ 
  proof(cases v = Null)
    case True with exec stk xcp show ?thesis
    by(fastforce elim!: exec-meth.cases simp add: exec-move-def intro: bisim1AAssFail Red1AAssNull)
  next
    case False
    with exec stk xcp obtain U A len I where [simp]: v = Addr A v' = Intg I
    and hA: typeof-addr h A = [Array-type U len]
    by(fastforce simp add: exec-move-def exec-meth-instr is-Ref-def)
    from ST' stk obtain T3 where wt3': typeofh v2 = [T3] by(auto simp add: list-all2-Cons1 conf-def)
    show ?thesis
    proof(cases 0 <= s I  $\wedge$  sint I < int len)
      case True
      note I = True
      show ?thesis
      proof(cases P  $\vdash$  T3  $\leq$  U)
        case True
        with exec stk xcp True hA I wt3' show ?thesis
        by(fastforce elim!: exec-meth.cases simp add: compP2-def exec-move-def ta-bisim-def ta-upd-simps intro: Red1AAss bisim1AAss4 split: if-split-asm)
      next
        case False
        with exec stk xcp True hA I wt3' show ?thesis
        by(fastforce elim!: exec-meth.cases simp add: compP2-def exec-move-def intro: Red1AAssStore bisim1AAssFail split: if-split-asm)
      qed
    next
      case False
      with exec stk xcp hA show ?thesis
      by(fastforce elim!: exec-meth.cases intro: bisim1AAssFail Red1AAssBounds simp add: exec-move-def split: if-split-asm)
      qed
    qed
    ultimately show ?thesis using exec xcp stk by(fastforce simp add: no-call2-def)
  qed
next
  case (bisim1AAssThrow1 A n a xs stk loc pc i e)
  note exec =  $\langle ?exec (A[i] := e) \text{ stk loc pc } [a] \text{ stk' loc' pc' xcp' } \rangle$ 
  note bisim1 =  $\langle P, A, h \vdash (Throw a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$ 
  from bisim1 have pc: pc < length (compE2 A) by(auto dest: bisim1-ThrowD)
  from bisim1 have match-ex-table (compP2 P) (cname-of h a) (0 + pc) (compxE2 A 0 0) = None
  unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
  with exec pc have False
  by(auto elim!: exec-meth.cases simp add: match-ex-table-not-pcs-None exec-move-def)
  thus ?case ..
next
  case (bisim1AAssThrow2 i n a xs stk loc pc A e v)
  note exec =  $\langle ?exec (A[i] := e) (stk @ [v]) \text{ loc } (length (compE2 A) + pc) [a] \text{ stk' loc' pc' xcp' } \rangle$ 
  note bisim2 =  $\langle P, i, h \vdash (Throw a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$ 
  from bisim2 have pc: pc < length (compE2 i) by(auto dest: bisim1-ThrowD)
  from bisim2 have match-ex-table (compP2 P) (cname-of h a) (length (compE2 A) + pc) (compxE2

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i (length (compE2 A)) 0) = None
  unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
  with exec pc have False
  apply(auto elim!: exec-meth.cases simp add: compxE2-stack-xlift-convs compxE2-size-convs exec-move-def)
  apply(auto simp add: match-ex-table-append-not-pcs)
  done
  thus ?case ..
next
  case (bisim1AAssThrow3 e n a xs stk loc pc A i v' v)
  note exec = ⟨?exec (A[i] := e) (stk @ [v', v]) loc (length (compE2 A) + length (compE2 i) + pc)
  [a] stk' loc' pc' xcp'⟩
  note bisim2 = ⟨P, e, h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
  from bisim2 have match-ex-table (compP2 P) (cname-of h a) (length (compE2 A) + length (compE2
  i) + pc) (compxE2 e (length (compE2 A) + length (compE2 i)) 0) = None
  unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
  with exec have False
  apply(auto elim!: exec-meth.cases simp add: compxE2-stack-xlift-convs compxE2-size-convs exec-move-def)
  apply(auto dest!: match-ex-table-stack-xliftD match-ex-table-shift-pcD dest: match-ex-table-pcsD
  simp add: match-ex-table-append match-ex-table-shift-pc-None)
  done
  thus ?case ..
next
  case (bisim1AAssFail a n i e ad xs v' v v'')
  note exec = ⟨?exec (a[i] := e) [v', v, v''] xs (length (compE2 a) + length (compE2 i) + length
  (compE2 e)) [ad] stk' loc' pc' xcp'⟩
  hence False
  by(auto elim!: exec-meth.cases simp add: match-ex-table-append exec-move-def
  dest!: match-ex-table-shift-pcD match-ex-table-pc-length-compE2)
  thus ?case ..
next
  case (bisim1AAss4 a n i e xs)
  have P, a[i] := e, h ⊢ (unit, xs) ↔ ([Unit], xs, length (compE2 (a[i] := e)), None) by(rule
  bisim1Val2) simp
  moreover have τmove2 (compP2 P) h [] (a[i] := e) (Suc (length (compE2 a) + length (compE2
  i) + length (compE2 e))) None
  by(simp add: τmove2-iff)
  moreover note ⟨?exec (a[i] := e) [] xs (Suc (length (compE2 a) + length (compE2 i) + length
  (compE2 e))) None stk' loc' pc' xcp'⟩
  ultimately show ?case
  by(fastforce elim!: exec-meth.cases simp add: ac-simps exec-move-def)
next
  case (bisim1ALength a n a' xs stk loc pc xcp)
  note IH = bisim1ALength.IH(2)
  note exec = ⟨?exec (a.length) stk loc pc xcp stk' loc' pc' xcp'⟩
  note bisim = ⟨P, a, h ⊢ (a', xs) ↔ (stk, loc, pc, xcp)⟩
  note len = ⟨n + max-vars (a'.length) ≤ length xs⟩
  note bsok = ⟨bsok (a.length) n⟩
  from bisim have pc: pc ≤ length (compE2 a) by(rule bisim1-pc-length-compE2)
  show ?case
  proof(cases pc < length (compE2 a))
    case True
    with exec have exec': ?exec a stk loc pc xcp stk' loc' pc' xcp' by(auto simp add: exec-move-ALength)
    from True have τ: τmove2 (compP2 P) h stk (a.length) pc xcp = τmove2 (compP2 P) h stk a pc
    xcp by(simp add: τmove2-iff)

```

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with IH[OF exec' - - <P,h ⊢ stk [:≤] ST> <conf-xcp' (compP2 P) h xcp>] len bsok obtain e'' xs''
  where bisim': P,a,h' ⊢ (e'', xs'') ↔ (stk', loc', pc', xcp')
  and red: ?red a' xs e'' xs'' a stk pc pc' xcp xcp' by auto
from bisim' have P,a·length,h' ⊢ (e''·length, xs'') ↔ (stk', loc', pc', xcp')
  by(rule bisim1-bisims1.bisim1ALength)
with red τ show ?thesis by(fastforce intro: ALength1Red elim!: ALength-τred1r-xt ALength-τred1t-xt
simp add: no-call2-def)
next
case False
with pc have pc: pc = length (compE2 a) by auto
with bisim obtain v where stk: stk = [v] and xcp: xcp = None
  by(auto dest: bisim1-pc-length-compE2D)
with bisim pc len bsok have τred1r P t h (a', xs) (Val v, loc)
  by(auto intro: bisim1-Val-τred1r simp add: bsok-def)
hence τred1r P t h (a'·length, xs) (Val v·length, loc) by(rule ALength-τred1r-xt)
moreover
  moreover have τ: ¬ τmove2 (compP2 P) h [v] (a·length) (length (compE2 a)) None by(simp
add: τmove2-iff)
  moreover have ∃ ta' e''. P,a·length,h' ⊢ (e'',loc) ↔ (stk', loc', pc', xcp') ∧ True,P,t ⊢ 1 <Val
v·length, (h, loc)> -ta'→ <e'',(h', loc)> ∧ ta-bisim wbisim1 (extTA2J1 P ta') ta
  proof(cases v = Null)
    case True with exec stk xcp pc show ?thesis
      by(fastforce elim!: exec-meth.cases simp add: exec-move-def intro: bisim1ALengthNull Red1ALength-
Null)
  next
    case False
      with exec stk xcp pc <P,h ⊢ stk [:≤] ST>
      obtain U A len where [simp]: v = Addr A
        and hA: typeof-addr h A = [Array-type U len]
        by(fastforce simp add: exec-move-def exec-meth-instr is-Ref-def list-all2-Cons1)
      have P,a·length,h' ⊢ (Val (Intg (word-of-int (int len))),loc) ↔ ([Intg (word-of-int (int len))], loc,
length (compE2 (a·length))), None)
        by(rule bisim1Val2) simp
      thus ?thesis using exec stk xcp hA pc
        by(fastforce elim!: exec-meth.cases intro: Red1ALength simp add: exec-move-def)
  qed
ultimately show ?thesis using τ pc xcp stk by(fastforce elim!: rtranclp-trans simp add: no-call2-def)
qed
next
case (bisim1ALengthThrow A n a xs stk loc pc)
note exec = <?exec (A·length) stk loc pc [a] stk' loc' pc' xcp'>
note bisim1 = <P,A,h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])>
from bisim1 have pc: pc < length (compE2 A) by(auto dest: bisim1-ThrowD)
from bisim1 have match-ex-table (compP2 P) (cname-of h a) (0 + pc) (compE2 A 0 0) = None
  unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
with exec pc have False by(auto elim!: exec-meth.cases simp add: exec-move-def)
thus ?case ..
next
case (bisim1ALengthNull a n xs)
note exec = <?exec (a·length) [Null] xs (length (compE2 a)) [addr-of-sys-xcpt NullPointer] stk' loc'
pc' xcp'>
hence False by(auto elim!: exec-meth.cases dest!: match-ex-table-pc-length-compE2 simp add: exec-move-def)
thus ?case ..
next

```

```

case (bisim1FAcc e n e' xs stk loc pc xcp F D)
note IH = bisim1FAcc.IH(2)
note exec = ⟨?exec (e•F{D}) stk loc pc xcp stk' loc' pc' xcp'⟩
note bisim = ⟨P, e, h ⊢ (e', xs) ↔ (stk, loc, pc, xcp)⟩
note len = ⟨n + max-vars (e'•F{D}) ≤ length xs⟩
note bsok = ⟨bsok (e•F{D}) n⟩
from bisim have pc: pc ≤ length (compE2 e) by(rule bisim1-pc-length-compE2)
show ?case
proof(cases pc < length (compE2 e))
  case True
    with exec have exec': ?exec e stk loc pc xcp stk' loc' pc' xcp' by(simp add: exec-move-FAcc)
    from True have τ: τmove2 (compP2 P) h stk (e•F{D}) pc xcp = τmove2 (compP2 P) h stk e pc
    xcp by(simp add: τmove2-iff)
    with IH[OF exec' - - ⟨P, h ⊢ stk [:≤] ST⟩ ⟨conf-xcp' (compP2 P) h xcp⟩] len bsok obtain e'' xs''
      where bisim': P, e, h' ⊢ (e'', xs'') ↔ (stk', loc', pc', xcp')
      and red: ?red e' xs e'' xs'' e stk pc pc' xcp xcp' by auto
    from bisim' have P, e•F{D}, h' ⊢ (e''•F{D}, xs'') ↔ (stk', loc', pc', xcp')
      by(rule bisim1-bisims1.bisim1FAcc)
    with red τ show ?thesis by(fastforce intro: FAcc1Red elim!: FAcc-τred1r-xt FAcc-τred1t-xt simp
    add: no-call2-def)
  next
    case False
      with pc have pc: pc = length (compE2 e) by auto
      with bisim obtain v where stk: stk = [v] and xcp: xcp = None
      by(auto dest: bisim1-pc-length-compE2D)
      with bisim pc len bsok have τred1r P t h (e', xs) (Val v, loc)
      by(auto intro: bisim1-Val-τred1r simp add: bsok-def)
      hence τred1r P t h (e'•F{D}, xs) (Val v•F{D}, loc) by(rule FAcc-τred1r-xt)
      moreover have τ: ¬ τmove2 (compP2 P) h [v] (e•F{D}) (length (compE2 e)) None by(simp add:
      τmove2-iff)
      moreover have ∃ ta' e''. P, e•F{D}, h' ⊢ (e'', loc) ↔ (stk', loc', pc', xcp') ∧ True, P, t ⊢ 1 ⟨Val
      v•F{D}, (h, loc)⟩ -ta'→ ⟨e'', (h', loc)⟩ ∧ ta-bisim wbisim1 (extTA2J1 P ta') ta
      proof(cases v = Null)
        case True with exec stk xcp pc show ?thesis
        by(fastforce elim!: exec-meth.cases simp add: exec-move-def intro: bisim1FAccNull Red1FAccNull)
      next
        case False
          with exec stk xcp pc ⟨P, h ⊢ stk [:≤] ST⟩
          obtain A where [simp]: v = Addr A
          by(fastforce simp add: exec-move-def exec-meth-instr is-Ref-def compP2-def)
          from exec False pc stk xcp obtain v' where v': heap-read h A (CField D F) v' stk' = [v']
          by(auto simp add: exec-move-def exec-meth-instr)
          have P, e•F{D}, h' ⊢ (Val v', loc) ↔ ([v'], loc, length (compE2 (e•F{D})), None)
          by(rule bisim1Val2) simp
          thus ?thesis using exec stk xcp pc v'
          by(fastforce elim!: exec-meth.cases intro: Red1FAcc simp add: exec-move-def ta-upd-simps
          ta-bisim-def)
        qed
      ultimately show ?thesis using τ pc xcp stk by(fastforce elim!: rtranclp-trans simp add: no-call2-def)
      qed
  next
    case (bisim1FAccThrow e n a xs stk loc pc F D)
    note exec = ⟨?exec (e•F{D}) stk loc pc [a] stk' loc' pc' xcp'⟩
    note bisim1 = ⟨P, e, h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩

```

```

from bisim1 have pc: pc < length (compE2 e) by(auto dest: bisim1-ThrowD)
from bisim1 have match-ex-table (compP2 P) (cname-of h a) (0 + pc) (compxE2 e 0 0) = None
  unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
with exec pc have False by(auto elim!: exec-meth.cases simp add: exec-move-def)
thus ?case ..
next
  case (bisim1FAccNull e n F D xs)
  note exec = ⟨?exec (e.F{D}) [Null] xs (length (compE2 e)) [addr-of-sys-xcpt NullPointer] stk' loc'
pc' xcp'⟩
  hence False by(auto elim!: exec-meth.cases dest!: match-ex-table-pc-length-compE2 simp add: exec-move-def)
  thus ?case ..
next
  case (bisim1FAss1 e n e' xs stk loc pc xcp e2 F D)
  note IH1 = bisim1FAss1.IH(2)
  note IH2 = bisim1FAss1.IH(4)
  note exec = ⟨?exec (e.F{D} := e2) stk loc pc xcp stk' loc' pc' xcp'⟩
  note bisim1 = ⟨P, e, h ⊢ (e', xs) ↔ (stk, loc, pc, xcp)⟩
  note bisim2 = ⟨P, e2, h ⊢ (e2, loc) ↔ ([], loc, 0, None)⟩
  note len = ⟨n + max-vars (e'.F{D} := e2) ≤ length xs⟩
  note bsok = ⟨bsok (e.F{D} := e2) n⟩
  from bisim1 have pc: pc ≤ length (compE2 e) by(rule bisim1-pc-length-compE2)
  show ?case
  proof(cases pc < length (compE2 e))
    case True
    with exec have exec': ?exec e stk loc pc xcp stk' loc' pc' xcp' by(simp add: exec-move-FAss1)
    from True have τ: τmove2 (compP2 P) h stk (e.F{D} := e2) pc xcp = τmove2 (compP2 P) h
stk e pc xcp
      by(simp add: τmove2-iff)
    with IH1[OF exec' - - ⟨P, h ⊢ stk [:≤] ST⟩ ⟨conf-xcp' (compP2 P) h xcp⟩] len bsok obtain e'' xs''
      where bisim': P, e, h' ⊢ (e'', xs'') ↔ (stk', loc', pc', xcp')
      and red: ?red e' xs e'' xs'' e stk pc pc' xcp xcp' by auto
    from bisim' have P, e.F{D} := e2, h' ⊢ (e''.F{D} := e2, xs'') ↔ (stk', loc', pc', xcp')
      by(rule bisim1-bisims1.bisim1FAss1)
    with red τ show ?thesis by(fastforce intro: FAss1Red1 elim!: FAss-τred1r-xt1 FAss-τred1t-xt1 simp
add: no-call2-def)
    next
    case False
    with pc have pc: pc = length (compE2 e) by auto
    with bisim1 obtain v where stk: stk = [v] and xcp: xcp = None
      by(auto dest: bisim1-pc-length-compE2D)
    with bisim1 pc len bsok have rede1': τred1r P t h (e', xs) (Val v, loc)
      by(auto intro: bisim1-Val-τred1r simp add: bsok-def)
    hence τred1r P t h (e'.F{D} := e2, xs) (Val v.F{D} := e2, loc) by(rule FAss-τred1r-xt1)
    moreover from pc exec stk xcp
      have exec': exec-meth-d (compP2 P) (compE2 e @ compE2 e2 @ [Putfield F D, Push Unit])
(compxE2 e 0 0 @ shift (length (compE2 e)) (stack-xlift (length [v]) (compxE2 e2 0 0))) t h ([v] @ [v],
loc, length (compE2 e) + 0, None) ta h' (stk', loc', pc', xcp')
      by(simp add: compxE2-size-convs compxE2-stack-xlift-convs exec-move-def)
    hence exec-meth-d (compP2 P) (compE2 e2 @ [Putfield F D, Push Unit]) (stack-xlift (length [v])
(compxE2 e2 0 0)) t h ([v] @ [v], loc, 0, None) ta h' (stk', loc', pc' - length (compE2 e), xcp')
      by(rule exec-meth-drop-xt) auto
    hence exec-meth-d (compP2 P) (compE2 e2) (stack-xlift (length [v]) (compxE2 e2 0 0)) t h ([v] @
[v], loc, 0, None) ta h' (stk', loc', pc' - length (compE2 e), xcp')
      by(rule exec-meth-take) simp

```

**with** *bisim2* **obtain** *stk''* **where** *stk'*: *stk'* = *stk''* @ [*v*]  
**and** *exec''*: *exec-move-d* *P t e2 h* ([], *loc*, 0, *None*) *ta h'* (*stk''*, *loc'*, *pc'* - *length* (*compE2* *e*), *xcp'*)  
**unfolding** *exec-move-def* **by**(*blast dest: exec-meth-stk-split*)  
**with** *pc xcp* **have**  $\tau$ :  $\tau\text{move2}$  (*compP2* *P*) *h* [*v*] (*e*·*F*{*D*} := *e2*) (*length* (*compE2* *e*)) *None* =  $\tau\text{move2}$  (*compP2* *P*) *h* [] *e2* 0 *None*  
**using**  $\tau\text{instr-stk-drop-exec-move}$ [**where** *stk*=[] **and** *vs* = [*v*]] **by**(*simp add:  $\tau\text{move2-iff}$* )  
**from** *bisim1* **have** *length xs* = *length loc* **by**(*rule bisim1-length-xs*)  
**with** *IH2*[*OF exec''*, *of* []] *len bsok* **obtain** *e'' xs''*  
**where** *bisim'*: *P, e2, h' ⊢ (e'', xs'') ↔ (stk'', loc', pc' - length (compE2 e), xcp')*  
**and** *red*: *?red e2 loc e'' xs'' e2 [] 0 (pc' - length (compE2 e)) None xcp'* **by** *auto*  
**from** *bisim'*  
**have** *P, e·F{D} := e2, h' ⊢ (Val v·F{D} := e'', xs'') ↔ (stk'' @ [v], loc', length (compE2 e) + (pc' - length (compE2 e)), xcp')*  
**by**(*rule bisim1-bisims1.bisim1FAss2*)  
**moreover from** *red  $\tau$*   
**have** *?red (Val v·F{D} := e2) loc (Val v·F{D} := e'') xs'' (e·F{D} := e2) [v] (length (compE2 e)) pc' None xcp'*  
**by**(*fastforce intro: FAss1Red2 elim!: FAss- $\tau\text{red1r-xt2}$  FAss- $\tau\text{red1t-xt2}$  split: if-split-asm simp add: no-call2-def*)  
**moreover from** *exec'* **have** *pc' ≥ length (compE2 e)*  
**by**(*rule exec-meth-drop-xt-pc*) *auto*  
**moreover have** *no-call2 (e·F{D} := e2) pc* **using** *pc* **by**(*simp add: no-call2-def*)  
**ultimately show** *?thesis* **using**  $\tau$  *stk' pc xcp stk* **by**(*fastforce elim!: rtranclp-trans*)  
**qed**  
**next**  
**case** (*bisim1FAss2 e2 n e' xs stk loc pc xcp e F D v*)  
**note** *IH2 = bisim1FAss2.IH(2)*  
**note** *exec = ⟨?exec (e·F{D} := e2) (stk @ [v]) loc (length (compE2 e) + pc) xcp stk' loc' pc' xcp'⟩*  
**note** *bisim2 = ⟨P, e2, h ⊢ (e', xs) ↔ (stk, loc, pc, xcp)⟩*  
**note** *len = ⟨n + max-vars (Val v·F{D} := e') ≤ length xs⟩*  
**note** *bsok = ⟨bsok (e·F{D} := e2) n⟩*  
**note** *ST = ⟨P, h ⊢ stk @ [v] [≤] ST'⟩*  
**then obtain** *T ST'* **where** *ST'*: *P, h ⊢ stk [≤] ST'* **and** *T: typeof<sub>h</sub> v = [T]*  
**by**(*auto simp add: list-all2-append1 list-all2-Cons1 conf-def*)  
**from** *bisim2* **have** *pc: pc ≤ length (compE2 e2)* **by**(*rule bisim1-pc-length-compE2*)  
**show** *?case*  
**proof**(*cases pc < length (compE2 e2)*)  
**case True**  
**from** *exec* **have** *exec'*: *exec-meth-d* (*compP2* *P*) (*compE2* *e* @ *compE2* *e2* @ [*Putfield F D, Push Unit*]) (*compxE2* *e* 0 0 @ *shift* (*length* (*compE2* *e*)) (*stack-xlift* (*length* [*v*]) (*compxE2* *e2* 0 0))) *t h* (*stk* @ [*v*], *loc*, *length* (*compE2* *e*) + *pc*, *xcp*) *ta h'* (*stk'*, *loc'*, *pc'*, *xcp'*)  
**by**(*simp add: shift-compxE2 stack-xlift-compxE2 exec-move-def*)  
**hence** *exec-meth-d* (*compP2* *P*) (*compE2* *e2* @ [*Putfield F D, Push Unit*]) (*stack-xlift* (*length* [*v*]) (*compxE2* *e2* 0 0)) *t h* (*stk* @ [*v*], *loc*, *pc*, *xcp*) *ta h'* (*stk'*, *loc'*, *pc' - length (compE2 e)*, *xcp'*)  
**by**(*rule exec-meth-drop-xt*) *auto*  
**hence** *exec-meth-d* (*compP2* *P*) (*compE2* *e2*) (*stack-xlift* (*length* [*v*]) (*compxE2* *e2* 0 0)) *t h* (*stk* @ [*v*], *loc*, *pc*, *xcp*) *ta h'* (*stk'*, *loc'*, *pc' - length (compE2 e)*, *xcp'*)  
**using** *True* **by**(*rule exec-meth-take*)  
**with** *bisim2* **obtain** *stk''* **where** *stk'*: *stk' = stk''* @ [*v*]  
**and** *exec''*: *exec-move-d* *P t e2 h* (*stk*, *loc*, *pc*, *xcp*) *ta h'* (*stk''*, *loc'*, *pc' - length (compE2 e)*, *xcp'*)  
**unfolding** *exec-move-def* **by**(*blast dest: exec-meth-stk-split*)

**with** *True* **have**  $\tau: \tau \text{move2} (\text{compP2 } P) \ h \ (stk \ @ \ [v]) \ (e \cdot F\{D\} := e2) \ (\text{length} (\text{compE2 } e) + pc)$   
 $xcp = \tau \text{move2} (\text{compP2 } P) \ h \ stk \ e2 \ pc \ xcp$   
**by**(*auto simp add:  $\tau \text{move2-iff}$   $\tau \text{instr-stk-drop-exec-move}$* )  
**moreover from**  $IH2[OF \ \text{exec}'' - - ST' \langle \text{conf-xcp}' (\text{compP2 } P) \ h \ xcp \rangle] \ \text{len} \ bsok$  **obtain**  $e'' \ xs''$   
**where**  $\text{bisim}' : P, e2, h' \vdash (e'', xs'') \leftrightarrow (stk'', loc', pc' - \text{length} (\text{compE2 } e), xcp')$   
**and**  $\text{red} : ?\text{red } e' \ xs \ e'' \ xs'' \ e2 \ stk \ pc \ (pc' - \text{length} (\text{compE2 } e)) \ xcp \ xcp' \ \text{by} \ \text{auto}$   
**from**  $\text{bisim}'$  **have**  $P, e \cdot F\{D\} := e2, h' \vdash (Val \ v \cdot F\{D\} := e'', xs'') \leftrightarrow (stk'' \ @ \ [v], loc', \text{length} (\text{compE2 } e) + (pc' - \text{length} (\text{compE2 } e)), xcp')$   
**by**(*rule bisim1-bisims1.bisim1FAss2*)  
**moreover from**  $\text{exec}'$  **have**  $pc' \geq \text{length} (\text{compE2 } e)$   
**by**(*rule exec-meth-drop-xt-pc*) *auto*  
**ultimately show**  $?thesis$  **using**  $\text{red } \tau \ stk' \ True$   
**by**(*fastforce intro: FAss1Red2 elim!: FAss- $\tau \text{red1r-xt2}$  FAss- $\tau \text{red1t-xt2}$  split: if-split-asm simp add: no-call2-def*)  
**next**  
**case** *False*  
**with**  $pc$  **have**  $[simp]: pc = \text{length} (\text{compE2 } e2)$  **by** *simp*  
**with**  $\text{bisim2}$  **obtain**  $v2$  **where**  $stk: stk = [v2]$  **and**  $xcp: xcp = None$   
**by**(*auto dest: bisim1-pc-length-compE2D*)  
**with**  $\text{bisim2}$   $pc \ \text{len} \ bsok$  **have**  $\text{red}: \tau \text{red1r } P \ t \ h \ (e', xs) \ (Val \ v2, loc)$   
**by**(*auto intro: bisim1-Val- $\tau \text{red1r}$  simp add: bsok-def*)  
**hence**  $\tau \text{red1r } P \ t \ h \ (Val \ v \cdot F\{D\} := e', xs) \ (Val \ v \cdot F\{D\} := Val \ v2, loc)$  **by**(*rule FAss- $\tau \text{red1r-xt2}$* )  
**moreover have**  $\tau: \neg \tau \text{move2} (\text{compP2 } P) \ h \ [v2, v] \ (e \cdot F\{D\} := e2) \ (\text{length} (\text{compE2 } e) + \text{length} (\text{compE2 } e2)) \ None$  **by**(*simp add:  $\tau \text{move2-iff}$* )  
**moreover**  
**have**  $\exists ta' \ e''. P, e \cdot F\{D\} := e2, h' \vdash (e'', loc) \leftrightarrow (stk', loc', pc', xcp') \wedge True, P, t \vdash 1 \langle Val \ v \cdot F\{D\} := Val \ v2, (h, loc) \rangle - ta' \rightarrow \langle e'', (h', loc) \rangle \wedge ta \text{-bisim} \ w\text{bisim1} \ (extTA2J1 \ P \ ta') \ ta$   
**proof**(*cases v = Null*)  
**case** *True* **with**  $\text{exec } stk \ xcp$  **show**  $?thesis$   
**by**(*fastforce elim!: exec-meth.cases simp add: exec-move-def intro: bisim1FAssNull Red1FAssNull*)  
**next**  
**case** *False* **with**  $\text{exec } stk \ xcp \ T$  **show**  $?thesis$   
**by**(*fastforce simp add: exec-move-def compP2-def exec-meth-instr is-Ref-def ta-upd-simps ta-bisim-def intro: bisim1FAss3 Red1FAss*)  
**qed**  
**ultimately show**  $?thesis$  **using**  $\text{exec } xcp \ stk$  **by**(*fastforce simp add: no-call2-def*)  
**qed**  
**next**  
**case** ( $\text{bisim1FAssThrow1 } e \ n \ a \ xs \ stk \ loc \ pc \ e2 \ F \ D$ )  
**note**  $\text{exec} = \langle ?\text{exec} \ (e \cdot F\{D\} := e2) \ stk \ loc \ pc \ [a] \ stk' \ loc' \ pc' \ xcp' \rangle$   
**note**  $\text{bisim1} = \langle P, e, h \vdash (Throw \ a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$   
**from**  $\text{bisim1}$  **have**  $pc: pc < \text{length} (\text{compE2 } e)$  **by**(*auto dest: bisim1-ThrowD*)  
**from**  $\text{bisim1}$  **have**  $\text{match-ex-table} (\text{compP2 } P) \ (\text{cname-of } h \ a) \ (0 + pc) \ (\text{compE2 } e \ 0 \ 0) = None$   
**unfolding**  $\text{compP2-def}$  **by**(*rule bisim1-xcp-Some-not-caught*)  
**with**  $\text{exec } pc$  **have** *False*  
**by**(*auto elim!: exec-meth.cases simp add: exec-move-def match-ex-table-not-pcs-None*)  
**thus**  $?case \dots$   
**next**  
**case** ( $\text{bisim1FAssThrow2 } e2 \ n \ a \ xs \ stk \ loc \ pc \ e \ F \ D \ v$ )  
**note**  $\text{exec} = \langle ?\text{exec} \ (e \cdot F\{D\} := e2) \ (stk \ @ \ [v]) \ loc \ (\text{length} (\text{compE2 } e) + pc) \ [a] \ stk' \ loc' \ pc' \ xcp' \rangle$   
**note**  $\text{bisim2} = \langle P, e2, h \vdash (Throw \ a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$   
**hence**  $\text{match-ex-table} (\text{compP2 } P) \ (\text{cname-of } h \ a) \ (\text{length} (\text{compE2 } e) + pc) \ (\text{compE2 } e2 \ (\text{length} (\text{compE2 } e)) \ 0) = None$   
**unfolding**  $\text{compP2-def}$  **by**(*rule bisim1-xcp-Some-not-caught*)

```

with exec have False
  by(auto elim!: exec-meth.cases simp add: compxE2-stack-xlift-conv exec-move-def)(auto dest!:
match-ex-table-stack-xliftD simp add: match-ex-table-append-not-pcs)
  thus ?case ..
next
  case (bisim1FAssNull e n e2 F D xs v)
    note exec =  $\langle ?exec (e \cdot F\{D\} := e2) [v, \text{Null}] xs (\text{length} (\text{compE2 } e) + \text{length} (\text{compE2 } e2))$ 
[addr-of-sys-xcpt NullPointer] stk' loc' pc' xcp' \rangle
    hence False
    by(auto elim!: exec-meth.cases simp add: match-ex-table-append-not-pcs compxE2-size-conv exec-move-def
      dest!: match-ex-table-shift-pcD match-ex-table-pc-length-compE2)
    thus ?case ..
next
  case (bisim1FAss3 e n e2 F D xs)
    have  $P, e \cdot F\{D\} := e2, h \vdash (\text{unit}, xs) \leftrightarrow ([\text{Unit}], xs, \text{length} (\text{compE2 } (e \cdot F\{D\} := e2)), \text{None})$  by(rule
bisim1Val2) simp
    moreover have  $\tau \text{move2 } (\text{compP2 } P) h \sqcap (e \cdot F\{D\} := e2) (\text{Suc } (\text{length} (\text{compE2 } e) + \text{length} (\text{compE2 } e2)))$  None by(simp add:  $\tau \text{move2-iff}$ )
    moreover note  $\langle ?exec (e \cdot F\{D\} := e2) \sqcap xs (\text{Suc } (\text{length} (\text{compE2 } e) + \text{length} (\text{compE2 } e2)))$  None
stk' loc' pc' xcp' \rangle
    ultimately show ?case
    by(fastforce elim!: exec-meth.cases simp add: ac-simps exec-move-def)
next
  case (bisim1CAS1 a n a' xs stk loc pc xcp i e D F)
    note IH1 = bisim1CAS1.IH(2)
    note IH2 = bisim1CAS1.IH(4)
    note exec =  $\langle ?exec (a \cdot \text{compareAndSwap}(D \cdot F, i, e)) \text{ stk loc pc xcp stk' loc' pc' xcp' \rangle$ 
    note bisim1 =  $\langle P, a, h \vdash (a', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \rangle$ 
    note bisim2 =  $\langle P, i, h \vdash (i, \text{loc}) \leftrightarrow ([], \text{loc}, 0, \text{None}) \rangle$ 
    note len =  $\langle n + \text{max-vars} - \leq \text{length } xs \rangle$ 
    note bsok =  $\langle \text{bsok } (a \cdot \text{compareAndSwap}(D \cdot F, i, e)) n \rangle$ 
    from bisim1 have pc: pc  $\leq \text{length} (\text{compE2 } a)$  by(rule bisim1-pc-length-compE2)
    show ?case
    proof(cases pc < length (compE2 a))
      case True
        with exec have exec':  $?exec a \text{ stk loc pc xcp stk' loc' pc' xcp'}$  by(simp add: exec-move-CAS1)
        from True have  $\tau$ :  $\tau \text{move2 } (\text{compP2 } P) h \text{ stk } (a \cdot \text{compareAndSwap}(D \cdot F, i, e)) \text{ pc xcp} = \tau \text{move2}$ 
(compP2 P) h stk a pc xcp by(simp add:  $\tau \text{move2-iff}$ )
        with IH1[OF exec' - -  $\langle P, h \vdash \text{stk } [: \leq] ST \rangle \langle \text{conf-xcp' } (\text{compP2 } P) h \text{ xcp} \rangle$  len bsok obtain e'' xs''
          where bisim':  $P, a, h' \vdash (e'', xs'') \leftrightarrow (\text{stk}', \text{loc}', \text{pc}', \text{xcp}')$ 
          and red:  $?red a' xs e'' xs'' a \text{ stk pc pc' xcp xcp'}$  by auto
          from bisim' have  $P, a \cdot \text{compareAndSwap}(D \cdot F, i, e), h' \vdash (e'' \cdot \text{compareAndSwap}(D \cdot F, i, e), xs'') \leftrightarrow$ 
(stk', loc', pc', xcp')
          by(rule bisim1-bisims1.bisim1CAS1)
          moreover from True have no-call2  $(a \cdot \text{compareAndSwap}(D \cdot F, i, e)) \text{ pc} = \text{no-call2 } a \text{ pc}$  by(simp
add: no-call2-def)
          ultimately show ?thesis using red  $\tau$  by(fastforce intro: CAS1Red1 elim!: CAS- $\tau \text{red1r-xt1}$  CAS- $\tau \text{red1t-xt1}$ )
        next
          case False
            with pc have pc: pc =  $\text{length} (\text{compE2 } a)$  by auto
            with bisim1 obtain v where a': is-val a'  $\longrightarrow a' = \text{Val } v$ 
              and stk: stk = [v] and xcp: xcp = None and call: call1 a' = None
              by(auto dest: bisim1-pc-length-compE2D)
            with bisim1 pc len bsok have rede1':  $\tau \text{red1r } P \text{ t h } (a', xs) (\text{Val } v, \text{loc})$ 

```



by(auto intro: bisim1-Val- $\tau$ red1r simp add: bsok-def)  
 hence  $\tau$ red1r  $P$   $t$   $h$  ( $a \cdot \text{compareAndSwap}(D \cdot F, i, e)$ ,  $xs$ ) ( $\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, i, e)$ ,  $loc$ )  
 by(rule CAS- $\tau$ red1r-xt1)  
 moreover from  $pc$   $\text{exec}$   $stk$   $xcp$   
 have  $\text{exec}'$ :  $\text{exec-meth-d}$  ( $\text{compP2 } P$ ) ( $\text{compE2 } a @ \text{compE2 } i @ \text{compE2 } e @ [\text{CAS } F \ D]$ ) ( $\text{compxE2 } a \ 0 \ 0 @ \text{shift}(\text{length}(\text{compE2 } a))(\text{stack-xlift}(\text{length } [v])(\text{compxE2 } i \ 0 \ 0 @ \text{shift}(\text{length}(\text{compE2 } i))(\text{compxE2 } e \ 0 (\text{Suc}(\text{Suc } 0))))))$   $t$   $h$  ( $[] @ [v]$ ,  $loc$ ,  $\text{length}(\text{compE2 } a) + 0$ ,  $\text{None}$ )  $ta$   $h'$  ( $stk'$ ,  $loc'$ ,  $pc'$ ,  $xcp'$ )  
 by(simp add: compxE2-size-convs compxE2-stack-xlift-convs exec-move-def)  
 hence  $\text{exec-meth-d}$  ( $\text{compP2 } P$ ) ( $\text{compE2 } i @ \text{compE2 } e @ [\text{CAS } F \ D]$ ) ( $\text{stack-xlift}(\text{length } [v])(\text{compxE2 } i \ 0 \ 0 @ \text{shift}(\text{length}(\text{compE2 } i))(\text{compxE2 } e \ 0 (\text{Suc}(\text{Suc } 0))))$ )  $t$   $h$  ( $[] @ [v]$ ,  $loc$ ,  $0$ ,  $\text{None}$ )  $ta$   $h'$  ( $stk'$ ,  $loc'$ ,  $pc' - \text{length}(\text{compE2 } a)$ ,  $xcp'$ )  
 by(rule exec-meth-drop-xt) auto  
 hence  $\text{exec-meth-d}$  ( $\text{compP2 } P$ ) ( $\text{compE2 } i$ ) ( $\text{stack-xlift}(\text{length } [v])(\text{compxE2 } i \ 0 \ 0)$ )  $t$   $h$  ( $[] @ [v]$ ,  $loc$ ,  $0$ ,  $\text{None}$ )  $ta$   $h'$  ( $stk'$ ,  $loc'$ ,  $pc' - \text{length}(\text{compE2 } a)$ ,  $xcp'$ )  
 by(rule exec-meth-take-xt) simp  
 with bisim2 obtain  $stk''$  where  $stk'$ :  $stk' = stk'' @ [v]$   
 and  $\text{exec}''$ :  $\text{exec-move-d } P \ t \ h$  ( $[]$ ,  $loc$ ,  $0$ ,  $\text{None}$ )  $ta$   $h'$  ( $stk''$ ,  $loc'$ ,  $pc' - \text{length}(\text{compE2 } a)$ ,  $xcp'$ )  
 unfolding exec-move-def by(blast dest: exec-meth-stk-split)  
 with  $pc$   $xcp$  have  $\tau$ :  $\tau \text{move2}$  ( $\text{compP2 } P$ )  $h$   $[v]$  ( $a \cdot \text{compareAndSwap}(D \cdot F, i, e)$ ) ( $\text{length}(\text{compE2 } a)$ )  $\text{None} = \tau \text{move2}$  ( $\text{compP2 } P$ )  $h$   $[]$   $i \ 0 \ \text{None}$   
 using  $\tau \text{instr-stk-drop-exec-move}$  [where  $stk=[]$  and  $vs=[v]$ ]  
 by(auto simp add:  $\tau \text{move2-iff}$ )  
 from bisim1 have  $\text{length } xs = \text{length } loc$  by(rule bisim1-length-xs)  
 with  $IH2[OF \ \text{exec}']$  len bsok obtain  $e''$   $xs''$   
 where bisim':  $P, i, h' \vdash (e'', xs'') \leftrightarrow (stk'', loc', pc' - \text{length}(\text{compE2 } a), xcp')$   
 and red:  $?red \ i \ loc \ e'' \ xs'' \ i \ [] \ 0 \ (pc' - \text{length}(\text{compE2 } a)) \ \text{None} \ xcp'$  by fastforce  
 from bisim'  
 have  $P, a \cdot \text{compareAndSwap}(D \cdot F, i, e), h' \vdash (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, e'', e), xs'') \leftrightarrow (stk'' @ [v], loc', \text{length}(\text{compE2 } a) + (pc' - \text{length}(\text{compE2 } a)), xcp')$   
 by(rule bisim1-bisims1.bisim1CAS2)  
 moreover from red  $\tau$  have  $?red$  ( $\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, i, e)$ )  $loc$  ( $\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, e'', e)$ )  $xs''$  ( $a \cdot \text{compareAndSwap}(D \cdot F, i, e)$ )  $[v]$  ( $\text{length}(\text{compE2 } a)$ )  $pc'$   $\text{None}$   $xcp'$   
 by(fastforce intro: CAS1Red2 elim!: CAS- $\tau$ red1r-xt2 CAS- $\tau$ red1t-xt2 split: if-split-asm simp add: no-call2-def)  
 moreover from  $\text{exec}'$  have  $pc' \geq \text{length}(\text{compE2 } a)$   
 by(rule exec-meth-drop-xt-pc) auto  
 moreover have no-call2 ( $a \cdot \text{compareAndSwap}(D \cdot F, i, e)$ )  $pc$  using  $pc$  by(simp add: no-call2-def)  
 ultimately show  $?thesis$  using  $\tau$   $stk'$   $pc$   $xcp$   $stk$  by(fastforce elim!: rtranclp-trans)  
 qed  
 next  
 case ( $\text{bisim1CAS2 } i \ n \ i' \ xs \ stk \ loc \ pc \ xcp \ a \ e \ D \ F \ v$ )  
 note  $IH2 = \text{bisim1CAS2.IH}(2)$   
 note  $IH3 = \text{bisim1CAS2.IH}(6)$   
 note  $\text{exec} = \langle ?exec \ (a \cdot \text{compareAndSwap}(D \cdot F, i, e)) \ (stk @ [v]) \ loc \ (\text{length}(\text{compE2 } a) + pc) \ xcp \rangle$   
 note  $\text{bisim2} = \langle P, i, h \vdash (i', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$   
 note  $\text{bisim3} = \langle P, e, h \vdash (e, loc) \leftrightarrow ([] , loc, 0, \text{None}) \rangle$   
 note  $\text{len} = \langle n + \text{max-vars}(\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, i', e)) \leq \text{length } xs \rangle$   
 note  $\text{bsok} = \langle \text{bsok} \ (a \cdot \text{compareAndSwap}(D \cdot F, i, e)) \ n \rangle$   
 from bisim2 have  $pc$ :  $pc \leq \text{length}(\text{compE2 } i)$  by(rule bisim1-pc-length-compE2)  
 show ?case  
 proof(cases  $pc < \text{length}(\text{compE2 } i)$ )  
 case True

**from** *exec* **have** *exec'*: *exec-meth-d* (*compP2* *P*) (*compE2* *a* @ *compE2* *i* @ *compE2* *e* @ [*CAS F D*]) (*compxE2* *a* 0 0 @ *shift* (*length* (*compE2* *a*)) (*stack-xlift* (*length* [*v*]) (*compxE2* *i* 0 0) @ *shift* (*length* (*compE2* *i*)) (*compxE2* *e* 0 (Suc (Suc 0))))) *t h* (*stk* @ [*v*], *loc*, *length* (*compE2* *a*) + *pc*, *xcp*) *ta* *h'* (*stk'*, *loc'*, *pc'*, *xcp'*)

**by**(*simp add: shift-compxE2 stack-xlift-compxE2 ac-simps exec-move-def*)

**hence** *exec-meth-d* (*compP2* *P*) (*compE2* *i* @ *compE2* *e* @ [*CAS F D*]) (*stack-xlift* (*length* [*v*]) (*compxE2* *i* 0 0) @ *shift* (*length* (*compE2* *i*)) (*compxE2* *e* 0 (Suc (Suc 0))))) *t h* (*stk* @ [*v*], *loc*, *pc*, *xcp*) *ta* *h'* (*stk'*, *loc'*, *pc'* - *length* (*compE2* *a*), *xcp'*)

**by**(*rule exec-meth-drop-xt*) *auto*

**hence** *exec-meth-d* (*compP2* *P*) (*compE2* *i*) (*stack-xlift* (*length* [*v*]) (*compxE2* *i* 0 0)) *t h* (*stk* @ [*v*], *loc*, *pc*, *xcp*) *ta* *h'* (*stk'*, *loc'*, *pc'* - *length* (*compE2* *a*), *xcp'*)

**using** *True* **by**(*rule exec-meth-take-xt*)

**with** *bisim2* **obtain** *stk''* **where** *stk'*: *stk'* = *stk''* @ [*v*]

**and** *exec''*: *exec-move-d* *P t i h* (*stk*, *loc*, *pc*, *xcp*) *ta* *h'* (*stk''*, *loc'*, *pc'* - *length* (*compE2* *a*), *xcp'*)

**unfolding** *exec-move-def* **by**(*blast dest: exec-meth-stk-split*)

**with** *True* **have**  $\tau$ :  $\tau\text{move2}$  (*compP2* *P*) *h* (*stk* @ [*v*]) (*a*·*compareAndSwap*(*D*·*F*, *i*, *e*)) (*length* (*compE2* *a*) + *pc*) *xcp* =  $\tau\text{move2}$  (*compP2* *P*) *h* *stk i pc xcp*

**by**(*auto simp add:  $\tau\text{move2-iff}$   $\tau\text{instr-stk-drop-exec-move}$* )

**moreover from**  $\langle P, h \vdash \text{stk} @ [v] [: \leq] ST \rangle$  **obtain** *ST2* **where**  $P, h \vdash \text{stk} [: \leq] ST2$  **by**(*auto simp add: list-all2-append1*)

**from** *IH2*[*OF exec''* - - *this*  $\langle \text{conf-xcp}' (\text{compP2 } P) h \text{ xcp} \rangle$  *len bsok* **obtain** *e'' xs''*

**where** *bisim'*:  $P, i, h' \vdash (e'', xs'') \leftrightarrow (\text{stk}'', \text{loc}', \text{pc}' - \text{length} (\text{compE2 } a), \text{xcp}')$

**and** *red*:  $?red\ i'\ xs\ e''\ xs''\ i\ \text{stk}\ \text{pc}\ (\text{pc}' - \text{length} (\text{compE2 } a))\ \text{xcp}\ \text{xcp}'$  **by** *fastforce*

**from** *bisim'*

**have**  $P, a \cdot \text{compareAndSwap}(D \cdot F, i, e), h' \vdash (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, e'', e), xs'') \leftrightarrow (\text{stk}'' @ [v], \text{loc}', \text{length} (\text{compE2 } a) + (\text{pc}' - \text{length} (\text{compE2 } a)), \text{xcp}')$

**by**(*rule bisim1-bisims1.bisim1CAS2*)

**moreover from** *exec'* **have**  $\text{pc}' \geq \text{length} (\text{compE2 } a)$

**by**(*rule exec-meth-drop-xt-pc*) *auto*

**moreover have** *no-call2* *i pc*  $\implies$  *no-call2* (*a*·*compareAndSwap*(*D*·*F*, *i*, *e*)) (*length* (*compE2* *a*) + *pc*) **by**(*simp add: no-call2-def*)

**ultimately show** *?thesis* **using** *red*  $\tau$  *stk'* *True*

**by**(*fastforce intro: CAS1Red2 elim!: CAS- $\tau$ red1r-xt2 CAS- $\tau$ red1t-xt2 split: if-split-asm*)

**next**

**case** *False*

**with** *pc* **have** [*simp*]: *pc* = *length* (*compE2* *i*) **by** *simp*

**with** *bisim2* **obtain** *v2* **where** *i'*: *is-val* *i'*  $\longrightarrow$  *i'* = *Val* *v2*

**and** *stk*: *stk* = [*v2*] **and** *xcp*: *xcp* = *None* **and** *call*: *call1* *i'* = *None*

**by**(*auto dest: bisim1-pc-length-compE2D*)

**with** *bisim2* *pc len bsok* **have** *red*:  $\tau\text{red1r } P\ t\ h\ (i', xs)\ (\text{Val } v2, \text{loc})$

**by**(*auto intro: bisim1-Val- $\tau$ red1r simp add: bsok-def*)

**hence**  $\tau\text{red1r } P\ t\ h\ (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, i', e), xs)\ (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v2, e), \text{loc})$  **by**(*rule CAS- $\tau$ red1r-xt2*)

**moreover from** *pc exec stk xcp*

**have** *exec'*: *exec-meth-d* (*compP2* *P*) ((*compE2* *a* @ *compE2* *i*) @ *compE2* *e* @ [*CAS F D*]) ((*compxE2* *a* 0 0 @ *compxE2* *i* (*length* (*compE2* *a*)) (*Suc* 0)) @ *shift* (*length* (*compE2* *a* @ *compE2* *i*)) (*stack-xlift* (*length* [*v2*, *v*]) (*compxE2* *e* 0 0))) *t h* ( $[] @ [v2, v], \text{loc}, \text{length} (\text{compE2 } a @ \text{compE2 } i) + 0, \text{None}$ ) *ta* *h'* (*stk'*, *loc'*, *pc'*, *xcp'*)

**by**(*simp add: compxE2-size-convs compxE2-stack-xlift-convs exec-move-def*)

**hence** *exec-meth-d* (*compP2* *P*) (*compE2* *e* @ [*CAS F D*]) (*stack-xlift* (*length* [*v2*, *v*]) (*compxE2* *e* 0 0)) *t h* ( $[] @ [v2, v], \text{loc}, 0, \text{None}$ ) *ta* *h'* (*stk'*, *loc'*, *pc'* - *length* (*compE2* *a* @ *compE2* *i*), *xcp'*)

**by**(*rule exec-meth-drop-xt*) *auto*

**hence** *exec-meth-d* (*compP2* *P*) (*compE2* *e*) (*stack-xlift* (*length* [*v2*, *v*]) (*compxE2* *e* 0 0)) *t h* ( $[] @ [v2, v], \text{loc}, 0, \text{None}$ ) *ta* *h'* (*stk'*, *loc'*, *pc'* - *length* (*compE2* *a* @ *compE2* *i*), *xcp'*)

by(rule exec-meth-take) simp  
 with bisim3 obtain stk'' where stk': stk' = stk'' @ [v2, v]  
 and exec'': exec-move-d P t e h ([], loc, 0, None) ta h' (stk'', loc', pc' - length (compE2 a @ compE2 i), xcp')  
 unfolding exec-move-def by(blast dest: exec-meth-stk-split)  
 with pc xcp have  $\tau$ :  $\tau \text{move2} (\text{compP2 } P) h [v2, v] (a \cdot \text{compareAndSwap}(D \cdot F, i, e)) (\text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i)) \text{None} = \tau \text{move2} (\text{compP2 } P) h [] e 0 \text{None}$   
 using  $\tau \text{instr-stk-drop-exec-move}$  [where  $\text{stk}=[]$  and  $\text{vs}=[v2, v]$ ] by(simp add:  $\tau \text{move2-iff}$ )  
 from bisim2 have length xs = length loc by(rule bisim1-length-xs)  
 with IH3[OF exec'', of []] len bsok obtain e'' xs''  
 where bisim':  $P, e, h' \vdash (e'', xs'') \leftrightarrow (stk'', loc', pc' - \text{length} (\text{compE2 } a) - \text{length} (\text{compE2 } i), xcp')$   
 and red:  $?red\ e\ loc\ e''\ xs''\ e\ []\ 0\ (pc' - \text{length} (\text{compE2 } a) - \text{length} (\text{compE2 } i)) \text{None}\ xcp'$   
 by auto (fastforce simp only: length-append diff-diff-left)  
 from bisim'  
 have  $P, a \cdot \text{compareAndSwap}(D \cdot F, i, e), h' \vdash (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v2, e''), xs'') \leftrightarrow (stk'' @ [v2, v], loc', \text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i) + (pc' - \text{length} (\text{compE2 } a) - \text{length} (\text{compE2 } i)), xcp')$   
 by(rule bisim1-bisims1.bisim1CAS3)  
 moreover from red  $\tau$   
 have  $?red\ (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v2, e))\ loc\ (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v2, e''))\ xs''\ (a \cdot \text{compareAndSwap}(D \cdot F, i, e))\ [v2, v]\ (\text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i))\ pc'\ \text{None}\ xcp'$   
 by(fastforce intro: CAS1Red3 elim!: CAS- $\tau$ red1r-xt3 CAS- $\tau$ red1t-xt3 split: if-split-asm simp add: no-call2-def)  
 moreover from exec' have  $pc' \geq \text{length} (\text{compE2 } a @ \text{compE2 } i)$   
 by(rule exec-meth-drop-xt-pc) auto  
 moreover have no-call2 ( $a \cdot \text{compareAndSwap}(D \cdot F, i, e)$ ) ( $\text{length} (\text{compE2 } a) + pc$ ) by(simp add: no-call2-def)  
 ultimately show ?thesis using  $\tau\ stk'\ pc\ xcp\ stk$  by(fastforce elim!: rtranclp-trans)  
 qed  
 next  
 case (bisim1CAS3 e n e' xs stk loc pc xcp a i D F v v')  
 note IH3 = bisim1CAS3.IH(2)  
 note exec =  $\langle ?exec\ (a \cdot \text{compareAndSwap}(D \cdot F, i, e))\ (stk @ [v', v])\ loc\ (\text{length} (\text{compE2 } a) + \text{length} (\text{compE2 } i) + pc)\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$   
 note bisim3 =  $\langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$   
 note len =  $\langle n + \text{max-vars} - \leq \text{length } xs \rangle$   
 note bsok =  $\langle bsok\ (a \cdot \text{compareAndSwap}(D \cdot F, i, e))\ n \rangle$   
 from  $\langle P, h \vdash stk @ [v', v] [: \leq] ST \rangle$  obtain T T' ST'  
 where [simp]:  $ST = ST' @ [T', T]$   
 and wtv:  $P, h \vdash v : \leq T$  and wtv':  $P, h \vdash v' : \leq T'$  and ST':  $P, h \vdash stk [: \leq] ST'$   
 by(auto simp add: list-all2-Cons1 list-all2-append1)  
 from bisim3 have pc:  $pc \leq \text{length} (\text{compE2 } e)$  by(rule bisim1-pc-length-compE2)  
 show ?case  
 proof(cases  $pc < \text{length} (\text{compE2 } e)$ )  
 case True  
 from exec have exec': exec-meth-d (compP2 P) ((compE2 a @ compE2 i) @ compE2 e @ [CAS F D]) ((compE2 a 0 0 @ compE2 i (length (compE2 a)) (Suc 0)) @ shift (length (compE2 a @ compE2 i)) (stack-xlift (length [v', v]) (compE2 e 0 0))) t h (stk @ [v', v], loc, length (compE2 a @ compE2 i) + pc, xcp) ta h' (stk', loc', pc', xcp')  
 by(simp add: shift-compE2 stack-xlift-compE2 exec-move-def)  
 hence exec-meth-d (compP2 P) (compE2 e @ [CAS F D]) (stack-xlift (length [v', v]) (compE2 e 0 0)) t h (stk @ [v', v], loc, pc, xcp) ta h' (stk', loc', pc' - length (compE2 a @ compE2 i), xcp')  
 by(rule exec-meth-drop-xt) auto

**hence** *exec-meth-d* (*compP2 P*) (*compE2 e*) (*stack-xfift* (*length* [*v'*, *v*]) (*compE2 e 0 0*)) *t h* (*stk*  
 @ [*v'*, *v*], *loc*, *pc*, *xcp*) *ta h'* (*stk'*, *loc'*, *pc' - length (compE2 a @ compE2 i)*, *xcp'*)  
**using** *True* **by**(*rule exec-meth-take*)  
**with** *bisim3* **obtain** *stk''* **where** *stk': stk' = stk'' @ [v', v]*  
**and** *exec'': exec-move-d P t e h* (*stk*, *loc*, *pc*, *xcp*) *ta h'* (*stk''*, *loc'*, *pc' - length (compE2 a @*  
*compE2 i)*, *xcp'*)  
**unfolding** *exec-move-def* **by**(*blast dest: exec-meth-stk-split*)  
**with** *True* **have**  $\tau: \tau \text{move2 } (\text{compP2 } P) \ h \ (stk \ @ \ [v', v]) \ (a \cdot \text{compareAndSwap}(D \cdot F, i, e)) \ (\text{length}$   
 $(\text{compE2 } a) + \text{length } (\text{compE2 } i) + pc) \ xcp = \tau \text{move2 } (\text{compP2 } P) \ h \ stk \ e \ pc \ xcp$   
**by**(*auto simp add:  $\tau \text{move2-iff } \tau \text{instr-stk-drop-exec-move}$* )  
**moreover from** *IH3[OF exec'' - - ST' <conf-xcp' (compP2 P) h xcp'] len bsok* **obtain** *e'' xs''*  
**where** *bisim': P, e, h' ⊢ (e'', xs'') ↔ (stk'', loc', pc' - length (compE2 a) - length (compE2 i),*  
*xcp')*  
**and** *red: ?red e' xs e'' xs'' e stk pc* (*pc' - length (compE2 a) - length (compE2 i)*) *xcp xcp'*  
**by** *auto(fastforce simp only: length-append diff-diff-left)*  
**from** *bisim'*  
**have** *P, a · compareAndSwap(D · F, i, e), h' ⊢ (Val v · compareAndSwap(D · F, Val v', e''), xs'') ↔ (stk''*  
 @ [*v'*, *v*], *loc'*, *length (compE2 a) + length (compE2 i) + (pc' - length (compE2 a) - length (compE2*  
*i)*), *xcp')*  
**by**(*rule bisim1-bisims1.bisim1CAS3*)  
**moreover from** *exec'* **have** *pc' ≥ length (compE2 a @ compE2 i)*  
**by**(*rule exec-meth-drop-xt-pc*) *auto*  
**moreover have** *no-call2 e pc ⇒ no-call2 (a · compareAndSwap(D · F, i, e)) (length (compE2 a) +*  
*length (compE2 i) + pc)*  
**by**(*simp add: no-call2-def*)  
**ultimately show** *?thesis* **using** *red  $\tau$  stk' True*  
**by**(*fastforce intro: CAS1Red3 elim!: CAS- $\tau$ red1r-xt3 CAS- $\tau$ red1t-xt3 split: if-split-asm*)  
**next**  
**case** *False*  
**with** *pc* **have** [*simp*]: *pc = length (compE2 e)* **by** *simp*  
**with** *bisim3* **obtain** *v2* **where** *stk: stk = [v2]* **and** *xcp: xcp = None*  
**by**(*auto dest: bisim1-pc-length-compE2D*)  
**with** *bisim3 pc len bsok* **have** *red:  $\tau \text{red1r } P \ t \ h \ (e', xs) \ (Val \ v2, loc)$*   
**by**(*auto intro: bisim1-Val- $\tau \text{red1r}$  simp add: bsok-def*)  
**hence**  *$\tau \text{red1r } P \ t \ h \ (Val \ v \cdot \text{compareAndSwap}(D \cdot F, Val \ v', e'), xs) \ (Val \ v \cdot \text{compareAndSwap}(D \cdot F,$*   
*Val v', Val v2), loc)* **by**(*rule CAS- $\tau \text{red1r-xt3}$* )  
**moreover have**  $\tau: \neg \tau \text{move2 } (\text{compP2 } P) \ h \ [v2, v', v] \ (a \cdot \text{compareAndSwap}(D \cdot F, i, e)) \ (\text{length}$   
 $(\text{compE2 } a) + \text{length } (\text{compE2 } i) + \text{length } (\text{compE2 } e)) \ None$   
**by**(*simp add:  $\tau \text{move2-iff}$* )  
**moreover**  
**have**  $\exists ta' \ e''. P, a \cdot \text{compareAndSwap}(D \cdot F, i, e), h' \vdash (e'', loc) \leftrightarrow (stk', loc', pc', xcp') \wedge True, P, t$   
 $\vdash 1 \langle Val \ v \cdot \text{compareAndSwap}(D \cdot F, Val \ v', Val \ v2), (h, loc) \rangle -ta' \rightarrow \langle e'', (h', loc) \rangle \wedge ta \text{-bisim } w \text{bisim1}$   
 $(extTA2J1 \ P \ ta') \ ta$   
**proof**(*cases v = Null*)  
**case** *True* **with** *exec stk xcp* **show** *?thesis*  
**by**(*fastforce elim!: exec-meth.cases simp add: exec-move-def intro: bisim1CASFail CAS1Null*)  
**next**  
**case** *False*  
**have** *P, a · compareAndSwap(D · F, i, e), h' ⊢ (Val (Bool b), loc) ↔ ([Bool b], loc, length (compE2*  
*(a · compareAndSwap(D · F, i, e))), None)* **for** *b*  
**by**(*rule bisim1Val2*) *simp*  
**with** *False exec stk xcp* **show** *?thesis*  
**by** (*auto elim!: exec-meth.cases simp add: exec-move-def is-Ref-def intro: Red1CASSucceed*  
*Red1CASFail*)

```

    (fastforce intro!: Red1CASSucceed Red1CASFail simp add: ta-bisim-def ac-simps)+
  qed
  ultimately show ?thesis using exec xcp stk by(fastforce simp add: no-call2-def)
  qed
next
case (bisim1CASThrow1 A n a xs stk loc pc i e D F)
note exec = ⟨?exec (A.compareAndSwap(D·F, i, e)) stk loc pc [a] stk' loc' pc' xcp⟩
note bisim1 = ⟨P, A, h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
from bisim1 have pc: pc < length (compE2 A) by(auto dest: bisim1-ThrowD)
from bisim1 have match-ex-table (compP2 P) (cname-of h a) (0 + pc) (compxE2 A 0 0) = None
  unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
with exec pc have False
  by(auto elim!: exec-meth.cases simp add: match-ex-table-not-pcs-None exec-move-def)
thus ?case ..
next
case (bisim1CASThrow2 i n a xs stk loc pc A e D F v)
note exec = ⟨?exec (A.compareAndSwap(D·F, i, e)) (stk @ [v]) loc (length (compE2 A) + pc) [a]
stk' loc' pc' xcp⟩
note bisim2 = ⟨P, i, h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
from bisim2 have pc: pc < length (compE2 i) by(auto dest: bisim1-ThrowD)
from bisim2 have match-ex-table (compP2 P) (cname-of h a) (length (compE2 A) + pc) (compxE2
i (length (compE2 A)) 0) = None
  unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
with exec pc have False
  apply(auto elim!: exec-meth.cases simp add: compxE2-stack-xlift-convs compxE2-size-convs exec-move-def)
  apply(auto simp add: match-ex-table-append-not-pcs)
  done
thus ?case ..
next
case (bisim1CASThrow3 e n a xs stk loc pc A i D F v' v)
note exec = ⟨?exec (A.compareAndSwap(D·F, i, e)) (stk @ [v', v]) loc (length (compE2 A) + length
(compE2 i) + pc) [a] stk' loc' pc' xcp⟩
note bisim2 = ⟨P, e, h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
from bisim2 have match-ex-table (compP2 P) (cname-of h a) (length (compE2 A) + length (compE2
i) + pc) (compxE2 e (length (compE2 A) + length (compE2 i)) 0) = None
  unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
with exec have False
  apply(auto elim!: exec-meth.cases simp add: compxE2-stack-xlift-convs compxE2-size-convs exec-move-def)
  apply(auto dest!: match-ex-table-stack-xliftD match-ex-table-shift-pcD dest: match-ex-table-pcsD
simp add: match-ex-table-append match-ex-table-shift-pc-None)
  done
thus ?case ..
next
case (bisim1CASFail a n i e D F ad xs v' v v'')
note exec = ⟨?exec (a.compareAndSwap(D·F, i, e)) [v', v, v''] xs (length (compE2 a) + length
(compE2 i) + length (compE2 e)) [ad] stk' loc' pc' xcp⟩
hence False
  by(auto elim!: exec-meth.cases simp add: match-ex-table-append exec-move-def
dest!: match-ex-table-shift-pcD match-ex-table-pc-length-compE2)
thus ?case ..
next
case (bisim1Call1 obj n obj' xs stk loc pc xcp ps M')
note IH1 = bisim1Call1.IH(2)
note IH2 = bisim1Call1.IH(4)

```

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note  $exec = \langle ?exec (obj \cdot M'(ps)) \text{ stk } loc \text{ pc } xcp \text{ stk}' \text{ loc}' \text{ pc}' \text{ xcp}' \rangle$ 
note  $bisim1 = \langle P, obj, h \vdash (obj', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
note  $bisim2 = \langle P, ps, h \vdash (ps, loc) [\leftrightarrow] ([], loc, 0, None) \rangle$ 
note  $len = \langle n + max\text{-vars } (obj' \cdot M'(ps)) \leq length \text{ xs} \rangle$ 
note  $bsok = \langle bsok (obj \cdot M'(ps)) \text{ n} \rangle$ 
from  $bisim1$  have  $pc: pc \leq length (compE2 \text{ obj})$  by (rule  $bisim1\text{-pc-length-compE2}$ )
from  $bisim1$  have  $lenxs: length \text{ xs} = length \text{ loc}$  by (rule  $bisim1\text{-length-xs}$ )
show  $?case$ 
proof (cases  $pc < length (compE2 \text{ obj})$ )
  case  $True$ 
    with  $exec$  have  $exec': ?exec \text{ obj } \text{stk } \text{loc } \text{pc } \text{xcp } \text{stk}' \text{ loc}' \text{ pc}' \text{ xcp}'$  by (simp add:  $exec\text{-move-Call1}$ )
    from  $True$  have  $\tau move2 (compP2 \text{ P}) \text{ h } \text{stk } (obj \cdot M'(ps)) \text{ pc } \text{xcp} = \tau move2 (compP2 \text{ P}) \text{ h } \text{stk } \text{obj}$ 
     $pc \text{ xcp}$  by (simp add:  $\tau move2\text{-iff}$ )
    moreover from  $True$  have  $no\text{-call2 } (obj \cdot M'(ps)) \text{ pc} = no\text{-call2 } \text{obj } \text{pc}$  by (simp add:  $no\text{-call2-def}$ )
    ultimately show  $?thesis$ 
      using  $IH1[OF \text{ exec}' - - \langle P, h \vdash \text{stk } [: \leq] ST \rangle \langle conf\text{-xcp}' (compP2 \text{ P}) \text{ h } \text{xcp} \rangle]$   $bisim2 \text{ len } bsok$ 
      by (fastforce intro:  $bisim1\text{-bisims1.bisim1Call1 Call1Obj elim!:$   $Call\text{-}\tau red1r\text{-obj}$   $Call\text{-}\tau red1t\text{-obj}$ )
    next
      case  $False$ 
        with  $pc$  have  $pc: pc = length (compE2 \text{ obj})$  by auto
        with  $bisim1$  obtain  $v$  where  $stk: stk = [v]$  and  $xcp: xcp = None$  and  $call: call1 \text{ obj}' = None$ 
        and  $v: is\text{-val } \text{obj}' \implies \text{obj}' = Val \text{ v} \wedge xs = loc$ 
        by (auto dest:  $bisim1\text{-pc-length-compE2D}$ )
        with  $bisim1 \text{ pc } len \text{ bsok}$  have  $\tau red1r \text{ P } t \text{ h } (obj', xs) (Val \text{ v}, loc)$ 
        by (auto intro:  $bisim1\text{-Val-}\tau red1r$  simp add:  $bsok\text{-def}$ )
        hence  $red: \tau red1r \text{ P } t \text{ h } (obj' \cdot M'(ps), xs) (Val \text{ v} \cdot M'(ps), loc)$  by (rule  $Call\text{-}\tau red1r\text{-obj}$ )
        show  $?thesis$ 
        proof (cases  $ps$ )
          case  $(Cons \text{ p } ps')$ 
            from  $pc \text{ exec } \text{stk } \text{xcp}$ 
            have  $exec\text{-meth-d } (compP2 \text{ P}) (compE2 (obj \cdot M'(ps))) (compxE2 (obj \cdot M'(ps)) \text{ 0 } \text{0}) \text{ t h } ([[] @ [v],$ 
             $loc, length (compE2 \text{ obj}) + 0, None) \text{ ta } h' (stk', loc', pc', xcp')$ 
            by (simp add:  $compxE2\text{-size-conv}$   $compxE2\text{-stack-xlift-conv}$   $exec\text{-move-def}$ )
            hence  $exec': exec\text{-meth-d } (compP2 \text{ P}) (compEs2 \text{ ps}) (stack\text{-xlift } (length [v]) (compxEs2 \text{ ps } \text{0 } \text{0}))$ 
             $t \text{ h } ([[] @ [v], loc, 0, None) \text{ ta } h' (stk', loc', pc' - length (compE2 \text{ obj}), xcp')$ 
            and  $pc': pc' \geq length (compE2 \text{ obj})$  using  $Cons$ 
            by (safe dest!:  $Call\text{-}execParamD$ ) simp-all
            from  $exec' \text{ bisim2}$  obtain  $stk''$  where  $stk': stk' = stk'' @ [v]$ 
            and  $exec'': exec\text{-moves-d } \text{P } t \text{ ps } h ([], loc, 0, None) \text{ ta } h' (stk'', loc', pc' - length (compE2 \text{ obj}),$ 
             $xcp')$ 
            unfolding  $exec\text{-moves-def}$  by  $-(drule (1) \text{ exec-meth-stk-splits, auto})$ 
            with  $pc \text{ xcp}$  have  $\tau: \tau move2 (compP2 \text{ P}) \text{ h } [v] (obj \cdot M'(ps)) (length (compE2 \text{ obj})) \text{ None} =$ 
             $\tau moves2 (compP2 \text{ P}) \text{ h } [] \text{ ps } \text{0 } \text{None}$ 
            using  $\tau instr\text{-stk-drop-exec-moves}$  where  $stk=[]$  and  $vs=[v]$ 
            by (auto simp add:  $\tau move2\text{-iff}$   $\tau moves2\text{-iff}$ )
            from  $IH2[OF \text{ exec}'', of [] \text{ len } lenxs \text{ bsok}]$  obtain  $es'' \text{ xs''}$ 
            where  $bisim': P, ps, h' \vdash (es'', xs'') [\leftrightarrow] (stk'', loc', pc' - length (compE2 \text{ obj}), xcp')$ 
            and  $red': ?reds \text{ ps } \text{loc } es'' \text{ xs'' } \text{ps } [] \text{ 0 } (pc' - length (compE2 \text{ obj})) \text{ None } xcp' \text{ by auto}$ 
            from  $bisim'$  have  $P, obj \cdot M'(ps), h' \vdash (Val \text{ v} \cdot M'(es''), xs'') \leftrightarrow (stk'' @ [v], loc', length (compE2$ 
             $\text{obj}) + (pc' - length (compE2 \text{ obj})), xcp')$ 
            by (rule  $bisim1CallParams$ )
            moreover from  $pc \text{ Cons}$  have  $no\text{-call2 } (obj \cdot M'(ps)) \text{ pc}$  by (simp add:  $no\text{-call2-def}$ )
            ultimately show  $?thesis$  using  $red \text{ red}' \tau \text{ stk}' \text{ pc } \text{xcp } \text{pc}' \text{ stk } \text{call}$ 
            by (fastforce elim!:  $rtrancp\text{-trans}$   $Call\text{-}\tau red1r\text{-param}$   $Call\text{-}\tau red1t\text{-param}$  intro:  $Call1Params$ )

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rtrancpl-trancpl-trancpl split: if-split-asm)
next
  case [simp]: Nil
  from exec pc stk xcp
  have  $v = \text{Null} \vee (\text{is-Addr } v \wedge (\exists T C' Ts' Tr' D'. \text{typeof}_h v = \lfloor T \rfloor \wedge \text{class-type-of}' T = \lfloor C' \rfloor \wedge P \vdash C' \text{ sees } M': Ts' \rightarrow Tr' = \text{Native in } D'))$  (is -  $\vee$  ?rest)
  by(fastforce elim!: exec-meth.cases simp add: is-Ref-def exec-move-def compP2-def has-method-def split: if-split-asm)
  thus ?thesis
  proof
    assume [simp]:  $v = \text{Null}$ 
    have  $P, \text{obj} \cdot M'(\lfloor \rfloor), h \vdash (\text{THROW NullPointer}, \text{loc}) \leftrightarrow (\lfloor \rfloor @ [\text{Null}], \text{loc}, \text{length}(\text{compE2 obj}) + \text{length}(\text{compEs2}(\lfloor \rfloor :: \text{'addr expr1 list}), \lfloor \text{addr-of-sys-xcpt NullPointer} \rfloor))$ 
    by(safe intro!: bisim1CallThrow) simp-all
    moreover have  $\text{True}, P, t \vdash 1 \langle \text{null} \cdot M'(\text{map Val } \lfloor \rfloor), (h, \text{loc}) \rangle -\varepsilon \rightarrow \langle \text{THROW NullPointer}, (h, \text{loc}) \rangle$ 
    by(rule Red1CallNull)
    moreover have  $\tau \text{move1 } P h (\text{Val } v \cdot M'(\lfloor \rfloor)) \tau \text{move2 } (\text{compP2 } P) h [\text{Null}] (\text{obj} \cdot M'(ps)) (\text{length}(\text{compE2 obj})) \text{None}$ 
    by(simp-all add:  $\tau \text{move2-iff}$ )
    ultimately show ?thesis using exec pc stk xcp red
    by(fastforce elim!: exec-meth.cases intro: rtrancpl-trans simp add: exec-move-def)
  next
    assume ?rest
    then obtain  $a Ta Ts' Tr' D'$  where [simp]:  $v = \text{Addr } a$ 
    and  $Ta: \text{typeof-addr } h a = \lfloor Ta \rfloor$ 
    and  $\text{iec}: P \vdash \text{class-type-of } Ta \text{ sees } M': Ts' \rightarrow Tr' = \text{Native in } D'$  by auto
    with exec pc stk xcp
    obtain  $ta' va h'' U$  where  $\text{redex}: (ta', va, h'') \in \text{red-external-aggr}(\text{compP2 } P) t a M' \lfloor \rfloor h$ 
    and  $\text{ret}: (xcp', h', [(stk', \text{loc}', \text{undefined}, \text{undefined}, pc')]) = \text{extRet2JVM } 0 h'' [\text{Addr } a] \text{loc}$ 
    undefined undefined  $(\text{length}(\text{compE2 obj})) \lfloor \rfloor va$ 
    and  $wtext': P, h \vdash a \cdot M'(\lfloor \rfloor) : U$ 
    and  $ta': ta = \text{extTA2JVM}(\text{compP2 } P) ta'$ 
    by(fastforce simp add: is-Ref-def exec-move-def compP2-def external-WT'-iff exec-meth-instr)
    from  $Ta \text{ iec}$  have  $\tau: \tau \text{move1 } P h (\text{Val } v \cdot M'(\lfloor \rfloor)) \longleftrightarrow \tau \text{move2 } (\text{compP2 } P) h [\text{Addr } a] (\text{obj} \cdot M'(ps))$ 
     $(\text{length}(\text{compE2 obj})) \text{None}$ 
    by(auto simp add:  $\tau \text{move2-iff compP2-def}$ )
    from  $\text{ret}$  have [simp]:  $h'' = h'$  by simp
    from  $wtext'$  have  $wtext'': \text{compP2 } P, h \vdash a \cdot M'(\lfloor \rfloor) : U$  by(simp add: external-WT'-iff compP2-def)
    from  $wf$  have  $wf\text{-jvm-prog}(\text{compP2 } P)$  by(rule wt-compP2)
    then obtain  $wf\text{-md}$  where  $wf': wf\text{-prog } wf\text{-md}(\text{compP2 } P)$ 
    unfolding  $wf\text{-jvm-prog-def}$  by(blast dest: wt-jvm-progD)
    from  $tconf$  have  $tconf': \text{compP2 } P, h \vdash t \sqrt{t}$  by(simp add: compP2-def tconf-def)
    from  $\text{redex}$  have  $\text{redex}'': \text{compP2 } P, t \vdash \langle a \cdot M'(\lfloor \rfloor), h \rangle -ta' \rightarrow \text{ext} \langle va, h' \rangle$ 
    by(simp add: WT-red-external-list-conv[OF  $wf' wtext'' tconf'$ , symmetric])
    hence  $\text{redex}': P, t \vdash \langle a \cdot M'(\lfloor \rfloor), h \rangle -ta' \rightarrow \text{ext} \langle va, h' \rangle$  by(simp add: compP2-def)
    with  $Ta \text{ iec}$  have  $\text{True}, P, t \vdash 1 \langle \text{addr } a \cdot M'(\text{map Val } \lfloor \rfloor), (h, \text{loc}) \rangle -ta' \rightarrow \langle \text{extRet2J}(\text{addr } a \cdot M'(\text{map Val } \lfloor \rfloor)) va, (h', \text{loc}) \rangle$ 
    by -(rule Red1CallExternal, auto)
    moreover have  $P, \text{obj} \cdot M'(ps), h' \vdash (\text{extRet2J}(\text{addr } a \cdot M'(\text{map Val } \lfloor \rfloor)) va, \text{loc}) \leftrightarrow (stk', \text{loc}', pc', xcp')$ 
    proof(cases va)
      case (RetVal v)
      have  $P, \text{obj} \cdot M'(\lfloor \rfloor), h' \vdash (\text{Val } v, \text{loc}) \leftrightarrow ([v], \text{loc}, \text{length}(\text{compE2}(\text{obj} \cdot M'(\lfloor \rfloor))), \text{None})$ 

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    by(rule bisim1Val2) simp
  with ret RetVal show ?thesis by simp
next
  case (RetExc ad)
    have  $P, obj \cdot M'(\square), h' \vdash (\text{Throw } ad, loc) \leftrightarrow (\square @ [v], loc, \text{length } (\text{compE2 } (obj)) + \text{length } (\text{compEs2 } (\square :: 'addr \text{ expr1 list})), [ad])$ 
    by(rule bisim1CallThrow) simp
  with ret RetExc show ?thesis by simp
next
  case RetStaySame
    have  $P, obj \cdot M'(\square), h' \vdash (\text{addr } a \cdot M'(\square), loc) \leftrightarrow ([\text{Addr } a], loc, \text{length } (\text{compE2 } obj), \text{None})$ 
    by(rule bisim1-bisims1.bisim1Call1)(rule bisim1Val2, simp)
  thus ?thesis using ret RetStaySame by simp
qed
  moreover from  $\langle \text{preallocated } h \rangle \text{ red-external-hext}[OF \text{ redex}]$  have preallocated  $h'$  by(rule preallocated-hext)
  from redex'' wtext''  $\langle hconf \ h \rangle$  have  $hconf \ h'$  by(rule external-call-hconf)
  with ta' redex'  $\langle \text{preallocated } h' \rangle$ 
  have  $ta\text{-bisim } w\text{bisim1 } (\text{extTA2J1 } P \ ta') \ ta$  by(auto intro: red-external-ta-bisim21[OF wf])
  moreover have  $\tau\text{move1 } P \ h \ (\text{Val } v \cdot M'(\square)) \implies ta' = \varepsilon \wedge h' = h$  using redex' Ta iec
    by(fastforce dest:  $\tau\text{external}'\text{-red-external-heap-unchanged } \tau\text{external}'\text{-red-external-TA-empty}$ 
      sees-method-fun simp add:  $\tau\text{external}'\text{-def } \tau\text{external-def}$ )
  moreover from v call
  have  $\text{call1 } (obj' \cdot M'(ps)) \neq \text{None} \implies \text{Val } v \cdot M'(ps) = obj' \cdot M'(ps) \wedge loc = xs$ 
    by(auto split: if-split-asm)
  ultimately show ?thesis using red  $\tau \ pc \ xcp \ stk \ Ta \ call \ iec$ 
    apply(auto simp del: call1.simps calls1.simps)
    apply(rule exI conjI | assumption |erule rtranclp.rtrancl-into-rtrancl rtranclp-into-tranclp1 | drule
      (1) sees-method-fun | clarsimp) +
  done
qed
qed
qed
next
  case (bisim1CallParams ps n ps' xs stk loc pc xcp obj M' v)
  note bisim2 =  $\langle P, ps, h \vdash (ps', xs) [\leftrightarrow] (stk, loc, pc, xcp) \rangle$ 
  note bisim1 =  $\langle P, obj, h \vdash (obj, xs) \leftrightarrow (\square, xs, 0, \text{None}) \rangle$ 
  note IH2 = bisim1CallParams.IH(2)
  note exec =  $\langle \text{exec-move-d } P \ t \ (obj \cdot M'(ps)) \ h \ (stk @ [v], loc, \text{length } (\text{compE2 } obj) + pc, xcp) \ ta \ h' \ (stk', loc', pc', xcp') \rangle$ 
  note len =  $\langle n + \text{max-vars } (\text{Val } v \cdot M'(ps')) \leq \text{length } xs \rangle$ 
  note bsok =  $\langle \text{bsok } (obj \cdot M'(ps)) \ n \rangle$ 
  from  $\langle P, h \vdash stk @ [v] [\leq] ST \rangle$  obtain  $T \ ST'$  where  $ST': P, h \vdash stk [\leq] ST'$  and  $T: \text{typeof}_h \ v = [T]$ 
    by(auto simp add: list-all2-Cons1 list-all2-append1 conf-def)
  from bisim2 have  $pc: pc \leq \text{length } (\text{compEs2 } ps)$  by(rule bisims1-pc-length-compEs2)
  show ?case
  proof(cases  $pc < \text{length } (\text{compEs2 } ps)$ )
    case True
      from exec have  $\text{exec-meth-d } (\text{compP2 } P) \ (\text{compE2 } (obj \cdot M'(ps))) \ (\text{compxE2 } (obj \cdot M'(ps)) \ 0 \ 0) \ t \ h$ 
       $(stk @ [v], loc, \text{length } (\text{compE2 } obj) + pc, xcp) \ ta \ h' \ (stk', loc', pc', xcp')$ 
      by(simp add: exec-move-def)
      with True have  $\text{exec}': \text{exec-meth-d } (\text{compP2 } P) \ (\text{compEs2 } ps) \ (\text{stack-xlift } (\text{length } [v]) \ (\text{compxE2 } ps \ 0 \ 0)) \ t \ h$ 
       $(stk @ [v], loc, pc, xcp) \ ta \ h' \ (stk', loc', pc' - \text{length } (\text{compE2 } obj), xcp')$ 

```



and  $pc': pc' \geq \text{length}(\text{compE2 } obj)$  **by**(*safe dest!:* *Call-execParamD*)  
**from** *exec' bisim2* **obtain** *stk''* **where**  $stk': stk' = stk'' @ [v]$   
 and *exec'': exec-moves-d*  $P \ t \ ps \ h \ (stk, loc, pc, xcp) \ ta \ h' \ (stk'', loc', pc' - \text{length}(\text{compE2 } obj),$   
*xcp')*  
**by**(*unfold exec-moves-def*)(*drule* (1) *exec-meth-stk-splits, auto*)  
**with** *True* **have**  $\tau: \tau \text{move2}(\text{compP2 } P) \ h \ (stk @ [v]) \ (obj \cdot M'(ps)) \ (\text{length}(\text{compE2 } obj) + pc)$   
*xcp =  $\tau \text{moves2}(\text{compP2 } P) \ h \ stk \ ps \ pc \ xcp$*   
**by**(*auto simp add:  $\tau \text{move2-iff} \ \tau \text{moves2-iff} \ \tau \text{instr-stk-drop-exec-moves}$* )  
**from**  $IH2[OF \ \text{exec''} \ - \ ST' \ \langle \text{conf-xcp'}(\text{compP2 } P) \ h \ xcp \rangle \ \text{len} \ bsok]$  **obtain**  $es'' \ xs''$   
**where**  $\text{bisim'}: P, ps, h' \vdash (es'', xs'') [\leftrightarrow] (stk'', loc', pc' - \text{length}(\text{compE2 } obj), xcp')$   
**and**  $\text{red'}: ?reds \ ps' \ xs \ es'' \ xs'' \ ps \ stk \ pc \ (pc' - \text{length}(\text{compE2 } obj)) \ xcp \ xcp' \ \text{by} \ \text{auto}$   
**from** *bisim'* **have**  $P, obj \cdot M'(ps), h' \vdash (Val \ v \cdot M'(es''), xs'') \leftrightarrow (stk'' @ [v], loc', \text{length}(\text{compE2 } obj) +$   
 $(pc' - \text{length}(\text{compE2 } obj)), xcp')$   
**by**(*rule bisim1-bisims1.bisim1CallParams*)  
**moreover from** *True* **have**  $\text{no-call2}(obj \cdot M'(ps)) \ (\text{length}(\text{compE2 } obj) + pc) = \text{no-calls2} \ ps \ pc$   
**by**(*simp add: no-calls2-def no-call2-def*)  
**moreover have**  $\text{calls1} \ ps' = \text{None} \implies \text{call1} \ (Val \ v \cdot M'(ps')) = \text{None} \vee \text{is-vals} \ ps' \ \text{by} \ \text{simp}$   
**ultimately show** *?thesis* **using** *red'  $\tau \ stk' \ pc \ pc'$*   
**by**(*fastforce intro: Call1Params elim!: Call- $\tau \text{red1r-param}$  Call- $\tau \text{red1t-param}$  split: if-split-asm*  
*simp add: is-vals-conv*)  
**next**  
**case** *False*  
**with** *pc* **have** [*simp*]:  $pc = \text{length}(\text{compEs2 } ps)$  **by** *simp*  
**with** *bisim2* **obtain** *vs* **where** [*simp*]:  $stk = \text{rev } vs \ xcp = \text{None}$   
**and** *lenvs*:  $\text{length } vs = \text{length } ps$  **and** *vs*:  $\text{is-vals} \ ps' \longrightarrow ps' = \text{map } Val \ vs \wedge xs = loc$   
**and** *call*:  $\text{calls1} \ ps' = \text{None}$   
**by**(*auto dest: bisims1-pc-length-compEs2D*)  
**with** *bisim2* *len bsok* **have** *reds*:  $\tau \text{reds1r} \ P \ t \ h \ (ps', xs) \ (\text{map } Val \ vs, loc)$   
**by**(*auto intro: bisims1-Val- $\tau \text{Reds1r}$  simp add: bsok-def*)  
**from** *exec T lenvs*  
**have**  $v = \text{Null} \vee (\text{is-Addr } v \wedge (\exists T \ C' \ Ts' \ Tr' \ D'. \ \text{typeof}_h \ v = [T] \wedge \text{class-type-of'} \ T = [C'] \wedge$   
 $P \vdash C' \ \text{sees } M': Ts' \rightarrow Tr' = \text{Native in } D')) \ (\text{is} \ - \vee \ ?rest)$   
**by**(*fastforce elim!: exec-meth.cases simp add: is-Ref-def exec-move-def compP2-def has-method-def*  
*split: if-split-asm*)  
**thus** *?thesis*  
**proof**  
**assume** [*simp*]:  $v = \text{Null}$   
**hence**  $\tau: \tau \text{move1} \ P \ h \ (Val \ v \cdot M'(\text{map } Val \ vs))$   
 $\tau \text{move2}(\text{compP2 } P) \ h \ (stk @ [v]) \ (obj \cdot M'(ps)) \ (\text{length}(\text{compE2 } obj) + \text{length}(\text{compEs2 } ps))$   
*None*  
**using** *lenvs* **by**(*auto simp add:  $\tau \text{move2-iff}$* )  
**from** *lenvs*  
**have**  $P, obj \cdot M'(ps), h \vdash (THROW \ \text{NullPointer}, loc) \leftrightarrow (\text{rev } vs @ [\text{Null}], loc, \text{length}(\text{compE2 } obj) +$   
 $\text{length}(\text{compEs2 } ps), [\text{addr-of-sys-xcpt } \text{NullPointer}])$   
**by**(*safe intro!: bisim1CallThrow*) *simp*  
**moreover have**  $\text{True}, P, t \vdash 1 \ \langle \text{null} \cdot M'(\text{map } Val \ vs), (h, loc) \rangle \ -\varepsilon \rightarrow \langle \text{THROW } \text{NullPointer}, (h,$   
 $loc) \rangle$   
**by**(*rule Red1CallNull*)  
**ultimately show** *?thesis* **using** *exec pc  $\tau \ lenvs \ reds$*   
**apply**(*auto elim!: exec-meth.cases simp add: exec-move-def*)  
**apply**(*rule exI conjI assumption*) +  
**apply**(*rule rtranclp.rtrancl-into-rtrancl*)  
**apply**(*erule Call- $\tau \text{red1r-param}$* )  
**by** *auto*

```

next
  assume ?rest
  then obtain a Ta C' Ts' Tr' D' where [simp]: v = Addr a
    and Ta: typeof-addr h a = [Ta]
    and iec: P ⊢ class-type-of Ta sees M': Ts' → Tr' = Native in D' by auto
  with exec pc lenvs
  obtain ta' va h'' U Ts Ts' where redex: (ta', va, h'') ∈ red-external-aggr (compP2 P) t a M' vs
h
  and ret: (xcp', h', [(stk', loc', undefined, undefined, pc')]) = extRet2JVM (length vs) h'' (rev vs
  @ [Addr a]) loc undefined undefined (length (compE2 obj) + length (compEs2 ps)) [] va
  and wtext': P, h ⊢ a.M'(vs) : U
  and Ts: map typeofh vs = map Some Ts
  and ta': ta = extTA2JVM (compP2 P) ta'
  by(fastforce simp add: is-Ref-def exec-move-def compP2-def external-WT'-iff exec-meth-instr
  confs-conv-map)
  have τ: τmove1 P h (Val v.M'(map Val vs)) ⟷ τmove2 (compP2 P) h (stk @ [v]) (obj.M'(ps))
  (length (compE2 obj) + length (compEs2 ps)) None
  using Ta iec lenvs
  by(auto simp add: τmove2-iff map-eq-append-conv compP2-def)
  from ret have [simp]: h'' = h' by simp
  from wtext' have wtext'': compP2 P, h ⊢ a.M'(vs) : U by(simp add: compP2-def external-WT'-iff)
  from wf have wf-jvm-prog (compP2 P) by(rule wt-compP2)
  then obtain wf-md where wf': wf-prog wf-md (compP2 P)
  unfolding wf-jvm-prog-def by(blast dest: wt-jvm-progD)
  from tconf have tconf': compP2 P, h ⊢ t √t by(simp add: compP2-def tconf-def)
  from redex have redex'': compP2 P, t ⊢ ⟨a.M'(vs), h⟩ -ta'→ext ⟨va, h'⟩
  by(simp add: WT-red-external-list-conv[OF wf' wtext'' tconf', symmetric])
  hence redex': P, t ⊢ ⟨a.M'(vs), h⟩ -ta'→ext ⟨va, h'⟩ by(simp add: compP2-def)
  with Ta iec have True, P, t ⊢ 1 ⟨addr a.M'(map Val vs), (h, loc)⟩ -ta'→ ⟨extRet2J (addr a.M'(map
  Val vs)) va, (h', loc)⟩
  by -(rule Red1CallExternal, auto)
  moreover have P, obj.M'(ps), h' ⊢ (extRet2J (addr a.M'(map Val vs)) va, loc) ↔ (stk', loc', pc',
  xcp')
  proof(cases va)
  case (RetVal v)
  have P, obj.M'(ps), h' ⊢ (Val v, loc) ↔ ([v], loc, length (compE2 (obj.M'(ps))), None)
  by(rule bisim1Val2)(simp)
  with ret RetVal show ?thesis by simp
  next
  case (RetExc ad)
  from lenvs have length ps = length (rev vs) by simp
  hence P, obj.M'(ps), h' ⊢ (Throw ad, loc) ↔ (rev vs @ [v], loc, length (compE2 (obj)) + length
  (compEs2 ps), [ad])
  by(rule bisim1CallThrow)
  with ret RetExc show ?thesis by simp
  next
  case RetStaySame
  from lenvs have length ps = length vs by simp
  from bisims1-map-Val-append[OF bisims1Nil this, of P h' loc]
  have P, ps, h' ⊢ (map Val vs, loc) [↔] (rev vs, loc, length (compEs2 ps), None) by simp
  hence P, obj.M'(ps), h' ⊢ (addr a.M'(map Val vs), loc) ↔ (rev vs @ [Addr a], loc, length (compE2
  obj) + length (compEs2 ps), None)
  by(rule bisim1-bisims1.bisim1CallParams)
  thus ?thesis using ret RetStaySame by simp

```

```

qed
moreover from ⟨preallocated h⟩ red-external-heap[OF redex'] have preallocated h' by(rule preal-
located-heap)
from redex'' wtext'' ⟨hconf h⟩ have hconf h' by(rule external-call-hconf)
with ta' redex' ⟨preallocated h'⟩
have ta-bisim wbisim1 (extTA2J1 P ta') ta by(auto intro: red-external-ta-bisim21[OF wf])
moreover have τmove1 P h (Val v.M'(map Val vs)) ⇒ ta' = ε ∧ h' = h
using Ta iec redex'
by(fastforce dest: τexternal'-red-external-heap-unchanged τexternal'-red-external-TA-empty
sees-method-fun simp add: τexternal'-def τexternal-def map-eq-append-conv)
moreover from vs call have call1 (Val v.M'(ps')) ≠ None ⇒ ps' = map Val vs ∧ loc = xs
by(auto split: if-split-asm simp add: is-vals-conv)
ultimately show ?thesis using reds τ pc Ta call
apply(auto simp del: split-paired-Ex call1.simps calls1.simps split: if-split-asm simp add:
map-eq-append-conv)
apply(auto 4 4 simp del: split-paired-Ex call1.simps calls1.simps intro: rtrancpl.rtrancpl-into-rtrancpl[OF
Call-τred1r-param] rtrancpl-into-trancpl1[OF Call-τred1r-param])[3]
apply((assumption|rule exI conjI|erule Call-τred1r-param|simp add: map-eq-append-conv)+)
done
qed
qed
next
case (bisim1CallThrowObj obj n a xs stk loc pc ps M')
note exec = ⟨?exec (obj.M'(ps)) stk loc pc [a] stk' loc' pc' xcp'⟩
note bisim1 = ⟨P,obj,h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
from bisim1 have pc: pc < length (compE2 obj) by(auto dest: bisim1-ThrowD)
from bisim1 have match-ex-table (compP2 P) (cname-of h a) (0 + pc) (compxE2 obj 0 0) = None
unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
with exec pc have False
by(auto elim!: exec-meth.cases simp add: exec-move-def match-ex-table-not-pcs-None)
thus ?case ..
next
case (bisim1CallThrowParams ps n vs a ps' xs stk loc pc obj M' v)
note exec = ⟨?exec (obj.M'(ps)) (stk @ [v]) loc (length (compE2 obj) + pc) [a] stk' loc' pc' xcp'⟩
note bisim2 = ⟨P,ps,h ⊢ (map Val vs @ Throw a # ps', xs) [↔] (stk, loc, pc, [a])⟩
from bisim2 have pc: pc < length (compEs2 ps) by(auto dest: bisims1-ThrowD)
from bisim2 have match-ex-table (compP2 P) (cname-of h a) (0 + pc) (compxEs2 ps 0 0) = None
unfolding compP2-def by(rule bisims1-xcp-Some-not-caught)
with exec pc have False
apply(auto elim!: exec-meth.cases simp add: compxEs2-size-convs compxEs2-stack-xlift-convs exec-move-def)
apply(auto simp add: match-ex-table-append dest!: match-ex-table-shift-pcD match-ex-table-stack-xliftD
match-ex-table-pc-length-compE2)
done
thus ?case ..
next
case (bisim1CallThrow ps vs obj n M' a xs v)
note lenvs = ⟨length ps = length vs⟩
note exec = ⟨?exec (obj.M'(ps)) (vs @ [v]) xs (length (compE2 obj) + length (compEs2 ps)) [a] stk'
loc' pc' xcp'⟩
with lenvs have False
by(auto elim!: exec-meth.cases simp add: match-ex-table-append-not-pcs exec-move-def dest!: match-ex-table-pc-length-compE2)
thus ?case ..
next
case (bisim1BlockSome1 e n V Ty v xs)

```

```

have  $\tau_{move2}$  (compP2 P) h [] { V:Ty=[v]; e } 0 None by(simp add:  $\tau_{move2}$ -iff)
with  $\langle ?exec \{ V:Ty=[v]; e \} [] xs \ 0 \ None \ stk' \ loc' \ pc' \ xcp' \rangle \langle P, e, h \vdash (e, xs) \leftrightarrow ([], xs, 0, None) \rangle$ 
show ?case by(fastforce elim!: exec-meth.cases intro: bisim1BlockSome2 simp add: exec-move-def)
next
  case (bisim1BlockSome2 e n V Ty v xs)
  have  $\tau_{move2}$  (compP2 P) h [v] { V:Ty=[v]; e } (Suc 0) None by(simp add:  $\tau_{move2}$ -iff)
  with  $\langle ?exec \{ V:Ty=[v]; e \} [v] xs \ (Suc \ 0) \ None \ stk' \ loc' \ pc' \ xcp' \rangle \langle P, e, h \vdash (e, xs) \leftrightarrow ([], xs, 0, None) \rangle$ 
  show ?case by(fastforce intro: r-into-rtranclp Block1Some bisim1BlockSome4 [OF bisim1-refl] simp
    add: exec-move-def
    elim!: exec-meth.cases)
next
  case (bisim1BlockSome4 e n e' xs stk loc pc xcp V Ty v)
  note IH = bisim1BlockSome4.IH(2)
  note exec =  $\langle ?exec \{ V:Ty=[v]; e \} stk \ loc \ (Suc \ (Suc \ pc)) \ xcp \ stk' \ loc' \ pc' \ xcp' \rangle$ 
  note bisim =  $\langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
  note bsok =  $\langle bsok \{ V:Ty=[v]; e \} n \rangle$ 
  with  $\langle n + max\text{-}vars \{ V:Ty=None; e' \} \leq length \ xs \rangle$ 
  have V: V < length xs and len: Suc n + max-vars e' ≤ length xs and [simp]: n = V by simp-all
  let ?pre = [Push v, Store V]
  from exec have exec': exec-meth-d (compP2 P) (?pre @ compE2 e) (shift (length ?pre) (compxE2 e 0 0)) t h (stk, loc, length ?pre + pc, xcp) ta h' (stk', loc', pc', xcp')
  by(simp add: compxE2-size-convs exec-move-def)
  hence pc': pc' ≥ length ?pre
  by(rule exec-meth-drop-pc) auto
  hence pc'': Suc (Suc (pc' - Suc (Suc 0))) = pc' by simp
  moreover from exec' have exec-move-d P t e h (stk, loc, pc, xcp) ta h' (stk', loc', pc' - length ?pre, xcp')
  unfolding exec-move-def by(rule exec-meth-drop) auto
  from IH [OF this len -  $\langle P, h \vdash stk \ [:\leq] \ ST \rangle \langle conf\text{-}xcp' \ (compP2 \ P) \ h \ xcp' \rangle$  bsok] obtain e'' xs''
  where bisim':  $P, e, h \vdash (e'', xs'') \leftrightarrow (stk', loc', pc' - length ?pre, xcp')$ 
  and red': ?red e' xs e'' xs'' e stk pc (pc' - length ?pre) xcp xcp' by auto
  from bisim' have P, { V:Ty=[v]; e }, h' ⊢ ({ V:Ty=None; e'' }, xs'') ↔ (stk', loc', Suc (Suc (pc' - length ?pre)), xcp')
  by(rule bisim1-bisims1.bisim1BlockSome4)
  moreover from pc' pc'' have  $\tau_{move2}$  (compP2 P) h stk { V:Ty=[v]; e } (Suc (Suc pc)) xcp =
 $\tau_{move2}$  (compP2 P) h stk e pc xcp by(simp add:  $\tau_{move2}$ -iff)
  moreover from red' have length xs'' = length xs
  by(auto split: if-split-asm dest!:  $\tau_{red1r}$ -preserves-len  $\tau_{red1t}$ -preserves-len red1-preserves-len)
  ultimately show ?case using red' pc'' V
  by(fastforce elim!: Block-None- $\tau_{red1r}$ -xt Block-None- $\tau_{red1t}$ -xt intro: Block1Red split: if-split-asm
    simp add: no-call2-def)
next
  case (bisim1BlockThrowSome e n a xs stk loc pc V Ty v)
  note exec =  $\langle ?exec \{ V:Ty=[v]; e \} stk \ loc \ (Suc \ (Suc \ pc)) \ [a] \ stk' \ loc' \ pc' \ xcp' \rangle$ 
  note bisim =  $\langle P, e, h \vdash (Throw \ a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$ 
  from bisim have pc: pc < length (compE2 e) by(auto dest: bisim1-ThrowD)
  from bisim have match-ex-table (compP2 P) (cname-of h a) (0 + pc) (compxE2 e 0 0) = None
  unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
  with exec pc have False
  apply(auto elim!: exec-meth.cases simp add: match-ex-table-not-pcs-None exec-move-def)
  apply(auto simp only: compxE2-size-convs dest!: match-ex-table-shift-pcD)
  apply simp
  done

```

```

thus ?case ..
next
  case (bisim1BlockNone e n e' xs stk loc pc xcp V Ty)
  note IH = bisim1BlockNone.IH(2)
  note exec = ⟨?exec { V:Ty=None; e } stk loc pc xcp stk' loc' pc' xcp'⟩
  note bisim = ⟨P,e,h ⊢ (e', xs) ↔ (stk, loc, pc, xcp)⟩
  note bsok = ⟨bsok { V:Ty=None; e } n⟩
  with ⟨n + max-vars { V:Ty=None; e' } ≤ length xs⟩
  have V: V < length xs and len: Suc n + max-vars e' ≤ length xs by simp-all
  have τmove2 (compP2 P) h stk { V:Ty=None; e } pc xcp = τmove2 (compP2 P) h stk e pc xcp
by(simp add: τmove2-iff)
  moreover
  from exec have ?exec e stk loc pc xcp stk' loc' pc' xcp' by(simp add: exec-move-BlockNone)
  from IH[OF this len - ⟨P,h ⊢ stk [:≤] ST⟩ ⟨conf-xcp' (compP2 P) h xcp⟩ bsok] obtain e'' xs''
    where bisim': P,e,h' ⊢ (e'', xs'') ↔ (stk', loc', pc', xcp')
    and red': ?red e' xs e'' xs'' e stk pc pc' xcp xcp' by auto
  from bisim' have P,{ V:Ty=None; e },h' ⊢ ({ V:Ty=None; e' }, xs'') ↔ (stk', loc', pc', xcp')
    by(rule bisim1-bisims1.bisim1BlockNone)
  ultimately show ?case using V red'
    by(fastforce elim!: Block1Red Block-None-τred1r-xt Block-None-τred1t-xt simp add: no-call2-def)
next
  case (bisim1BlockThrowNone e n a xs stk loc pc V Ty)
  note exec = ⟨?exec { V:Ty=None; e } stk loc pc [a] stk' loc' pc' xcp'⟩
  note bisim = ⟨P,e,h ⊢ (Throw a, xs) ↔ (stk, loc, pc, [a])⟩
  from bisim have pc: pc < length (compE2 e) by(auto dest: bisim1-ThrowD)
  from bisim have match-ex-table (compP2 P) (cname-of h a) (0 + pc) (compE2 e 0 0) = None
    unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
  with exec pc have False by(auto elim!: exec-meth.cases simp add: exec-move-def)
  thus ?case ..
next
  case (bisim1Sync1 e1 n e' xs stk loc pc xcp e2 V)
  note IH = bisim1Sync1.IH(2)
  note exec = ⟨?exec (sync_V (e1) e2) stk loc pc xcp stk' loc' pc' xcp'⟩
  note bisim = ⟨P,e1,h ⊢ (e', xs) ↔ (stk, loc, pc, xcp)⟩
  note bisim2 = ⟨P,e2,h ⊢ (e2, xs) ↔ ([], xs, 0, None)⟩
  note len = ⟨n + max-vars (sync_V (e') e2) ≤ length xs⟩
  note bsok = ⟨bsok (sync_V (e1) e2) n⟩
  from bisim have pc: pc ≤ length (compE2 e1) by(rule bisim1-pc-length-compE2)
  show ?case
  proof(cases pc < length (compE2 e1))
    case True
    with exec have exec': ?exec e1 stk loc pc xcp stk' loc' pc' xcp' by(simp add: exec-move-Sync1)
    from True have τmove2 (compP2 P) h stk (sync_V (e1) e2) pc xcp = τmove2 (compP2 P) h stk
e1 pc xcp by(simp add: τmove2-iff)
    with IH[OF exec' - - ⟨P,h ⊢ stk [:≤] ST⟩ ⟨conf-xcp' (compP2 P) h xcp⟩] len bisim2 bsok show
?thesis
    by(fastforce intro: bisim1-bisims1.bisim1Sync1 Synchronized1Red1 elim!: Sync-τred1r-xt Sync-τred1t-xt
simp add: no-call2-def)
  next
  case False
  with pc have [simp]: pc = length (compE2 e1) by simp
  with bisim obtain v where stk: stk = [v] and xcp: xcp = None
    by(auto dest: bisim1-pc-length-compE2D)
  with bisim pc len bsok have red: τred1r P t h (e', xs) (Val v, loc)

```

by(auto intro: bisim1-Val- $\tau$ red1r simp add: bsok-def)  
 hence  $\tau$ red1r  $P \ t \ h \ (sync_V \ (e') \ e2, \ xs) \ (sync_V \ (Val \ v) \ e2, \ loc)$  by(rule Sync- $\tau$ red1r-xt)  
 moreover have  $\tau: \tau move2 \ (compP2 \ P) \ h \ [v] \ (sync_V \ (e1) \ e2) \ pc \ None$  by(simp add:  $\tau move2$ -iff)  
 moreover  
 have  $P, (sync_V \ (e1) \ e2), h \vdash ((sync_V \ (Val \ v) \ e2), loc) \leftrightarrow ([v, v], loc, Suc \ (length \ (compE2 \ e1)), None)$   
 by(rule bisim1Sync2)  
 ultimately show ?thesis using exec stk xcp  
 by(fastforce elim!: exec-meth.cases simp add: exec-move-def)  
 qed  
 next  
 case (bisim1Sync2 e1 n e2 V v xs)  
 note exec =  $\langle ?exec \ (sync_V \ (e1) \ e2) \ [v, v] \ xs \ (Suc \ (length \ (compE2 \ e1))) \ None \ stk' \ loc' \ pc' \ xcp' \rangle$   
 note bisim =  $\langle P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, None) \rangle$   
 note bisim2 =  $\langle P, e2, h \vdash (e2, xs) \leftrightarrow ([], xs, 0, None) \rangle$   
 have  $\tau move2 \ (compP2 \ P) \ h \ [v, v] \ (sync_V \ (e1) \ e2) \ (Suc \ (length \ (compE2 \ e1))) \ None$  by(rule  $\tau move2$ Sync3)  
 thus ?case using exec  
 by(fastforce elim!: exec-meth.cases intro: bisim1Sync3 simp add: exec-move-def)  
 next  
 case (bisim1Sync3 e1 n e2 V v xs)  
 note exec =  $\langle ?exec \ (sync_V \ (e1) \ e2) \ [v] \ (xs[V := v]) \ (Suc \ (Suc \ (length \ (compE2 \ e1)))) \ None \ stk' \ loc' \ pc' \ xcp' \rangle$   
 note bisim =  $\langle P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, None) \rangle$   
 note bisim2 =  $\langle \bigwedge xs. P, e2, h \vdash (e2, xs) \leftrightarrow ([], xs, 0, None) \rangle$   
 note len =  $\langle n + max\text{-}vars \ (sync_V \ (Val \ v) \ e2) \leq length \ xs \rangle$   
 note bsok =  $\langle bsok \ (sync_V \ (e1) \ e2) \ n \rangle$   
 with len have  $V: V < length \ xs$  by simp  
 have  $\tau: \neg \tau move2 \ (compP2 \ P) \ h \ [v] \ (sync_V \ (e1) \ e2) \ (Suc \ (Suc \ (length \ (compE2 \ e1)))) \ None$  by(simp add:  $\tau move2$ -iff)  
 from exec have  $(\exists a. v = Addr \ a \wedge stk' = [] \wedge loc' = xs[V := v] \wedge ta = \{Lock \rightarrow a, SyncLock \ a\} \wedge xcp' = None \wedge pc' = Suc \ (Suc \ (Suc \ (length \ (compE2 \ e1)))) \vee (v = Null \wedge stk' = [v] \wedge loc' = xs[V := v] \wedge ta = \varepsilon \wedge xcp' = [addr\text{-}of\text{-}sys\text{-}xcpt \ NullPointer] \wedge pc' = Suc \ (Suc \ (length \ (compE2 \ e1))))$  (is ?c1  $\vee$  ?c2)  
 by(fastforce elim!: exec-meth.cases simp add: is-Ref-def expand-funfun-eq fun-eq-iff finfun-upd-apply exec-move-def)  
 thus ?case  
 proof  
 assume ?c1  
 then obtain a where [simp]:  $v = Addr \ a \wedge stk' = [] \wedge loc' = xs[V := v] \wedge ta = \{Lock \rightarrow a, SyncLock \ a\} \wedge xcp' = None \wedge pc' = Suc \ (Suc \ (Suc \ (length \ (compE2 \ e1))))$  by blast  
 have  $True, P, t \vdash 1 \ \langle sync_V \ (addr \ a) \ e2, (h, xs) \rangle - \{Lock \rightarrow a, SyncLock \ a\} \rightarrow \langle insync_V \ (a) \ e2, (h, xs[V := Addr \ a]) \rangle$   
 using V by(rule Lock1Synchronized)  
 moreover from bisim2 have  $P, sync_V \ (e1) \ e2, h \vdash (insync_V \ (a) \ e2, xs[V := Addr \ a]) \leftrightarrow ([], xs[V := Addr \ a], Suc \ (Suc \ (Suc \ (length \ (compE2 \ e1)))) \ (None))$   
 by(auto intro: bisim1Sync4[where pc = 0, simplified])  
 ultimately show ?case using exec  $\tau$   
 by(fastforce elim!: exec-meth.cases split: if-split-asm simp add: is-Ref-def exec-move-def ta-bisim-def ta-upd-simps)  
 next  
 assume ?c2  
 hence [simp]:  $v = Null \wedge stk' = [v] \wedge loc' = xs[V := v] \wedge ta = \varepsilon \wedge xcp' = [addr\text{-}of\text{-}sys\text{-}xcpt \ NullPointer] \wedge pc' = Suc \ (Suc \ (length \ (compE2 \ e1)))$  by simp-all

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from  $V$  have  $\text{True}, P, t \vdash 1 \langle \text{sync}_V (\text{null}) \ e2, (h, \text{xs}) \rangle -\varepsilon \rightarrow \langle \text{THROW NullPointer}, (h, \text{xs}[V := \text{Null}]) \rangle$ 
  by(rule Synchronized1Null)
moreover
  have  $P, \text{sync}_V (e1) \ e2, h \vdash (\text{THROW NullPointer}, \text{xs}[V := \text{Null}]) \leftrightarrow ([\text{Null}], \text{xs}[V := \text{Null}], \text{Suc} (\text{Suc} (\text{length} (\text{compE2 } e1))))$ ,  $[\text{addr-of-sys-xcpt NullPointer}]$ 
  by(rule bisim1Sync11)
ultimately show  $?case \text{ using } \text{exec } \tau$ 
  by(fastforce elim!: exec-meth.cases split: if-split-asm simp add: is-Ref-def exec-move-def)
qed
next
case ( $\text{bisim1Sync4 } e2 \ n \ e' \ \text{xs} \ \text{stk} \ \text{loc} \ \text{pc} \ \text{xcp} \ e1 \ V \ a$ )
  note  $IH = \text{bisim1Sync4}.IH(2)$ 
  note  $\text{exec} = \langle ?\text{exec} (\text{sync}_V (e1) \ e2) \ \text{stk} \ \text{loc} \ (\text{Suc} (\text{Suc} (\text{Suc} (\text{length} (\text{compE2 } e1) + \text{pc})))) \ \text{xcp} \ \text{stk}' \ \text{loc}' \ \text{pc}' \ \text{xcp}' \rangle$ 
  note  $\text{bisim1} = \langle P, e1, h \vdash (e1, \text{xs}) \leftrightarrow ([], \text{xs}, 0, \text{None}) \rangle$ 
  note  $\text{bisim2} = \langle P, e2, h \vdash (e', \text{xs}) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \rangle$ 
  note  $\text{len} = \langle n + \text{max-vars} (\text{insync}_V (a) \ e') \leq \text{length } \text{xs} \rangle$ 
  note  $\text{bsok} = \langle \text{bsok} (\text{sync}_V (e1) \ e2) \ n \rangle$ 
  with  $\text{len}$  have  $V: V < \text{length } \text{xs}$  and  $\text{len}': \text{Suc } n + \text{max-vars } e' \leq \text{length } \text{xs}$  by simp-all
  from  $\text{bisim2}$  have  $\text{pc}: \text{pc} \leq \text{length} (\text{compE2 } e2)$  by(rule bisim1-pc-length-compE2)
  let  $?pre = \text{compE2 } e1 \ @ \ [\text{Dup}, \text{Store } V, \text{MEnter}]$ 
  let  $?post = [\text{Load } V, \text{MExit}, \text{Goto } 4, \text{Load } V, \text{MExit}, \text{ThrowExc}]$ 
  from  $\text{exec}$  have  $\text{exec}': \text{exec-meth-d} (\text{compP2 } P) (?pre \ @ \ \text{compE2 } e2 \ @ \ ?post)$ 
  ( $\text{compxE2 } e1 \ 0 \ 0 \ @ \ \text{shift} (\text{length } ?pre) (\text{compxE2 } e2 \ 0 \ 0 \ @ \ [(0, \text{length} (\text{compE2 } e2), \text{None}, 3 + \text{length} (\text{compE2 } e2), 0)])$ )  $t$ 
   $h (\text{stk}, \text{loc}, \text{length } ?pre + \text{pc}, \text{xcp}) \text{ ta } h' (\text{stk}', \text{loc}', \text{pc}', \text{xcp}')$ 
  by(simp add: ac-simps eval-nat-numeral shift-compxE2 exec-move-def)
  hence  $\text{pc}': \text{pc}' \geq \text{length } ?pre$ 
  by(rule exec-meth-drop-xt-pc[where  $n'=1$ ]) auto
  from  $\text{exec}'$  have  $\text{exec}'': \text{exec-meth-d} (\text{compP2 } P) (\text{compE2 } e2 \ @ \ ?post)$ 
  ( $\text{compxE2 } e2 \ 0 \ 0 \ @ \ [(0, \text{length} (\text{compE2 } e2), \text{None}, 3 + \text{length} (\text{compE2 } e2), 0)]$ )  $t$ 
   $h (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \text{ ta } h' (\text{stk}', \text{loc}', \text{pc}' - \text{length } ?pre, \text{xcp}')$ 
  by(rule exec-meth-drop-xt[where  $n=1$ ]) auto
  show  $?case$ 
  proof(cases  $\text{pc} < \text{length} (\text{compE2 } e2)$ )
    case  $\text{True}$ 
    note  $\text{pc} = \text{True}$ 
    hence  $\tau: \tau \text{move2} (\text{compP2 } P) \ h \ \text{stk} \ (\text{sync}_V (e1) \ e2) \ (\text{Suc} (\text{Suc} (\text{Suc} (\text{length} (\text{compE2 } e1) + \text{pc}))))$ 
     $\text{xcp} = \tau \text{move2} (\text{compP2 } P) \ h \ \text{stk} \ e2 \ \text{pc} \ \text{xcp}$ 
    by(simp add:  $\tau \text{move2}$ -iff)
    show  $?thesis$ 
    proof(cases  $\text{xcp} = \text{None} \vee (\exists a'. \text{xcp} = [a'] \wedge \text{match-ex-table} (\text{compP2 } P) (\text{cname-of } h \ a') \ \text{pc} (\text{compxE2 } e2 \ 0 \ 0) \neq \text{None})$ )
      case  $\text{False}$ 
      then obtain  $a'$  where  $\text{Some}: \text{xcp} = [a']$ 
      and  $\text{True}: \text{match-ex-table} (\text{compP2 } P) (\text{cname-of } h \ a') \ \text{pc} (\text{compxE2 } e2 \ 0 \ 0) = \text{None}$  by(auto simp del: not-None-eq)
      from  $\text{Some} \langle \text{conf-xcp}' (\text{compP2 } P) \ h \ \text{xcp} \rangle$  obtain  $D$ 
      where  $ha': \text{typeof-addr } h \ a' = [\text{Class-type } D]$  and  $\text{subcls}: P \vdash D \preceq^* \text{Throwable}$  by(auto simp add:  $\text{compP2}$ -def)
      from  $ha' \ \text{subcls} \ \text{bisim2} \ \text{True} \ \text{bsok}$  have  $\tau \text{red1r } P \ t \ h \ (e', \text{xs}) \ (\text{Throw } a', \text{loc})$ 
      using  $\text{len}'$  unfolding  $\text{Some } \text{compP2}$ -def by(auto dest!: bisim1-xcp- $\tau \text{Red}$  simp add: bsok-def)
      moreover from  $\text{pc}$  have  $\tau \text{move2} (\text{compP2 } P) \ h \ \text{stk} \ (\text{sync}_V (e1) \ e2) \ (\text{Suc} (\text{Suc} (\text{Suc} (\text{pc} +$ 

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length (compE2 e1)))) [a']
  by(auto intro:  $\tau$ move2xcp)
  moreover
  have  $P, \text{sync}_V(e1) e2, h \vdash (\text{insync}_V(a) \text{Throw } a', \text{loc}) \leftrightarrow ([\text{Addr } a'], \text{loc}, 6 + \text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2), \text{None})$ 
  by(rule bisim1Sync7)
  ultimately show ?thesis using exec True pc Some ha' subcls
  apply(auto elim!: exec-meth.cases simp add: ac-simps eval-nat-numeral match-ex-table-append
    matches-ex-entry-def compP2-def exec-move-def)

  apply(simp-all only: compxE2-size-convcs, auto dest: match-ex-table-shift-pcD match-ex-table-pc-length-compE2)
  apply(fastforce elim!: InSync- $\tau$ red1r-xt)
  done
next
case True
  with exec'' pc have exec-meth-d (compP2 P) (compE2 e2 @ ?post) (compxE2 e2 0 0) t
    h (stk, loc, pc, xcp) ta h' (stk', loc', pc' - length ?pre, xcp')
  by(auto elim!: exec-meth.cases intro: exec-meth.intros simp add: match-ex-table-append exec-move-def)
  hence exec-move-d P t e2 h (stk, loc, pc, xcp) ta h' (stk', loc', pc' - length ?pre, xcp')
    using pc unfolding exec-move-def by(rule exec-meth-take)
  from IH[OF this len' -  $\langle P, h \vdash \text{stk} [:\leq] ST \rangle \langle \text{conf-xcp}' (\text{compP2 } P) h \text{xcp}' \rangle$  bsok] obtain e'' xs''
    where bisim':  $P, e2, h' \vdash (e'', xs'') \leftrightarrow (stk', loc', pc' - \text{length } ?pre, xcp')$ 
    and red':  $?red e' xs e'' xs'' e2 \text{stk pc} (pc' - \text{length } ?pre) \text{xcp xcp}'$  by auto
  from bisim'
  have  $P, \text{sync}_V(e1) e2, h' \vdash (\text{insync}_V(a) e'', xs'') \leftrightarrow (stk', loc', \text{Suc}(\text{Suc}(\text{Suc}(\text{length}(\text{compE2 } e1) + (pc' - \text{length } ?pre))))), \text{xcp}'$ 
  by(rule bisim1-bisims1.bisim1Sync4)
  moreover from pc' have  $\text{Suc}(\text{Suc}(\text{Suc}(\text{length}(\text{compE2 } e1) + (pc' - \text{Suc}(\text{Suc}(\text{Suc}(\text{length}(\text{compE2 } e1))))))) = pc'$ 
     $\text{Suc}(\text{Suc}(\text{Suc}(pc' - \text{Suc}(\text{Suc}(\text{Suc } 0)))))) = pc'$ 
  by simp-all
  ultimately show ?thesis using red'  $\tau$ 
    by(fastforce intro: Synchronized1Red2 simp add: eval-nat-numeral no-call2-def elim!: In-Sync- $\tau$ red1r-xt InSync- $\tau$ red1t-xt split: if-split-asm)
qed
next
case False
  with pc have [simp]:  $pc = \text{length}(\text{compE2 } e2)$  by simp
  with bisim2 obtain v where [simp]:  $\text{stk} = [v] \text{xcp} = \text{None}$  by(auto dest: bisim1-pc-length-compE2D)
  have  $\tau\text{move2}(\text{compP2 } P) h [v] (\text{sync}_V(e1) e2) (\text{Suc}(\text{Suc}(\text{Suc}(\text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2)))) \text{None}$  by(simp add:  $\tau\text{move2-iff}$ )
  moreover from bisim2 pc len bsok have  $\text{red}: \tau\text{red1r } P t h (e', xs) (\text{Val } v, \text{loc})$ 
    by(auto intro!: bisim1-Val- $\tau\text{red1r}$  simp add: bsok-def)
  hence  $\tau\text{red1r } P t h (\text{insync}_V(a) e', xs) (\text{insync}_V(a) (\text{Val } v), \text{loc})$  by(rule InSync- $\tau\text{red1r-xt}$ )
  moreover
  have  $P, \text{sync}_V(e1) e2, h \vdash (\text{insync}_V(a) (\text{Val } v), \text{loc}) \leftrightarrow ([\text{loc} ! V, v], \text{loc}, 4 + \text{length}(\text{compE2 } e1) + \text{length}(\text{compE2 } e2), \text{None})$ 
  by(rule bisim1Sync5)
  ultimately show ?thesis using exec
    by(auto elim!: exec-meth.cases simp add: eval-nat-numeral exec-move-def) blast
qed
next
case (bisim1Sync5 e1 n e2 V a v xs)
  note bisim1 =  $\langle P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$ 

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note bisim2 =  $\langle P, e2, h \vdash (e2, xs) \leftrightarrow ([], xs, 0, None) \rangle$ 
note exec =  $\langle ?exec (sync_V (e1) e2) [xs ! V, v] xs (4 + length (compE2 e1) + length (compE2 e2))$ 
   $None\ stk' loc' pc' xcp' \rangle$ 
from  $\langle n + max-vars (insync_V (a) Val v) \leq length xs \rangle \langle bsok (sync_V (e1) e2) n \rangle$  have  $V: V < length$ 
 $xs$  by simp
have  $\tau: \neg \tau move2 (compP2 P) h [xs ! V, v] (sync_V (e1) e2) (4 + length (compE2 e1) + length$ 
 $(compE2 e2)) None$ 
by (simp add:  $\tau move2$ -iff)
have  $\tau': \neg \tau move1 P h (insync_V (a) Val v)$  by auto
from exec have  $(\exists a'. xs ! V = Addr a') \vee xs ! V = Null$  (is  $?c1 \vee ?c2$ )
by (auto elim!: exec-meth.cases simp add: split-beta is-Ref-def exec-move-def split: if-split-asm)
thus ?case
proof
  assume ?c1
  then obtain  $a'$  where  $xsV [simp]: xs ! V = Addr a' ..$ 
  have  $P, sync_V (e1) e2, h \vdash (Val v, xs) \leftrightarrow ([v], xs, 5 + length (compE2 e1) + length (compE2 e2),$ 
 $None)$ 
     $P, sync_V (e1) e2, h \vdash (THROW IllegalMonitorState, xs) \leftrightarrow ([Addr a', v], xs, 4 + length (compE2$ 
 $e1) + length (compE2 e2), [addr-of-sys-xcpt IllegalMonitorState])$ 
    by (rule bisim1Sync6, rule bisim1Sync12)
  moreover from  $xsV V$  have  $True, P, t \vdash 1 \langle insync_V (a) Val v, (h, xs) \rangle -\llbracket Unlock \rightarrow a', SyncUnlock$ 
 $a' \rrbracket \rightarrow \langle Val v, (h, xs) \rangle$ 
    by (rule Unlock1Synchronized)
  moreover from  $xsV V$  have  $True, P, t \vdash 1 \langle insync_V (a) Val v, (h, xs) \rangle -\llbracket UnlockFail \rightarrow a' \rrbracket \rightarrow$ 
 $\langle THROW IllegalMonitorState, (h, xs) \rangle$ 
    by (rule Unlock1SynchronizedFail[OF TrueI])
  ultimately show ?case using  $\tau \tau' exec$ 
    by (fastforce elim!: exec-meth.cases simp add: is-Ref-def ta-bisim-def exec-move-def ac-simps
  ta-upd-simps
    simp del: conj.left-commute)
next
  assume  $xsV: xs ! V = Null$ 
  have  $P, sync_V (e1) e2, h \vdash (THROW NullPointer, xs) \leftrightarrow ([Null, v], xs, 4 + length (compE2 e1) +$ 
 $length (compE2 e2), [addr-of-sys-xcpt NullPointer])$ 
    by (rule bisim1Sync12)
  thus ?case using  $\tau \tau' exec xsV V$ 
    by (fastforce elim!: exec-meth.cases intro: Unlock1SynchronizedNull simp add: is-Ref-def ta-bisim-def
  ac-simps exec-move-def
    simp del: conj.left-commute)
qed
next
case (bisim1Sync6  $e1 n e2 V v x$ )
note bisim1 =  $\langle P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, None) \rangle$ 
note bisim2 =  $\langle P, e2, h \vdash (e2, xs) \leftrightarrow ([], xs, 0, None) \rangle$ 
note exec =  $\langle ?exec (sync_V (e1) e2) [v] x (5 + length (compE2 e1) + length (compE2 e2)) None$ 
 $stk' loc' pc' xcp' \rangle$ 
have  $\tau move2 (compP2 P) h [v] (sync_V (e1) e2) (5 + length (compE2 e1) + length (compE2 e2))$ 
 $None$  by (simp add:  $\tau move2$ -iff)
moreover
have  $P, sync_V (e1) e2, h \vdash (Val v, x) \leftrightarrow ([v], x, length (compE2 (sync_V (e1) e2)), None)$ 
    by (rule bisim1Val2) simp
moreover have  $nat (9 + (int (length (compE2 e1)) + int (length (compE2 e2)))) = 9 + length$ 
 $(compE2 e1) + length (compE2 e2)$  by arith
ultimately show ?case using exec

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    by(fastforce elim!: exec-meth.cases simp add: eval-nat-numeral exec-move-def)
next
  case (bisim1Sync7 e1 n e2 V a a' xs)
  note ⟨?exec (syncV (e1) e2) [Addr a'] xs (6 + length (compE2 e1) + length (compE2 e2)) None
stk' loc' pc' xcp'⟩
  moreover
  have P, syncV (e1) e2, h' ⊢ (insyncV (a) Throw a', xs) ↔
    ([xs ! V, Addr a'], xs, 7 + length (compE2 e1) + length (compE2 e2), None)
  by(rule bisim1Sync8)
  moreover have τmove2 (compP2 P) h [Addr a'] (syncV (e1) e2) (6 + length (compE2 e1) +
length (compE2 e2)) None
  by(simp add: τmove2-iff)
  ultimately show ?case by(fastforce elim!: exec-meth.cases simp add: eval-nat-numeral exec-move-def)
next
  case (bisim1Sync8 e1 n e2 V a a' xs)
  from ⟨n + max-vars (insyncV (a) Throw a') ≤ length xs⟩ bsok (syncV (e1) e2) n have V: V <
length xs by simp
  note ⟨?exec (syncV (e1) e2) [xs ! V, Addr a'] xs (7 + length (compE2 e1) + length (compE2 e2))
None stk' loc' pc' xcp'⟩
  moreover have ¬ τmove2 (compP2 P) h [xs ! V, Addr a'] (syncV (e1) e2) (7 + length (compE2
e1) + length (compE2 e2)) None
  by(simp add: τmove2-iff)
  moreover
  have P, syncV (e1) e2, h ⊢ (Throw a', xs) ↔ ([Addr a'], xs, 8 + length (compE2 e1) + length
(compE2 e2), None)
  P, syncV (e1) e2, h ⊢ (THROW IllegalMonitorState, xs) ↔ ([xs ! V, Addr a'], xs, 7 + length (compE2
e1) + length (compE2 e2), |addr-of-sys-xcpt IllegalMonitorState|)
  P, syncV (e1) e2, h ⊢ (THROW NullPointerException, xs) ↔ ([Null, Addr a'], xs, 7 + length (compE2 e1) +
length (compE2 e2), |addr-of-sys-xcpt NullPointerException|)
  by(rule bisim1Sync9 bisim1Sync14)+
  moreover {
    fix A
    assume xs ! V = Addr A
    hence True, P, t ⊢ 1 ⟨insyncV (a) Throw a', (h, xs)⟩ −⟦Unlock→A, SyncUnlock A⟧→ ⟨Throw a',
(h, xs)⟩
    True, P, t ⊢ 1 ⟨insyncV (a) Throw a', (h, xs)⟩ −⟦UnlockFail→A⟧→ ⟨THROW IllegalMonitorState,
(h, xs)⟩
    using V by(rule Synchronized1Throw2 Synchronized1Throw2Fail[OF TrueI])+ }
  moreover {
    assume xs ! V = Null
    hence True, P, t ⊢ 1 ⟨insyncV (a) Throw a', (h, xs)⟩ −ε→ ⟨THROW NullPointerException, (h, xs)⟩
    using V by(rule Synchronized1Throw2Null) }
  moreover have ¬ τmove1 P h (insyncV (a) Throw a') by fastforce
  ultimately show ?case
  by(fastforce elim!: exec-meth.cases simp add: eval-nat-numeral is-Ref-def ta-bisim-def ta-upd-simps
exec-move-def split: if-split-asm)
next
  case (bisim1Sync9 e1 n e2 V a xs)
  note ⟨?exec (syncV (e1) e2) [Addr a] xs (8 + length (compE2 e1) + length (compE2 e2)) None
stk' loc' pc' xcp'⟩
  moreover
  have P, syncV (e1) e2, h ⊢ (Throw a, xs) ↔ ([Addr a], xs, 8 + length (compE2 e1) + length (compE2
e2), [a])
  by(rule bisim1Sync10)

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moreover have  $\tau_{move2}$  ( $compP2$   $P$ )  $h$  [ $Addr$   $a$ ] ( $sync_V$  ( $e1$ )  $e2$ ) ( $8 + length$  ( $compE2$   $e1$ ) +  $length$  ( $compE2$   $e2$ ))  $None$ 
  by( $rule$   $\tau_{move2Sync8}$ )
ultimately show  $?case$ 
  by( $fastforce$   $elim!$ :  $exec-meth.cases$   $simp$   $add$ :  $eval-nat-numeral$   $exec-move-def$   $split$ :  $if-split-asm$ )
next
  case ( $bisim1Sync10$   $e1$   $n$   $e2$   $V$   $a$   $xs$ )
  from  $\langle ?exec$  ( $sync_V$  ( $e1$ )  $e2$ ) [ $Addr$   $a$ ]  $xs$  ( $8 + length$  ( $compE2$   $e1$ ) +  $length$  ( $compE2$   $e2$ )) [ $a$ ]  $stk'$   $loc'$   $pc'$   $xcp'$   $\rangle$ 
  have  $False$  by( $auto$   $elim!$ :  $exec-meth.cases$   $simp$   $add$ :  $matches-ex-entry-def$   $match-ex-table-append-not-pcs$   $exec-move-def$ )
  thus  $?case$  ..
next
  case ( $bisim1Sync11$   $e1$   $n$   $e2$   $V$   $xs$ )
  from  $\langle ?exec$  ( $sync_V$  ( $e1$ )  $e2$ ) [ $Null$ ]  $xs$  ( $Suc$  ( $Suc$  ( $length$  ( $compE2$   $e1$ )))) [ $addr-of-sys-xcpt$   $Null-Pointer$ ]  $stk'$   $loc'$   $pc'$   $xcp'$   $\rangle$ 
  have  $False$  by( $auto$   $elim!$ :  $exec-meth.cases$   $simp$   $add$ :  $matches-ex-entry-def$   $match-ex-table-append-not-pcs$   $exec-move-def$ )
  thus  $?case$  ..
next
  case ( $bisim1Sync12$   $e1$   $n$   $e2$   $V$   $a$   $xs$   $v$   $v'$ )
  from  $\langle ?exec$  ( $sync_V$  ( $e1$ )  $e2$ ) [ $v$ ,  $v'$ ]  $xs$  ( $4 + length$  ( $compE2$   $e1$ ) +  $length$  ( $compE2$   $e2$ )) [ $a$ ]  $stk'$   $loc'$   $pc'$   $xcp'$   $\rangle$ 
  have  $False$  by( $auto$   $elim!$ :  $exec-meth.cases$   $simp$   $add$ :  $matches-ex-entry-def$   $match-ex-table-append-not-pcs$   $exec-move-def$ )
  thus  $?case$  ..
next
  case ( $bisim1Sync14$   $e1$   $n$   $e2$   $V$   $a$   $xs$   $v$   $a'$ )
  from  $\langle ?exec$  ( $sync_V$  ( $e1$ )  $e2$ ) [ $v$ ,  $Addr$   $a'$ ]  $xs$  ( $7 + length$  ( $compE2$   $e1$ ) +  $length$  ( $compE2$   $e2$ )) [ $a$ ]  $stk'$   $loc'$   $pc'$   $xcp'$   $\rangle$ 
  have  $False$  by( $auto$   $elim!$ :  $exec-meth.cases$   $simp$   $add$ :  $matches-ex-entry-def$   $match-ex-table-append-not-pcs$   $exec-move-def$ )
  thus  $?case$  ..
next
  case  $bisim1InSync$  thus  $?case$  by  $simp$ 
next
  case ( $bisim1SyncThrow$   $e1$   $n$   $a$   $xs$   $stk$   $loc$   $pc$   $e2$   $V$ )
  note  $exec = \langle ?exec$  ( $sync_V$  ( $e1$ )  $e2$ )  $stk$   $loc$   $pc$  [ $a$ ]  $stk'$   $loc'$   $pc'$   $xcp'$   $\rangle$ 
  note  $bisim1 = \langle P, e1, h \vdash (Throw\ a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$ 
  from  $bisim1$  have  $pc$ :  $pc < length$  ( $compE2$   $e1$ ) by( $auto$   $dest$ :  $bisim1-ThrowD$ )
  from  $bisim1$  have  $match-ex-table$  ( $compP2$   $P$ ) ( $cname-of$   $h$   $a$ ) ( $0 + pc$ ) ( $compE2$   $e1$   $0$   $0$ ) =  $None$ 
  unfolding  $compP2-def$  by( $rule$   $bisim1-xcp-Some-not-caught$ )
  with  $exec$   $pc$  have  $False$ 
  apply( $auto$   $elim!$ :  $exec-meth.cases$   $simp$   $add$ :  $match-ex-table-append-not-pcs$   $exec-move-def$ )
  apply( $auto$   $dest!$ :  $match-ex-table-shift-pcD$   $simp$   $add$ :  $matches-ex-entry-def$ )
  done
  thus  $?case$  ..
next
  case ( $bisim1Seq1$   $e1$   $n$   $e'$   $xs$   $stk$   $loc$   $pc$   $xcp$   $e2$ )
  note  $IH = bisim1Seq1.IH(2)$ 
  note  $exec = \langle ?exec$  ( $e1;; e2$ )  $stk$   $loc$   $pc$   $xcp$   $stk'$   $loc'$   $pc'$   $xcp'$   $\rangle$ 
  note  $bisim = \langle P, e1, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
  note  $len = \langle n + max-vars$  ( $e';; e2$ )  $\leq length$   $xs \rangle$ 
  note  $bsok = \langle bsok$  ( $e1;; e2$ )  $n \rangle$ 

```

```

from bisim have pc:  $pc \leq \text{length} (\text{compE2 } e1)$  by(rule bisim1-pc-length-compE2)
show ?case
proof(cases pc < length (compE2 e1))
  case True
    let ?post = Pop # compE2 e2
      from exec have exec': ?exec e1 stk loc pc xcp stk' loc' pc' xcp' using True by(simp add: exec-move-Seq1)
        from True have  $\tau\text{move2} (\text{compP2 } P) h \text{ stk } (e1;;e2) pc xcp = \tau\text{move2} (\text{compP2 } P) h \text{ stk } e1 pc xcp$  by(simp add: \tau move2-iff)
          with  $IH[OF \text{exec}' - - \langle P, h \vdash \text{stk} [\leq] ST \rangle \langle \text{conf-xcp}' (\text{compP2 } P) h xcp \rangle \text{len bsok}]$  show ?thesis
            by(fastforce intro: bisim1-bisims1.bisim1Seq1 Seq1Red elim!: Seq-\tau red1r-xt Seq-\tau red1t-xt simp add: no-call2-def)
          next
            case False
              with pc have [simp]:  $pc = \text{length} (\text{compE2 } e1)$  by simp
              with bisim obtain v where stk:  $\text{stk} = [v]$  and xcp:  $xcp = \text{None}$ 
                by(auto dest: bisim1-pc-length-compE2D)
              with bisim pc len bsok have red:  $\tau\text{red1r } P t h (e', xs) (\text{Val } v, \text{loc})$ 
                by(auto intro: bisim1-Val-\tau red1r simp add: bsok-def)
              hence  $\tau\text{red1r } P t h (e';; e2, xs) (\text{Val } v;; e2, \text{loc})$  by(rule Seq-\tau red1r-xt)
              also have  $\tau\text{move1 } P h (\text{Val } v;; e2)$  by(rule \tau move1SeqRed)
              hence  $\tau\text{red1r } P t h (\text{Val } v;; e2, \text{loc}) (e2, \text{loc})$  by(auto intro: Red1Seq r-into-rtrancpl)
              also have  $\tau$ :  $\tau\text{move2} (\text{compP2 } P) h [v] (e1;;e2) pc \text{None}$  by(simp add: \tau move2-iff)
              moreover from  $\langle P, e2, h \vdash (e2, \text{loc}) \leftrightarrow ([], \text{loc}, 0, \text{None}) \rangle$ 
                have  $P, e1;; e2, h \vdash (e2, \text{loc}) \leftrightarrow ([], \text{loc}, \text{Suc} (\text{length} (\text{compE2 } e1) + 0), \text{None})$ 
                by(rule bisim1Seq2)
              ultimately show ?thesis using exec stk xcp
                by(fastforce elim!: exec-meth.cases simp add: exec-move-def)
            qed
          next
            case (bisim1SeqThrow1 e1 n a xs stk loc pc e2)
              note exec =  $\langle ?\text{exec} (e1;;e2) \text{stk loc pc } [a] \text{stk}' \text{loc}' \text{pc}' \text{xcp}' \rangle$ 
              note bisim1 =  $\langle P, e1, h \vdash (\text{Throw } a, xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, [a]) \rangle$ 
              from bisim1 have pc:  $pc < \text{length} (\text{compE2 } e1)$  by(auto dest: bisim1-ThrowD)
              from bisim1 have match-ex-table (compP2 P) (cname-of h a) ( $0 + pc$ ) (compxE2 e1 0 0) = None
                unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
              with exec pc have False
                by(auto elim!: exec-meth.cases simp add: match-ex-table-not-pcs-None exec-move-def)
              thus ?case ..
            next
              case (bisim1Seq2 e2 n e' xs stk loc pc xcp e1)
                note IH = bisim1Seq2.IH(2)
                note bisim1 =  $\langle \bigwedge xs. P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$ 
                note bisim2 =  $\langle P, e2, h \vdash (e', xs) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \text{xcp}) \rangle$ 
                note len =  $\langle n + \text{max-vars } e' \leq \text{length } xs \rangle$ 
                note exec =  $\langle ?\text{exec} (e1;; e2) \text{stk loc } (\text{Suc} (\text{length} (\text{compE2 } e1) + pc)) \text{xcp stk}' \text{loc}' \text{pc}' \text{xcp}' \rangle$ 
                note bsok =  $\langle \text{bsok} (e1;; e2) n \rangle$ 
                let ?pre = compE2 e1 @ [Pop]
                from exec have exec': exec-meth-d (compP2 P) (?pre @ compE2 e2) (compxE2 e1 0 0 @ shift (length ?pre) (compxE2 e2 0 0)) t h (stk, loc, length ?pre + pc, xcp) ta h' (stk', loc', pc', xcp')
                  by(simp add: shift-compxE2 exec-move-def)
                hence ?exec e2 stk loc pc xcp stk' loc' (pc' - length ?pre) xcp'
                  unfolding exec-move-def by(rule exec-meth-drop-xt, auto)
                from  $IH[OF \text{this len} - \langle P, h \vdash \text{stk} [\leq] ST \rangle \langle \text{conf-xcp}' (\text{compP2 } P) h xcp \rangle]$  bsok obtain e'' xs''

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where  $\text{bisim}' : P, e2, h' \vdash (e'', xs'') \leftrightarrow (stk', loc', pc' - \text{length } ?pre, xcp')$   
 and  $\text{red} : ?red\ e'\ xs\ e''\ xs''\ e2\ stk\ pc\ (pc' - \text{length } ?pre)\ xcp\ xcp'$  **by** *auto*  
**from**  $\text{bisim}'$   
**have**  $P, e1;;e2, h' \vdash (e'', xs'') \leftrightarrow (stk', loc', \text{Suc } (\text{length } (\text{compE2 } e1) + (pc' - \text{length } ?pre)), xcp')$   
**by** (rule  $\text{bisim1-bisims1.bisim1Seq2}$ )  
**moreover have**  $\tau : \tau\text{move2 } (\text{compP2 } P)\ h\ stk\ (e1;;e2)\ (\text{Suc } (\text{length } (\text{compE2 } e1) + pc))\ xcp =$   
 $\tau\text{move2 } (\text{compP2 } P)\ h\ stk\ e2\ pc\ xcp$   
**by** (simp add:  $\tau\text{move2-iff}$ )  
**moreover from**  $\text{exec}'$  **have**  $pc' \geq \text{length } ?pre$   
**by** (rule  $\text{exec-meth-drop-xt-pc}$ ) *auto*  
**ultimately show**  $?case$  **using**  $\text{red}$  **by** (fastforce split: if-split-asm simp add: no-call2-def)

**next**  
**case** ( $\text{bisim1Cond1 } e\ n\ e'\ xs\ stk\ loc\ pc\ xcp\ e1\ e2$ )  
**note**  $IH = \text{bisim1Cond1.IH}(2)$   
**note**  $\text{bisim} = \langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$   
**note**  $\text{bisim1} = \langle \bigwedge xs. P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$   
**note**  $\text{bisim2} = \langle \bigwedge xs. P, e2, h \vdash (e2, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$   
**from**  $\langle n + \text{max-vars } (if\ (e')\ e1\ \text{else}\ e2) \leq \text{length } xs \rangle$   
**have**  $\text{len} : n + \text{max-vars } e' \leq \text{length } xs$  **by** *simp*  
**note**  $\text{exec} = \langle ?exec\ (if\ (e)\ e1\ \text{else}\ e2)\ stk\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$   
**note**  $\text{bsok} = \langle \text{bsok}\ (if\ (e)\ e1\ \text{else}\ e2)\ n \rangle$   
**from**  $\text{bisim}$  **have**  $pc : pc \leq \text{length } (\text{compE2 } e)$  **by** (rule  $\text{bisim1-pc-length-compE2}$ )  
**show**  $?case$   
**proof** (cases  $pc < \text{length } (\text{compE2 } e)$ )  
**case** *True*  
**let**  $?post = \text{IfFalse } (2 + \text{int } (\text{length } (\text{compE2 } e1))) \# \text{compE2 } e1 \ @\ \text{Goto } (1 + \text{int } (\text{length } (\text{compE2 } e2))) \# \text{compE2 } e2$   
**from**  $\text{exec}$  **have**  $\text{exec}' : ?exec\ e\ stk\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp'$  **using** *True*  
**by** (simp add:  $\text{exec-move-Cond1}$ )  
**from** *True* **have**  $\tau\text{move2 } (\text{compP2 } P)\ h\ stk\ (if\ (e)\ e1\ \text{else}\ e2)\ pc\ xcp = \tau\text{move2 } (\text{compP2 } P)\ h$   
 $stk\ e\ pc\ xcp$   
**by** (simp add:  $\tau\text{move2-iff}$ )  
**with**  $IH[OF\ \text{exec}' - \langle P, h \vdash stk\ [: \leq]\ ST \rangle \langle \text{conf-xcp}'\ (\text{compP2 } P)\ h\ xcp \rangle]$   $\text{len}\ \text{bsok}$  **show**  $?thesis$   
**by** (fastforce intro:  $\text{bisim1-bisims1.bisim1Cond1}\ \text{Cond1Red}\ \text{elim!} : \text{Cond-}\tau\text{red1r-xt}\ \text{Cond-}\tau\text{red1t-xt}$   
 simp add: no-call2-def)

**next**  
**case** *False*  
**with**  $pc$  **have**  $[\text{simp}] : pc = \text{length } (\text{compE2 } e)$  **by** *simp*  
**with**  $\text{bisim}$  **obtain**  $v$  **where**  $stk : stk = [v]$  **and**  $xcp : xcp = \text{None}$   
**by** (auto dest:  $\text{bisim1-pc-length-compE2D}$ )  
**with**  $\text{bisim}\ pc\ \text{len}\ \text{bsok}$  **have**  $\text{red} : \tau\text{red1r } P\ t\ h\ (e', xs)\ (\text{Val } v, loc)$   
**by** (auto intro:  $\text{bisim1-Val-}\tau\text{red1r}\ \text{simp add: bsok-def}$ )  
**hence**  $\tau\text{red1r } P\ t\ h\ (if\ (e')\ e1\ \text{else}\ e2, xs)\ (if\ (\text{Val } v)\ e1\ \text{else}\ e2, loc)$  **by** (rule  $\text{Cond-}\tau\text{red1r-xt}$ )  
**moreover have**  $\tau\text{move1 } P\ h\ (if\ (\text{Val } v)\ e1\ \text{else}\ e2)$  **by** (rule  $\tau\text{move1CondRed}$ )  
**moreover have**  $\tau : \tau\text{move2 } (\text{compP2 } P)\ h\ [v]\ (if\ (e)\ e1\ \text{else}\ e2)\ pc\ \text{None}$  **by** (simp add:  $\tau\text{move2-iff}$ )  
**moreover from**  $\text{bisim1}[of\ loc]$   
**have**  $P, if\ (e)\ e1\ \text{else}\ e2, h \vdash (e1, loc) \leftrightarrow ([], loc, \text{Suc } (\text{length } (\text{compE2 } e) + 0), \text{None})$   
**by** (rule  $\text{bisim1CondThen}$ )  
**moreover from**  $\text{bisim2}[of\ loc]$   
**have**  $P, if\ (e)\ e1\ \text{else}\ e2, h \vdash (e2, loc) \leftrightarrow ([], loc, \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e) + \text{length } (\text{compE2 } e1) + 0)), \text{None})$   
**by** (rule  $\text{bisim1CondElse}$ )  
**moreover have**  $\text{nat } (\text{int } (\text{length } (\text{compE2 } e)) + (2 + \text{int } (\text{length } (\text{compE2 } e1)))) = \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e) + \text{length } (\text{compE2 } e1) + 0))$  **by** *simp*

**moreover from**  $\text{exec } xcp \text{ stk have } \text{typeof}_h v = [\text{Boolean}] \text{ by}(\text{auto simp add: exec-move-def exec-meth-instr})$   
**ultimately show**  $?thesis \text{ using } \text{exec } stk \text{ xcp}$   
**by**( $\text{fastforce elim!:: exec-meth.cases intro: Red1CondT Red1CondF elim!: rtrancpl.rtranc1-into-rtranc1 simp add: eval-nat-numeral exec-move-def}$ )  
**qed**  
**next**  
**case** ( $\text{bisim1CondThen } e1 \ n \ e' \ xs \ stk \ loc \ pc \ xcp \ e \ e2$ )  
**note**  $IH = \text{bisim1CondThen.IH}(2)$   
**note**  $\text{bisim1} = \langle P, e1, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$   
**note**  $\text{bisim} = \langle \bigwedge xs. P, e, h \vdash (e, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$   
**note**  $\text{bisim2} = \langle \bigwedge xs. P, e2, h \vdash (e2, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$   
**note**  $\text{len} = \langle n + \text{max-vars } e' \leq \text{length } xs \rangle$   
**note**  $\text{exec} = \langle ?\text{exec} \text{ (if } (e) \ e1 \text{ else } e2) \text{ stk } loc \text{ (Suc (length (compE2 } e) + pc)) \text{ xcp } stk' \text{ loc' } pc' \text{ xcp'} \rangle$   
**note**  $\text{bsok} = \langle \text{bsok} \text{ (if } (e) \ e1 \text{ else } e2) \ n \rangle$   
**from**  $\text{bisim1 have } pc: pc \leq \text{length (compE2 } e1) \text{ by}(\text{rule bisim1-pc-length-compE2})$   
**show**  $?case$   
**proof**( $\text{cases } pc < \text{length (compE2 } e1)$ )  
**case**  $\text{True}$   
**let**  $?pre = \text{compE2 } e \ @ \ [\text{IfFalse } (2 + \text{int (length (compE2 } e1)))]$   
**let**  $?post = \text{Goto } (1 + \text{int (length (compE2 } e2))) \ # \ \text{compE2 } e2$   
**from**  $\text{exec have } \text{exec}': \text{exec-meth-d (compP2 } P) \text{ (?pre @ compE2 } e1 \ @ \ ?post)$   
 $(\text{compxE2 } e \ 0 \ 0 \ @ \ \text{shift (length ?pre) (compxE2 } e1 \ 0 \ 0 \ @ \ \text{shift (length (compE2 } e1)) (compxE2 } e2 \text{ (Suc } 0) \ 0))) \ t$   
 $h \text{ (stk, loc, length ?pre + pc, xcp) ta } h' \text{ (stk', loc', pc', xcp')}$   
**by**( $\text{simp add: shift-compxE2 ac-simps exec-move-def}$ )  
**hence**  $\text{exec-meth-d (compP2 } P) \text{ (compE2 } e1 \ @ \ ?post) \text{ (compxE2 } e1 \ 0 \ 0 \ @ \ \text{shift (length (compE2 } e1)) (compxE2 } e2 \text{ (Suc } 0) \ 0))) \ t$   
 $h \text{ (stk, loc, pc, xcp) ta } h' \text{ (stk', loc', pc' - length ?pre, xcp')}$   
**by**( $\text{rule exec-meth-drop-xt}$ )  $\text{auto}$   
**hence**  $\text{exec-move-d } P \ t \ e1 \ h \text{ (stk, loc, pc, xcp) ta } h' \text{ (stk', loc', pc' - length ?pre, xcp')}$   
**using**  $\text{True unfolding exec-move-def by}(\text{rule exec-meth-take-xt})$   
**from**  $IH[OF \text{ this len - } \langle P, h \vdash stk \text{ [:} \leq \text{ST} \rangle \langle \text{conf-xcp'} \text{ (compP2 } P) \ h \text{ xcp} \rangle] \text{ bsok obtain } e'' \ xs''$   
**where**  $\text{bisim}': P, e1, h' \vdash (e'', xs'') \leftrightarrow (stk', loc', pc' - \text{length ?pre, xcp'})$   
**and**  $\text{red: ?red } e' \ xs \ e'' \ xs'' \ e1 \text{ stk } pc \text{ (pc' - length ?pre) xcp xcp' by auto}$   
**from**  $\text{bisim}'$   
**have**  $P, \text{if } (e) \ e1 \text{ else } e2, h' \vdash (e'', xs'') \leftrightarrow (stk', loc', \text{Suc (length (compE2 } e) + (pc' - \text{length ?pre}))}, xcp')$   
**by**( $\text{rule bisim1-bisims1.bisim1CondThen}$ )  
**moreover from**  $\text{True have } \tau: \tau\text{move2 (compP2 } P) \ h \text{ stk (if } (e) \ e1 \text{ else } e2) \text{ (Suc (length (compE2 } e) + pc)) \text{ xcp} = \tau\text{move2 (compP2 } P) \ h \text{ stk } e1 \text{ pc xcp}$   
**by**( $\text{simp add: } \tau\text{move2-iff}$ )  
**moreover from**  $\text{exec' have } pc' \geq \text{length ?pre}$   
**by**( $\text{rule exec-meth-drop-xt-pc}$ )  $\text{auto}$   
**ultimately show**  $?thesis \text{ using red by}(\text{fastforce split: if-split-asm simp add: no-call2-def})$   
**next**  
**case**  $\text{False}$   
**with**  $pc \text{ have [simp]: } pc = \text{length (compE2 } e1) \text{ by simp}$   
**with**  $\text{bisim1 obtain } v \text{ where } stk: stk = [v] \text{ and } xcp: xcp = \text{None}$   
**by**( $\text{auto dest: bisim1-pc-length-compE2D}$ )  
**with**  $\text{bisim1 pc len bsok have red: } \tau\text{red1r } P \ t \ h \text{ (e', xs) (Val v, loc)}$   
**by**( $\text{auto intro: bisim1-Val-}\tau\text{red1r simp add: bsok-def}$ )  
**moreover have**  $\tau: \tau\text{move2 (compP2 } P) \ h \ [v] \text{ (if } (e) \ e1 \text{ else } e2) \text{ (Suc (length (compE2 } e) + \text{length (compE2 } e1))) \text{ None}$

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    by(simp add:  $\tau$ move2-iff)
  moreover
    have  $P, \text{if } (e) \ e1 \ \text{else } e2, h \vdash (\text{Val } v, \text{loc}) \leftrightarrow ([v], \text{loc}, \text{length } (\text{compE2 } (\text{if } (e) \ e1 \ \text{else } e2))), \text{None})$ 
      by(rule bisim1Val2) simp
    moreover have  $\text{nat } (2 + (\text{int } (\text{length } (\text{compE2 } e)) + (\text{int } (\text{length } (\text{compE2 } e1)) + \text{int } (\text{length } (\text{compE2 } e2)))) = \text{length } (\text{compE2 } (\text{if } (e) \ e1 \ \text{else } e2))$  by simp
    ultimately show ?thesis using exec xcp stk by(fastforce elim!: exec-meth.cases simp add: exec-move-def)
  qed
next
case (bisim1CondElse  $e2 \ n \ e' \ xs \ stk \ loc \ pc \ xcp \ e \ e1$ )
note  $IH = \text{bisim1CondElse.IH}(2)$ 
note  $\text{bisim2} = \langle P, e2, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
note  $\text{bisim} = \langle \bigwedge xs. P, e, h \vdash (e, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$ 
note  $\text{bisim1} = \langle \bigwedge xs. P, e1, h \vdash (e1, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$ 
note  $\text{len} = \langle n + \text{max-vars } e' \leq \text{length } xs \rangle$ 
note  $\text{exec} = \langle ?\text{exec } (\text{if } (e) \ e1 \ \text{else } e2) \ stk \ loc \ (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e) + \text{length } (\text{compE2 } e1) + pc))) \ xcp \ stk' \ loc' \ pc' \ xcp' \rangle$ 
note  $\text{bsok} = \langle \text{bsok } (\text{if } (e) \ e1 \ \text{else } e2) \ n \rangle$ 
let  $?pre = \text{compE2 } e \ @ \ \text{IfFalse } (2 + \text{int } (\text{length } (\text{compE2 } e1))) \ \# \ \text{compE2 } e1 \ @ \ [\text{Goto } (1 + \text{int } (\text{length } (\text{compE2 } e2)))]$ 
from exec have  $\text{exec}' : \text{exec-meth-d } (\text{compP2 } P) \ (?pre \ @ \ \text{compE2 } e2) \ ((\text{compxE2 } e \ 0 \ 0 \ @ \ \text{compxE2 } e1 \ (\text{Suc } (\text{length } (\text{compE2 } e))) \ 0) \ @ \ \text{shift } (\text{length } ?pre) \ (\text{compxE2 } e2 \ 0 \ 0)) \ t$ 
  h (stk, loc, length ?pre + pc, xcp) ta h' (stk', loc', pc', xcp')
  by(simp add: shift-compxE2 ac-simps exec-move-def)
hence  $?exec \ e2 \ stk \ loc \ pc \ xcp \ stk' \ loc' \ (pc' - \text{length } ?pre) \ xcp'$ 
  unfolding exec-move-def by(rule exec-meth-drop-xt) auto
from  $IH[OF \ \text{this } \text{len} - \langle P, h \vdash \text{stk } [: \leq] \ ST \rangle \langle \text{conf-xcp}' \ (\text{compP2 } P) \ h \ xcp \rangle \ \text{bsok}]$  obtain  $e'' \ xs''$ 
  where  $\text{bisim}' : P, e2, h' \vdash (e'', xs'') \leftrightarrow (stk', loc', pc' - \text{length } ?pre, xcp')$ 
  and  $\text{red} : ?\text{red } e' \ xs \ e'' \ xs'' \ e2 \ stk \ pc \ (pc' - \text{length } ?pre) \ xcp \ xcp' \ \text{by auto}$ 
from  $\text{bisim}'$ 
  have  $P, \text{if } (e) \ e1 \ \text{else } e2, h' \vdash (e'', xs'') \leftrightarrow (stk', loc', \text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e) + \text{length } (\text{compE2 } e1) + (pc' - \text{length } ?pre))), xcp')$ 
  by(rule bisim1-bisims1.bisim1CondElse)
  moreover have  $\tau : \tau\text{move2 } (\text{compP2 } P) \ h \ stk \ (\text{if } (e) \ e1 \ \text{else } e2) \ (\text{Suc } (\text{Suc } (\text{length } (\text{compE2 } e) + \text{length } (\text{compE2 } e1) + pc))) \ xcp = \tau\text{move2 } (\text{compP2 } P) \ h \ stk \ e2 \ pc \ xcp$ 
  by(simp add:  $\tau$ move2-iff)
  moreover from  $\text{exec}'$  have  $pc' \geq \text{length } ?pre$ 
  by(rule exec-meth-drop-xt-pc) auto
  moreover hence  $\text{Suc } (\text{Suc } (pc' - \text{Suc } (\text{Suc } 0))) = pc'$  by simp
  ultimately show ?case using red by(fastforce simp add: eval-nat-numeral no-call2-def split: if-split-asm)
next
case (bisim1CondThrow  $e \ n \ a \ xs \ stk \ loc \ pc \ e1 \ e2$ )
note  $\text{exec} = \langle ?\text{exec } (\text{if } (e) \ e1 \ \text{else } e2) \ stk \ loc \ pc \ [a] \ stk' \ loc' \ pc' \ xcp' \rangle$ 
note  $\text{bisim} = \langle P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$ 
from  $\text{bisim}$  have  $pc : pc < \text{length } (\text{compE2 } e)$  by(auto dest: bisim1-ThrowD)
from  $\text{bisim}$  have  $\text{match-ex-table } (\text{compP2 } P) \ (\text{cname-of } h \ a) \ (0 + pc) \ (\text{compxE2 } e \ 0 \ 0) = \text{None}$ 
  unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
with  $\text{exec } pc$  have  $\text{False}$ 
  by(auto elim!: exec-meth.cases simp add: match-ex-table-not-pcs-None exec-move-def)
thus ?case ..
next
case (bisim1While1  $c \ n \ e \ xs$ )
note  $IH = \text{bisim1While1.IH}(2)$ 

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note bisim =  $\langle \bigwedge xs. P, c, h \vdash (c, xs) \leftrightarrow ([], xs, 0, None) \rangle$ 
note bisim1 =  $\langle \bigwedge xs. P, e, h \vdash (e, xs) \leftrightarrow ([], xs, 0, None) \rangle$ 
from  $\langle n + \text{max-vars } (while\ (c)\ e) \leq \text{length } xs \rangle$ 
have len:  $n + \text{max-vars } c \leq \text{length } xs$  by simp
note exec =  $\langle ?exec\ (while\ (c)\ e) \ []\ xs\ 0\ None\ stk'\ loc'\ pc'\ xcp' \rangle$ 
note bsok =  $\langle bsok\ (while\ (c)\ e)\ n \rangle$ 
let ?post =  $IfFalse\ (int\ (\text{length}\ (compE2\ e)) + 3)\ \# \text{compE2}\ e\ @\ [Pop,\ Goto\ (-2 + (-\ int\ (\text{length}\ (compE2\ e)) - \text{int}\ (\text{length}\ (compE2\ c))))],\ Push\ Unit]$ 
from exec have ?exec c  $\ []\ xs\ 0\ None\ stk'\ loc'\ pc'\ xcp'$  by (simp add: exec-move-While1)
from IH[OF this len] bsok obtain e'' xs''
  where bisim':  $P, c, h' \vdash (e'', xs'') \leftrightarrow (stk', loc', pc', xcp')$ 
  and red:  $?red\ c\ xs\ e''\ xs''\ c\ []\ 0\ pc'\ None\ xcp'$  by fastforce
from bisim'
have  $P, while\ (c)\ e, h' \vdash (if\ (e'')\ (e;;while(c)\ e)\ else\ unit,\ xs'') \leftrightarrow (stk', loc', pc', xcp')$ 
  by (rule bisim1While3)
moreover have  $True, P, t \vdash 1\ \langle while\ (c)\ e,\ (h,\ xs) \rangle -\varepsilon \rightarrow \langle if\ (c)\ (e;;while\ (c)\ e)\ else\ unit,\ (h,\ xs) \rangle$ 
  by (rule Red1While)
hence  $\tau red1r\ P\ t\ h\ (while\ (c)\ e,\ xs)\ (if\ (c)\ (e;;while\ (c)\ e)\ else\ unit,\ xs)$ 
  by (auto intro: r-into-rtrancpl  $\tau move1WhileRed$ )
moreover have  $\tau move2\ (compP2\ P)\ h\ []\ (while\ (c)\ e)\ 0\ None = \tau move2\ (compP2\ P)\ h\ []\ c\ 0\ None$ 
by (simp add:  $\tau move2$ -iff)
ultimately show ?case using red
  by (fastforce elim!: rtrancpl-trans rtrancpl-trancpl-trancpl intro: Cond1Red elim!: Cond- $\tau red1r$ -xt Cond- $\tau red1t$ -xt simp add: no-call2-def)
next
case (bisim1While3 c n e' xs stk loc pc xcp e)
note IH = bisim1While3.IH(2)
note bisim =  $\langle P, c, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
note bisim1 =  $\langle \bigwedge xs. P, e, h \vdash (e, xs) \leftrightarrow ([], xs, 0, None) \rangle$ 
from  $\langle n + \text{max-vars } (if\ (e')\ (e;;while\ (c)\ e)\ else\ unit) \leq \text{length } xs \rangle$ 
have len:  $n + \text{max-vars } e' \leq \text{length } xs$  by simp
note bsok =  $\langle bsok\ (while\ (c)\ e)\ n \rangle$ 
note exec =  $\langle ?exec\ (while\ (c)\ e)\ stk\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$ 
from bisim have pc:  $pc \leq \text{length}\ (compE2\ c)$  by (rule bisim1-pc-length-compE2)
show ?case
proof (cases pc < length (compE2 c))
  case True
    let ?post =  $IfFalse\ (int\ (\text{length}\ (compE2\ e)) + 3)\ \# \text{compE2}\ e\ @\ [Pop,\ Goto\ (-2 + (-\ int\ (\text{length}\ (compE2\ e)) - \text{int}\ (\text{length}\ (compE2\ c))))],\ Push\ Unit]$ 
    from exec have exec-meth-d (compP2 P) (compE2 c @ ?post) (compxE2 c 0 0 @ shift (length (compE2 c)) (compxE2 e (Suc 0) 0)) t h (stk, loc, pc, xcp) ta h' (stk', loc', pc', xcp')
      by (simp add: shift-compxE2 exec-move-def)
    hence  $?exec\ c\ stk\ loc\ pc\ xcp\ stk'\ loc'\ pc'\ xcp'$ 
      using True unfolding exec-move-def by (rule exec-meth-take-xt)
    from IH[OF this len -  $\langle P, h \vdash stk\ [: \leq] ST \rangle \langle conf-xcp' (compP2 P) h xcp \rangle$ ] bsok obtain e'' xs''
      where bisim':  $P, c, h' \vdash (e'', xs'') \leftrightarrow (stk', loc', pc', xcp')$ 
      and red:  $?red\ e'\ xs\ e''\ xs''\ c\ stk\ pc\ pc'\ xcp\ xcp'$  by auto
    from bisim'
    have  $P, while\ (c)\ e, h' \vdash (if\ (e'')\ (e;;while(c)\ e)\ else\ unit,\ xs'') \leftrightarrow (stk', loc', pc', xcp')$ 
      by (rule bisim1-bisims1.bisim1While3)
    moreover have  $\tau move2\ (compP2\ P)\ h\ stk\ (while\ (c)\ e)\ pc\ xcp = \tau move2\ (compP2\ P)\ h\ stk\ c\ pc$ 
using True
      by (simp add:  $\tau move2$ -iff)
    ultimately show ?thesis using red

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  by(fastforce elim!: rtrancpl-trans intro: Cond1Red elim!: Cond- $\tau$ red1r-xt Cond- $\tau$ red1t-xt simp add:
no-call2-def)
next
  case False
  with pc have [simp]: pc = length (compE2 c) by simp
  with bisim obtain v where stk: stk = [v] and xcp: xcp = None
  by(auto dest: bisim1-pc-length-compE2D)
  with bisim pc len bsok have red:  $\tau$ red1r P t h (e', xs) (Val v, loc)
  by(auto intro: bisim1-Val- $\tau$ red1r simp add: bsok-def)
  hence  $\tau$ red1r P t h (if (e') (e;; while (c) e) else unit, xs) (if (Val v) (e;; while (c) e) else unit, loc)
  by(rule Cond- $\tau$ red1r-xt)
  moreover have  $\tau$ :  $\tau$ move2 (compP2 P) h [v] (while (c) e) (length (compE2 c)) None by(simp
add:  $\tau$ move2-iff)
  moreover have  $\tau$ move1 P h (if (Val v) (e;;while (c) e) else unit) by(rule  $\tau$ move1CondRed)
  moreover from bisim1[of loc]
  have P,while (c) e, h  $\vdash$  (e;;while(c) e, loc)  $\leftrightarrow$  ([], loc, Suc (length (compE2 c) + 0), None)
  by(rule bisim1While4)
  moreover
  have P,while (c) e, h  $\vdash$  (unit, loc)  $\leftrightarrow$  ([], loc, Suc (Suc (Suc (length (compE2 c) + length (compE2
e))))), None)
  by(rule bisim1While7)
  moreover from exec stk xcp have typeofh v = [Boolean]
  by(auto simp add: exec-meth-instr exec-move-def)
  moreover have nat (int (length (compE2 c)) + (3 + int (length (compE2 e)))) = Suc (Suc (Suc
(length (compE2 c) + length (compE2 e)))) by simp
  ultimately show ?thesis using exec stk xcp
  by(fastforce elim!: exec-meth.cases rtrancpl-trans intro: Red1CondT Red1CondF simp add:
eval-nat-numeral exec-move-def)
qed
next
  case (bisim1While4 e n e' xs stk loc pc xcp c)
  note IH = bisim1While4.IH(2)
  note bisim =  $\langle \bigwedge xs. P, c, h \vdash (c, xs) \leftrightarrow ([], xs, 0, None) \rangle$ 
  note bisim1 =  $\langle P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
  from  $\langle n + \text{max-vars } (e'; \text{while } (c) e) \leq \text{length } xs \rangle$ 
  have len:  $n + \text{max-vars } e' \leq \text{length } xs$  by simp
  note exec =  $\langle ?exec \text{ (while } (c) e) \text{ stk loc (Suc (length (compE2 c) + pc)) xcp stk' loc' pc' xcp' } \rangle$ 
  note bsok =  $\langle bsok \text{ (while } (c) e) \text{ n} \rangle$ 
  from bisim1 have pc:  $pc \leq \text{length } (compE2 e)$  by(rule bisim1-pc-length-compE2)
  show ?case
  proof(cases  $pc < \text{length } (compE2 e)$ )
  case True
  let ?pre = compE2 c @ [IfFalse (int (length (compE2 e)) + 3)]
  let ?post = [Pop, Goto (-2 + (- int (length (compE2 e)) - int (length (compE2 c))))], Push Unit]
  from exec have exec-meth-d (compP2 P) ((?pre @ compE2 e) @ ?post) (compE2 c 0 0 @ shift
(length ?pre) (compE2 e 0 0)) t h (stk, loc, length ?pre + pc, xcp) ta h' (stk', loc', pc', xcp')
  by(simp add: shift-compE2 exec-move-def)
  hence exec': exec-meth-d (compP2 P) (?pre @ compE2 e) (compE2 c 0 0 @ shift (length ?pre)
(compE2 e 0 0)) t h (stk, loc, length ?pre + pc, xcp) ta h' (stk', loc', pc', xcp')
  by(rule exec-meth-take)(simp add: True)
  hence ?exec e stk loc pc xcp stk' loc' (pc' - length ?pre) xcp'
  unfolding exec-move-def by(rule exec-meth-drop-xt) auto
  from IH[OF this len -  $\langle P, h \vdash stk [\leq] ST \rangle$   $\langle \text{conf-xcp' (compP2 P) h xcp} \rangle$ ] bsok obtain e'' xs''
  where bisim':  $P, e, h' \vdash (e'', xs'') \leftrightarrow (stk', loc', pc' - \text{length } ?pre, xcp')$ 

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    and red: ?red e' xs e'' xs'' e stk pc (pc' - length ?pre) xcp xcp' by auto
  from red have ?red (e'; while (c) e) xs (e''; while (c) e) xs'' e stk pc (pc' - length ?pre) xcp xcp'
    by(fastforce intro: Seq1Red elim!: Seq-τred1r-xt Seq-τred1t-xt split: if-split-asm)
  moreover from bisim'
  have P, while (c) e, h' ⊢ (e''; while(c) e, xs'') ↔ (stk', loc', Suc (length (compE2 c) + (pc' - length
    ?pre)), xcp')
    by(rule bisim1-bisims1.bisim1While4)
  moreover have τmove2 (compP2 P) h stk (while (c) e) (Suc (length (compE2 c) + pc)) xcp =
    τmove2 (compP2 P) h stk e pc xcp
    using True by(simp add: τmove2-iff)
  moreover from exec' have pc' ≥ length ?pre
    by(rule exec-meth-drop-xt-pc) auto
  moreover have no-call2 e pc ⇒ no-call2 (while (c) e) (Suc (length (compE2 c) + pc))
    by(simp add: no-call2-def)
  ultimately show ?thesis
    apply(auto split: if-split-asm)
    apply(fastforce+)[6]
    apply(rule exI conjI|assumption|rule rtranclp.rtrancl-refl|simp)+
    done
next
case False
with pc have [simp]: pc = length (compE2 e) by simp
with bisim1 obtain v where stk: stk = [v] and xcp: xcp = None
  by(auto dest: bisim1-pc-length-compE2D)
with bisim1 pc len bsok have red: τred1r P t h (e', xs) (Val v, loc)
  by(auto intro: bisim1-Val-τred1r simp add: bsok-def)
hence τred1r P t h (e'; while (c) e, xs) (Val v; while (c) e, loc) by(rule Seq-τred1r-xt)
  moreover have τ: τmove2 (compP2 P) h [v] (while (c) e) (Suc (length (compE2 c) + length
    (compE2 e))) None
    by(simp add: τmove2-iff)
  moreover have τmove1 P h (Val v; while (c) e) by(rule τmove1SeqRed)
  moreover
  have P, while (c) e, h ⊢ (while(c) e, loc) ↔ ([], loc, Suc (Suc (length (compE2 c) + length (compE2
    e))), None)
    by(rule bisim1While6)
  ultimately show ?thesis using exec stk xcp
    by(fastforce elim!: exec-meth.cases rtranclp-trans intro: Red1Seq simp add: eval-nat-numeral
    exec-move-def)
qed
next
case (bisim1While6 c n e xs)
note exec = ⟨?exec (while (c) e) [] xs (Suc (Suc (length (compE2 c) + length (compE2 e)))) None
  stk' loc' pc' xcp'⟩
  moreover have τmove2 (compP2 P) h [] (while (c) e) (Suc (Suc (length (compE2 c) + length
    (compE2 e)))) None
    by(simp add: τmove2-iff)
  moreover
  have P, while (c) e, h' ⊢ (if (c) (e; while (c) e) else unit, xs) ↔ ([], xs, 0, None)
    by(rule bisim1While3[OF bisim1-refl])
  moreover have τred1t P t h (while (c) e, xs) (if (c) (e; while (c) e) else unit, xs)
    by(rule tranclp.r-into-trancl)(auto intro: Red1While)
  ultimately show ?case
    by(fastforce elim!: exec-meth.cases simp add: exec-move-def)
next

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case (bisim1While7 c n e xs)
  note ⟨?exec (while (c) e) [] xs (Suc (Suc (Suc (length (compE2 c) + length (compE2 e)))))) None
  stk' loc' pc' xcp'⟩
  moreover have  $\tau\text{move2}$  (compP2 P) h [] (while (c) e) (Suc (Suc (Suc (length (compE2 c) + length
  (compE2 e)))))) None
  by(simp add:  $\tau\text{move2-iff}$ )
  moreover have  $P, \text{while } (c) \ e, h' \vdash (\text{unit}, xs) \leftrightarrow ([\text{Unit}], xs, \text{length } (\text{compE2 } (\text{while } (c) \ e)), \text{None})$ 
  by(rule bisim1Val2) simp
  ultimately show ?case by(fastforce elim!: exec-meth.cases simp add: exec-move-def)
next
case (bisim1WhileThrow1 c n a xs stk loc pc e)
  note exec = ⟨?exec (while (c) e) stk loc pc [a] stk' loc' pc' xcp'⟩
  note bisim1 = ⟨ $P, c, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a])$ ⟩
  from bisim1 have pc:  $pc < \text{length } (\text{compE2 } c)$  by(auto dest: bisim1-ThrowD)
  from bisim1 have match-ex-table (compP2 P) (cname-of h a) (0 + pc) (compE2 c 0 0) = None
  unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
  with exec pc have False
  by(auto elim!: exec-meth.cases simp add: match-ex-table-not-pcs-None exec-move-def)
  thus ?case ..
next
case (bisim1WhileThrow2 e n a xs stk loc pc c)
  note exec = ⟨?exec (while (c) e) stk loc (Suc (length (compE2 c) + pc)) [a] stk' loc' pc' xcp'⟩
  note bisim = ⟨ $P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a])$ ⟩
  from bisim have pc:  $pc < \text{length } (\text{compE2 } e)$  by(auto dest: bisim1-ThrowD)
  from bisim have match-ex-table (compP2 P) (cname-of h a) (0 + pc) (compE2 e 0 0) = None
  unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)
  with exec pc have False
  apply(auto elim!: exec-meth.cases simp add: match-ex-table-not-pcs-None exec-move-def)
  apply(auto dest!: match-ex-table-shift-pcD simp only: compE2-size-convs)
  apply simp
  done
  thus ?case ..
next
case (bisim1Throw1 e n e' xs stk loc pc xcp)
  note IH = bisim1Throw1.IH(2)
  note exec = ⟨?exec (throw e) stk loc pc xcp stk' loc' pc' xcp'⟩
  note bisim = ⟨ $P, e, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp)$ ⟩
  note len =  $\langle n + \text{max-vars } (\text{throw } e') \leq \text{length } xs \rangle$ 
  note bsok = ⟨bsok (throw e) n⟩
  from bisim have pc:  $pc \leq \text{length } (\text{compE2 } e)$  by(rule bisim1-pc-length-compE2)
  show ?case
  proof(cases pc < length (compE2 e))
    case True
    with exec have exec': ?exec e stk loc pc xcp stk' loc' pc' xcp' by(simp add: exec-move-Throw)
    from True have  $\tau\text{move2}$  (compP2 P) h stk (throw e) pc xcp =  $\tau\text{move2}$  (compP2 P) h stk e pc xcp
  by(simp add:  $\tau\text{move2-iff}$ )
  with IH[OF exec' - -  $\langle P, h \vdash stk [: \leq] ST \rangle \langle \text{conf-xcp}' (\text{compP2 } P) h xcp \rangle$  len bsok] show ?thesis
  by(fastforce intro: bisim1-bisims1.bisim1Throw1 Throw1Red elim!: Throw- $\tau\text{red1r-xt}$  Throw- $\tau\text{red1t-xt}$ 
  simp add: no-call2-def)
  next
  case False
  with pc have [simp]:  $pc = \text{length } (\text{compE2 } e)$  by simp
  with bisim obtain v where stk:  $stk = [v]$  and xcp:  $xcp = \text{None}$ 
  by(auto dest: bisim1-pc-length-compE2D)

```

**with** *bisim pc len bsok* **have** *red*:  $\tau\text{red1r } P \text{ t h } (e', xs) \text{ (Val } v, loc)$   
**by**(*auto intro: bisim1-Val- $\tau\text{red1r}$  simp add: bsok-def*)  
**hence**  $\tau\text{red1r } P \text{ t h } (throw \ e', xs) \text{ (throw (Val } v), loc)$  **by**(*rule Throw- $\tau\text{red1r-xt}$* )  
**moreover** **have**  $\tau$ :  $\tau\text{move2 (compP2 } P) \text{ h [v] (throw } e) \text{ pc None}$  **by**(*simp add:  $\tau\text{move2-iff}$* )  
**moreover**  
**have**  $\bigwedge a. P, throw \ e, h \vdash (Throw \ a, loc) \leftrightarrow ([Addr \ a], loc, length \ (compE2 \ e), [a])$   
**by**(*rule bisim1Throw2*)  
**moreover**  
**have**  $P, throw \ e, h \vdash (THROW \ NullPointer, loc) \leftrightarrow ([Null], loc, length \ (compE2 \ e), [addr\text{-of-sys-xcpt} \ NullPointer])$   
**by**(*rule bisim1ThrowNull*)  
**moreover from** *exec stk xcp*  $\langle P, h \vdash \text{stk } [: \leq] \text{ ST} \rangle$  **obtain** *T'* **where** *T'*:  $\text{typeof}_h \ v = [T'] \ P \vdash \ T' \leq \text{Class Throwable}$   
**by**(*auto simp add: exec-move-def exec-meth-instr list-all2-Cons1 conf-def compP2-def*)  
**moreover with** *T'* **have**  $v \neq \text{Null} \implies \exists C. T' = \text{Class } C$  **by**(*cases T' (auto dest: Array-widen)*)  
**moreover** **have**  $\tau\text{red1r } P \text{ t h } (throw \ \text{null}, loc) \text{ (THROW NullPointer, loc)}$   
**by**(*auto intro: r-into-rtrancpl Red1ThrowNull  $\tau\text{move1ThrowNull}$* )  
**ultimately show** *?thesis* **using** *exec stk xcp T' unfolding exec-move-def*  
**by**(*cases v (fastforce elim!: exec-meth.cases intro: rtrancpl-trans) +*)  
**qed**  
**next**  
**case** (*bisim1Throw2 e n a xs*)  
**note** *exec* =  $\langle ?\text{exec (throw } e) [Addr \ a] \ xs \ (length \ (compE2 \ e)) \ [a] \ \text{stk}' \ \text{loc}' \ \text{pc}' \ \text{xcp}' \rangle$   
**hence** *False* **by**(*auto elim!: exec-meth.cases dest: match-ex-table-pc-length-compE2 simp add: exec-move-def*)  
**thus** *?case ..*  
**next**  
**case** (*bisim1ThrowNull e n xs*)  
**note** *exec* =  $\langle ?\text{exec (throw } e) [Null] \ xs \ (length \ (compE2 \ e)) \ [addr\text{-of-sys-xcpt} \ NullPointer] \ \text{stk}' \ \text{loc}' \ \text{pc}' \ \text{xcp}' \rangle$   
**hence** *False* **by**(*auto elim!: exec-meth.cases dest: match-ex-table-pc-length-compE2 simp add: exec-move-def*)  
**thus** *?case ..*  
**next**  
**case** (*bisim1ThrowThrow e n a xs stk loc pc*)  
**note** *exec* =  $\langle ?\text{exec (throw } e) \text{ stk loc pc } [a] \ \text{stk}' \ \text{loc}' \ \text{pc}' \ \text{xcp}' \rangle$   
**note** *bisim1* =  $\langle P, e, h \vdash (Throw \ a, xs) \leftrightarrow (\text{stk}, loc, pc, [a]) \rangle$   
**from** *bisim1* **have** *pc*:  $pc < length \ (compE2 \ e)$  **by**(*auto dest: bisim1-ThrowD*)  
**from** *bisim1* **have** *match-ex-table (compP2 P) (cname-of h a) (0 + pc) (compE2 e 0 0) = None*  
**unfolding** *compP2-def* **by**(*rule bisim1-xcp-Some-not-caught*)  
**with** *exec pc* **have** *False* **by**(*auto elim!: exec-meth.cases simp add: exec-move-def*)  
**thus** *?case ..*  
**next**  
**case** (*bisim1Try e n e' xs stk loc pc xcp e2 C' V*)  
**note** *IH* = *bisim1Try.IH(2)*  
**note** *bisim* =  $\langle P, e, h \vdash (e', xs) \leftrightarrow (\text{stk}, loc, pc, xcp) \rangle$   
**note** *bisim1* =  $\langle \bigwedge xs. P, e2, h \vdash (e2, xs) \leftrightarrow ([], xs, 0, \text{None}) \rangle$   
**note** *exec* =  $\langle ?\text{exec (try } e \text{ catch } (C' \ V) \ e2) \text{ stk loc pc xcp stk}' \ \text{loc}' \ \text{pc}' \ \text{xcp}' \rangle$   
**note** *bsok* =  $\langle \text{bsok (try } e \text{ catch } (C' \ V) \ e2) \ n \rangle$   
**with**  $\langle n + \text{max-vars (try } e' \text{ catch } (C' \ V) \ e2) \leq length \ xs \rangle$   
**have** *len*:  $n + \text{max-vars } e' \leq length \ xs$  **and** *V*:  $V < length \ xs$  **by** *simp-all*  
**from** *bisim* **have** *pc*:  $pc \leq length \ (compE2 \ e)$  **by**(*rule bisim1-pc-length-compE2*)  
**show** *?case*  
**proof**(*cases pc < length (compE2 e)*)  
**case** *True*  
**note** *pc* = *True*

```

show ?thesis
proof(cases xcp = None  $\vee$  ( $\exists a'. xcp = \lfloor a' \rfloor \wedge \text{match-ex-table } (\text{compP2 } P) (\text{cname-of } h \ a') \text{ pc}$ 
  ( $\text{compxE2 } e \ 0 \ 0) \neq \text{None}$ ))
  case False
  then obtain  $a'$  where  $\text{Some: } xcp = \lfloor a' \rfloor$ 
    and  $\text{True: match-ex-table } (\text{compP2 } P) (\text{cname-of } h \ a') \text{ pc } (\text{compxE2 } e \ 0 \ 0) = \text{None}$  by(auto
  simp del: not-None-eq)
    from  $\text{Some bisim } \langle \text{conf-xcp}' (\text{compP2 } P) \ h \ xcp \rangle$  have  $\exists C. \text{typeof}_h (\text{Addr } a') = \lfloor \text{Class } C \rfloor \wedge P$ 
 $\vdash C \preceq^* \text{Throwable}$ 
    by(auto simp add: compP2-def)
    then obtain  $C''$  where  $\text{ha': typeof-addr } h \ a' = \lfloor \text{Class-type } C'' \rfloor$ 
    and  $\text{subclsThrow: } P \vdash C'' \preceq^* \text{Throwable}$  by(auto)
    with  $\text{exec True Some pc}$  have  $\text{subcls: } P \vdash C'' \preceq^* C'$ 
    apply(auto elim!: exec-meth.cases simp add: match-ex-table-append compP2-def matches-ex-entry-def
  exec-move-def cname-of-def split: if-split-asm)
    apply(simp only: compxE2-size-convs, simp)
    done
  moreover from  $\text{ha' subclsThrow bsok}$  have  $\text{red: } \tau \text{red1r } P \ t \ h \ (e', xs) (\text{Throw } a', \text{loc})$ 
    and  $\text{bisim': } P, e, h \vdash (\text{Throw } a', \text{loc}) \leftrightarrow (\text{stk}, \text{loc}, \text{pc}, \lfloor a' \rfloor)$  using  $\text{bisim True len}$ 
    unfolding  $\text{Some compP2-def}$  by(auto dest!: bisim1-xcp- $\tau$ Red simp add: bsok-def)
  from  $\text{red}$  have  $\text{lenloc: length loc} = \text{length xs}$  by(rule  $\tau \text{red1r}$ -preserves-len)
  from  $\text{red}$  have  $\tau \text{red1r } P \ t \ h \ (\text{try } e' \text{ catch}(C' \ V) \ e2, xs) (\text{try } (\text{Throw } a') \text{ catch}(C' \ V) \ e2, \text{loc})$ 
    by(rule Try- $\tau \text{red1r}$ -xt)
  hence  $\tau \text{red1r } P \ t \ h \ (\text{try } e' \text{ catch}(C' \ V) \ e2, xs) (\{V:\text{Class } C'=\text{None}; e2\}, \text{loc}[V := \text{Addr } a'])$ 
    using  $\text{ha' subcls } V$  unfolding  $\text{lenloc[symmetric]}$ 
    by(auto intro: rtranclp.rtrancl-into-rtrancl Red1TryCatch  $\tau \text{move1TryThrow}$ )
  moreover from  $\text{pc}$  have  $\tau \text{move2 } (\text{compP2 } P) \ h \ \text{stk} \ (\text{try } e \text{ catch}(C' \ V) \ e2) \ \text{pc } \lfloor a' \rfloor$  by(simp
  add:  $\tau \text{move2-iff}$ )
  moreover from  $\text{bisim' ha' subcls}$ 
    have  $P, \text{try } e \text{ catch}(C' \ V) \ e2, h \vdash (\{V:\text{Class } C'=\text{None}; e2\}, \text{loc}[V := \text{Addr } a']) \leftrightarrow ([\text{Addr } a'],$ 
 $\text{loc}, \text{Suc } (\text{length } (\text{compE2 } e)), \text{None})$ 
    by(rule bisim1TryCatch1)
  ultimately show ?thesis using  $\text{exec True pc Some ha' subclsThrow}$ 
    apply(auto elim!: exec-meth.cases simp add: ac-simps eval-nat-numeral match-ex-table-append
  matches-ex-entry-def compP2-def exec-move-def cname-of-def)
    apply fastforce
    apply(simp-all only: compxE2-size-convs, auto dest: match-ex-table-shift-pcD)
    done
next
  case True
  let  $?post = \text{Goto } (\text{int } (\text{length } (\text{compE2 } e2)) + 2) \ \# \ \text{Store } V \ \# \ \text{compE2 } e2$ 
  from  $\text{exec True}$  have  $\text{exec-meth-d } (\text{compP2 } P) (\text{compE2 } e \ @ \ ?post) (\text{compxE2 } e \ 0 \ 0 \ @ \ \text{shift}$ 
 $(\text{length } (\text{compE2 } e)) (\text{compxE2 } e2 \ (\text{Suc } (\text{Suc } 0)) \ 0)) \ t \ h \ (\text{stk}, \text{loc}, \text{pc}, xcp) \text{ ta } h' (\text{stk}', \text{loc}', \text{pc}', xcp')$ 
    by(auto elim!: exec-meth.cases intro: exec-meth.intros simp add: match-ex-table-append shift-compxE2
  exec-move-def)
  hence  $?exec \ e \ \text{stk} \ \text{loc} \ \text{pc} \ xcp \ \text{stk}' \ \text{loc}' \ \text{pc}' \ xcp'$ 
    using  $\text{pc}$  unfolding  $\text{exec-move-def}$  by(rule exec-meth-take-xt)
  from  $\text{IH}[OF \text{ this len} - \langle P, h \vdash \text{stk } [: \leq] \ ST \rangle \langle \text{conf-xcp}' (\text{compP2 } P) \ h \ xcp \rangle]$   $\text{bsok}$  obtain  $e'' \ xs''$ 
    where  $\text{bisim': } P, e, h' \vdash (e'', xs'') \leftrightarrow (\text{stk}', \text{loc}', \text{pc}', xcp')$ 
    and  $\text{red': } ?red \ e' \ xs \ e'' \ xs'' \ e \ \text{stk} \ \text{pc} \ \text{pc}' \ xcp \ xcp'$  by auto
  from  $\text{bisim'}$ 
    have  $P, \text{try } e \text{ catch}(C' \ V) \ e2, h' \vdash (\text{try } e'' \text{ catch}(C' \ V) \ e2, xs'') \leftrightarrow (\text{stk}', \text{loc}', \text{pc}', xcp')$ 
    by(rule bisim1-bisims1.bisim1Try)
  moreover from  $\text{pc}$  have  $\tau \text{move2 } (\text{compP2 } P) \ h \ \text{stk} \ (\text{try } e \text{ catch}(C' \ V) \ e2) \ \text{pc} \ xcp = \tau \text{move2}$ 

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(compP2 P) h stk e pc xcp
  by(simp add:  $\tau$ move2-iff)
  ultimately show ?thesis using red' by(fastforce intro: Try1Red elim!: Try- $\tau$ red1r-xt Try- $\tau$ red1t-xt
simp add: no-call2-def)
qed
next
case False
with pc have [simp]: pc = length (compE2 e) by simp
with bisim obtain v where stk: stk = [v] and xcp: xcp = None
  by(auto dest: bisim1-pc-length-compE2D)
with bisim pc len bsok have red:  $\tau$ red1r P t h (e', xs) (Val v, loc)
  by(auto intro: bisim1-Val- $\tau$ red1r simp add: bsok-def)
  hence  $\tau$ red1r P t h (try e' catch(C' V) e2, xs) (try (Val v) catch(C' V) e2, loc) by(rule
Try- $\tau$ red1r-xt)
  hence  $\tau$ red1r P t h (try e' catch(C' V) e2, xs) (Val v, loc)
    by(auto intro: rtranclp.rtrancl-into-rtrancl Red1Try  $\tau$ move1TryRed)
  moreover have  $\tau$ :  $\tau$ move2 (compP2 P) h [v] (try e catch(C' V) e2) pc None by(simp add:
 $\tau$ move2-iff)
  moreover
    have P, try e catch(C' V) e2, h  $\vdash$  (Val v, loc)  $\leftrightarrow$  ([v], loc, length (compE2 (try e catch(C' V) e2)),
None)
      by(rule bisim1Val2) simp
    moreover have nat (int (length (compE2 e)) + (int (length (compE2 e2)) + 2)) = length (compE2
(try e catch(C' V) e2)) by simp
    ultimately show ?thesis using exec stk xcp
      by(fastforce elim!: exec-meth.cases simp add: exec-move-def)
qed
next
case (bisim1TryCatch1 e n a xs stk loc pc C'' C' e2 V)
note exec =  $\langle ?exec (try e catch(C' V) e2) [Addr a] loc (Suc (length (compE2 e))) None stk' loc' pc' xcp \rangle$ 
note bisim2 =  $\langle P, e2, h \vdash (e2, loc[V := Addr a]) \leftrightarrow ([], loc[V := Addr a], 0, None) \rangle$ 
note bisim1 =  $\langle P, e, h \vdash (Throw a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$ 
hence [simp]: xs = loc by(auto dest: bisim1-ThrowD)
from bisim2
have P, try e catch(C' V) e2, h  $\vdash$  ( $\{V:Class\ C'=None; e2\}$ , loc[V := Addr a])  $\leftrightarrow$  ([], loc[V :=
Addr a], Suc (Suc (length (compE2 e) + 0)), None)
  by(rule bisim1TryCatch2)
moreover have  $\tau$ move2 (compP2 P) h [Addr a] (try e catch(C' V) e2) (Suc (length (compE2 e)))
None by(simp add:  $\tau$ move2-iff)
ultimately show ?case using exec by(fastforce elim!: exec-meth.cases simp add: exec-move-def)
next
case (bisim1TryCatch2 e2 n e' xs stk loc pc xcp e C' V)
note IH = bisim1TryCatch2.IH(2)
note bisim2 =  $\langle P, e2, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, xcp) \rangle$ 
note bisim =  $\langle \bigwedge xs. P, e, h \vdash (e, xs) \leftrightarrow ([], xs, 0, None) \rangle$ 
note exec =  $\langle ?exec (try e catch(C' V) e2) stk loc (Suc (Suc (length (compE2 e) + pc))) xcp stk' loc' pc' xcp \rangle$ 
note bsok =  $\langle bsok (try e catch(C' V) e2) n \rangle$ 
with  $\langle n + max\text{-}vars \{V:Class\ C'=None; e'\} \leq length\ xs \rangle$ 
have len: Suc n + max-vars e'  $\leq$  length xs and V: V < length xs by simp-all
let ?pre = compE2 e @ [Goto (int (length (compE2 e2)) + 2), Store V]
from exec have exec-meth-d (compP2 P) (?pre @ compE2 e2)
  (compE2 e 0 0 @ shift (length ?pre) (compE2 e2 0 0) @ [(0, length (compE2 e), [C'], Suc (length

```

$(\text{compE2 } e), 0)) t$   
 $h (stk, loc, length \text{ ?pre} + pc, xcp) ta h' (stk', loc', pc', xcp')$   
 $\text{by}(\text{simp add: shift-compxE2 exec-move-def})$   
 $\text{hence } exec': \text{exec-meth-d } (\text{compP2 } P) (\text{?pre} @ \text{compE2 } e2) (\text{compxE2 } e \ 0 \ 0 @ \text{shift } (length \text{ ?pre})$   
 $(\text{compxE2 } e2 \ 0 \ 0)) t$   
 $h (stk, loc, length \text{ ?pre} + pc, xcp) ta h' (stk', loc', pc', xcp')$   
 $\text{by}(\text{auto elim!: exec-meth.cases intro: exec-meth.intros simp add: match-ex-table-append matches-ex-entry-def})$   
 $\text{hence } ?exec \ e2 \ stk \ loc \ pc \ xcp \ stk' \ loc' \ (pc' - length \text{ ?pre}) \ xcp'$   
 $\text{unfolding exec-move-def by(rule exec-meth-drop-xt) auto}$   
 $\text{from } IH[OF \text{ this len} - \langle P, h \vdash stk \text{ [:}\leq] ST \rangle \langle \text{conf-xcp}' (\text{compP2 } P) h \ xcp \rangle] \text{ bsok obtain } e'' \ xs''$   
 $\text{where } bisim': P, e2, h' \vdash (e'', xs'') \leftrightarrow (stk', loc', pc' - length \text{ ?pre}, xcp')$   
 $\text{and red: ?red } e' \ xs \ e'' \ xs'' \ e2 \ stk \ pc \ (pc' - length \text{ ?pre}) \ xcp \ xcp' \text{ by auto}$   
 $\text{from red have } length \ xs'' = length \ xs$   
 $\text{by(auto dest!: } \tau red1r\text{-preserves-len } \tau red1t\text{-preserves-len red1-preserves-len split: if-split-asm)}$   
 $\text{with red V have ?red } \{V:Class \ C'=None; e'\} \ xs \ \{V:Class \ C'=None; e''\} \ xs'' \ e2 \ stk \ pc \ (pc' -$   
 $length \text{ ?pre}) \ xcp \ xcp'$   
 $\text{by(fastforce elim!: Block-None-}\tau red1r\text{-xt Block-None-}\tau red1t\text{-xt intro: Block1Red split: if-split-asm)}$   
 $\text{moreover}$   
 $\text{from } bisim'$   
 $\text{have } P, \text{try } e \text{ catch}(C' \ V) \ e2, h' \vdash (\{V:Class \ C'=None; e''\}, xs'') \leftrightarrow (stk', loc', Suc (Suc (length$   
 $(\text{compE2 } e) + (pc' - length \text{ ?pre}))), xcp')$   
 $\text{by(rule bisim1-bisims1.bisim1TryCatch2)}$   
 $\text{moreover have } \tau move2 (\text{compP2 } P) h \ stk \ (\text{try } e \text{ catch}(C' \ V) \ e2) (Suc (Suc (length (\text{compE2 } e)$   
 $+ pc))) \ xcp = \tau move2 (\text{compP2 } P) h \ stk \ e2 \ pc \ xcp$   
 $\text{by(simp add: } \tau move2\text{-iff)}$   
 $\text{moreover from } exec' \text{ have } pc' \geq length \text{ ?pre}$   
 $\text{by(rule exec-meth-drop-xt-pc) auto}$   
 $\text{moreover hence } Suc (Suc (pc' - Suc (Suc 0))) = pc' \text{ by simp}$   
 $\text{moreover have no-call2 } e2 \ pc \implies \text{no-call2 } (\text{try } e \text{ catch}(C' \ V) \ e2) (Suc (Suc (length (\text{compE2 } e)$   
 $+ pc)))$   
 $\text{by(simp add: no-call2-def)}$   
 $\text{ultimately show ?case using red V by(fastforce simp add: eval-nat-numeral split: if-split-asm)}$   
 $\text{next}$   
 $\text{case } (bisim1TryFail \ e \ n \ a \ xs \ stk \ loc \ pc \ C'' \ C' \ e2 \ V)$   
 $\text{note } bisim = \langle P, e, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$   
 $\text{from } bisim \text{ have } pc: pc < length (\text{compE2 } e) \text{ by(auto dest: bisim1-ThrowD)}$   
 $\text{from } bisim \text{ have match-ex-table } (\text{compP2 } P) (\text{cname-of } h \ a) \ (0 + pc) (\text{compxE2 } e \ 0 \ 0) = None$   
 $\text{unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)}$   
 $\text{with } \langle ?exec (\text{try } e \text{ catch}(C' \ V) \ e2) \ stk \ loc \ pc \ [a] \ stk' \ loc' \ pc' \ xcp' \rangle \text{ pc } \langle \text{typeof-addr } h \ a = [Class\text{-type}$   
 $C''] \rangle \langle \neg P \vdash C'' \preceq^* C' \rangle$   
 $\text{have False by(auto elim!: exec-meth.cases simp add: matches-ex-entry-def compP2-def match-ex-table-append-not-pcs}$   
 $\text{exec-move-def cname-of-def)}$   
 $\text{thus ?case ..}$   
 $\text{next}$   
 $\text{case } (bisim1TryCatchThrow \ e2 \ n \ a \ xs \ stk \ loc \ pc \ e \ C' \ V)$   
 $\text{note } bisim = \langle P, e2, h \vdash (\text{Throw } a, xs) \leftrightarrow (stk, loc, pc, [a]) \rangle$   
 $\text{from } bisim \text{ have } pc: pc < length (\text{compE2 } e2) \text{ by(auto dest: bisim1-ThrowD)}$   
 $\text{from } bisim \text{ have match-ex-table } (\text{compP2 } P) (\text{cname-of } h \ a) \ (0 + pc) (\text{compxE2 } e2 \ 0 \ 0) = None$   
 $\text{unfolding compP2-def by(rule bisim1-xcp-Some-not-caught)}$   
 $\text{with } \langle ?exec (\text{try } e \text{ catch}(C' \ V) \ e2) \ stk \ loc \ (Suc (Suc (length (\text{compE2 } e) + pc))) \ [a] \ stk' \ loc' \ pc'$   
 $xcp' \rangle \text{ pc}$   
 $\text{have False apply(auto elim!: exec-meth.cases simp add: compxE2-size-conv match-ex-table-append-not-pcs}$   
 $\text{exec-move-def)}$   
 $\text{apply(auto dest!: match-ex-table-shift-pcD simp add: match-ex-table-append matches-ex-entry-def)}$

```

compP2-def)
  done
  thus ?case ..
next
  case bisims1Nil
  hence False by(auto elim!: exec-meth.cases simp add: exec-moves-def)
  thus ?case ..
next
  case (bisims1List1 e n e' xs stk loc pc xcp es)
  note IH1 = bisims1List1.IH(2)
  note IH2 = bisims1List1.IH(4)
  note exec = ⟨?execs (e # es) stk loc pc xcp stk' loc' pc' xcp'⟩
  note bisim1 = ⟨P,e,h ⊢ (e', xs) ↔ (stk, loc, pc, xcp)⟩
  note bisim2 = ⟨P,es,h ⊢ (es, loc) [↔] ([], loc, 0, None)⟩
  note len = ⟨n + max-varss (e' # es) ≤ length xs⟩
  note bsok = ⟨bsoks (e # es) n⟩
  from bisim1 have pc: pc ≤ length (compE2 e) by(rule bisim1-pc-length-compE2)
  from bisim1 have lenxs: length xs = length loc by(rule bisim1-length-xs)
  show ?case
  proof(cases pc < length (compE2 e))
    case True
    with exec have exec': ?exec e stk loc pc xcp stk' loc' pc' xcp'
    by(auto simp add: compxEs2-size-convs exec-moves-def exec-move-def intro: exec-meth-take-xt)
    from True have τmoves2 (compP2 P) h stk (e # es) pc xcp = τmove2 (compP2 P) h stk e pc xcp
    by(simp add: τmove2-iff τmoves2-iff)
    moreover from True have no-calls2 (e # es) pc = no-call2 e pc
    by(simp add: no-call2-def no-calls2-def)
    ultimately show ?thesis
    using IH1[OF exec' - - ⟨P,h ⊢ stk [:≤] ST⟩ ⟨conf-xcp' (compP2 P) h xcp⟩] bsok len
    by(fastforce intro: bisim1-bisims1.bisims1List1 elim!: τred1r-inj-τreds1r τred1t-inj-τreds1t List1Red1)
  next
    case False
    with pc have pc [simp]: pc = length (compE2 e) by simp
    with bisim1 obtain v where stk: stk = [v] and xcp: xcp = None
    and v: is-val e' ⇒ e' = Val v ∧ xs = loc and call: call1 e' = None
    by(auto dest: bisim1-pc-length-compE2D)
    with bisim1 pc len bsok have red: τred1r P t h (e', xs) (Val v, loc)
    by(auto intro: bisim1-Val-τred1r simp add: bsok-def)
    hence τreds1r P t h (e' # es, xs) (Val v # es, loc) by(rule τred1r-inj-τreds1r)
    moreover from exec stk xcp
    have exec': exec-meth-d (compP2 P) (compE2 e @ compEs2 es) (compxE2 e 0 0 @ shift (length
    (compE2 e)) (stack-xlift (length [v]) (compxEs2 es 0 0))) t h ([] @ [v], loc, length (compE2 e) + 0,
    None) ta h' (stk', loc', pc', xcp')
    by(simp add: shift-compxEs2 stack-xlift-compxEs2 exec-moves-def)
    hence exec-meth-d (compP2 P) (compEs2 es) (stack-xlift (length [v]) (compxEs2 es 0 0)) t h ([] @
    [v], loc, 0, None) ta h' (stk', loc', pc' - length (compE2 e), xcp')
    by(rule exec-meth-drop-xt) auto
    with bisim2 obtain stk'' where stk': stk' = stk'' @ [v]
    and exec'': exec-moves-d P t es h ([], loc, 0, None) ta h' (stk'', loc', pc' - length (compE2 e),
    xcp')
    by(unfold exec-moves-def)(drule (1) exec-meth-stk-splits, auto)
    from IH2[OF exec''] len lenxs bsok obtain es'' xs''
    where bisim': P,es,h' ⊢ (es'', xs'') [↔] (stk'', loc', pc' - length (compE2 e), xcp')
    and red': ?reds es loc es'' xs'' es [] 0 (pc' - length (compE2 e)) None xcp' by fastforce
  
```



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from bisim' have  $P, e \# es, h' \vdash (Val\ v \# es'', xs'') [\leftrightarrow] (stk'' @ [v], loc', length\ (compE2\ e) +$ 
 $(pc' - length\ (compE2\ e)), xcp')$ 
  by(rule bisims1List2)
moreover from exec''
have  $\tau moves2\ (compP2\ P)\ h\ [v]\ (e \# es)\ (length\ (compE2\ e))\ None = \tau moves2\ (compP2\ P)\ h\ []$ 
 $es\ 0\ None$ 
  using  $\tau instr\text{-}stk\text{-}drop\text{-}exec\text{-}moves$  where  $stk = []$  and  $vs = [v]$  by(simp add:  $\tau moves2\text{-}iff$ )
moreover have  $\tau: \bigwedge es'. \tau moves1\ P\ h\ (Val\ v \# es') \implies \tau moves1\ P\ h\ es'$  by simp
from exec' have  $pc' \geq length\ (compE2\ e)$ 
  by(rule exec-meth-drop-xt-pc) auto
moreover have  $no\text{-}calls2\ es\ 0 \implies no\text{-}calls2\ (e \# es)\ (length\ (compE2\ e))$ 
  by(simp add: no-calls2-def)
ultimately show ?thesis using red' xcp stk stk' call v
  apply(auto simp add: split-paired-Ex)
  apply(blast 25 intro: rtranclp-trans rtranclp-tranclp-tranclp  $\tau reds1r\text{-}cons\text{-}\tau reds1r\ List1Red2$ 
 $\tau reds1t\text{-}cons\text{-}\tau reds1t\ dest: \tau$ ) +
  done
qed
next
case (bisims1List2 es n es' xs stk loc pc xcp e v)
note  $IH = bisims1List2.IH(2)$ 
note  $exec = \langle ?execs\ (e \# es)\ (stk @ [v])\ loc\ (length\ (compE2\ e) + pc)\ xcp\ stk'\ loc'\ pc'\ xcp' \rangle$ 
note  $bisim1 = \langle P, e, h \vdash (e, xs) \leftrightarrow ([], xs, 0, None) \rangle$ 
note  $bisim2 = \langle P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, xcp) \rangle$ 
note  $len = \langle n + max\text{-}varss\ (Val\ v \# es') \leq length\ xs \rangle$ 
note  $bsok = \langle bsoks\ (e \# es)\ n \rangle$ 
from  $\langle P, h \vdash stk @ [v] [: \leq] ST \rangle$  obtain  $ST'$  where  $ST': P, h \vdash stk [: \leq] ST'$ 
  by(auto simp add: list-all2-append1)
from exec have  $exec': exec\text{-}meth\text{-}d\ (compP2\ P)\ (compE2\ e @ compEs2\ es)\ (compxE2\ e\ 0\ 0 @ shift$ 
 $(length\ (compE2\ e))\ (stack\text{-}xlift\ (length\ [v])\ (compxEs2\ es\ 0\ 0)))\ t\ h\ (stk @ [v], loc, length\ (compE2$ 
 $e) + pc, xcp)\ ta\ h'\ (stk', loc', pc', xcp')$ 
  by(simp add: shift-compxEs2 stack-xlift-compxEs2 exec-moves-def)
hence  $exec\text{-}meth\text{-}d\ (compP2\ P)\ (compEs2\ es)\ (stack\text{-}xlift\ (length\ [v])\ (compxEs2\ es\ 0\ 0))\ t\ h\ (stk @$ 
 $[v], loc, pc, xcp)\ ta\ h'\ (stk', loc', pc' - length\ (compE2\ e), xcp')$ 
  by(rule exec-meth-drop-xt) auto
with bisim2 obtain  $stk''$  where  $stk': stk' = stk'' @ [v]$ 
  and  $exec'': exec\text{-}moves\text{-}d\ P\ t\ es\ h\ (stk, loc, pc, xcp)\ ta\ h'\ (stk'', loc', pc' - length\ (compE2\ e), xcp')$ 
  by(unfold exec-moves-def)(drule (1) exec-meth-stk-splits, auto)
from  $IH[OF\ exec'' - - ST' \langle conf\text{-}xcp' (compP2\ P)\ h\ xcp \rangle]\ len\ bsok$  obtain  $es'' xs''$ 
  where  $bisim': P, es, h' \vdash (es'', xs'') [\leftrightarrow] (stk'', loc', pc' - length\ (compE2\ e), xcp')$ 
  and  $red': ?reds\ es'\ xs\ es'' xs'' es\ stk\ pc\ (pc' - length\ (compE2\ e))\ xcp\ xcp'$  by auto
from bisim' have  $P, e \# es, h' \vdash (Val\ v \# es'', xs'') [\leftrightarrow] (stk'' @ [v], loc', length\ (compE2\ e) + (pc'$ 
 $- length\ (compE2\ e)), xcp')$ 
  by(rule bisim1-bisims1.bisims1List2)
moreover from exec'' have  $\tau moves2\ (compP2\ P)\ h\ (stk @ [v])\ (e \# es)\ (length\ (compE2\ e) + pc)$ 
 $xcp = \tau moves2\ (compP2\ P)\ h\ stk\ es\ pc\ xcp$ 
  by(auto simp add:  $\tau moves2\text{-}iff\ \tau instr\text{-}stk\text{-}drop\text{-}exec\text{-}moves$ )
moreover have  $\tau: \bigwedge es'. \tau moves1\ P\ h\ (Val\ v \# es') \implies \tau moves1\ P\ h\ es'$  by simp
from exec' have  $pc' \geq length\ (compE2\ e)$ 
  by(rule exec-meth-drop-xt-pc) auto
moreover have  $no\text{-}calls2\ es\ pc \implies no\text{-}calls2\ (e \# es)\ (length\ (compE2\ e) + pc)$ 
  by(simp add: no-calls2-def)
ultimately show ?case using red' stk'
  by(auto split: if-split-asm simp add: split-paired-Ex)(blast intro: rtranclp-trans rtranclp-tranclp-tranclp

```

$\tau\text{reds1r-cons-}\tau\text{reds1r List1Red2 } \tau\text{reds1t-cons-}\tau\text{reds1t dest: } \tau)+$   
**qed**

**end**

**inductive** *sim21-size-aux* :: *nat*  $\Rightarrow$  (*pc*  $\times$  'addr option)  $\Rightarrow$  (*pc*  $\times$  'addr option)  $\Rightarrow$  *bool*

**for** *len* :: *nat*

**where**

$\llbracket pc1 \leq len; pc2 \leq len; xcp1 \neq None \wedge xcp2 = None \wedge pc1 = pc2 \vee xcp1 = None \wedge pc1 > pc2 \rrbracket$   
 $\implies \text{sim21-size-aux } len \ (pc1, xcp1) \ (pc2, xcp2)$

**definition** *sim21-size* :: 'addr jvm-prog  $\Rightarrow$  'addr jvm-thread-state  $\Rightarrow$  'addr jvm-thread-state  $\Rightarrow$  *bool*

**where**

*sim21-size* *P xcpfrs xcpfrs'*  $\longleftrightarrow$   
 (*xcpfrs*, *xcpfrs'*)  $\in$   
*inv-image* (*less-than*  $\langle *lex* \rangle$  *same-fst* ( $\lambda n. \text{True}$ ) ( $\lambda n. \{(pcxcp, pcxcp'). \text{sim21-size-aux } n \ pcxcp \}$ )  
 $(\lambda(xcp, frs). (\text{length } frs, \text{case } frs \text{ of } [] \Rightarrow \text{undefined}$   
 $\mid (stk, loc, C, M, pc) \# frs \Rightarrow (\text{length } (fst \ (snd \ (snd \ (the \ (snd \ (snd \ (snd \ (method$   
 $P \ C \ M))))))))), pc, xcp)))$

**lemma** *wfP-sim21-size-aux*: *wfP* (*sim21-size-aux* *n*)

**proof** –

**let** *?f* =  $\lambda(pc, xcp). \text{case } xcp \text{ of } None \Rightarrow Suc \ (2 * (n - pc)) \mid Some \ - \Rightarrow 2 * (n - pc)$   
**have** *wf*  $\{(m, m'). (m :: nat) < m'\}$  **by**(*rule wf-less*)  
**hence** *wf* (*inv-image*  $\{(m, m'). m < m'\}$  *?f*) **by**(*rule wf-inv-image*)  
**moreover have**  $\{(pcxcp1, pcxcp2). \text{sim21-size-aux } n \ pcxcp1 \ pcxcp2\} \subseteq \text{inv-image } \{(m, m'). m < m'\}$  *?f*  
**by**(*auto elim!: sim21-size-aux.cases*)  
**ultimately show** *?thesis* **unfolding** *wfP-def* **by**(*rule wf-subset*)  
**qed**

**lemma** *Collect-split-mem*:  $\{(x, y). (x, y) \in Q\} = Q$  **by** *simp*

**lemma** *wfP-sim21-size*: *wfP* (*sim21-size* *P*)

**unfolding** *wfP-def* *Collect-split-mem* *sim21-size-def* [*abs-def*]

**apply**(*rule wf-inv-image*)

**apply**(*rule wf-lex-prod*)

**apply**(*rule wf-less-than*)

**apply**(*rule wf-same-fst*)

**apply**(*rule wfP-sim21-size-aux*[*unfolded wfP-def*])

**done**

**declare** *split-beta*[*simp*]

**context** *J1-JVM-heap-base* **begin**

**lemma** *bisim1-Invoke-Red*:

$\llbracket P, E, h \vdash (e, xs) \leftrightarrow (\text{rev } vs @ \text{Addr } a \# stk', loc, pc, None); pc < \text{length } (compE2 \ E);$   
 $\text{compE2 } E ! pc = \text{Invoke } M \ (\text{length } vs); n + \text{max-vars } e \leq \text{length } xs; \text{bsok } E \ n \rrbracket$   
 $\implies \exists e' \ xs'. \tau\text{red1r } P \ t \ h \ (e, xs) \ (e', xs') \wedge P, E, h \vdash (e', xs') \leftrightarrow (\text{rev } vs @ \text{Addr } a \# stk', loc, pc,$   
 $None) \wedge \text{call1 } e' = \lfloor (a, M, vs) \rfloor$   
 $(\text{is } \llbracket -; -; -; - \rrbracket \implies ?\text{concl } e \ xs \ E \ n \ pc \ stk' \ loc)$

**and** *bisims1-Invoke- $\tau$ Reds*:  
 $\llbracket P, Es, h \vdash (es, xs) [\leftrightarrow] (rev\ vs\ @\ Addr\ a\ \# \ stk',\ loc,\ pc,\ None); pc < length\ (compEs2\ Es);$   
 $compEs2\ Es\ !\ pc = Invoke\ M\ (length\ vs); n + max-varss\ es \leq length\ xs; bsoks\ Es\ n \rrbracket$   
 $\implies \exists es'\ xs'. \tau reds1r\ P\ t\ h\ (es, xs)\ (es', xs') \wedge P, Es, h \vdash (es', xs') [\leftrightarrow] (rev\ vs\ @\ Addr\ a\ \# \ stk', loc,$   
 $pc, None) \wedge calls1\ es' = \llbracket (a, M, vs) \rrbracket$   
 $(is\ \llbracket -; -; -; - \rrbracket \implies ?concls\ es\ xs\ Es\ n\ pc\ stk'\ loc)$   
**proof**(*induct*  $E\ n\ e\ xs\ stk \equiv rev\ vs\ @\ Addr\ a\ \# \ stk'\ loc\ pc\ xcp \equiv None::'addr\ option$   
**and**  $Es\ n\ es\ xs\ stk \equiv rev\ vs\ @\ Addr\ a\ \# \ stk'\ loc\ pc\ xcp \equiv None::'addr\ option$   
*arbitrary: stk' and stk' rule: bisim1-bisims1-inducts-split*)  
**case** *bisim1Val2* **thus** *?case* **by** *simp*  
**next**  
**case** *bisim1New* **thus** *?case* **by** *simp*  
**next**  
**case** *bisim1NewArray* **thus** *?case*  
**by**(*fastforce split: if-split-asm intro: bisim1-bisims1.bisim1NewArray dest: bisim1-pc-length-compE2*  
*elim!: NewArray- $\tau$ red1r-xt*)  
**next**  
**case** *bisim1Cast* **thus** *?case*  
**by**(*fastforce split: if-split-asm intro: bisim1-bisims1.bisim1Cast dest: bisim1-pc-length-compE2 elim!:*  
*Cast- $\tau$ red1r-xt*)  
**next**  
**case** *bisim1InstanceOf* **thus** *?case*  
**by**(*fastforce split: if-split-asm intro: bisim1-bisims1.bisim1InstanceOf dest: bisim1-pc-length-compE2*  
*elim!: InstanceOf- $\tau$ red1r-xt*)  
**next**  
**case** *bisim1Val* **thus** *?case* **by** *simp*  
**next**  
**case** *bisim1Var* **thus** *?case* **by** *simp*  
**next**  
**case** *bisim1BinOp1* **thus** *?case*  
**apply**(*auto split: if-split-asm intro: bisim1-bisims1.bisim1BinOp1 dest: bisim1-pc-length-compE2*  
*elim: BinOp- $\tau$ red1r-xt1*)  
**apply**(*fastforce elim!: BinOp- $\tau$ red1r-xt1 intro: bisim1-bisims1.bisim1BinOp1*)  
**done**  
**next**  
**case** (*bisim1BinOp2*  $e2\ n\ e'\ xs\ stk\ loc\ pc\ e1\ bop\ v1$ )  
**note**  $IH = \langle \bigwedge stk'. \llbracket stk = rev\ vs\ @\ Addr\ a\ \# \ stk'; pc < length\ (compE2\ e2); compE2\ e2\ !\ pc =$   
 $Invoke\ M\ (length\ vs); n + max-vars\ e' \leq length\ xs; bsok\ e2\ n$   
 $\rrbracket \implies ?concl\ e'\ xs\ e2\ n\ pc\ stk'\ loc \rangle$   
**note**  $inv = \langle compE2\ (e1\ \ll bop \gg e2)\ !\ (length\ (compE2\ e1) + pc) = Invoke\ M\ (length\ vs) \rangle$   
**with**  $\langle length\ (compE2\ e1) + pc < length\ (compE2\ (e1\ \ll bop \gg e2)) \rangle$  **have**  $pc: pc < length\ (compE2$   
 $e2)$   
**by**(*auto split: bop.splits if-split-asm*)  
**moreover** **with**  $inv$  **have**  $compE2\ e2\ !\ pc = Invoke\ M\ (length\ vs)$  **by** *simp*  
**moreover** **with**  $\langle P, e2, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, None) \rangle pc$   
**obtain**  $vs''\ v''\ stk''$  **where**  $stk = vs''\ @\ v''\ \# \ stk''$  **and**  $length\ vs'' = length\ vs$   
**by**(*auto dest!: bisim1-Invoke-stkD*)  
**with**  $\langle stk\ @\ [v1] = rev\ vs\ @\ Addr\ a\ \# \ stk' \rangle$  **obtain**  $stk'''$   
**where**  $stk''': stk = rev\ vs\ @\ Addr\ a\ \# \ stk'''$  **and**  $stk: stk' = stk'''\ @\ [v1]$   
**by**(*cases stk' rule: rev-cases*) *auto*  
**from**  $\langle n + max-vars\ (Val\ v1\ \ll bop \gg e') \leq length\ xs \rangle$  **have**  $n + max-vars\ e' \leq length\ xs$  **by** *simp*  
**moreover** **from**  $\langle bsok\ (e1\ \ll bop \gg e2)\ n \rangle$  **have**  $bsok\ e2\ n$  **by** *simp*  
**ultimately** **have** *?concl*  $e'\ xs\ e2\ n\ pc\ stk''' loc$  **using**  $stk'''$  **by**  $-(rule\ IH)$   
**then** **obtain**  $e''\ xs'$  **where**  $IH': \tau red1r\ P\ t\ h\ (e', xs)\ (e'', xs')\ call1\ e'' = \llbracket (a, M, vs) \rrbracket$

**and**  $\text{bisim}: P, e2, h \vdash (e'', xs') \leftrightarrow (\text{rev } vs @ \text{Addr } a \# \text{stk}''', \text{loc}, pc, \text{None})$  **by** *blast*  
**from**  $\text{bisim}$  **have**  $P, e1 \llbracket \text{bop} \rrbracket e2, h \vdash (\text{Val } v1 \llbracket \text{bop} \rrbracket e'', xs') \leftrightarrow ((\text{rev } vs @ \text{Addr } a \# \text{stk}''') @ [v1], \text{loc}, \text{length } (\text{compE2 } e1) + pc, \text{None})$   
**by**(*rule bisim1-bisims1.bisim1BinOp2*)  
**with**  $\text{IH}' \text{stk}$  **show**  $?case$  **by**(*fastforce elim!: BinOp- $\tau$ red1r-xt2*)  
**next**  
**case** *bisim1LAss1* **thus**  $?case$   
**by**(*fastforce split: if-split-asm intro: bisim1-bisims1.bisim1LAss1 dest: bisim1-pc-length-compE2 elim!: LAss- $\tau$ red1r*)  
**next**  
**case** *bisim1LAss2* **thus**  $?case$  **by** *simp*  
**next**  
**case** *bisim1AAcc1* **thus**  $?case$   
**apply**(*auto split: if-split-asm intro: bisim1-bisims1.bisim1AAcc1 dest: bisim1-pc-length-compE2 elim!: AAacc- $\tau$ red1r-xt1*)  
**apply**(*fastforce elim!: AAacc- $\tau$ red1r-xt1 intro: bisim1-bisims1.bisim1AAcc1*)  
**done**  
**next**  
**case** (*bisim1AAcc2*  $e2 \ n \ e' \ xs \ \text{stk} \ \text{loc} \ pc \ e1 \ v1$ )  
**note**  $\text{IH} = \langle \bigwedge \text{stk}'. \llbracket \text{stk} = \text{rev } vs @ \text{Addr } a \# \text{stk}' \rrbracket; pc < \text{length } (\text{compE2 } e2); \text{compE2 } e2 ! pc = \text{Invoke } M \ (\text{length } vs); n + \text{max-vars } e' \leq \text{length } xs; \text{bsok } e2 \ n \rrbracket \Rightarrow ?concl \ e' \ xs \ e2 \ n \ pc \ \text{stk}' \ \text{loc} \rangle$   
**note**  $\text{inv} = \langle \text{compE2 } (e1 \llbracket e2 \rrbracket) ! (\text{length } (\text{compE2 } e1) + pc) = \text{Invoke } M \ (\text{length } vs) \rangle$   
**with**  $\langle \text{length } (\text{compE2 } e1) + pc < \text{length } (\text{compE2 } (e1 \llbracket e2 \rrbracket)) \rangle$  **have**  $pc: pc < \text{length } (\text{compE2 } e2)$   
**by**(*auto split: if-split-asm*)  
**moreover with**  $\text{inv}$  **have**  $\text{compE2 } e2 ! pc = \text{Invoke } M \ (\text{length } vs)$  **by** *simp*  
**moreover with**  $\langle P, e2, h \vdash (e', xs) \leftrightarrow (\text{stk}, \text{loc}, pc, \text{None}) \rangle$   $pc$   
**obtain**  $vs'' \ v'' \ \text{stk}''$  **where**  $\text{stk} = vs'' @ v'' \# \text{stk}''$  **and**  $\text{length } vs'' = \text{length } vs$   
**by**(*auto dest!: bisim1-Invoke-stkD*)  
**with**  $\langle \text{stk} @ [v1] = \text{rev } vs @ \text{Addr } a \# \text{stk}' \rangle$  **obtain**  $\text{stk}'''$   
**where**  $\text{stk}''': \text{stk} = \text{rev } vs @ \text{Addr } a \# \text{stk}'''$  **and**  $\text{stk}: \text{stk}' = \text{stk}''' @ [v1]$   
**by**(*cases stk' rule: rev-cases*) *auto*  
**from**  $\langle n + \text{max-vars } (\text{Val } v1 \llbracket e \rrbracket) \leq \text{length } xs \rangle$  **have**  $n + \text{max-vars } e' \leq \text{length } xs$  **by** *simp*  
**moreover from**  $\langle \text{bsok } (e1 \llbracket e2 \rrbracket) \ n \rangle$  **have**  $\text{bsok } e2 \ n$  **by** *simp*  
**ultimately have**  $?concl \ e' \ xs \ e2 \ n \ pc \ \text{stk}''' \ \text{loc}$  **using**  $\text{stk}'''$  **by**-(*rule IH*)  
**then obtain**  $e'' \ xs'$  **where**  $\text{IH}': \tau \text{red1r } P \ t \ h \ (e', xs) \ (e'', xs') \ \text{call1 } e'' = \llbracket (a, M, vs) \rrbracket$   
**and**  $\text{bisim}: P, e2, h \vdash (e'', xs') \leftrightarrow (\text{rev } vs @ \text{Addr } a \# \text{stk}''', \text{loc}, pc, \text{None})$  **by** *blast*  
**from**  $\text{bisim}$  **have**  $P, e1 \llbracket e2 \rrbracket, h \vdash (\text{Val } v1 \llbracket e' \rrbracket, xs') \leftrightarrow ((\text{rev } vs @ \text{Addr } a \# \text{stk}''') @ [v1], \text{loc}, \text{length } (\text{compE2 } e1) + pc, \text{None})$   
**by**(*rule bisim1-bisims1.bisim1AAcc2*)  
**with**  $\text{IH}' \text{stk}$  **show**  $?case$  **by**(*fastforce elim!: AAacc- $\tau$ red1r-xt2*)  
**next**  
**case** (*bisim1AAss1*  $e \ n \ e' \ xs \ \text{loc} \ pc \ e2 \ e3$ )  
**note**  $\text{IH} = \langle \llbracket pc < \text{length } (\text{compE2 } e); \text{compE2 } e ! pc = \text{Invoke } M \ (\text{length } vs); n + \text{max-vars } e' \leq \text{length } xs; \text{bsok } e \ n \rrbracket \Rightarrow ?concl \ e' \ xs \ e \ n \ pc \ \text{stk}' \ \text{loc} \rangle$   
**note**  $\text{bisim} = \langle P, e, h \vdash (e', xs) \leftrightarrow (\text{rev } vs @ \text{Addr } a \# \text{stk}', \text{loc}, pc, \text{None}) \rangle$   
**note**  $\text{len} = \langle n + \text{max-vars } (e' \llbracket e2 \rrbracket := e3) \leq \text{length } xs \rangle$   
**hence**  $\text{len}': n + \text{max-vars } e' \leq \text{length } xs$  **by** *simp*  
**note**  $\text{inv} = \langle \text{compE2 } (e \llbracket e2 \rrbracket := e3) ! pc = \text{Invoke } M \ (\text{length } vs) \rangle$   
**with**  $\langle pc < \text{length } (\text{compE2 } (e \llbracket e2 \rrbracket := e3)) \rangle$   $\text{bisim}$  **have**  $pc: pc < \text{length } (\text{compE2 } e)$   
**by**(*auto split: if-split-asm dest: bisim1-pc-length-compE2*)  
**moreover with**  $\text{inv}$  **have**  $\text{compE2 } e ! pc = \text{Invoke } M \ (\text{length } vs)$  **by** *simp*  
**moreover from**  $\langle \text{bsok } (e \llbracket e2 \rrbracket := e3) \ n \rangle$  **have**  $\text{bsok } e \ n$  **by** *simp*

ultimately have  $?concl\ e'\ xs\ e\ n\ pc\ stk'\ loc$  using  $len'$  by  $-(rule\ IH)$   
 thus  $?case$   
 by  $(fastforce\ intro:\ bisim1-bisims1.bisim1AAss1\ elim!:\ AAAss-\tau red1r-xt1)$   
 next  
 case  $(bisim1AAss2\ e2\ n\ e'\ xs\ stk\ loc\ pc\ e1\ e3\ v1)$   
 note  $IH = \langle \bigwedge stk'. \llbracket stk = rev\ vs\ @\ Addr\ a\ \# \ stk' \rrbracket; pc < length\ (compE2\ e2); compE2\ e2\ !\ pc =$   
 $Invoke\ M\ (length\ vs); n + max-vars\ e' \leq length\ xs; bsok\ e2\ n$   
 $\rrbracket \implies ?concl\ e'\ xs\ e2\ n\ pc\ stk'\ loc \rangle$   
 note  $inv = \langle compE2\ (e1[e2] := e3) ! (length\ (compE2\ e1) + pc) = Invoke\ M\ (length\ vs) \rangle$   
 note  $bisim = \langle P, e2, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, None) \rangle$   
 with  $inv\ \langle length\ (compE2\ e1) + pc < length\ (compE2\ (e1[e2] := e3)) \rangle$  have  $pc: pc < length$   
 $(compE2\ e2)$   
 by  $(auto\ split:\ if-split-asm\ dest:\ bisim1-pc-length-compE2)$   
 moreover with  $inv$  have  $compE2\ e2\ !\ pc = Invoke\ M\ (length\ vs)$  by  $simp$   
 moreover with  $bisim\ pc$   
 obtain  $vs''\ v''\ stk''$  where  $stk = vs''\ @\ v''\ \# \ stk''$  and  $length\ vs'' = length\ vs$   
 by  $(auto\ dest!:\ bisim1-Invoke-stkD)$   
 with  $\langle stk\ @\ [v1] = rev\ vs\ @\ Addr\ a\ \# \ stk' \rangle$  obtain  $stk'''$   
 where  $stk''': stk = rev\ vs\ @\ Addr\ a\ \# \ stk'''$  and  $stk: stk' = stk'''\ @\ [v1]$   
 by  $(cases\ stk'\ rule:\ rev-cases)\ auto$   
 from  $\langle n + max-vars\ (Val\ v1[e'] := e3) \leq length\ xs \rangle$  have  $n + max-vars\ e' \leq length\ xs$  by  $simp$   
 moreover from  $\langle bsok\ (e1[e2] := e3)\ n \rangle$  have  $bsok\ e2\ n$  by  $simp$   
 ultimately have  $?concl\ e'\ xs\ e2\ n\ pc\ stk''' \ loc$  using  $stk'''$  by  $-(rule\ IH)$   
 then obtain  $e''\ xs'$  where  $IH': \tau red1r\ P\ t\ h\ (e', xs)\ (e'', xs')\ call1\ e'' = \lfloor (a, M, vs) \rfloor$   
 and  $bisim: P, e2, h \vdash (e'', xs') \leftrightarrow (rev\ vs\ @\ Addr\ a\ \# \ stk''', loc, pc, None)$  by  $blast$   
 from  $bisim$   
 have  $P, e1[e2] := e3, h \vdash (Val\ v1[e'] := e3, xs') \leftrightarrow ((rev\ vs\ @\ Addr\ a\ \# \ stk''')\ @\ [v1], loc, length$   
 $(compE2\ e1) + pc, None)$   
 by  $(rule\ bisim1-bisims1.bisim1AAss2)$   
 with  $IH'\ stk$  show  $?case$  by  $(fastforce\ elim!:\ AAAss-\tau red1r-xt2)$   
 next  
 case  $(bisim1AAss3\ e3\ n\ e'\ xs\ stk\ loc\ pc\ e1\ e2\ v1\ v2)$   
 note  $IH = \langle \bigwedge stk'. \llbracket stk = rev\ vs\ @\ Addr\ a\ \# \ stk' \rrbracket; pc < length\ (compE2\ e3); compE2\ e3\ !\ pc =$   
 $Invoke\ M\ (length\ vs); n + max-vars\ e' \leq length\ xs; bsok\ e3\ n$   
 $\rrbracket \implies ?concl\ e'\ xs\ e3\ n\ pc\ stk'\ loc \rangle$   
 note  $inv = \langle compE2\ (e1[e2] := e3) ! (length\ (compE2\ e1) + length\ (compE2\ e2) + pc) = Invoke$   
 $M\ (length\ vs) \rangle$   
 with  $\langle length\ (compE2\ e1) + length\ (compE2\ e2) + pc < length\ (compE2\ (e1[e2] := e3)) \rangle$   
 have  $pc: pc < length\ (compE2\ e3)$  by  $(simp\ add:\ nth-Cons\ split:\ nat.split-asm\ if-split-asm)$   
 moreover with  $inv$  have  $compE2\ e3\ !\ pc = Invoke\ M\ (length\ vs)$  by  $simp$   
 moreover with  $\langle P, e3, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, None) \rangle$   $pc$   
 obtain  $vs''\ v''\ stk''$  where  $stk = vs''\ @\ v''\ \# \ stk''$  and  $length\ vs'' = length\ vs$   
 by  $(auto\ dest!:\ bisim1-Invoke-stkD)$   
 with  $\langle stk\ @\ [v2, v1] = rev\ vs\ @\ Addr\ a\ \# \ stk' \rangle$  obtain  $stk'''$   
 where  $stk''': stk = rev\ vs\ @\ Addr\ a\ \# \ stk'''$  and  $stk: stk' = stk'''\ @\ [v2, v1]$   
 by  $(cases\ stk'\ rule:\ rev-cases)\ auto$   
 from  $\langle n + max-vars\ (Val\ v1[Val\ v2] := e') \leq length\ xs \rangle$  have  $n + max-vars\ e' \leq length\ xs$  by  $simp$   
 moreover from  $\langle bsok\ (e1[e2] := e3)\ n \rangle$  have  $bsok\ e3\ n$  by  $simp$   
 ultimately have  $?concl\ e'\ xs\ e3\ n\ pc\ stk''' \ loc$  using  $stk'''$  by  $-(rule\ IH)$   
 then obtain  $e''\ xs'$  where  $IH': \tau red1r\ P\ t\ h\ (e', xs)\ (e'', xs')\ call1\ e'' = \lfloor (a, M, vs) \rfloor$   
 and  $bisim: P, e3, h \vdash (e'', xs') \leftrightarrow (rev\ vs\ @\ Addr\ a\ \# \ stk''', loc, pc, None)$  by  $blast$   
 from  $bisim$   
 have  $P, e1[e2] := e3, h \vdash (Val\ v1[Val\ v2] := e'', xs') \leftrightarrow ((rev\ vs\ @\ Addr\ a\ \# \ stk''')\ @\ [v2, v1], loc,$   
 $length\ (compE2\ e1) + length\ (compE2\ e2) + pc, None)$

```

    by  $\text{--}(rule\ bisim1\text{-}bisims1.bisim1AAss3, auto)$ 
    with  $IH' \text{ stk}$  show ?case by(fastforce elim!:  $AAss\text{-}\tau red1r\text{-}xt3$ )
next
  case  $bisim1AAss4$  thus ?case by simp
next
  case  $bisim1ALength$  thus ?case
    by(fastforce split: if-split-asm intro:  $bisim1\text{-}bisims1.bisim1ALength$  dest:  $bisim1\text{-}pc\text{-}length\text{-}compE2$ 
    elim!:  $ALength\text{-}\tau red1r\text{-}xt$ )
next
  case  $bisim1FAcc$  thus ?case
    by(fastforce split: if-split-asm intro:  $bisim1\text{-}bisims1.bisim1FAcc$  dest:  $bisim1\text{-}pc\text{-}length\text{-}compE2$  elim!:
     $FAcc\text{-}\tau red1r\text{-}xt$ )
next
  case  $bisim1FAss1$  thus ?case
    apply(auto split: if-split-asm intro:  $bisim1\text{-}bisims1.bisim1FAss1$  dest:  $bisim1\text{-}pc\text{-}length\text{-}compE2$ 
    elim!:  $FAss\text{-}\tau red1r\text{-}xt1$ )
    by(fastforce intro:  $bisim1\text{-}bisims1.bisim1FAss1$  elim!:  $FAss\text{-}\tau red1r\text{-}xt1$ )
next
  case ( $bisim1FAss2\ e2\ n\ e'\ xs\ stk\ loc\ pc\ e1\ F\ D\ v1$ )
    note  $IH = \langle \bigwedge stk'. \llbracket stk = rev\ vs\ @\ Addr\ a\ \# \ stk' \rrbracket; pc < length\ (compE2\ e2); compE2\ e2\ !\ pc =$ 
     $Invoke\ M\ (length\ vs); n + max\text{-}vars\ e' \leq length\ xs; bsok\ e2\ n$ 
     $\rrbracket \implies ?concl\ e'\ xs\ e2\ n\ pc\ stk'\ loc \rangle$ 
    note  $inv = \langle compE2\ (e1 \cdot F\{D\} := e2) !\ (length\ (compE2\ e1) + pc) = Invoke\ M\ (length\ vs) \rangle$ 
    with  $\langle length\ (compE2\ e1) + pc < length\ (compE2\ (e1 \cdot F\{D\} := e2)) \rangle$  have  $pc: pc < length\ (compE2\ e2)$ 
    by(simp split: if-split-asm nat.split-asm add: nth-Cons)
    moreover with  $inv$  have  $compE2\ e2\ !\ pc = Invoke\ M\ (length\ vs)$  by simp
    moreover with  $\langle P, e2, h \vdash (e', xs) \leftrightarrow (stk, loc, pc, None) \rangle pc$ 
    obtain  $vs''\ v''\ stk''$  where  $stk = vs'' @ v'' \# stk''$  and  $length\ vs'' = length\ vs$ 
    by(auto dest!:  $bisim1\text{-}Invoke\text{-}stkD$ )
    with  $\langle stk @ [v1] = rev\ vs @ Addr\ a\ \# \ stk' \rangle$  obtain  $stk'''$ 
    where  $stk''': stk = rev\ vs @ Addr\ a\ \# \ stk'''$  and  $stk: stk' = stk''' @ [v1]$ 
    by(cases  $stk'$  rule: rev-cases) auto
    from  $\langle n + max\text{-}vars\ (Val\ v1 \cdot F\{D\} := e') \leq length\ xs \rangle$  have  $n + max\text{-}vars\ e' \leq length\ xs$  by simp
    moreover from  $\langle bsok\ (e1 \cdot F\{D\} := e2)\ n \rangle$  have  $bsok\ e2\ n$  by simp
    ultimately have ?concl  $e'\ xs\ e2\ n\ pc\ stk''' loc$  using  $stk'''$  by $\text{--}(rule\ IH)$ 
    then obtain  $e''\ xs'$  where  $IH': \tau red1r\ P\ t\ h\ (e', xs) (e'', xs') call1\ e'' = \lfloor (a, M, vs) \rfloor$ 
    and  $bisim: P, e2, h \vdash (e'', xs') \leftrightarrow (rev\ vs @ Addr\ a\ \# \ stk''', loc, pc, None)$  by blast
    from  $bisim$  have  $P, e1 \cdot F\{D\} := e2, h \vdash (Val\ v1 \cdot F\{D\} := e'', xs') \leftrightarrow ((rev\ vs @ Addr\ a\ \# \ stk''') @$ 
     $[v1], loc, length\ (compE2\ e1) + pc, None)$ 
    by(rule  $bisim1\text{-}bisims1.bisim1FAss2$ )
    with  $IH' \text{ stk}$  show ?case by(fastforce elim!:  $FAss\text{-}\tau red1r\text{-}xt2$ )
next
  case  $bisim1FAss3$  thus ?case by simp
next
  case ( $bisim1CAS1\ e\ n\ e'\ xs\ loc\ pc\ e2\ e3\ D\ F$ )
    note  $IH = \langle \llbracket pc < length\ (compE2\ e); compE2\ e\ !\ pc = Invoke\ M\ (length\ vs); n + max\text{-}vars\ e' \leq$ 
     $length\ xs; bsok\ e\ n$ 
     $\rrbracket \implies ?concl\ e'\ xs\ e\ n\ pc\ stk'\ loc \rangle$ 
    note  $bisim = \langle P, e, h \vdash (e', xs) \leftrightarrow (rev\ vs @ Addr\ a\ \# \ stk', loc, pc, None) \rangle$ 
    note  $len = \langle n + max\text{-}vars - \leq length\ xs \rangle$ 
    hence  $len': n + max\text{-}vars\ e' \leq length\ xs$  by simp
    note  $inv = \langle compE2 - !\ pc = Invoke\ M\ (length\ vs) \rangle$ 
    with  $\langle pc < length \rightarrow bisim \rangle$  have  $pc: pc < length\ (compE2\ e)$ 

```

by(auto split: if-split-asm dest: bisim1-pc-length-compE2)  
 moreover with inv have compE2 e ! pc = Invoke M (length vs) by simp  
 moreover from ⟨bsok - n⟩ have bsok e n by simp  
 ultimately have ?concl e' xs e n pc stk' loc using len' by-(rule IH)  
 thus ?case  
 by(fastforce intro: bisim1-bisims1.bisim1CAS1 elim!: CAS-τred1r-xt1)  
 next  
 case (bisim1CAS2 e2 n e' xs stk loc pc e1 e3 D F v1)  
 note IH = ⟨ $\bigwedge$ stk'.  $\llbracket \text{stk} = \text{rev vs} @ \text{Addr } a \# \text{stk}' \rrbracket$ ;  $pc < \text{length}(\text{compE2 } e2)$ ;  $\text{compE2 } e2 ! pc = \text{Invoke } M(\text{length vs})$ ;  $n + \text{max-vars } e' \leq \text{length } xs$ ;  $\text{bsok } e2 n$   
 $\rrbracket \implies ?\text{concl } e' xs e2 n pc \text{stk}' \text{loc}$ ⟩  
 note inv = ⟨compE2 - ! (length (compE2 e1) + pc) = Invoke M (length vs)⟩  
 note bisim = ⟨ $P, e2, h \vdash (e', xs) \leftrightarrow (stk, \text{loc}, pc, \text{None})$ ⟩  
 with inv ⟨length (compE2 e1) + pc < length (compE2 -)⟩ have pc: pc < length (compE2 e2)  
 by(auto split: if-split-asm dest: bisim1-pc-length-compE2)  
 moreover with inv have compE2 e2 ! pc = Invoke M (length vs) by simp  
 moreover with bisim pc  
 obtain vs'' v'' stk'' where stk = vs'' @ v'' # stk'' and length vs'' = length vs  
 by(auto dest!: bisim1-Invoke-stkD)  
 with ⟨stk @ [v1] = rev vs @ Addr a # stk'⟩ obtain stk'''  
 where stk''': stk = rev vs @ Addr a # stk''' and stk: stk' = stk''' @ [v1]  
 by(cases stk' rule: rev-cases) auto  
 from ⟨n + max-vars - ≤ length xs⟩ have n + max-vars e' ≤ length xs by simp  
 moreover from ⟨bsok - n⟩ have bsok e2 n by simp  
 ultimately have ?concl e' xs e2 n pc stk''' loc using stk''' by-(rule IH)  
 then obtain e'' xs' where IH': τred1r P t h (e', xs) (e'', xs') call1 e'' = [(a, M, vs)]  
 and bisim:  $P, e2, h \vdash (e'', xs') \leftrightarrow (\text{rev vs} @ \text{Addr } a \# \text{stk}''', \text{loc}, pc, \text{None})$  by blast  
 from bisim  
 have  $P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), h \vdash (\text{Val } v1 \cdot \text{compareAndSwap}(D \cdot F, e'', e3), xs') \leftrightarrow ((\text{rev vs} @ \text{Addr } a \# \text{stk}''') @ [v1], \text{loc}, \text{length}(\text{compE2 } e1) + pc, \text{None})$   
 by(rule bisim1-bisims1.bisim1CAS2)  
 with IH' stk show ?case by(fastforce elim!: CAS-τred1r-xt2)  
 next  
 case (bisim1CAS3 e3 n e' xs stk loc pc e1 e2 D F v1 v2)  
 note IH = ⟨ $\bigwedge$ stk'.  $\llbracket \text{stk} = \text{rev vs} @ \text{Addr } a \# \text{stk}' \rrbracket$ ;  $pc < \text{length}(\text{compE2 } e3)$ ;  $\text{compE2 } e3 ! pc = \text{Invoke } M(\text{length vs})$ ;  $n + \text{max-vars } e' \leq \text{length } xs$ ;  $\text{bsok } e3 n$   
 $\rrbracket \implies ?\text{concl } e' xs e3 n pc \text{stk}' \text{loc}$ ⟩  
 note inv = ⟨compE2 - ! (length (compE2 e1) + length (compE2 e2) + pc) = Invoke M (length vs)⟩  
 with ⟨length (compE2 e1) + length (compE2 e2) + pc < length (compE2 -)⟩  
 have pc: pc < length (compE2 e3) by(simp add: nth-Cons split: nat.split-asm if-split-asm)  
 moreover with inv have compE2 e3 ! pc = Invoke M (length vs) by simp  
 moreover with ⟨ $P, e3, h \vdash (e', xs) \leftrightarrow (stk, \text{loc}, pc, \text{None})$ ⟩ pc  
 obtain vs'' v'' stk'' where stk = vs'' @ v'' # stk'' and length vs'' = length vs  
 by(auto dest!: bisim1-Invoke-stkD)  
 with ⟨stk @ [v2, v1] = rev vs @ Addr a # stk'⟩ obtain stk'''  
 where stk''': stk = rev vs @ Addr a # stk''' and stk: stk' = stk''' @ [v2, v1]  
 by(cases stk' rule: rev-cases) auto  
 from ⟨n + max-vars - ≤ length xs⟩ have n + max-vars e' ≤ length xs by simp  
 moreover from ⟨bsok - n⟩ have bsok e3 n by simp  
 ultimately have ?concl e' xs e3 n pc stk''' loc using stk''' by-(rule IH)  
 then obtain e'' xs' where IH': τred1r P t h (e', xs) (e'', xs') call1 e'' = [(a, M, vs)]  
 and bisim:  $P, e3, h \vdash (e'', xs') \leftrightarrow (\text{rev vs} @ \text{Addr } a \# \text{stk}''', \text{loc}, pc, \text{None})$  by blast  
 from bisim  
 have  $P, e1 \cdot \text{compareAndSwap}(D \cdot F, e2, e3), h \vdash (\text{Val } v1 \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v2, e''), xs') \leftrightarrow$

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((rev vs @ Addr a # stk'') @ [v2, v1], loc, length (compE2 e1) + length (compE2 e2) + pc, None)
  by  $\neg$ (rule bisim1-bisims1.bisim1CAS3, auto)
  with IH' stk show ?case by(fastforce elim!: CAS- $\tau$ red1r-xt3)
next
  case (bisim1Call1 obj n obj' xs loc pc ps M')
  note IH =  $\langle \llbracket pc < \text{length}(\text{compE2 } obj); \text{compE2 } obj ! pc = \text{Invoke } M(\text{length } vs); n + \text{max-vars } obj' \leq \text{length } xs; \text{bsok } obj \ n \rrbracket \implies ?\text{concl } obj' \ xs \ obj \ n \ pc \ stk' \ loc \rangle$ 
  note bisim =  $\langle P, obj, h \vdash (obj', xs) \leftrightarrow (rev \ vs \ @ \ \text{Addr } a \ \# \ stk', \ loc, \ pc, \ \text{None}) \rangle$ 
  note len =  $\langle n + \text{max-vars } (obj' \cdot M'(ps)) \leq \text{length } xs \rangle$ 
  hence len':  $n + \text{max-vars } obj' \leq \text{length } xs$  by simp
  from  $\langle \text{bsok } (obj \cdot M'(ps)) \ n \rangle$  have bsok: bsok obj n by simp
  note inv =  $\langle \text{compE2 } (obj \cdot M'(ps)) ! pc = \text{Invoke } M(\text{length } vs) \rangle$ 
  with  $\langle pc < \text{length}(\text{compE2 } (obj \cdot M'(ps))) \rangle$  bisim
  have pc:  $pc < \text{length}(\text{compE2 } obj) \vee ps = [] \wedge pc = \text{length}(\text{compE2 } obj)$ 
  by(cases ps)(auto split: if-split-asm dest: bisim1-pc-length-compE2)
  thus ?case
proof
  assume pc < length (compE2 obj)
  moreover with inv have compE2 obj ! pc = Invoke M (length vs) by simp
  ultimately have ?concl obj' xs obj n pc stk' loc using len' bsok by(rule IH)
  thus ?thesis by(fastforce intro: bisim1-bisims1.bisim1Call1 elim!: Call- $\tau$ red1r-obj)
next
  assume [simp]:  $ps = [] \wedge pc = \text{length}(\text{compE2 } obj)$ 
  with inv have [simp]:  $vs = [] \ M' = M$  by simp-all
  with bisim have [simp]:  $vs = [] \ stk' = []$  by(auto dest: bisim1-pc-length-compE2D)
  with bisim len' bsok have  $\tau\text{red1r } P \ t \ h \ (obj', xs) \ (\text{addr } a, \ loc)$ 
  by(auto intro: bisim1-Val- $\tau$ red1r simp add: bsok-def)
  moreover
  have  $P, obj \cdot M([], h) \vdash (\text{addr } a \cdot M([], loc) \leftrightarrow ([\text{Addr } a], \ loc, \ \text{length}(\text{compE2 } obj), \ \text{None}))$ 
  by(rule bisim1-bisims1.bisim1Call1[OF bisim1Val2]) simp-all
  ultimately show ?thesis by auto(fastforce elim!: Call- $\tau$ red1r-obj)
qed
next
  case (bisim1CallParams ps n ps' xs stk loc pc obj M' v)
  note IH =  $\langle \bigwedge stk'. \llbracket stk = rev \ vs \ @ \ \text{Addr } a \ \# \ stk'; pc < \text{length}(\text{compEs2 } ps); \text{compEs2 } ps ! pc = \text{Invoke } M(\text{length } vs); n + \text{max-varss } ps' \leq \text{length } xs; \text{bsoks } ps \ n \rrbracket \implies ?\text{concls } ps' \ xs \ ps \ n \ pc \ stk' \ loc \rangle$ 
  note bisim =  $\langle P, ps, h \vdash (ps', xs) [\leftrightarrow] (stk, \ loc, \ pc, \ \text{None}) \rangle$ 
  note len =  $\langle n + \text{max-vars } (\text{Val } v \cdot M'(ps')) \leq \text{length } xs \rangle$ 
  hence len':  $n + \text{max-varss } ps' \leq \text{length } xs$  by simp
  note stk =  $\langle stk \ @ \ [v] = rev \ vs \ @ \ \text{Addr } a \ \# \ stk' \rangle$ 
  note inv =  $\langle \text{compE2 } (obj \cdot M'(ps)) ! (\text{length}(\text{compE2 } obj) + pc) = \text{Invoke } M(\text{length } vs) \rangle$ 
  from  $\langle \text{bsok } (obj \cdot M'(ps)) \ n \rangle$  have bsok: bsoks ps n by simp
  from  $\langle \text{length}(\text{compE2 } obj) + pc < \text{length}(\text{compE2 } (obj \cdot M'(ps))) \rangle$ 
  have pc < length (compEs2 ps)  $\vee pc = \text{length}(\text{compEs2 } ps)$  by(auto)
  thus ?case
proof
  assume pc:  $pc < \text{length}(\text{compEs2 } ps)$ 
  moreover with inv have compEs2 ps ! pc = Invoke M (length vs) by simp
  moreover with bisim pc
  obtain  $vs'' \ v'' \ stk''$  where  $stk = vs'' \ @ \ v'' \ \# \ stk''$  and  $\text{length } vs'' = \text{length } vs$ 
  by(auto dest!: bisims1-Invoke-stkD)
  with  $\langle stk \ @ \ [v] = rev \ vs \ @ \ \text{Addr } a \ \# \ stk' \rangle$  obtain  $stk'''$ 

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where  $stk'''$ :  $stk = rev\ vs\ @\ Addr\ a\ \# \ stk'''$  and  $stk$ :  $stk' = stk''' @ [v]$   
 by(cases  $stk'$  rule: rev-cases) auto  
 note  $len'\ stk'''$   
 ultimately have ?concls  $ps'\ xs\ ps\ n\ pc\ stk''' loc$  using  $bsok$  by—(rule  $IH$ )  
 then obtain  $es''\ xs'$  where  $IH'$ :  $\tau reds1r\ P\ t\ h\ (ps',\ xs)\ (es'',\ xs')\ calls1\ es'' = \lfloor (a,\ M,\ vs) \rfloor$   
 and  $bisim$ :  $P, ps, h \vdash (es'',\ xs') [\leftrightarrow] (rev\ vs\ @\ Addr\ a\ \# \ stk''',\ loc,\ pc,\ None)$  by blast  
 from  $bisim$  have  $P, obj \cdot M'(ps), h \vdash (Val\ v \cdot M'(es''),\ xs') \leftrightarrow ((rev\ vs\ @\ Addr\ a\ \# \ stk''') @ [v],\ loc,$   
 $length\ (compE2\ obj) + pc,\ None)$   
 by(rule  $bisim1$ -bisims1.bisim1CallParams)  
 with  $IH'$   $stk$  show ?case  
 by(fastforce elim!: Call- $\tau red1r$ -param simp add: is-vals-conv)  
 next  
 assume [simp]:  $pc = length\ (compEs2\ ps)$   
 from  $bisim$  obtain  $vs'$  where [simp]:  $stk = rev\ vs'$   
 and  $psvs'$ :  $length\ ps = length\ vs'$  by(auto dest: bisims1-pc-length-compEs2D)  
 from  $inv$  have [simp]:  $M' = M$  and  $vsps$ :  $length\ vs = length\ ps$  by simp-all  
 with  $stk\ psvs'$  have [simp]:  $v = Addr\ a\ stk' = []\ vs' = vs$  by simp-all  
 from  $bisim\ len'\ bsok$  have  $\tau reds1r\ P\ t\ h\ (ps',\ xs)\ (map\ Val\ vs,\ loc)$   
 by(auto intro: bisims1-Val- $\tau Red1r$  simp add: bsoks-def)  
 moreover from  $bisims1$ -map-Val-append[OF  $bisims1Nil\ vsps$ [symmetric], simplified, of  $P\ h\ loc$ ]  
 have  $P, obj \cdot M(ps), h \vdash (addr\ a \cdot M(map\ Val\ vs),\ loc) \leftrightarrow (rev\ vs\ @\ [Addr\ a],\ loc,\ length\ (compE2\ obj)$   
 $+ length\ (compEs2\ ps),\ None)$   
 by(rule  $bisim1$ -bisims1.bisim1CallParams)  
 ultimately show ?thesis by(fastforce elim!: Call- $\tau red1r$ -param)  
 qed  
 next  
 case  $bisim1BlockSome1$  thus ?case by simp  
 next  
 case  $bisim1BlockSome2$  thus ?case by simp  
 next  
 case ( $bisim1BlockSome4\ e\ n\ e'\ xs\ loc\ pc\ V\ T\ v$ )  
 note  $IH = \langle \lfloor pc < length\ (compE2\ e); compE2\ e ! pc = Invoke\ M\ (length\ vs); Suc\ n + max-vars\ e' \leq length\ xs; bsok\ e\ (Suc\ n) \rfloor \implies ?concl\ e'\ xs\ e\ (Suc\ V)\ pc\ stk'\ loc \rangle$   
 from  $\langle Suc\ (Suc\ pc) < length\ (compE2\ \{V:T=\lfloor v \rfloor; e\}) \rangle$  have  $pc < length\ (compE2\ e)$  by simp  
 moreover from  $\langle compE2\ \{V:T=\lfloor v \rfloor; e\} ! Suc\ (Suc\ pc) = Invoke\ M\ (length\ vs) \rangle$   
 have  $compE2\ e ! pc = Invoke\ M\ (length\ vs)$  by simp  
 moreover note  $len = \langle n + max-vars\ \{V:T=None; e'\} \leq length\ xs \rangle$   
 hence  $Suc\ n + max-vars\ e' \leq length\ xs$  by simp  
 moreover from  $\langle bsok\ \{V:T=\lfloor v \rfloor; e\}\ n \rangle$  have  $bsok\ e\ (Suc\ n)$  by simp  
 ultimately have ?concl  $e'\ xs\ e\ (Suc\ V)\ pc\ stk'\ loc$  by(rule  $IH$ )  
 then obtain  $e''\ xs'$  where  $red$ :  $\tau red1r\ P\ t\ h\ (e',\ xs)\ (e'',\ xs')$   
 and  $bisim'$ :  $P, e, h \vdash (e'',\ xs') \leftrightarrow (rev\ vs\ @\ Addr\ a\ \# \ stk',\ loc,\ pc,\ None)$   
 and  $call$ :  $call1\ e'' = \lfloor (a,\ M,\ vs) \rfloor$  by blast  
 from  $red$  have  $\tau red1r\ P\ t\ h\ (\{V:T=None; e'\},\ xs)\ (\{V:T=None; e''\},\ xs')$  by(rule  $Block$ -None- $\tau red1r$ -xt)  
 with  $bisim'$   $call$  show ?case by(fastforce intro:  $bisim1$ -bisims1.bisim1BlockSome4)  
 next  
 case ( $bisim1BlockNone\ e\ n\ e'\ xs\ loc\ pc\ V\ T$ )  
 note  $IH = \langle \lfloor pc < length\ (compE2\ e); compE2\ e ! pc = Invoke\ M\ (length\ vs); Suc\ n + max-vars\ e' \leq length\ xs; bsok\ e\ (Suc\ n) \rfloor \implies ?concl\ e'\ xs\ e\ (Suc\ V)\ pc\ stk'\ loc \rangle$   
 from  $\langle pc < length\ (compE2\ \{V:T=None; e\}) \rangle$  have  $pc < length\ (compE2\ e)$  by simp  
 moreover from  $\langle compE2\ \{V:T=None; e\} ! pc = Invoke\ M\ (length\ vs) \rangle$   
 have  $compE2\ e ! pc = Invoke\ M\ (length\ vs)$  by simp

**moreover note**  $\text{len} = \langle n + \text{max-vars } \{V:T=\text{None}; e'\} \leq \text{length } xs \rangle$   
**hence**  $\text{Suc } n + \text{max-vars } e' \leq \text{length } xs$  **by** *simp*  
**moreover from**  $\langle \text{bsok } \{V:T=\text{None}; e\} n \rangle$  **have**  $\text{bsok } e (\text{Suc } n)$  **by** *simp*  
**ultimately have**  $?concl\ e'\ xs\ e\ (\text{Suc } V)\ pc\ stk'\ loc$  **by** (rule *IH*)  
**then obtain**  $e''\ xs'$  **where**  $\text{red}: \tau\text{red1r } P\ t\ h\ (e', xs)\ (e'', xs')$   
**and**  $\text{bisim}': P, e, h \vdash (e'', xs') \leftrightarrow (\text{rev } vs\ @\ \text{Addr } a\ \# \text{stk}', loc, pc, \text{None})$   
**and**  $\text{call}: \text{call1 } e'' = \lfloor (a, M, vs) \rfloor$  **by** *blast*  
**from**  $\text{red}$  **have**  $\tau\text{red1r } P\ t\ h\ (\{V:T=\text{None}; e'\}, xs)\ (\{V:T=\text{None}; e''\}, xs')$  **by** (rule *Block-None- $\tau\text{red1r-xt}$* )  
**with**  $\text{bisim}'\ \text{call}$  **show**  $?case$  **by** (*fastforce intro: bisim1-bisims1.bisim1BlockNone*)  
**next**  
**case** *bisim1Sync1* **thus**  $?case$   
**apply** (*auto split: if-split-asm intro: bisim1-bisims1.bisim1Sync1 dest: bisim1-pc-length-compE2*  
*elim!: Sync- $\tau\text{red1r-xt}$* )  
**by** (*fastforce split: if-split-asm intro: bisim1-bisims1.bisim1Sync1 elim!: Sync- $\tau\text{red1r-xt}$* )  
**next**  
**case** *bisim1Sync2* **thus**  $?case$  **by** *simp*  
**next**  
**case** *bisim1Sync3* **thus**  $?case$  **by** *simp*  
**next**  
**case** *bisim1Sync4* **thus**  $?case$   
**apply** (*auto split: if-split-asm intro: bisim1-bisims1.bisim1Sync4 dest: bisim1-pc-length-compE2*  
*elim!: InSync- $\tau\text{red1r-xt}$* )  
**by** (*fastforce split: if-split-asm intro: bisim1-bisims1.bisim1Sync4 elim!: InSync- $\tau\text{red1r-xt}$* )  
**next**  
**case** *bisim1Sync5* **thus**  $?case$  **by** *simp*  
**next**  
**case** *bisim1Sync6* **thus**  $?case$  **by** *simp*  
**next**  
**case** *bisim1Sync7* **thus**  $?case$  **by** *simp*  
**next**  
**case** *bisim1Sync8* **thus**  $?case$  **by** *simp*  
**next**  
**case** *bisim1Sync9* **thus**  $?case$  **by** *simp*  
**next**  
**case** *bisim1InSync* **thus**  $?case$  **by** *simp*  
**next**  
**case** *bisim1Seq1* **thus**  $?case$   
**apply** (*auto split: if-split-asm intro: bisim1-bisims1.bisim1Seq1 dest: bisim1-pc-length-compE2 elim!:*  
*Seq- $\tau\text{red1r-xt}$* )  
**by** (*fastforce split: if-split-asm intro: bisim1-bisims1.bisim1Seq1 elim!: Seq- $\tau\text{red1r-xt}$* )  
**next**  
**case** *bisim1Seq2* **thus**  $?case$   
**by** (*auto split: if-split-asm intro: bisim1-bisims1.bisim1Seq2 dest: bisim1-pc-length-compE2*)  
**next**  
**case** *bisim1Cond1* **thus**  $?case$   
**apply** (*clarsimp split: if-split-asm*)  
**apply** (*fastforce intro!: exI intro: bisim1-bisims1.bisim1Cond1 elim!: Cond- $\tau\text{red1r-xt}$* )  
**by** (*fastforce dest: bisim1-pc-length-compE2*)  
**next**  
**case** *bisim1CondThen* **thus**  $?case$   
**apply** (*clarsimp split: if-split-asm*)  
**apply** (*fastforce intro!: exI intro: bisim1-bisims1.bisim1CondThen*)  
**by** (*fastforce dest: bisim1-pc-length-compE2*)  
**next**

```

case bisim1CondElse thus ?case
  by(clarsimp split: if-split-asm)(fastforce intro!: exI intro: bisim1-bisims1.bisim1CondElse)
next
  case bisim1While1 thus ?case by simp
next
  case bisim1While3 thus ?case
    apply(auto split: if-split-asm intro: bisim1-bisims1.bisim1While3 dest: bisim1-pc-length-compE2
elim!: Cond-τred1r-xt)
    by(fastforce split: if-split-asm intro: bisim1-bisims1.bisim1While3 elim!: Cond-τred1r-xt)
next
  case bisim1While4 thus ?case
    apply(auto split: if-split-asm intro: bisim1-bisims1.bisim1While4 dest: bisim1-pc-length-compE2
elim!: Seq-τred1r-xt)
    by(fastforce split: if-split-asm intro: bisim1-bisims1.bisim1While4 elim!: Seq-τred1r-xt)
next
  case bisim1While6 thus ?case by simp
next
  case bisim1While7 thus ?case by simp
next
  case bisim1Throw1 thus ?case
    by(fastforce split: if-split-asm intro: bisim1-bisims1.bisim1Throw1 dest: bisim1-pc-length-compE2
elim!: Throw-τred1r-xt)
next
  case bisim1Try thus ?case
    apply(auto split: if-split-asm intro: bisim1-bisims1.bisim1Try dest: bisim1-pc-length-compE2 elim!:
Try-τred1r-xt)
    by(fastforce split: if-split-asm intro: bisim1-bisims1.bisim1Try elim!: Try-τred1r-xt)
next
  case bisim1TryCatch1 thus ?case by simp
next
  case (bisim1TryCatch2 e n e' xs loc pc e1 C V)
    note IH =  $\langle \llbracket pc < \text{length}(\text{compE2 } e); \text{compE2 } e ! pc = \text{Invoke } M(\text{length } vs); \text{Suc } n + \text{max-vars } e' \leq \text{length } xs; \text{bsok } e(\text{Suc } n) \rrbracket \implies ?\text{concl } e' \text{ xs } e(\text{Suc } V) \text{ pc stk}' \text{ loc} \rangle$ 
    from  $\langle \text{Suc}(\text{Suc}(\text{length}(\text{compE2 } e1) + pc)) < \text{length}(\text{compE2}(\text{try } e1 \text{ catch}(C \text{ V}) e)) \rangle$ 
    have  $pc < \text{length}(\text{compE2 } e)$  by simp
    moreover from  $\langle \text{compE2}(\text{try } e1 \text{ catch}(C \text{ V}) e) ! \text{Suc}(\text{Suc}(\text{length}(\text{compE2 } e1) + pc)) = \text{Invoke } M(\text{length } vs) \rangle$ 
    have  $\text{compE2 } e ! pc = \text{Invoke } M(\text{length } vs)$  by simp
    moreover note  $\text{len} = \langle n + \text{max-vars } \{V:\text{Class } C=\text{None}; e'\} \leq \text{length } xs \rangle$ 
    hence  $\text{Suc } n + \text{max-vars } e' \leq \text{length } xs$  by simp
    moreover from  $\langle \text{bsok}(\text{try } e1 \text{ catch}(C \text{ V}) e) n \rangle$  have  $\text{bsok } e(\text{Suc } n)$  by simp
    ultimately have  $?concl e' \text{ xs } e(\text{Suc } V) \text{ pc stk}' \text{ loc}$  by (rule IH)
    then obtain  $e'' \text{ xs}'$  where  $\text{red}: \tau\text{red1r } P \text{ t h } (e', \text{xs}) (e'', \text{xs}')$ 
    and  $\text{bisim}' : P, e, h \vdash (e'', \text{xs}') \leftrightarrow (\text{rev } vs @ \text{Addr } a \# \text{stk}', \text{loc}, pc, \text{None})$ 
    and  $\text{call}: \text{call1 } e'' = \llbracket (a, M, vs) \rrbracket$  by blast
    from  $\text{red}$  have  $\tau\text{red1r } P \text{ t h } (\{V:\text{Class } C=\text{None}; e'\}, \text{xs}) (\{V:\text{Class } C=\text{None}; e''\}, \text{xs}')$ 
    by (rule Block-None-τred1r-xt)
    with  $\text{bisim}' \text{ call}$  show ?case by (fastforce intro: bisim1-bisims1.bisim1TryCatch2)
next
  case bisims1Nil thus ?case by simp
next
  case (bisims1List1 e n e' xs loc pc es)
    note IH =  $\langle \llbracket pc < \text{length}(\text{compE2 } e); \text{compE2 } e ! pc = \text{Invoke } M(\text{length } vs); n + \text{max-vars } e' \leq$ 

```

$length\ xs; bsok\ e\ n$   
 $\quad \quad \quad ] \implies ?concl\ e'\ xs\ e\ n\ pc\ stk'\ loc$   
**note**  $bisim = \langle P, e, h \vdash (e', xs) \leftrightarrow (rev\ vs\ @\ Addr\ a\ \# \ stk', loc, pc, None) \rangle$   
**note**  $len = \langle n + max-varss\ (e' \# es) \leq length\ xs \rangle$   
**hence**  $len': n + max-vars\ e' \leq length\ xs$  **by** *simp*  
**from**  $\langle bsoks\ (e \# es)\ n \rangle$  **have**  $bsok: bsok\ e\ n$  **by** *simp*  
**note**  $inv = \langle compEs2\ (e \# es) ! pc = Invoke\ M\ (length\ vs) \rangle$   
**with**  $\langle pc < length\ (compEs2\ (e \# es)) \rangle$  **bisim** **have**  $pc: pc < length\ (compE2\ e)$   
**by**  $(cases\ es)(auto\ split: if-split-asm\ dest: bisim1-pc-length-compE2)$   
**moreover with**  $inv$  **have**  $compE2\ e ! pc = Invoke\ M\ (length\ vs)$  **by** *simp*  
**ultimately have**  $?concl\ e'\ xs\ e\ n\ pc\ stk'\ loc$  **using**  $len'\ bsok$  **by**  $(rule\ IH)$   
**thus**  $?case$  **by**  $(fastforce\ intro: bisim1-bisims1.bisims1List1\ elim!: \tau red1r-inj-\tau reds1r)$   
**next**  
**case**  $(bisims1List2\ es\ n\ es'\ xs\ stk\ loc\ pc\ e\ v)$   
**note**  $IH = \langle \bigwedge stk'. [stk = rev\ vs\ @\ Addr\ a\ \# \ stk'; pc < length\ (compEs2\ es); compEs2\ es ! pc =$   
 $Invoke\ M\ (length\ vs); n + max-varss\ es' \leq length\ xs; bsoks\ es\ n$   
 $\quad \quad \quad ] \implies ?concls\ es'\ xs\ es\ n\ pc\ stk'\ loc \rangle$   
**note**  $bisim = \langle P, es, h \vdash (es', xs) [\leftrightarrow] (stk, loc, pc, None) \rangle$   
**note**  $len = \langle n + max-varss\ (Val\ v \# es') \leq length\ xs \rangle$   
**hence**  $len': n + max-varss\ es' \leq length\ xs$  **by** *simp*  
**from**  $\langle bsoks\ (e \# es)\ n \rangle$  **have**  $bsok: bsoks\ es\ n$  **by** *simp*  
**note**  $stk = \langle stk\ @\ [v] = rev\ vs\ @\ Addr\ a\ \# \ stk' \rangle$   
**note**  $inv = \langle compEs2\ (e \# es) ! (length\ (compE2\ e) + pc) = Invoke\ M\ (length\ vs) \rangle$   
**from**  $\langle length\ (compE2\ e) + pc < length\ (compEs2\ (e \# es)) \rangle$  **have**  $pc: pc < length\ (compEs2\ es)$   
**by** *auto*  
**moreover with**  $inv$  **have**  $compEs2\ es ! pc = Invoke\ M\ (length\ vs)$  **by** *simp*  
**moreover with**  $bisim\ pc$   
**obtain**  $vs''\ v''\ stk''$  **where**  $stk = vs''\ @\ v''\ \# \ stk''$  **and**  $length\ vs'' = length\ vs$   
**by**  $(auto\ dest!: bisims1-Invoke-stkD)$   
**with**  $\langle stk\ @\ [v] = rev\ vs\ @\ Addr\ a\ \# \ stk' \rangle$  **obtain**  $stk'''$   
**where**  $stk''': stk = rev\ vs\ @\ Addr\ a\ \# \ stk'''$  **and**  $stk: stk' = stk''' @ [v]$   
**by**  $(cases\ stk'\ rule: rev-cases)\ auto$   
**note**  $len'\ stk'''$   
**ultimately have**  $?concls\ es'\ xs\ es\ n\ pc\ stk''' loc$  **using**  $bsok$  **by**  $-(rule\ IH)$   
**then obtain**  $es''\ xs'$  **where**  $red: \tau reds1r\ P\ t\ h\ (es', xs)\ (es'', xs')$   
**and**  $call: calls1\ es'' = [(a, M, vs)]$   
**and**  $bisim: P, es, h \vdash (es'', xs') [\leftrightarrow] (rev\ vs\ @\ Addr\ a\ \# \ stk''', loc, pc, None)$  **by** *blast*  
**from**  $bisim$  **have**  $P, e \# es, h \vdash (Val\ v \# es'', xs') [\leftrightarrow]$   
 $((rev\ vs\ @\ Addr\ a\ \# \ stk''') @ [v], loc, length\ (compE2\ e) + pc, None)$   
**by**  $(rule\ bisim1-bisims1.bisims1List2)$   
**moreover from**  $red$  **have**  $\tau reds1r\ P\ t\ h\ (Val\ v \# es', xs)\ (Val\ v \# es'', xs')$  **by**  $(rule\ \tau reds1r-cons-\tau reds1r)$   
**ultimately show**  $?case$  **using**  $stk\ call$  **by** *fastforce*  
**qed**  
**end**  
  
**declare** *split-beta* [*simp del*]  
  
**context** *J1-JVM-conf-read* **begin**  
  
**lemma**  $\tau Red1-simulates-exec-1-\tau$ :  
**assumes**  $wf: wf\ J1-prog\ P$   
**and**  $exec: exec-1-d\ (compP2\ P)\ t\ (Normal\ (xcp, h, frs))\ ta\ (Normal\ (xcp', h', frs'))$   
**and**  $bisim: bisim1-list1\ t\ h\ (e, xs)\ exs\ xcp\ frs$

```

and  $\tau$ :  $\tau\text{Move2}$  ( $\text{compP2 } P$ ) ( $xcp, h, frs$ )
shows  $h = h' \wedge (\exists e' xs' exs'. (\text{if sim21-size } (\text{compP2 } P) (xcp', frs') (xcp, frs) \text{ then } \tau\text{Red1r else } \tau\text{Red1t}) P \text{ t h } ((e, xs), exs) ((e', xs'), exs') \wedge \text{bisim1-list1 t h } (e', xs') exs' xcp' frs')$ 
using bisim
proof(cases)
  case (bl1-Normal stk loc C M pc FRS Ts T body D)
  hence [simp]:  $frs = (stk, loc, C, M, pc) \# FRS$ 
  and conf:  $\text{compTP } P \vdash t: (xcp, h, (stk, loc, C, M, pc) \# FRS) \checkmark$ 
  and sees:  $P \vdash C \text{ sees } M: Ts \rightarrow T = \lfloor \text{body} \rfloor \text{ in } D$ 
  and bisim:  $P, \text{blocks1 } 0 \text{ (Class } D \# Ts) \text{ body}, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp)$ 
  and lenxs:  $\text{max-vars } e \leq \text{length } xs$ 
  and bisims:  $\text{list-all2 } (\text{bisim1-fr } P h) \text{ exs } FRS \text{ by auto}$ 
  from sees-method-compP[OF sees, where  $f = \lambda C M Ts T. \text{compMb2}$ ]
  have sees':  $\text{compP2 } P \vdash C \text{ sees } M: Ts \rightarrow T = \lfloor (\text{max-stack body}, \text{max-vars body}, \text{compE2 body } @ [\text{Return}], \text{compxE2 body } 0 \ 0) \rfloor \text{ in } D$ 
  by(simp add: compP2-def compMb2-def)
  from bisim have pc:  $pc \leq \text{length } (\text{compE2 body})$  by(auto dest: bisim1-pc-length-compE2)
  from conf have hconf:  $hconf \ h \text{ preallocated } h$  by(simp-all add: correct-state-def)

  from sees wf have bsok:  $bsok \ (\text{blocks1 } 0 \text{ (Class } D \# Ts) \text{ body}) \ 0$ 
  by(auto dest!: sees-wf-mdecl simp add: bsok-def wf-mdecl-def WT1-expr-locks)

  from exec obtain check:  $\text{check } (\text{compP2 } P) (xcp, h, frs)$ 
  and exec:  $\text{compP2 } P, t \vdash (xcp, h, frs) \text{ --ta-jvm--> } (xcp', h', frs')$ 
  by(rule jvmd-NormalE)(auto simp add: exec-1-iff)

  from wt-compTP-compP2[OF wf] exec conf
  have conf':  $\text{compTP } P \vdash t: (xcp', h', frs') \checkmark$  by(auto intro: BV-correct-1)

  from conf have tconf:  $P, h \vdash t \checkmark$  unfolding correct-state-def
  by(simp add: compP2-def tconf-def)

  show ?thesis
  proof(cases xcp)
    case [simp]: None
    from exec have execi:  $(ta, xcp', h', frs') \in \text{exec-instr } (\text{instrs-of } (\text{compP2 } P) \ C \ M \ ! \ pc) \ (\text{compP2 } P) \ t \ h \ stk \ loc \ C \ M \ pc \ FRS$ 
    by(simp add: exec-1-iff)
    show ?thesis
    proof(cases pc < length (compE2 body))
      case True
      with execi sees' have execi:  $(ta, xcp', h', frs') \in \text{exec-instr } (\text{compE2 body } ! \ pc) \ (\text{compP2 } P) \ t \ h \ stk \ loc \ C \ M \ pc \ FRS$ 
      by(simp)
      from  $\tau \text{ sees' } True$  have  $\tau i$ :  $\tau\text{move2 } (\text{compP2 } P) \ h \ stk \ body \ pc \ None$  by(simp add: \tau\text{move2-iff})

      show ?thesis
      proof(cases length frs' = Suc (length FRS))
        case False
        with execi sees True compE2-not-Return[of body]
        have  $(\exists M \ n. \text{compE2 body } ! \ pc = \text{Invoke } M \ n)$ 
        apply(cases compE2 body ! pc)
        apply(auto split: if-split-asm sum.split-asm simp add: split-beta compP2-def compMb2-def)
        apply(metis in-set-conv-nth)+

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done
then obtain MM n where ins: compE2 body ! pc = Invoke MM n by blast
with bisim1-Invoke-stkD[OF bisim[unfolded None], of MM n] True obtain vs' v' stk'
  where [simp]: stk = vs' @ v' # stk' n = length vs' by auto
from check sees True ins have is-Ref v'
  by(auto split: if-split-asm simp add: split-beta compP2-def compMb2-def check-def)
moreover from execi sees True ins False sees' have v' ≠ Null by auto
ultimately obtain a' where [simp]: v' = Addr a' by(auto simp add: is-Ref-def)
from bisim have Bisim': P,blocks1 0 (Class D#Ts) body,h ⊢ (e, xs) ↔ (rev (rev vs') @ Addr
a' # stk', loc, pc, None)
  by simp
from bisim1-Invoke-τRed[OF this - - bsok, of MM t] True ins lenxs
obtain e' xs' where red: τred1r P t h (e, xs) (e', xs')
  and bisim': P,blocks1 0 (Class D#Ts) body,h ⊢ (e', xs') ↔ (rev (rev vs') @ Addr a' # stk',
loc, pc, None)
  and call': call1 e' = [(a', MM, rev vs')] by auto
from red have Red: τRed1r P t h ((e, xs), exs) ((e', xs'), exs)
  by(rule τred1r-into-τRed1r)

from False execi True check ins sees' obtain U' Ts' T' meth D'
  where ha': typeof-addr h a' = [U']
  and Sees': compP2 P ⊢ class-type-of U' sees MM:Ts' → T' = [meth] in D'
by(auto simp add: check-def has-method-def split: if-split-asm)(auto split: extCallRet.split-asm)
from sees-method-compPD[OF Sees'[unfolded compP2-def]] obtain body'
  where Sees: P ⊢ class-type-of U' sees MM:Ts' → T'=[body'] in D'
  and [simp]: meth = (max-stack body', max-vars body', compE2 body' @ [Return], compxE2
body' 0 0)
  by(auto simp add: compMb2-def)

let ?e = blocks1 0 (Class D#Ts') body'
let ?xs = Addr a' # rev vs' @ replicate (max-vars body') undefined-value
let ?e'xs' = (e', xs')
let ?f = (stk, loc, C, M, pc)
let ?f' = ([],Addr a' # rev vs' @ replicate (max-vars body') undefined-value, D', MM, 0)

from execi pc ins False ha' Sees' sees'
have [simp]: xcp' = None ta = ε frs' = ?f' # ?f # FRS h' = h
  by(auto split: if-split-asm simp add: split-beta)

from bisim' have bisim'': P,blocks1 0 (Class D#Ts) body,h ⊢ (e', xs') ↔ (rev (rev vs') @ Addr
a' # stk', loc, pc, None)
  by simp
have n = length vs' by simp
from conf' Sees' ins sees' True have n = length Ts'
  apply(auto simp add: correct-state-def)
  apply(drule (1) sees-method-fun)+
  apply(auto dest: sees-method-idemp sees-method-fun)
done
with ⟨n = length vs'⟩ have vs'Ts': length (rev vs') = length Ts' by simp

with call' ha' Sees
have True,P,t ⊢ 1 ⟨(e', xs')/exs,h⟩ -ε→ ⟨(e, ?xs)/ (e', xs') # exs, h⟩ by(rule red1Call)
hence True,P,t ⊢ 1 ⟨(e', xs')/exs,h⟩ -ε→ ⟨(e, ?xs)/ ?e'xs' # exs, h⟩ by(simp)
moreover from call' Sees ha' have τMove1 P h ((e', xs'), exs)

```

by(auto simp add: synthesized-call-def dest!:  $\tau$ move1-not-call1[where  $P=P$  and  $h=h$ ] dest:  
 sees-method-fun)  
 ultimately have  $\tau$ Red1t  $P$  t h  $((e', xs'), exs) ((?e, ?xs), ?e'xs' \# exs)$  by auto  
 moreover have bisim1-list1 t h  $(?e, ?xs) (?e'xs' \# exs)$  None  $(?f' \# ?f \# FRS)$   
 proof  
 from conf' show compTP  $P \vdash t:(None, h, ?f' \# ?f \# FRS) \checkmark$  by simp  
 from Sees show  $P \vdash D'$  sees MM:  $Ts' \rightarrow T' = [body']$  in  $D'$  by(rule sees-method-idemp)  
 show  $P, blocks1\ 0\ (Class\ D' \# Ts')\ body', h \vdash (blocks1\ 0\ (Class\ D' \# Ts')\ body', ?xs) \leftrightarrow ([], Addr\ a' \# rev\ vs' @ replicate\ (max-vars\ body')\ undefined-value, 0, None)$   
 by(rule bisim1-refl)  
 show  $max-vars\ (blocks1\ 0\ (Class\ D' \# Ts')\ body') \leq length\ ?xs$  using  $vs'Ts'$  by(simp add:  
 blocks1-max-vars)  
 from sees have bisim1-fr  $P\ h\ ?e'xs'\ ?f$   
 proof  
 show  $P, blocks1\ 0\ (Class\ D \# Ts)\ body, h \vdash (e', xs') \leftrightarrow (stk, loc, pc, None)$   
 using bisim'' by simp  
 from call' show call1  $e' = \lfloor (a', MM, rev\ vs') \rfloor$ .  
 from red have  $xs'xs: length\ xs' = length\ xs$  by(rule  $\tau$ red1r-preserves-len)  
 with red lenxs show  $max-vars\ e' \leq length\ xs'$  by(auto dest:  $\tau$ red1r-max-vars)  
 qed  
 with bisims show list-all2  $(bisim1-fr\ P\ h)\ (?e'xs' \# exs)\ (?f \# FRS)$  by simp  
 qed  
 ultimately show ?thesis using Red  
 by auto(blast intro: rtranclp-trans rtranclp-tranclp-tranclp tranclp-into-rtranclp)+  
 next  
 case True  
 note  $pc = \langle pc < length\ (compE2\ body) \rangle$   
 with execi True have  $\exists\ stk' loc' pc'. frs' = (stk', loc', C, M, pc') \# FRS$   
 by(cases  $(compE2\ body @ [Return]) ! pc$ )(auto split: if-split-asm sum.split-asm simp: split-beta,  
 auto split: extCallRet.splits)  
 then obtain  $stk' loc' pc'$  where  $[simp]: frs' = (stk', loc', C, M, pc') \# FRS$  by blast  
 from conf obtain  $ST$  where  $compP2\ P, h \vdash stk [: \leq] ST$  by(auto simp add: correct-state-def  
 conf-f-def2)  
 hence  $ST: P, h \vdash stk [: \leq] ST$  by(rule List.list-all2-mono)(simp add: compP2-def)  
 from execi sees pc check  
 have  $exec': exec-move-d\ P\ t\ (blocks1\ 0\ (Class\ D \# Ts)\ body)\ h\ (stk, loc, pc, xcp)\ ta\ h' (stk', loc',$   
 $pc', xcp')$   
 apply(auto simp add: compP2-def compMb2-def exec-move-def check-def exec-meth-instr split:  
 if-split-asm sum.split-asm)  
 apply(cases  $compE2\ body ! pc$ )  
 apply(auto simp add: neq-Nil-conv split-beta split: if-split-asm sum.split-asm)  
 apply(force split: extCallRet.split-asm)  
 apply(cases  $compE2\ body ! pc, auto\ simp\ add: split-beta\ neq-Nil-conv\ split: if-split-asm$   
 sum.split-asm)  
 done  
 from red1-simulates-exec-instr[OF wf hconf tconf bisim this - bsok ST] lenxs  $\tau i$  obtain  $e'' xs''$   
 where  $bisim': P, blocks1\ 0\ (Class\ D \# Ts)\ body, h' \vdash (e'', xs'') \leftrightarrow (stk', loc', pc', xcp')$   
 and red:  $(if\ xcp' = None \longrightarrow pc < pc' \text{ then } \tau red1r \text{ else } \tau red1t)\ P\ t\ h\ (e, xs)\ (e'', xs'')$  and  
 $[simp]: h' = h$   
 by(auto simp del: blocks1.simps)  
 have Red:  $(if\ sim21-size\ (compP2\ P)\ (xcp', frs')\ (xcp, frs) \text{ then } \tau Red1r \text{ else } \tau Red1t)\ P\ t\ h\ ((e,$   
 $xs), exs)\ ((e'', xs''), exs)$   
 proof(cases  $xcp' = None \longrightarrow pc < pc'$ )  
 case True

```

from bisim bisim' have  $pc \leq \text{Suc} (\text{length} (\text{compE2 } \text{body}))$   $pc' \leq \text{Suc} (\text{length} (\text{compE2 } \text{body}))$ 
  by(auto dest: bisim1-pc-length-compE2)
moreover {
  fix a assume  $xcp' = \lfloor a \rfloor$ 
  with exec' have  $pc = pc'$  by(auto dest: exec-move-raise-xcp-pcD) }
ultimately have sim21-size (compP2 P) (xcp', frs') (xcp, frs) using sees True
  by(auto simp add: sim21-size-def)(auto simp add: compP2-def compMb2-def intro!:
sim21-size-aux.intros)
  with red True show ?thesis by simp(rule  $\tau\text{red1r-into-}\tau\text{Red1r}$ )
next
  case False
  thus ?thesis using red by(auto intro:  $\tau\text{red1t-into-}\tau\text{Red1t}$   $\tau\text{red1r-into-}\tau\text{Red1r}$ )
qed
moreover from red lenxs
have max-vars  $e'' \leq \text{length } xs''$ 
  apply(auto dest:  $\tau\text{red1r-max-vars}$   $\tau\text{red1r-preserves-len}$   $\tau\text{red1t-max-vars}$   $\tau\text{red1t-preserves-len}$ 
split: if-split-asm)
  apply(frule  $\tau\text{red1r-max-vars}$   $\tau\text{red1t-max-vars}$ , drule  $\tau\text{red1r-preserves-len}$   $\tau\text{red1t-preserves-len}$ ,
simp) +
  done
with conf' sees bisim'
have bisim1-list1 t h ( $e'', xs''$ ) exs xcp' ( $(stk', loc', C, M, pc') \# FRS$ )
  unfolding  $\langle frs' = (stk', loc', C, M, pc') \# FRS \rangle \langle h' = h \rangle$ 
  using bisims by(rule bisim1-list1.bl1-Normal)
ultimately show ?thesis by(auto split del: if-split)
qed
next
  case False
  with pc have [simp]:  $pc = \text{length} (\text{compE2 } \text{body})$  by simp
  with execi sees have [simp]:  $xcp' = \text{None}$ 
  by(cases compE2 body ! pc)(auto split: if-split-asm simp add: compP2-def compMb2-def split-beta)
  from bisim have Bisim: P, blocks1 0 (Class D#Ts) body, h  $\vdash (e, xs) \leftrightarrow (stk, loc, \text{length} (\text{compE2}$ 
(blocks1 0 (Class D#Ts) body)), None) by simp
  then obtain v where [simp]:  $stk = [v]$  by(blast dest: bisim1-pc-length-compE2D)
  with Bisim lenxs bsok have red:  $\tau\text{red1r } P t h (e, xs) (Val v, loc)$ 
  by clarify (erule bisim1-Val- $\tau\text{red1r}$ [where n=0], simp-all add: bsok-def)
  hence Red:  $\tau\text{Red1r } P t h ((e, xs), exs) ((Val v, loc), exs)$  by(rule  $\tau\text{red1r-into-}\tau\text{Red1r}$ )
  show ?thesis
  proof(cases FRS)
  case [simp]: Nil
  with bisims have [simp]:  $exs = []$  by simp
  with exec sees' have [simp]:  $ta = \varepsilon$   $xcp' = \text{None}$   $h' = h$   $frs' = []$ 
  by(auto simp add: exec-1-iff)
  from hconf have bisim1-list1 t h ( $(Val v, loc) [] \text{None} []$ ) by(rule bl1-finalVal)
  then show ?thesis using Red
  by(auto intro: rtranclp.rtrancl-into-rtrancl rtranclp-into-tranclp1 simp del:  $\tau\text{Red1-conv}$  simp
add: sim21-size-def)
next
  case (Cons f' FRS')
  then obtain  $stk'' loc'' C'' M'' pc''$ 
  where [simp]:  $FRS = (stk'', loc'', C'', M'', pc'') \# FRS'$  by(cases f') fastforce
  from bisims obtain  $e'' xs'' EXS'$  where [simp]:  $exs = (e'', xs'') \# EXS'$ 
  by(auto simp add: list-all2-Cons2)
  with bisims have bisim1-fr P h ( $e'', xs''$ ) ( $stk'', loc'', C'', M'', pc''$ ) by simp

```



then obtain  $E'' \text{ Ts}'' T'' \text{ body}'' D'' a'' M''' \text{ vs}''$   
 where  $[simp]: e'' = E''$   
 and  $\text{sees}'': P \vdash C'' \text{ sees } M'': \text{Ts}'' \rightarrow T'' = \lfloor \text{body}'' \rfloor \text{ in } D''$   
 and  $\text{bisim}'': P, \text{blocks1 } 0 \text{ (Class } D'' \# \text{Ts}'') \text{ body}'', h \vdash (E'', \text{xs}'') \leftrightarrow (\text{stk}'', \text{loc}'', \text{pc}'', \text{None})$   
 and  $\text{call}'': \text{call1 } E'' = \lfloor (a'', M''', \text{vs}'') \rfloor$   
 and  $\text{lenxs}'': \text{max-vars } E'' \leq \text{length } \text{xs}''$   
 by(cases) fastforce  
 let  $?ee' = \text{inline-call } (\text{Val } v) E''$   
 let  $?e' = ?ee'$   
 let  $?xs' = \text{xs}''$   
  
 from  $\text{bisim}'' \text{ call}''$  have  $\text{pc}'': \text{pc}'' < \text{length } (\text{compE2 } (\text{blocks1 } 0 \text{ (Class } D'' \# \text{Ts}'') \text{ body}''))$   
 by(rule bisim1-call-pcD)  
 hence  $\text{pc}'': \text{pc}'' < \text{length } (\text{compE2 } \text{body}'')$  by simp  
 with  $\text{sees-method-compP}[OF \text{sees}'', \text{where } f = \lambda C M \text{ Ts } T. \text{compMb2}]$   
 $\text{sees-method-compP}[OF \text{sees}, \text{where } f = \lambda C M \text{ Ts } T. \text{compMb2}] \text{ conf}$   
 obtain  $ST \text{ LT}$  where  $\Phi: \text{compTP } P C'' M'' ! \text{pc}'' = \lfloor (ST, LT) \rfloor$   
 and  $\text{conf}'': \text{conf-f } (\text{compP } (\lambda C M \text{ Ts } T. \text{compMb2}) P) h (ST, LT) (\text{compE2 } \text{body}'' @ [\text{Return}])$   
 $(\text{stk}'', \text{loc}'', C'', M'', \text{pc}'')$   
 and  $\text{ins}: (\text{compE2 } \text{body}'' @ [\text{Return}]) ! \text{pc}'' = \text{Invoke } M (\text{length } \text{Ts})$   
 unfolding correct-state-def by(fastforce simp add: compP2-def compMb2-def dest: sees-method-fun)  
 from  $\text{bisim1-callD}[OF \text{bisim}'' \text{ call}'', \text{of } M \text{ length } \text{Ts}] \text{ ins } \text{pc}''$   
 have  $[simp]: M''' = M$  by simp  
  
 from  $\text{call}''$  have  $\text{call1 } E'' = \lfloor (a'', M''', \text{vs}'') \rfloor$  by simp  
 have  $\text{True}, P, t \vdash 1 \langle (\text{Val } v, \text{loc}) / (E'', \text{xs}'') \# \text{EXS}', h \rangle -\varepsilon \rightarrow$   
 $\langle (\text{inline-call } (\text{Val } v) E'', \text{xs}'') / \text{EXS}', h \rangle$   
 by(rule red1Return) simp  
 hence  $\text{True}, P, t \vdash 1 \langle (\text{Val } v, \text{loc}) / (E'', \text{xs}'') \# \text{EXS}', h \rangle -\varepsilon \rightarrow \langle (?e', ?xs') / \text{EXS}', h \rangle$   
 by simp  
 moreover have  $\tau \text{Move1 } P h ((\text{Val } v, \text{loc}), (E'', \text{xs}'') \# \text{EXS}')$  by auto  
 ultimately have  $\tau \text{Red1 } P t h ((\text{Val } v, \text{loc}), (E'', \text{xs}'') \# \text{EXS}') ((?e', ?xs'), \text{EXS}')$  by auto  
 moreover from  $\text{exec sees}$  have  $[simp]: \text{ta} = \varepsilon \text{ h}' = h$   
 and  $[simp]: \text{frs}' = (v \# \text{drop } (\text{length } \text{Ts} + 1) \text{stk}'', \text{loc}'', C'', M'', \text{pc}'' + 1) \# \text{FRS}'$   
 by(auto simp add: compP2-def compMb2-def exec-1-iff)  
  
 have  $\text{bisim1-list1 } t h (?e', ?xs') \text{EXS}' \text{None } ((v \# \text{drop } (\text{length } \text{Ts} + 1) \text{stk}'', \text{loc}'', C'', M'', \text{pc}'' + 1) \# \text{FRS}')$   
 proof  
 from  $\text{conf}'$  show  $\text{compTP } P \vdash t: (\text{None}, h, (v \# \text{drop } (\text{length } \text{Ts} + 1) \text{stk}'', \text{loc}'', C'', M'', \text{pc}'' + 1) \# \text{FRS}') \checkmark$  by simp  
 from  $\text{sees}''$  show  $P \vdash C'' \text{ sees } M'': \text{Ts}'' \rightarrow T'' = \lfloor \text{body}'' \rfloor \text{ in } D''$ .  
 from  $\text{bisim1-inline-call-Val}[OF \text{bisim}'' \text{ call}'', \text{of } \text{length } \text{Ts } v] \text{ ins } \text{pc}''$   
 show  $P, \text{blocks1 } 0 \text{ (Class } D'' \# \text{Ts}'') \text{ body}'', h \vdash (\text{inline-call } (\text{Val } v) E'', \text{xs}'') \leftrightarrow (v \# \text{drop } (\text{length } \text{Ts} + 1) \text{stk}'', \text{loc}'', \text{pc}'' + 1, \text{None})$  by simp  
 from  $\text{lenxs}'' \text{ max-vars-inline-call}[of \text{Val } v E'']$   
 show  $\text{max-vars } (\text{inline-call } (\text{Val } v) E'') \leq \text{length } \text{xs}''$  by simp  
 from  $\text{bisims}$  show  $\text{list-all2 } (\text{bisim1-fr } P h) \text{EXS}' \text{FRS}'$  by simp  
 qed  
 ultimately show  $?thesis$  using Red  
 by(auto simp del:  $\tau \text{Red1-conv}$  intro:  $\text{rtranclp-into-tranclp1 rtranclp.rtrancl-into-rtrancl}$ )  
 qed  
 qed  
 next

```

case [simp]: (Some a')
from exec have execs: (xcp', h', frs') = exception-step (compP2 P) a' h (stk, loc, C, M, pc) FRS
and [simp]: ta = ε by(auto simp add: exec-1-iff)
from conf have confxcp': conf-xcp' P h xcp
unfolding correct-state-def by(auto simp add: compP2-def)
then obtain D' where ha': typeof-addr h a' = [Class-type D'] and subclsD': P ⊢ D' ⋚* Throwable
by auto
from bisim have pc: pc < length (compE2 body) by(auto dest: bisim1-xcp-pcD)
show ?thesis
proof(cases match-ex-table (compP2 P) (cname-of h a') pc (ex-table-of (compP2 P) C M))
case None
from bisim have pc: pc < length (compE2 body) by(auto dest: bisim1-xcp-pcD)
with sees' None have match: match-ex-table (compP2 P) (cname-of h a') pc (compxE2 body 0) = None
by(auto)
with execs sees' have [simp]: ta = ε xcp' = [a'] h' = h frs' = FRS using match sees' by auto
from conf obtain CCC where ha: typeof-addr h a' = [Class-type CCC] and subcls: P ⊢ CCC
⋚* Throwable
unfolding correct-state-def by(auto simp add: conf-f-def2 compP2-def)
from bisim1-xcp-τRed[OF ha subcls bisim[unfolded Some], of λC M Ts T. compMb2] match lenxs
bsok
have red: τred1r P t h (e, xs) (Throw a', loc)
and b': P, blocks1 0 (Class D'#Ts) body, h ⊢ (Throw a', loc) ↔ (stk, loc, pc, [a'])
by(auto simp add: compP2-def bsok-def)
from red have Red: τRed1r P t h ((e, xs), exs) ((Throw a', loc), exs)
by(rule τred1r-into-τRed1r)
show ?thesis
proof(cases FRS)
case (Cons f' FRS')
then obtain stk'' loc'' C'' M'' pc''
where [simp]: FRS = (stk'', loc'', C'', M'', pc'') # FRS' by(cases f') fastforce
from bisims obtain e'' xs'' EXS' where [simp]: exs = (e'', xs'') # EXS'
by(auto simp add: list-all2-Cons2)
with bisims have bisim1-fr P h (e'', xs'') (stk'', loc'', C'', M'', pc'') by simp
then obtain E'' Ts'' T'' body'' D'' a'' M''' vs''
where [simp]: e'' = E''
and sees'': P ⊢ C'' sees M'': Ts'' → T'' = [body''] in D''
and bisim'': P, blocks1 0 (Class D''#Ts'') body'', h ⊢ (E'', xs'') ↔ (stk'', loc'', pc'', None)
and call'': call1 E'' = [(a'', M''', vs'')]
and lenxs'': max-vars E'' ≤ length xs''
by(cases) fastforce
let ?ee' = inline-call (Throw a') E''
let ?e' = ?ee'
let ?xs' = xs''

from bisim'' call'' have pc'': pc'' < length (compE2 (blocks1 0 (Class D''#Ts'') body''))
by(rule bisim1-call-pcD)
hence pc'': pc'' < length (compE2 body'') by simp
with sees-method-compP[OF sees'', where f=λC M Ts T. compMb2]
sees-method-compP[OF sees, where f=λC M Ts T. compMb2] conf
obtain ST LT where Φ: compTP P C'' M'' ! pc'' = [(ST, LT)]
and conf': conf-f (compP (λC M Ts T. compMb2) P) h (ST, LT) (compE2 body'' @ [Return])
(stk'', loc'', C'', M'', pc'')
and ins: (compE2 body'' @ [Return]) ! pc'' = Invoke M (length Ts)

```

```

unfolding correct-state-def
  by(fastforce simp add: compP2-def compMb2-def dest: sees-method-fun)
from bisim1-callD[OF bisim'' call'', of M length Ts] ins pc''
have [simp]:  $M''' = M$  by simp

  have  $\text{True}, P, t \vdash 1 \langle (\text{Throw } a', \text{loc}) / (E'', xs'') \# \text{EXS}', h \rangle \rightarrow \langle (\text{inline-call } (\text{Throw } a') E'', xs'') / \text{EXS}', h \rangle$ 
    by(rule red1Return) simp
  moreover have  $\tau \text{Move1 } P h ((\text{Throw } a', \text{loc}), (E'', xs'') \# \text{EXS}') \text{ by fastforce}$ 
  ultimately have  $\tau \text{Red1 } P t h ((\text{Throw } a', \text{loc}), (E'', xs'') \# \text{EXS}') ((?e', ?xs'), \text{EXS}')$  by simp
  moreover
  have bisim1-list1 t h (?e', ?xs') EXS' [a'] ((stk'', loc'', C'', M'', pc'') # FRS')
  proof
    from conf' show  $\text{compTP } P \vdash t: ([a'], h, (\text{stk}'', \text{loc}'', C'', M'', \text{pc}'') \# \text{FRS}') \checkmark$  by simp
    from sees'' show  $P \vdash C'' \text{ sees } M'': Ts'' \rightarrow T'' = [\text{body}''] \text{ in } D''$  .
    from bisim1-inline-call-Throw[OF bisim'' call'', of length Ts a'] ins pc''
    show  $P, \text{blocks1 } 0 (\text{Class } D'' \# Ts'') \text{ body}'', h \vdash (\text{inline-call } (\text{Throw } a') E'', xs'') \leftrightarrow (\text{stk}'', \text{loc}'', \text{pc}'', [a'])$ 
    by simp
    from lenxs'' max-vars-inline-call[of Throw a' E'']
    show  $\text{max-vars } (\text{inline-call } (\text{Throw } a') E'') \leq \text{length } xs''$  by simp
    from bisims show list-all2 (bisim1-fr P h) EXS' FRS' by simp
  qed
  ultimately show ?thesis using Red
    by(auto simp del:  $\tau \text{Red1-conv}$  intro: rtranclp.rtrancl-into-rtrancl rtranclp-into-tranclp1)
next
  case [simp]: Nil
  with bisims have [simp]:  $\text{exs} = []$  by simp
  from hconf have bisim1-list1 t h (Throw a', loc) [] [a'] [] by(rule bl1-finalThrow)
  thus ?thesis using Red
    by(auto simp del:  $\tau \text{Red1-conv}$  intro: rtranclp.rtrancl-into-rtrancl rtranclp-into-tranclp1 simp
add: sim21-size-def)
  qed
next
  case (Some pcd)
  then obtain pch d where match: match-ex-table (compP2 P) (cname-of h a') pc (ex-table-of (compP2 P) C M) = [(pch, d)]
    by(cases pcd) auto
  with  $\tau \text{sees}' pc$  have  $\tau': \tau \text{move2 } (\text{compP2 } P) h \text{ stk body pc } [a']$  by(simp add: compP2-def compMb2-def  $\tau \text{move2-iff}$ )
  from match execs have [simp]:  $h' = h \text{ xcp}' = \text{None}$ 
     $\text{frs}' = (\text{Addr } a' \# \text{drop } (\text{length } \text{stk} - d) \text{ stk}, \text{loc}, C, M, \text{pch}) \# \text{FRS}$  by simp-all
  from bisim match sees'
  have  $d \leq \text{length } \text{stk}$  by(auto intro: bisim1-match-Some-stk-length simp add: compP2-def compMb2-def)
  with match sees'
  have  $\text{execm}: \text{exec-move-d } P t (\text{blocks1 } 0 (\text{Class } D \# Ts) \text{ body}) h (\text{stk}, \text{loc}, \text{pc}, [a']) \text{ ta } h' (\text{Addr } a' \# \text{drop } (\text{length } \text{stk} - d) \text{ stk}, \text{loc}, \text{pch}, \text{None})$ 
    by(auto simp add: exec-move-def exec-meth-xcpt)
  from conf obtain ST where  $\text{compP2 } P, h \vdash \text{stk} [: \leq] ST$  by(auto simp add: correct-state-def conf-f-def2)
  hence  $ST: P, h \vdash \text{stk} [: \leq] ST$  by(rule List.list-all2-mono)(simp add: compP2-def)
  from red1-simulates-exec-instr[OF wf hconf tconf bisim[unfolded  $\langle \text{xcp} = [a'] \rangle$  execm - bsok ST]
  lenxs ha' subclsD'  $\tau'$ 
  obtain  $e'' xs''$ 

```

**where**  $b': P, \text{blocks1 } 0 \text{ (Class } D \# Ts) \text{ body}, h \vdash (e'', xs'') \leftrightarrow (\text{Addr } a' \# \text{drop (length stk - d) stk, loc, pch, None})$   
**and**  $\text{red: (if } pc < pch \text{ then } \tau\text{red1r} \text{ else } \tau\text{red1t}) P t h (e, xs) (e'', xs'')$  **and**  $[\text{simp}]: h' = h$   
**by**  $(\text{auto split: if-split-asm intro: } \tau\text{move2xcp simp add: compP2-def simp del: blocks1.simps})$   
**have**  $\text{Red: (if sim21-size (compP2 P) (xcp', frs') (xcp, frs) then } \tau\text{Red1r} \text{ else } \tau\text{Red1t}) P t h ((e, xs), \text{exs}) ((e'', xs''), \text{exs})$   
**proof**  $(\text{cases } pc < pch)$   
**case**  $\text{True}$   
**from**  $\text{bisim } b' \text{ have } pc \leq \text{Suc (length (compE2 body)) } pch \leq \text{Suc (length (compE2 body))}$   
**by**  $(\text{auto dest: bisim1-pc-length-compE2})$   
**with**  $\text{sees True have sim21-size (compP2 P) (xcp', frs') (xcp, frs)}$   
**by**  $(\text{auto simp add: sim21-size-def})(\text{auto simp add: compP2-def compMb2-def intro: sim21-size-aux.intros})$   
**with**  $\text{red True show ?thesis by simp(rule } \tau\text{red1r-into-}\tau\text{Red1r)}$   
**next**  
**case**  $\text{False}$   
**thus**  $?thesis \text{ using red by (auto intro: } \tau\text{red1t-into-}\tau\text{Red1t } \tau\text{red1r-into-}\tau\text{Red1r)}$   
**qed**  
**moreover from red lenxs**  
**have**  $\text{max-vars } e'' \leq \text{length } xs''$   
**apply**  $(\text{auto dest: } \tau\text{red1r-max-vars } \tau\text{red1r-preserves-len } \tau\text{red1t-max-vars } \tau\text{red1t-preserves-len split: if-split-asm})$   
**apply**  $(\text{frule } \tau\text{red1r-max-vars } \tau\text{red1t-max-vars, drule } \tau\text{red1r-preserves-len } \tau\text{red1t-preserves-len, simp})+$   
**done**  
**with**  $\text{conf' sees } b'$   
**have**  $\text{bisim1-list1 } t h (e'', xs'') \text{ exs None } ((\text{Addr } a' \# \text{drop (length stk - d) stk, loc, C, M, pch}) \# \text{FRS})$   
**using**  $\text{bisims unfolding } \langle h' = h \rangle \langle xcp' = \text{None} \rangle$   
 $\langle frs' = (\text{Addr } a' \# \text{drop (length stk - d) stk, loc, C, M, pch}) \# \text{FRS} \rangle$   
**by**  $\text{rule}$   
**ultimately show ?thesis by (auto split del: if-split)**  
**qed**  
**qed**  
**qed**  $(\text{insert exec, auto simp add: exec-1-iff elim!: jvmd-NormalE})$

**lemma**  $\tau\text{Red1-simulates-exec-1-not-}\tau$ :

**assumes**  $\text{wf: wf-J1-prog } P$   
**and**  $\text{exec: exec-1-d (compP2 P) t (Normal (xcp, h, frs)) ta (Normal (xcp', h', frs'))}$   
**and**  $\text{bisim: bisim1-list1 } t h (e, xs) \text{ exs } xcp \text{ frs}$   
**and**  $\tau: \neg \tau\text{Move2 (compP2 P) (xcp, h, frs)}$   
**shows**  $\exists e' xs' \text{exs' ta' } e'' xs'' \text{exs''}. \tau\text{Red1r } P t h ((e, xs), \text{exs}) ((e', xs'), \text{exs'}) \wedge$   
 $\text{True, } P, t \vdash 1 \langle (e', xs') / \text{exs'}, h \rangle - \text{ta'} \rightarrow \langle (e'', xs'') / \text{exs'', } h \rangle \wedge$   
 $\neg \tau\text{Move1 } P h ((e', xs'), \text{exs'}) \wedge \text{ta-bisim wbisim1 ta' ta} \wedge$   
 $\text{bisim1-list1 } t h' (e'', xs'') \text{ exs'' } xcp' \text{ frs'} \wedge$   
 $(\text{call1 } e = \text{None} \vee$   
 $(\text{case frs of Nil} \Rightarrow \text{False} \mid (\text{stk, loc, C, M, pc}) \# \text{FRS} \Rightarrow \forall M' n.$   
 $\text{instrs-of (compP2 P) C M ! pc} \neq \text{Invoke } M' n) \vee$   
 $e' = e \wedge xs' = xs \wedge \text{exs'} = \text{exs})$

**using**  $\text{bisim}$

**proof**  $\text{cases}$

**case**  $(\text{bl1-Normal stk loc C M pc FRS Ts T body D})$

**hence**  $[\text{simp}]: \text{frs} = (\text{stk, loc, C, M, pc}) \# \text{FRS}$

**and**  $\text{conf: compTP } P \vdash t: (xcp, h, (\text{stk, loc, C, M, pc}) \# \text{FRS}) \checkmark$

```

and sees:  $P \vdash C \text{ sees } M: Ts \rightarrow T = \lfloor \text{body} \rfloor \text{ in } D$ 
and bisim:  $P, \text{blocks1 } 0 \text{ (Class } D \# Ts) \text{ body}, h \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp)$ 
and lenxs:  $\text{max-vars } e \leq \text{length } xs$ 
and bisims:  $\text{list-all2 (bisim1-fr } P \text{ h) exs FRS by auto}$ 

from sees-method-compP[OF sees, where  $f = \lambda C M Ts T. \text{compMb2}$ ]
have sees':  $\text{compP2 } P \vdash C \text{ sees } M: Ts \rightarrow T = \lfloor (\text{max-stack body}, \text{max-vars body}, \text{compE2 body } @$ 
[Return],  $\text{compxE2 body } 0 \text{ } 0) \rfloor \text{ in } D$ 
by(simp add: compP2-def compMb2-def)
from bisim have pc:  $pc \leq \text{length (compE2 body)}$  by(auto dest: bisim1-pc-length-compE2)
from conf have hconf:  $\text{hconf } h \text{ preallocated } h$  and tconf:  $P, h \vdash t \sqrt{t}$ 
unfolding correct-state-def by(simp-all add: compP2-def tconf-def)

from sees wf have bsok:  $\text{bsok (blocks1 } 0 \text{ (Class } D \# Ts) \text{ body) } 0$ 
by(auto dest!: sees-wf-mdecl simp add: bsok-def wf-mdecl-def WT1-expr-locks)

from exec obtain check:  $\text{check (compP2 } P) (xcp, h, frs)$ 
and exec:  $\text{compP2 } P, t \vdash (xcp, h, frs) \rightarrow \text{ta-jvm} \rightarrow (xcp', h', frs')$ 
by(rule jvmd-NormalE)(auto simp add: exec-1-iff)

from wt-compTP-compP2[OF wf] exec conf
have conf':  $\text{compTP } P \vdash t: (xcp', h', frs') \sqrt{\quad}$  by(auto intro: BV-correct-1)

show ?thesis
proof(cases xcp)
  case [simp]: None
    from exec have execi:  $(ta, xcp', h', frs') \in \text{exec-instr (instrs-of (compP2 } P) C M ! pc) (compP2$ 
P)  $t h stk loc C M pc \text{ FRS}$ 
    by(simp add: exec-1-iff)
    show ?thesis
    proof(cases length frs' = Suc (length FRS))
      case True
        with pc execi sees' have pc:  $pc < \text{length (compE2 body)}$ 
        by(auto split: if-split-asm simp add: split-beta)
        with execi sees' have execi:  $(ta, xcp', h', frs') \in \text{exec-instr (compE2 body ! pc) (compP2 } P) t h$ 
stk loc C M pc FRS
        by(simp)
        from  $\tau$  sees' True pc have  $\tau i: \neg \tau \text{move2 (compP2 } P) h stk \text{ body } pc \text{ None}$  by(simp add:  $\tau \text{move2-iff}$ )
        from execi True sees' pc have  $\exists stk' loc' pc'. frs' = (stk', loc', C, M, pc') \# \text{FRS}$ 
        by(cases (compE2 body @ [Return]) ! pc)(auto split: if-split-asm sum.split-asm simp add:
split-beta, auto split: extCallRet.splits)
        then obtain  $stk' loc' pc'$  where [simp]:  $frs' = (stk', loc', C, M, pc') \# \text{FRS}$  by blast
        from conf obtain ST where  $\text{compP2 } P, h \vdash stk [: \leq] ST$  by(auto simp add: correct-state-def
conf-f-def2)
        hence ST:  $P, h \vdash stk [: \leq] ST$  by(rule List.list-all2-mono)(simp add: compP2-def)
        from execi sees True check pc
        have exec':  $\text{exec-move-d } P t (\text{blocks1 } 0 \text{ (Class } D \# Ts) \text{ body}) h (stk, loc, pc, xcp) ta h' (stk', loc',$ 
pc', xcp')
        apply(auto simp add: compP2-def compMb2-def exec-move-def check-def exec-meth-instr split:
if-split-asm sum.split-asm)
        apply(cases compE2 body ! pc)
        apply(auto simp add: neq-Nil-conv split-beta split: if-split-asm sum.split-asm)
        apply(force split: extCallRet.split-asm)
        apply(cases compE2 body ! pc, auto simp add: split-beta neq-Nil-conv split: if-split-asm

```

```

sum.split-asm)
  done
  from red1-simulates-exec-instr[OF wf hconf tconf bisim this - bsok ST] lenxs  $\tau i$  obtain  $e'' xs''$ 
  ta' e' xs'
    where bisim':  $P, \text{blocks1 } 0 \text{ (Class } D \# Ts) \text{ body}, h' \vdash (e'', xs'') \leftrightarrow (stk', loc', pc', xcp')$ 
    and red1:  $\tau \text{red1r } P \text{ t h } (e, xs) (e', xs') \text{ and red2: } \text{True}, P, t \vdash 1 \langle e', (h, xs') \rangle - ta' \rightarrow \langle e'', (h', xs'') \rangle$ 
    and  $\tau 1$ :  $\neg \tau \text{move1 } P \text{ h } e'$  and tabisim:  $ta\text{-bisim } wbisim1 \text{ (extTA2J1 } P \text{ ta') } ta$ 
    and call:  $\text{call1 } e = \text{None} \vee \text{no-call2 (blocks1 } 0 \text{ (Class } D \# Ts) \text{ body) } pc \vee e' = e \wedge xs' = xs$ 
    by(fastforce simp del: blocks1.simps)
  from red1 have Red1:  $\tau \text{Red1r } P \text{ t h } ((e, xs), \text{exs}) ((e', xs'), \text{exs})$ 
    by(rule  $\tau \text{red1r-into-}\tau \text{Red1r}$ )
  moreover from red2 have  $\text{True}, P, t \vdash 1 \langle (e', xs')/\text{exs}, h \rangle - \text{extTA2J1 } P \text{ ta'} \rightarrow \langle (e'', xs'')/\text{exs}, h' \rangle$ 
    by(rule red1Red)
  moreover from  $\tau 1$  red2 have  $\neg \tau \text{Move1 } P \text{ h } ((e', xs'), \text{exs})$  by auto
  moreover from  $\tau \text{red1r-max-vars}$ [OF red1] lenxs  $\tau \text{red1r-preserves-len}$ [OF red1]
  have  $\text{max-vars } e' \leq \text{length } xs'$  by simp
  with  $\text{red1-preserves-len}$ [OF red2]  $\text{red1-max-vars}$ [OF red2]
  have  $\text{max-vars } e'' \leq \text{length } xs''$  by simp
  with  $\text{conf' sees bisim'}$ 
  have  $\text{bisim1-list1 } t \text{ h' } (e'', xs'') \text{ exs } xcp' ((stk', loc', C, M, pc') \# \text{FRS})$ 
    unfolding  $\langle \text{frs}' = (stk', loc', C, M, pc') \# \text{FRS} \rangle$ 
  proof
    from red2 have  $\text{hext } h \text{ h'}$  by (auto dest: red1-hext-incr)
    from bisims show  $\text{list-all2 (bisim1-fr } P \text{ h') exs FRS}$ 
      by (rule List.list-all2-mono)(erule bisim1-fr-hext-mono[OF -  $\langle \text{hext } h \text{ h'} \rangle$ ])
  qed
  moreover from call sees'
  have  $\text{call1 } e = \text{None} \vee (\forall M' n. \text{instrs-of (compP2 } P) \text{ C M ! } pc \neq \text{Invoke } M' n) \vee e' = e \wedge xs' = xs$ 
    by (auto simp add: no-call2-def)
  ultimately show ?thesis using tabisim
    by (fastforce simp del: split-paired-Ex)
  next
  case False
  with  $\text{execi sees } pc \text{ compE2-not-Return[of body]}$ 
  have  $(pc = \text{length (compE2 body)} \vee (\exists M n. \text{compE2 body ! } pc = \text{Invoke } M n)) \wedge xcp' = \text{None}$ 
    apply (cases  $\text{compE2 body ! } pc$ )
    apply (auto split: if-split-asm sum.split-asm simp add: split-beta compP2-def compMb2-def)
    apply (auto split: extCallRet.splits)
    apply (metis in-set-conv-nth)+
  done
  hence [simp]:  $xcp' = \text{None}$ 
    and  $pc = \text{length (compE2 body)} \vee (\exists M n. \text{compE2 body ! } pc = \text{Invoke } M n)$  by simp-all
  moreover
  { assume [simp]:  $pc = \text{length (compE2 body)}$ 
    with  $\text{sees-method-compP[OF sees, where } f = \lambda C M Ts T. \text{compMb2}] \tau$  have False by (auto
      simp add: compMb2-def compP2-def)
    hence ?thesis .. }
  moreover {
    assume  $\exists M n. \text{compE2 body ! } pc = \text{Invoke } M n$ 
    and  $pc \neq \text{length (compE2 body)}$ 
    with  $pc$  obtain  $MM n$  where  $\text{ins: compE2 body ! } pc = \text{Invoke } MM n$ 
    and  $pc: pc < \text{length (compE2 body)}$  by auto
    with  $\text{bisim1-Invoke-stkD[OF bisim[unfolded None], of } MM n]$  obtain  $vs' v' stk'$ 

```

```

    where [simp]: stk = vs' @ v' # stk' n = length vs' by auto
    with False  $\tau$  sees' execi pc ins have False by auto (auto split: extCallRet.split-asm) }
    ultimately show ?thesis by blast
qed
next
case [simp]: (Some ad)
from bisim have pc: pc < length (compE2 body) by (auto dest: bisim1-xcp-pcD)
with  $\tau$  sees' have False by auto
thus ?thesis ..
qed
qed(insert exec, auto simp add: exec-1-iff elim!: jvmd-NormalE)

end

end

```

## 7.19 Correctness of Stage 2: The multithreaded setting

```

theory Correctness2
imports
  J1JVM
  JVMJ1
  ../BV/BVProgressThreaded
begin

declare Listn.lesub-list-impl-same-size[simp del]

context J1-JVM-heap-conf-base begin

lemma bisim1-list1-has-methodD: bisim1-list1 t h ex exs xcp ((stk, loc, C, M, pc) # frs)  $\impl$   $P \vdash C$ 
has M
by(fastforce elim!: bisim1-list1.cases intro: has-methodI)

end

declare compP-has-method [simp]

sublocale J1-JVM-heap-conf-base < Red1-exec:
  delay-bisimulation-base mred1 P t mexec (compP2 P) t wbisim1 t ta-bisim wbisim1  $\tau$ MOVE1 P
 $\tau$ MOVE2 (compP2 P)
  for t
.

sublocale J1-JVM-heap-conf-base < Red1-execd: delay-bisimulation-base
  mred1 P t
  mexecd (compP2 P) t
  wbisim1 t
  ta-bisim wbisim1
   $\tau$ MOVE1 P
   $\tau$ MOVE2 (compP2 P)
  for t
.

```

**context** *JVM-heap-base* **begin**

**lemma**  $\tau exec\text{-}1\text{-}d\text{-}silent\text{-}move$ :

$\tau exec\text{-}1\text{-}d\ P\ t\ (xcp, h, frs)\ (xcp', h', frs')$   
 $\implies \tau trsys.silent\text{-}move\ (mexecd\ P\ t)\ (\tau MOVE2\ P)\ ((xcp, frs), h)\ ((xcp', frs'), h')$

**apply**(*rule*  $\tau trsys.silent\text{-}move.intros$ )

**apply** *auto*

**apply**(*rule*  $exec\text{-}1\text{-}d\text{-}NormalI$ )

**apply**(*auto simp add: exec\text{-}1\text{-}iff exec\text{-}d\text{-}def*)

**done**

**lemma**  $silent\text{-}move\text{-}\tau exec\text{-}1\text{-}d$ :

$\tau trsys.silent\text{-}move\ (mexecd\ P\ t)\ (\tau MOVE2\ P)\ ((xcp, frs), h)\ ((xcp', frs'), h')$   
 $\implies \tau exec\text{-}1\text{-}d\ P\ t\ (xcp, h, frs)\ (xcp', h', frs')$

**apply**(*erule*  $\tau trsys.silent\text{-}move.cases$ )

**apply** *clarsimp*

**apply**(*erule*  $jvmd\text{-}NormalE$ )

**apply**(*auto simp add: exec\text{-}1\text{-}iff*)

**done**

**lemma**  $\tau Exec\text{-}1\text{-}dr\text{-}rtranclpD$ :

$\tau Exec\text{-}1\text{-}dr\ P\ t\ (xcp, h, frs)\ (xcp', h', frs')$   
 $\implies \tau trsys.silent\text{-}moves\ (mexecd\ P\ t)\ (\tau MOVE2\ P)\ ((xcp, frs), h)\ ((xcp', frs'), h')$

**by**(*induct rule: rtranclp-induct3*)(*blast intro: rtranclp.rtrancl\text{-}into\text{-}rtrancl*  $\tau exec\text{-}1\text{-}d\text{-}silent\text{-}move$ ) +

**lemma**  $\tau Exec\text{-}1\text{-}dt\text{-}tranclpD$ :

$\tau Exec\text{-}1\text{-}dt\ P\ t\ (xcp, h, frs)\ (xcp', h', frs')$   
 $\implies \tau trsys.silent\text{-}movet\ (mexecd\ P\ t)\ (\tau MOVE2\ P)\ ((xcp, frs), h)\ ((xcp', frs'), h')$

**by**(*induct rule: tranclp-induct3*)(*blast intro: tranclp.trancl\text{-}into\text{-}trancl*  $\tau exec\text{-}1\text{-}d\text{-}silent\text{-}move$ ) +

**lemma**  $rtranclp\text{-}\tau Exec\text{-}1\text{-}dr$ :

$\tau trsys.silent\text{-}moves\ (mexecd\ P\ t)\ (\tau MOVE2\ P)\ ((xcp, frs), h)\ ((xcp', frs'), h')$   
 $\implies \tau Exec\text{-}1\text{-}dr\ P\ t\ (xcp, h, frs)\ (xcp', h', frs')$

**by**(*induct rule: rtranclp-induct[of - ((ax, ay), az) ((bx, by), bz), split-rule, consumes 1]*)(*blast intro: rtranclp.rtrancl\text{-}into\text{-}rtrancl*  $silent\text{-}move\text{-}\tau exec\text{-}1\text{-}d$ ) +

**lemma**  $tranclp\text{-}\tau Exec\text{-}1\text{-}dt$ :

$\tau trsys.silent\text{-}movet\ (mexecd\ P\ t)\ (\tau MOVE2\ P)\ ((xcp, frs), h)\ ((xcp', frs'), h')$   
 $\implies \tau Exec\text{-}1\text{-}dt\ P\ t\ (xcp, h, frs)\ (xcp', h', frs')$

**by**(*induct rule: tranclp-induct[of - ((ax, ay), az) ((bx, by), bz), split-rule, consumes 1]*)(*blast intro: tranclp.trancl\text{-}into\text{-}trancl*  $silent\text{-}move\text{-}\tau exec\text{-}1\text{-}d$ ) +

**lemma**  $\tau Exec\text{-}1\text{-}dr\text{-}conv\text{-}rtranclp$ :

$\tau Exec\text{-}1\text{-}dr\ P\ t\ (xcp, h, frs)\ (xcp', h', frs') =$   
 $\tau trsys.silent\text{-}moves\ (mexecd\ P\ t)\ (\tau MOVE2\ P)\ ((xcp, frs), h)\ ((xcp', frs'), h')$

**by**(*blast intro:  $\tau Exec\text{-}1\text{-}dr\text{-}rtranclpD$  rtranclp\text{-}\tau Exec\text{-}1\text{-}dr*)

**lemma**  $\tau Exec\text{-}1\text{-}dt\text{-}conv\text{-}tranclp$ :

$\tau Exec\text{-}1\text{-}dt\ P\ t\ (xcp, h, frs)\ (xcp', h', frs') =$   
 $\tau trsys.silent\text{-}movet\ (mexecd\ P\ t)\ (\tau MOVE2\ P)\ ((xcp, frs), h)\ ((xcp', frs'), h')$

**by**(*blast intro:  $\tau Exec\text{-}1\text{-}dt\text{-}tranclpD$  tranclp\text{-}\tau Exec\text{-}1\text{-}dt*)

**end**



context *J1-JVM-conf-read* begin

lemma *Red1-execd-weak-bisim*:

assumes *wf*: *wf-J1-prog P*

shows *delay-bisimulation-measure* (*mred1 P t*) (*mexecd (compP2 P) t*) (*wbisim1 t*) (*ta-bisim wbisim1*)  
 ( $\tau\text{MOVE1 } P$ ) ( $\tau\text{MOVE2 } (\text{compP2 } P)$ ) ( $\lambda(((e, xs), eks), h) (((e', xs'), eks'), h')$ . *sim12-size e* < *sim12-size e'*) ( $\lambda(xcpfrs, h) (xcpfrs', h)$ . *sim21-size (compP2 P) xcpfrs xcpfrs'*)

proof

fix *s1 s2 s1'*

assume *wbisim1 t s1 s2* and  $\tau\text{trsys.silent-move } (\text{mred1 } P t) (\tau\text{MOVE1 } P) s1 s1'$

moreover obtain *e xs eks h* where *s1*: *s1* =  $((e, xs), eks), h$  by(*cases s1*) auto

moreover obtain *e' xs' eks' h1'* where *s1'*: *s1'* =  $((e', xs'), eks'), h1'$  by(*cases s1'*) auto

moreover obtain *xcp frs h2* where *s2*: *s2* =  $((xcp, frs), h2)$  by(*cases s2*) auto

ultimately have [*simp*]: *h2* = *h* and *red*: *True*, *P*, *t*  $\vdash 1 \langle (e, xs) / eks, h \rangle -\varepsilon \rightarrow \langle (e', xs') / eks', h1' \rangle$

and  $\tau$ :  $\tau\text{Move1 } P h ((e, xs), eks)$  and *bisim*: *bisim1-list1 t h (e, xs) eks xcp frs* by(*auto*)

from *red*  $\tau$  *bisim* have *h1'* [*simp*]: *h1'* = *h* by(*auto dest:  $\tau\text{move1-heap-unchanged elim! Red1.cases bisim1-list1.cases}$* )

from *exec-1-simulates-Red1- $\tau$* [*OF wf red*[*unfolded h1'*]] *bisim*  $\tau$  obtain *xcp' frs'*

where *exec*: (if *sim12-size e' < sim12-size e* then  $\tau\text{Exec-1-dr}$  else  $\tau\text{Exec-1-dt}$ ) (*compP2 P*) *t* (*xcp*, *h*, *frs*) (*xcp'*, *h*, *frs'*)

and *bisim'*: *bisim1-list1 t h (e', xs') eks' xcp' frs'* by *blast*

from *exec* have (if ( $\lambda(((e, xs), eks), h) (((e', xs'), eks'), h')$ . *sim12-size e* < *sim12-size e'*) ( $((e', xs'), eks'), h$ ) ( $((e, xs), eks), h$ ) then  $\tau\text{trsys.silent-moves } (\text{mexecd } (\text{compP2 } P) t) (\tau\text{MOVE2 } (\text{compP2 } P))$  else  $\tau\text{trsys.silent-movet } (\text{mexecd } (\text{compP2 } P) t) (\tau\text{MOVE2 } (\text{compP2 } P))$ ) ( $((xcp, frs), h)$ ) ( $((xcp', frs'), h)$ )

by(*auto simp add:  $\tau\text{Exec-1-dr-conv-rtranclp } \tau\text{Exec-1-dt-conv-tranclp}$* )

thus *wbisim1 t s1' s2*  $\wedge$  ( $\lambda(((e, xs), eks), h) (((e', xs'), eks'), h')$ . *sim12-size e* < *sim12-size e'*)<sup>++</sup> *s1' s1*  $\vee$

( $\exists s2'$ . ( $\tau\text{trsys.silent-movet } (\text{mexecd } (\text{compP2 } P) t) (\tau\text{MOVE2 } (\text{compP2 } P))$ ) *s2 s2'*  $\wedge$  *wbisim1 t s1' s2'*)

using *bisim' s1 s1' s2*

by  $-(\text{rule delay-bisimulation-base.simulation-silentII}', \text{auto split del: if-split})$

next

fix *s1 s2 s2'*

assume *wbisim1 t s1 s2* and  $\tau\text{trsys.silent-move } (\text{mexecd } (\text{compP2 } P) t) (\tau\text{MOVE2 } (\text{compP2 } P)) s2 s2'$

moreover obtain *e xs eks h1* where *s1*: *s1* =  $((e, xs), eks), h1$  by(*cases s1*) auto

moreover obtain *xcp frs h* where *s2*: *s2* =  $((xcp, frs), h)$  by(*cases s2*) auto

moreover obtain *xcp' frs' h2'* where *s2'*: *s2'* =  $((xcp', frs'), h2')$  by(*cases s2'*) auto

ultimately have [*simp*]: *h1* = *h* and *exec*: *exec-1-d* (*compP2 P*) *t* (*Normal (xcp, h, frs)*)  $\varepsilon$  (*Normal (xcp', h2', frs')*)

and  $\tau$ :  $\tau\text{Move2 } (\text{compP2 } P) (xcp, h, frs)$  and *bisim*: *bisim1-list1 t h (e, xs) eks xcp frs* by(*auto*)

from  $\tau\text{Red1-simulates-exec-1-}\tau$ [*OF wf exec bisim*  $\tau$ ]

obtain *e' xs' eks' h2'* where [*simp*]: *h2'* = *h*

and *red*: (if *sim21-size (compP2 P) (xcp', frs') (xcp, frs)* then  $\tau\text{Red1r}$  else  $\tau\text{Red1t}$ ) *P t h* ( $((e, xs), eks)$ ) ( $((e', xs'), eks')$ )

and *bisim'*: *bisim1-list1 t h (e', xs') eks' xcp' frs'* by *blast*

from *red* have (if ( $\lambda(xcpfrs, h) (xcpfrs', h)$ . *sim21-size (compP2 P) xcpfrs xcpfrs'*) ( $((xcp', frs'), h2')$ ) ( $((xcp, frs), h)$ ) then  $\tau\text{trsys.silent-moves } (\text{mred1 } P t) (\tau\text{MOVE1 } P)$  else  $\tau\text{trsys.silent-movet } (\text{mred1 } P t) (\tau\text{MOVE1 } P)$ ) ( $((e, xs), eks)$ ) ( $((e', xs'), eks')$ ) ( $((e', xs'), eks')$ )

by(*auto dest:  $\tau\text{Red1r-rtranclpD } \tau\text{Red1t-tranclpD}$* )

thus *wbisim1 t s1 s2'*  $\wedge$  ( $\lambda(xcpfrs, h) (xcpfrs', h)$ . *sim21-size (compP2 P) xcpfrs xcpfrs'*)<sup>++</sup> *s2' s2*

$\vee$

( $\exists s1'$ .  $\tau\text{trsys.silent-movet } (\text{mred1 } P t) (\tau\text{MOVE1 } P) s1 s1' \wedge \text{wbisim1 } t s1' s2'$ )

```

    using bisim' s1 s2 s2'
    by  $\neg(\text{rule } \text{delay-bisimulation-base.simulation-silent2I}', \text{auto split del: if-split})$ 
next
  fix s1 s2 tl1 s1'
  assume wbisim1 t s1 s2 and mred1 P t s1 tl1 s1' and  $\neg \tau \text{MOVE1 } P \text{ } s1 \text{ } tl1 \text{ } s1'$ 
  moreover obtain e xs exs h where s1: s1 = (((e, xs), exs), h) by(cases s1) auto
  moreover obtain e' xs' exs' h1' where s1': s1' = (((e', xs'), exs'), h1') by(cases s1') auto
  moreover obtain xcp frs h2 where s2: s2 = ((xcp, frs), h2) by(cases s2) auto
  ultimately have [simp]: h2 = h and red: True, P, t  $\vdash 1 \langle (e, xs)/exs, h \rangle -tl1 \rightarrow \langle (e', xs')/exs', h1' \rangle$ 
    and  $\tau$ :  $\neg \tau \text{Move1 } P \text{ } h \text{ } ((e, xs), exs)$  and bisim: bisim1-list1 t h (e, xs) exs xcp frs
    by(fastforce elim!: Red1.cases dest: red1- $\tau$ -taD) +
  from exec-1-simulates-Red1-not- $\tau$ [OF wf red bisim  $\tau$ ] obtain ta' xcp' frs' xcp'' frs''
    where exec1:  $\tau \text{Exec-1-dr } (compP2 \text{ } P) \text{ } t \text{ } (xcp, h, frs) \text{ } (xcp', h, frs')$ 
    and exec2: exec-1-d (compP2 P) t (Normal (xcp', h, frs')) ta' (Normal (xcp'', h1', frs''))
    and  $\tau'$ :  $\neg \tau \text{Move2 } (compP2 \text{ } P) \text{ } (xcp', h, frs')$ 
    and bisim': bisim1-list1 t h1' (e', xs') exs' xcp'' frs''
    and ta': ta-bisim wbisim1 tl1 ta' by blast
  from exec1 have  $\tau \text{trsys.silent-moves } (mexecd \text{ } (compP2 \text{ } P) \text{ } t) \text{ } (\tau \text{MOVE2 } (compP2 \text{ } P)) \text{ } ((xcp, frs),$ 
h) ((xcp', frs'), h)
    by(rule  $\tau \text{Exec-1-dr-rtrancld}$ )
  thus  $\exists s2' s2'' tl2. \tau \text{trsys.silent-moves } (mexecd \text{ } (compP2 \text{ } P) \text{ } t) \text{ } (\tau \text{MOVE2 } (compP2 \text{ } P)) \text{ } s2 \text{ } s2' \wedge$ 
mexecd (compP2 P) t s2' tl2 s2''  $\wedge \neg \tau \text{MOVE2 } (compP2 \text{ } P) \text{ } s2' \text{ } tl2 \text{ } s2'' \wedge$ 
wbisim1 t s1' s2''  $\wedge \text{ta-bisim } wbisim1 \text{ } tl1 \text{ } tl2$ 
    using bisim' exec2  $\tau' s1 s1' s2 ta'$  unfolding  $\langle h2 = h \rangle$ 
    apply(subst (1 2) split-paired-Ex)
    apply(subst (1 2) split-paired-Ex)
    by clarify ((rule exI conjI|assumption) +, auto)
next
  fix s1 s2 tl2 s2'
  assume wbisim1 t s1 s2 and mexecd (compP2 P) t s2 tl2 s2' and  $\neg \tau \text{MOVE2 } (compP2 \text{ } P) \text{ } s2 \text{ } tl2$ 
s2'
  moreover obtain e xs exs h1 where s1: s1 = (((e, xs), exs), h1) by(cases s1) auto
  moreover obtain xcp frs h where s2: s2 = ((xcp, frs), h) by(cases s2) auto
  moreover obtain xcp' frs' h2' where s2': s2' = ((xcp', frs'), h2') by(cases s2') auto
  ultimately have [simp]: h1 = h and exec: exec-1-d (compP2 P) t (Normal (xcp, h, frs)) tl2
(Normal (xcp', h2', frs'))
    and  $\tau$ :  $\neg \tau \text{Move2 } (compP2 \text{ } P) \text{ } (xcp, h, frs)$  and bisim: bisim1-list1 t h (e, xs) exs xcp frs
    apply auto
    apply(erule jvmd-NormalE)
    apply(cases xcp)
    apply auto
    apply(rename-tac stk loc C M pc frs)
    apply(case-tac instrs-of (compP2 P) C M ! pc)
    apply(simp-all split: if-split-asm)
    apply(auto dest!:  $\tau \text{external-red-external-aggr-TA-empty simp add: check-def has-method-def } \tau \text{external-def } \tau \text{external'-def}$ )
  done
  from  $\tau \text{Red1-simulates-exec-1-not-}\tau$ [OF wf exec bisim  $\tau$ ] obtain e' xs' exs' ta' e'' xs'' exs''
    where red1:  $\tau \text{Red1r } P \text{ } t \text{ } h \text{ } ((e, xs), exs) \text{ } ((e', xs'), exs')$ 
    and red2: True, P, t  $\vdash 1 \langle (e', xs')/exs', h \rangle -ta' \rightarrow \langle (e'', xs'')/exs'', h2' \rangle$ 
    and  $\tau'$ :  $\neg \tau \text{Move1 } P \text{ } h \text{ } ((e', xs'), exs')$  and ta': ta-bisim wbisim1 ta' tl2
    and bisim': bisim1-list1 t h2' (e'', xs'') exs'' xcp' frs' by blast
  from red1 have  $\tau \text{trsys.silent-moves } (mred1 \text{ } P \text{ } t) \text{ } (\tau \text{MOVE1 } P) \text{ } (((e, xs), exs), h) \text{ } (((e', xs'), exs'),$ 
h)

```

```

  by(rule  $\tau$ Red1r-rtrancpD)
thus  $\exists s1' s1'' tl1. \tau trsys.silent-moves (mred1 P t) (\tau MOVE1 P) s1 s1' \wedge mred1 P t s1' tl1 s1'' \wedge$ 
 $\neg \tau MOVE1 P s1' tl1 s1'' s2' \wedge ta-bisim wbisim1 tl1 tl2$ 
  using  $bisim' red2 \tau' s1 s2 s2' \langle h1 = h \rangle ta'$ 
  apply -
  apply(rule  $exI$ [where  $x=((e', xs'), exs'), h$ ])
  apply(rule  $exI$ [where  $x=((e'', xs''), exs''), h2$ ])
  apply(rule  $exI$ [where  $x=ta'$ ])
  apply auto
  done
next
  have  $wf (inv-image \{(x, y). x < y\} (\lambda(((e, xs), exs), h). sim12-size e))$ 
  by(rule  $wf-inv-image$ )(rule  $wf-less$ )
  also have  $inv-image \{(x, y). x < y\} (\lambda(((e, xs), exs), h). sim12-size e) =$ 
 $\{(x, y). (\lambda(((e, xs), exs), h) (((e', xs'), exs'), h'). sim12-size e < sim12-size e') x y\}$  by auto
  finally show  $wfP (\lambda(((e, xs), exs), h) (((e', xs'), exs'), h'). sim12-size e < sim12-size e')$ 
  unfolding  $wfP-def$  .
next
  from  $wfP-sim21-size$ 
  have  $wf \{(xcpfrs, xcpfrs'). sim21-size (compP2 P) xcpfrs xcpfrs'\}$  by(unfold  $wfP-def$ )
  hence  $wf (inv-image \{(xcpfrs, xcpfrs'). sim21-size (compP2 P) xcpfrs xcpfrs'\} fst)$  by(rule  $wf-inv-image$ )
  also have  $inv-image \{(xcpfrs, xcpfrs'). sim21-size (compP2 P) xcpfrs xcpfrs'\} fst =$ 
 $\{((xcpfrs, h), (xcpfrs', h)). sim21-size (compP2 P) xcpfrs xcpfrs'\}$  by auto
  also have  $\dots = \{(x, y). (\lambda(xcpfrs, h) (xcpfrs', h). sim21-size (compP2 P) xcpfrs xcpfrs') x y\}$ 
by(auto)
  finally show  $wfP (\lambda(xcpfrs, h) (xcpfrs', h). sim21-size (compP2 P) xcpfrs xcpfrs')$ 
  unfolding  $wfP-def$  .
qed

lemma Red1-execd-delay-bisim:
  assumes  $wf: wf-J1-prog P$ 
  shows  $delay-bisimulation-diverge (mred1 P t) (mexecd (compP2 P) t) (wbisim1 t) (ta-bisim wbisim1)$ 
 $(\tau MOVE1 P) (\tau MOVE2 (compP2 P))$ 
proof -
  interpret  $delay-bisimulation-measure$ 
   $mred1 P t mexecd (compP2 P) t wbisim1 t ta-bisim wbisim1 \tau MOVE1 P \tau MOVE2 (compP2 P)$ 
 $\lambda(((e, xs), exs), h) (((e', xs'), exs'), h'). sim12-size e < sim12-size e'$ 
 $\lambda(xcpfrs, h) (xcpfrs', h). sim21-size (compP2 P) xcpfrs xcpfrs'$ 
  using  $wf$  by(rule  $Red1-execd-weak-bisim$ )
  show ?thesis by(unfold-locale)
qed

end

definition bisim-wait1JVM ::
  ' $addr$   $jvm-prog \Rightarrow ('addr \text{ expr1} \times 'addr \text{ locals1}) \times ('addr \text{ expr1} \times 'addr \text{ locals1}) \text{ list} \Rightarrow 'addr$ 
 $jvm-thread-state \Rightarrow bool$ 
where
   $bisim-wait1JVM P \equiv$ 
 $\lambda((e1, xs1), exs1) (xcp, frs). call1 e1 \neq None \wedge$ 
 $(case frs of Nil \Rightarrow False \mid (stk, loc, C, M, pc) \# frs' \Rightarrow \exists M' n. instrs-of P C M ! pc = Invoke$ 
 $M' n)$ 

sublocale J1-JVM-heap-conf-base < Red1-execd:

```

```

FWbisimulation-base
  final-expr1
  mred1 P
  JVM-final
  mexecd (compP2 P)
  convert-RA
  wbisim1
  bisim-wait1JVM (compP2 P)
.

```

**sublocale** *JVM-heap-base* < *execd-mthr*:

```

  τmultithreaded
  JVM-final
  mexecd P
  convert-RA
  τMOVE2 P
  for P
by(unfold-locales)

```

**sublocale** *J1-JVM-heap-conf-base* < *Red1-execd*:

```

FWdelay-bisimulation-base
  final-expr1
  mred1 P
  JVM-final
  mexecd (compP2 P)
  convert-RA
  wbisim1
  bisim-wait1JVM (compP2 P)
  τMOVE1 P
  τMOVE2 (compP2 P)
by(unfold-locales)

```

**context** *J1-JVM-conf-read* **begin**

**theorem** *Red1-exec1-FWwbisim*:

**assumes** *wf*: *wf-J1-prog* *P*

**shows** *FWdelay-bisimulation-diverge* *final-expr1* (*mred1* *P*) *JVM-final* (*mexecd* (*compP2* *P*)) *wbisim1* (*bisim-wait1JVM* (*compP2* *P*)) (*τMOVE1* *P*) (*τMOVE2* (*compP2* *P*))

**proof** –

**let** *?exec* = *mexecd* (*compP2* *P*)

**let** *?τexec* = λ*t*. *τtrsys.silent-moves* (*mexecd* (*compP2* *P*) *t*) (*τMOVE2* (*compP2* *P*))

**let** *?τred* = λ*t*. *τtrsys.silent-moves* (*mred1* *P* *t*) (*τMOVE1* *P*)

**interpret** *delay-bisimulation-diverge*

*mred1* *P* *t* *?exec* *t* *wbisim1* *t* *ta-bisim* *wbisim1* *τMOVE1* *P* *τMOVE2* (*compP2* *P*)

**for** *t*

**using** *wf* **by**(*rule Red1-execd-delay-bisim*)

**show** *?thesis*

**proof**

**fix** *t s1 s2*

**assume** *wbisim1* *t s1 s2* (λ(*x1*, *m*). *final-expr1* *x1*) *s1*

**moreover obtain** *e xs exs m1* **where** [*simp*]: *s1* = (((*e*, *xs*), *exs*), *m1*) **by**(*cases* *s1*) *auto*

**moreover obtain** *xcp frs m2* **where** [*simp*]: *s2* = ((*xcp*, *frs*), *m2*) **by**(*cases* *s2*) *auto*

**ultimately have** [*simp*]: *m2* = *m1* *exs* = []

**and** *bisim1-list1* *t m1* (*e*, *xs*) [] *xcp frs*

and *final e* by *auto*  
 from  $\langle \text{bisim1-list1 } t \ m1 \ (e, xs) \ [] \ xcp \ frs \rangle \langle \text{final } e \rangle$   
 show  $\exists s2'. \ ?\tau \text{exec } t \ s2 \ s2' \wedge \text{wbisim1 } t \ s1 \ s2' \wedge (\lambda(x2, m). \text{JVM-final } x2) \ s2'$   
 proof cases  
 case (*bl1-Normal stk loc C M pc frs' Ts T body D*)  
 hence  $[simp]: frs = [(stk, loc, C, M, pc)]$   
 and  $\text{conf}: \text{compTP } P \vdash t:(xcp, m1, frs) \ \checkmark$   
 and  $\text{sees}: P \vdash C \text{ sees } M: Ts \rightarrow T = [body] \text{ in } D$   
 and  $\text{bisim}: P, \text{blocks1 } 0 \ (Class \ D \ \# \ Ts) \ body, m1 \vdash (e, xs) \leftrightarrow (stk, loc, pc, xcp)$   
 and  $\text{var}: \text{max-vars } e \leq \text{length } xs$  by *auto*  
 from  $\langle \text{final } e \rangle$  show *?thesis*  
 proof cases  
 fix *v*  
 assume  $[simp]: e = \text{Val } v$   
 with *bisim* have  $[simp]: xcp = \text{None } xs = loc$   
 and  $\text{exec}: \tau \text{Exec-mover-a } P \ t \ (\text{blocks1 } 0 \ (Class \ D \ \# \ Ts) \ body) \ m1 \ (stk, loc, pc, xcp) \ ([v], loc,$   
 $\text{length } (\text{compE2 } body), \text{None})$   
 by(*auto dest!: bisim1Val2D1*)  
 from *exec* have  $\tau \text{Exec-mover-a } P \ t \ body \ m1 \ (stk, loc, pc, xcp) \ ([v], loc, \text{length } (\text{compE2 } body),$   
 $\text{None})$   
 unfolding  $\tau \text{Exec-mover-blocks1}$  .  
 with *sees* have  $\tau \text{Exec-1r } (\text{compP2 } P) \ t \ (xcp, m1, frs) \ (\text{None}, m1, [[v], loc, C, M, \text{length}$   
 $(\text{compE2 } body)])$   
 by(*auto intro: \tau Exec-mover-\tau Exec-1r*)  
 with *wt-compTP-compP2[OF wf]*  
 have  $\text{execd}: \tau \text{Exec-1-dr } (\text{compP2 } P) \ t \ (xcp, m1, frs) \ (\text{None}, m1, [[v], loc, C, M, \text{length } (\text{compE2}$   
 $body)])$   
 using *conf* by(*rule \tau Exec-1r-\tau Exec-1-dr*)  
 also from *sees-method-compP[OF sees, of \lambda C M Ts T. compMb2]* *sees max-stack1[of body]*  
 have  $\tau \text{exec-1-d } (\text{compP2 } P) \ t \ (\text{None}, m1, [[v], loc, C, M, \text{length } (\text{compE2 } body)]) \ (\text{None}, m1,$   
 $[])$   
 by(*auto simp add: \tau exec-1-d-def compP2-def compMb2-def check-def has-methodI intro: exec-1I*)  
 finally have  $\tau \text{exec } t \ s2 \ ((\text{None}, []), m1)$   
 unfolding  $\tau \text{Exec-1-dr-conv-rtranclp}$  by *simp*  
 moreover have *JVM-final* (*None*,  $[]$ ) by *simp*  
 moreover from *conf* have *hconf m1 preallocated m1* unfolding *correct-state-def* by(*simp-all*)  
 hence  $\text{wbisim1 } t \ s1 \ ((\text{None}, []), m1)$  by(*auto intro: bisim1-list1.intros*)  
 ultimately show *?thesis* by *blast*  
 next  
 fix *a*  
 assume  $[simp]: e = \text{throw } (\text{addr } a)$   
 hence  $\exists stk' \ loc' \ pc'. \ \tau \text{Exec-mover-a } P \ t \ body \ m1 \ (stk, loc, pc, xcp) \ (stk', loc', pc', [a]) \wedge$   
 $P, \text{blocks1 } 0 \ (Class \ D \ \# \ Ts) \ body, m1 \vdash (\text{Throw } a, xs) \leftrightarrow (stk', loc', pc', [a])$   
 proof(*cases xcp*)  
 case *None*  
 with *bisim* show *?thesis*  
 by(*fastforce dest!: bisim1-Throw-\tau Exec-movet simp del: blocks1.simps intro: tranclp-into-rtranclp*)  
 next  
 case (*Some a'*)  
 with *bisim* have  $a = a'$  by(*auto dest: bisim1-ThrowD*)  
 with *Some bisim* show *?thesis* by(*auto*)  
 qed  
 then obtain  $stk' \ loc' \ pc'$   
 where  $\text{exec}: \tau \text{Exec-mover-a } P \ t \ body \ m1 \ (stk, loc, pc, xcp) \ (stk', loc', pc', [a])$

```

    and bisim': P, blocks1 0 (Class D # Ts) body, m1 ⊢ (throw (addr a), xs) ↔ (stk', loc', pc',
    [a]) by blast
    with sees have τExec-1r (compP2 P) t (xcp, m1, frs) ([a], m1, [(stk', loc', C, M, pc')])
    by (auto intro: τExec-mover-τExec-1r)
    with wt-compTP-compP2[OF wf]
    have execd: τExec-1-dr (compP2 P) t (xcp, m1, frs) ([a], m1, [(stk', loc', C, M, pc')])
    using conf by (rule τExec-1r-τExec-1-dr)
    also {
      from bisim1-xcp-Some-not-caught[OF bisim', of λC M Ts T. compMb2 0 0]
      have match-ex-table (compP2 P) (cname-of m1 a) pc' (compxE2 body 0 0) = None by (simp
add: compP2-def)
      moreover from bisim' have pc' < length (compE2 body) by (auto dest: bisim1-ThrowD)
      ultimately have τexec-1 (compP2 P) t ([a], m1, [(stk', loc', C, M, pc')]) ([a], m1, [])
      using sees-method-compP[OF sees, of λC M Ts T. compMb2] sees
      by (auto simp add: τexec-1-def compP2-def compMb2-def has-methodI intro: exec-1I)
      moreover from wt-compTP-compP2[OF wf] execd conf
    have compTP P ⊢ t:([a], m1, [(stk', loc', C, M, pc')]) √ by (rule τExec-1-dr-preserves-correct-state)
      ultimately have τexec-1-d (compP2 P) t ([a], m1, [(stk', loc', C, M, pc')]) ([a], m1, [])
      using wt-compTP-compP2[OF wf]
      by (auto simp add: τexec-1-def τexec-1-d-def welltyped-commute[symmetric] elim: jvmd-NormalE)
    }
    finally have ?τexec t s2 (([a], []), m1)
      unfolding τExec-1-dr-conv-rtranclp by simp
    moreover have JVM-final ([a], []) by simp
    moreover from conf have hconf m1 preallocated m1 by (simp-all add: correct-state-def)
    hence wbisim1 t s1 (([a], []), m1) by (auto intro: bisim1-list1.intros)
    ultimately show ?thesis by blast
  qed
  qed(auto intro!: exI bisim1-list1.intros)
next
  fix t s1 s2
  assume wbisim1 t s1 s2 (λ(x2, m). JVM-final x2) s2
  moreover obtain e xs exs m1 where [simp]: s1 = (((e, xs), exs), m1) by (cases s1) auto
  moreover obtain xcp frs m2 where [simp]: s2 = ((xcp, frs), m2) by (cases s2) auto
  ultimately have [simp]: m2 = m1 exs = [] frs = []
    and bisim: bisim1-list1 t m1 (e, xs) [] xcp [] by (auto elim: bisim1-list1.cases)
  hence final e by (auto elim: bisim1-list1.cases)
  thus ∃ s1'. ?τred t s1 s1' ∧ wbisim1 t s1' s2 ∧ (λ(x1, m). final-expr1 x1) s1' using bisim by auto
next
  fix t' x m1 xx m2 t x1 x2 x1' ta1 x1'' m1' x2' ta2 x2'' m2'
  assume b: wbisim1 t' (x, m1) (xx, m2) and b': wbisim1 t (x1, m1) (x2, m2)
    and τred: ?τred t (x1, m1) (x1', m1)
    and red: mred1 P t (x1', m1) ta1 (x1'', m1')
    and ¬ τMOVE1 P (x1', m1) ta1 (x1'', m1')
    and τexec: ?τexec t (x2, m2) (x2', m2)
    and exec: ?exec t (x2', m2) ta2 (x2'', m2')
    and ¬ τMOVE2 (compP2 P) (x2', m2) ta2 (x2'', m2')
    and b2: wbisim1 t (x1'', m1') (x2'', m2')
  from red have hext m1 m1' by (auto simp add: split-beta intro: Red1-hext-incr)
  moreover from b2 have m1' = m2' by (cases x1'', cases x2'') simp
  moreover from b2 have hconf m2'
    by (cases x1'', cases x2'')(auto elim!: bisim1-list1.cases simp add: correct-state-def)
  moreover from b' exec have preallocated m2
    by (cases x1, cases x2)(auto elim!: bisim1-list1.cases simp add: correct-state-def)

```

**moreover from**  $b' \tau_{red} red$  **have**  $tconf: compP2\ P, m2 \vdash t \sqrt{t}$   
**by**(cases  $x1$ , cases  $x2$ )(auto elim!: bisim1-list1.cases Red1.cases simp add: correct-state-def  
 $\tau mreds1\text{-}Val\text{-}Nil\ \tau mreds1\text{-}Throw\text{-}Nil$ )  
**from**  $\tau_{exec}$  **have**  $\tau_{exec}': \tau Exec\text{-}1\text{-}dr\ (compP2\ P)\ t\ (fst\ x2, m2, snd\ x2)\ (fst\ x2', m2, snd\ x2')$   
**unfolding**  $\tau Exec\text{-}1\text{-}dr\text{-}conv\text{-}rtranclp$  **by** simp  
**with**  $b' tconf$  **have**  $compTP\ P \vdash t: (fst\ x2', m2, snd\ x2') \sqrt{}$   
**using**  $\langle preallocated\ m2 \rangle$   
**apply**(cases  $x1$ , cases  $x2$ )  
**apply**(erule  $\tau Exec\text{-}1\text{-}dr\text{-}preserves\text{-}correct\text{-}state[OF\ wt\text{-}compTP\text{-}compP2[OF\ wf]]$ )  
**apply**(auto elim!: bisim1-list1.cases simp add: correct-state-def)  
**done**  
**ultimately show**  $wbisim1\ t'\ (x, m1')\ (xx, m2')$  **using**  $b\ exec$   
**apply**(cases  $x$ , cases  $xx$ )  
**apply**(auto elim!: bisim1-list1.cases intro!: bisim1-list1.intros simp add: split-beta intro: preallo-  
cated-hext)  
**apply**(erule (2) correct-state-heap-change[ $OF\ wt\text{-}compTP\text{-}compP2[OF\ wf]$ ])  
**apply**(erule (1) bisim1-hext-mono)  
**apply**(erule List.list-all2-mono)  
**apply**(erule (1) bisim1-fr-hext-mono)  
**done**  
**next**  
**fix**  $t\ x1\ m1\ x2\ m2\ x1'\ ta1\ x1''\ m1'\ x2'\ ta2\ x2''\ m2'\ w$   
**assume**  $b: wbisim1\ t\ (x1, m1)\ (x2, m2)$   
**and**  $\tau_{red}: ?\tau_{red}\ t\ (x1, m1)\ (x1', m1)$   
**and**  $red: mred1\ P\ t\ (x1', m1)\ ta1\ (x1'', m1')$   
**and**  $\neg \tau MOVE1\ P\ (x1', m1)\ ta1\ (x1'', m1')$   
**and**  $\tau_{exec}: ?\tau_{exec}\ t\ (x2, m2)\ (x2', m2)$   
**and**  $exec: ?exec\ t\ (x2', m2)\ ta2\ (x2'', m2')$   
**and**  $\neg \tau MOVE2\ (compP2\ P)\ (x2', m2)\ ta2\ (x2'', m2')$   
**and**  $b': wbisim1\ t\ (x1'', m1')\ (x2'', m2')$   
**and**  $ta\text{-}bisim\ wbisim1\ ta1\ ta2$   
**and**  $Suspend: Suspend\ w \in set\ \{ta1\}_w\ Suspend\ w \in set\ \{ta2\}_w$   
**from**  $red\ Suspend$   
**have**  $call1\ (fst\ (fst\ x1'')) \neq None$   
**by**(cases  $x1'$ )(cases  $x1''$ , auto dest: Red1-Suspend-is-call)  
**moreover from**  $mexecd\text{-}Suspend\text{-}Invoke[OF\ exec\ Suspend(2)]$   
**obtain**  $xcp\ stk\ loc\ C\ M\ pc\ frs'\ M'\ n$  **where**  $x2'' = (xcp, (stk, loc, C, M, pc) \# frs')$   
 $instrs\text{-}of\ (compP2\ P)\ C\ M!\ pc = Invoke\ M'\ n$  **by** blast  
**ultimately show**  $bisim\text{-}wait1JVM\ (compP2\ P)\ x1''\ x2''$   
**by**(simp add: bisim-wait1JVM-def split-beta)  
**next**  
**fix**  $t\ x1\ m1\ x2\ m2\ ta1\ x1'\ m1'$   
**assume**  $wbisim1\ t\ (x1, m1)\ (x2, m2)$   
**and**  $bisim\text{-}wait1JVM\ (compP2\ P)\ x1\ x2$   
**and**  $mred1\ P\ t\ (x1, m1)\ ta1\ (x1', m1')$   
**and**  $wakeup: Notified \in set\ \{ta1\}_w \vee WokenUp \in set\ \{ta1\}_w$   
**moreover obtain**  $e1\ xs1\ exs1$  **where** [simp]:  $x1 = ((e1, xs1), exs1)$  **by**(cases  $x1$ ) auto  
**moreover obtain**  $xcp\ frs$  **where** [simp]:  $x2 = (xcp, frs)$  **by**(cases  $x2$ )  
**moreover obtain**  $e1'\ xs1'\ exs1'$  **where** [simp]:  $x1' = ((e1', xs1'), exs1')$  **by**(cases  $x1'$ ) auto  
**ultimately have** [simp]:  $m1 = m2$   
**and**  $bisim: bisim1\text{-}list1\ t\ m2\ (e1, xs1)\ exs1\ xcp\ frs$   
**and**  $red: True, P, t \vdash 1\ \langle (e1, xs1)/exs1, m2 \rangle \text{-}ta1 \rightarrow \langle (e1', xs1')/exs1', m1' \rangle$   
**and**  $call: call1\ e1 \neq None$   
 $case\ frs\ of\ [] \Rightarrow False \mid (stk, loc, C, M, pc) \# frs' \Rightarrow \exists M'\ n. instrs\text{-}of\ (compP2\ P)\ C\ M$

```

! pc = Invoke M' n
  by(auto simp add: bisim-wait1JVM-def split-def)
  from red wakeup have  $\neg \tau \text{Move1 } P \ m2 \ ((e1, xs1), exs1)$ 
  by(auto elim!: Red1.cases dest: red1- $\tau$ -taD simp add: split-beta ta-upd-simps)
  from exec-1-simulates-Red1-not- $\tau$ [OF wf red bisim this] call
  show  $\exists ta2 \ x2' \ m2'. \ mexecd \ (compP2 \ P) \ t \ (x2, m2) \ ta2 \ (x2', m2') \wedge \ wbisim1 \ t \ (x1', m1') \ (x2', m2') \wedge \ ta\text{-bisim} \ wbisim1 \ ta1 \ ta2$ 
  by(auto simp del: not-None-eq simp add: split-paired-Ex ta-bisim-def ta-upd-simps split: list.split-asm)
next
  fix t x1 m1 x2 m2 ta2 x2' m2'
  assume wbisim1 t (x1, m1) (x2, m2)
  and bisim-wait1JVM (compP2 P) x1 x2
  and mexecd (compP2 P) t (x2, m2) ta2 (x2', m2')
  and wakeup: Notified  $\in \text{set } \llbracket ta2 \rrbracket_w \vee \text{WokenUp} \in \text{set } \llbracket ta2 \rrbracket_w$ 
  moreover obtain e1 xs1 exs1 where [simp]:  $x1 = ((e1, xs1), exs1)$  by(cases x1) auto
  moreover obtain xcp frs where [simp]:  $x2 = (xcp, frs)$  by(cases x2)
  moreover obtain xcp' frs' where [simp]:  $x2' = (xcp', frs')$  by(cases x2')
  ultimately have [simp]:  $m1 = m2$ 
  and bisim: bisim1-list1 t m2 (e1, xs1) exs1 xcp frs
  and exec:  $compP2 \ P, t \vdash \text{Normal } (xcp, m2, frs) \text{-}ta2\text{-jvmd} \rightarrow \text{Normal } (xcp', m2', frs')$ 
  and call: call1 e1  $\neq \text{None}$ 
  case frs of []  $\Rightarrow \text{False} \mid (stk, loc, C, M, pc) \# frs' \Rightarrow \exists M' n. \text{instrs-of } (compP2 \ P) \ C \ M$ 
! pc = Invoke M' n
  by(auto simp add: bisim-wait1JVM-def split-def)
  from exec wakeup have  $\neg \tau \text{Move2 } (compP2 \ P) \ (xcp, m2, frs)$ 
  by(auto dest:  $\tau \text{exec-1-taD}$  simp add: split-beta ta-upd-simps)
  from  $\tau \text{Red1-simulates-exec-1-not-}\tau$ [OF wf exec bisim this] call
  show  $\exists ta1 \ x1' \ m1'. \ mred1 \ P \ t \ (x1, m1) \ ta1 \ (x1', m1') \wedge \ wbisim1 \ t \ (x1', m1') \ (x2', m2') \wedge \ ta\text{-bisim} \ wbisim1 \ ta1 \ ta2$ 
  by(auto simp del: not-None-eq simp add: split-paired-Ex ta-bisim-def ta-upd-simps split: list.split-asm)
next
  show  $(\exists x. \text{final-expr1 } x) \longleftrightarrow (\exists x. \text{JVM-final } x)$ 
  by(auto simp add: split-paired-Ex final-iff)
qed
qed
end

```

**sublocale** J1-JVM-heap-conf-base < Red1-mexecd:

```

FWbisimulation-base
final-expr1
mred1 P
JVM-final
mexecd (compP2 P)
convert-RA
wbisim1
bisim-wait1JVM (compP2 P)
.

```

**context** J1-JVM-heap-conf **begin**

**lemma** bisim-J1-JVM-start:

```

  assumes wf: wf-J1-prog P
  and wf-start: wf-start-state P C M vs

```



```

shows Red1-execd.mbisim (J1-start-state P C M vs) (JVM-start-state (compP2 P) C M vs)
proof –
  from wf-start obtain Ts T body D where start: start-heap-ok
  and sees: P ⊢ C sees M: Ts → T = [body] in D and conf: P, start-heap ⊢ vs [⋅ ≤] Ts by cases

  let ?e = blocks1 0 (Class D # Ts) body
  let ?xs = Null # vs @ replicate (max-vars body) undefined-value

  from sees-wf-mdecl[OF wf sees] obtain T'
    where B: B body (Suc (length Ts))
    and wt: P, Class D # Ts ⊢ 1 body :: T'
    and da: D body [⋅ ≤ length Ts]
    and sv: syncvars body
    by(auto simp add: wf-mdecl-def)

  have P, ?e, start-heap ⊢ (?e, ?xs) ↔ ([], ?xs, 0, None) by(rule bisim1-refl)
  moreover
  from wf have wf': wf-jvm-progcompTP P (compP2 P) by(rule wt-compTP-compP2)
  from sees-method-compP[OF sees, of λC M Ts T. compMb2]
  have sees': compP2 P ⊢ C sees M: Ts → T = [compMb2 body] in D by(simp add: compP2-def)
  from conf have compP2 P, start-heap ⊢ vs [⋅ ≤] Ts by(simp add: compP2-def heap-base.compP-confs)
  from BV-correct-initial[OF wf' start sees' this] sees'
  have compTP P ⊢ start-tid:(None, start-heap, ([], ?xs, D, M, 0)) ✓
    by(simp add: JVM-start-state'-def compP2-def compMb2-def)
  hence bisim1-list1 start-tid start-heap (?e, ?xs) [] None ([], ?xs, D, M, 0)
    using sees-method-idemp[OF sees]
  proof
    show P, ?e, start-heap ⊢ (?e, ?xs) ↔ ([], ?xs, 0, None)
      by(rule bisim1-refl)
    show max-vars ?e ≤ length ?xs using conf
      by(auto simp add: blocks1-max-vars dest: list-all2-lengthD)
  qed simp
  thus ?thesis
    using sees sees' unfolding start-state-def
    by –(rule Red1-execd.mbisimI, auto split: if-split-asm intro: wset-thread-okI simp add: compP2-def
  compMb2-def)
qed

lemmas τred1-Val-simps = τred1r-Val τred1t-Val τreds1r-map-Val τreds1t-map-Val

end

end

```

## 7.20 Indexing variables in variable lists

**theory** ListIndex **imports** Main **begin**

In order to support local variables and arbitrarily nested blocks, the local variables are arranged as an indexed list. The outermost local variable (“this”) is the first element in the list, the most recently created local variable the last element. When descending into a block structure, a corresponding list  $Vs$  of variable names is maintained. To find the index of some variable  $V$ , we have to find the index of the *last* occurrence of  $V$  in  $Vs$ . This is what *index*

does:

```
primrec index :: 'a list  $\Rightarrow$  'a  $\Rightarrow$  nat
where
  index [] y = 0
| index (x#xs) y =
  (if x=y then if x  $\in$  set xs then index xs y + 1 else 0 else index xs y + 1)
```

```
definition hidden :: 'a list  $\Rightarrow$  nat  $\Rightarrow$  bool
where hidden xs i  $\equiv$  i < size xs  $\wedge$  xs!i  $\in$  set(drop (i+1) xs)
```

### 7.20.1 index

```
lemma [simp]: index (xs @ [x]) x = size xs
```

```
lemma [simp]: (index (xs @ [x]) y = size xs) = (x = y)
```

```
lemma [simp]: x  $\in$  set xs  $\implies$  xs ! index xs x = x
```

```
lemma [simp]: x  $\notin$  set xs  $\implies$  index xs x = size xs
```

```
lemma index-size-conv[simp]: (index xs x = size xs) = (x  $\notin$  set xs)
```

```
lemma size-index-conv[simp]: (size xs = index xs x) = (x  $\notin$  set xs)
```

```
lemma (index xs x < size xs) = (x  $\in$  set xs)
```

```
lemma [simp]:  $\llbracket y \in \text{set } xs; x \neq y \rrbracket \implies \text{index } (xs @ [x]) y = \text{index } xs y$ 
```

```
lemma index-less-size[simp]: x  $\in$  set xs  $\implies$  index xs x < size xs
```

```
lemma index-less-aux:  $\llbracket x \in \text{set } xs; \text{size } xs \leq n \rrbracket \implies \text{index } xs x < n$ 
```

```
lemma [simp]: x  $\in$  set xs  $\vee$  y  $\in$  set xs  $\implies$  (index xs x = index xs y) = (x = y)
```

```
lemma inj-on-index: inj-on (index xs) (set xs)
```

```
lemma index-drop:  $\bigwedge x i. \llbracket x \in \text{set } xs; \text{index } xs x < i \rrbracket \implies x \notin \text{set}(\text{drop } i \text{ } xs)$ 
```

### 7.20.2 hidden

```
lemma hidden-index: x  $\in$  set xs  $\implies$  hidden (xs @ [x]) (index xs x)
```

```
lemma hidden-inacc: hidden xs i  $\implies$  index xs x  $\neq$  i
```

```
lemma [simp]: hidden xs i  $\implies$  hidden (xs@[x]) i
```

```
lemma fun-upds-apply:  $\bigwedge m \text{ } ys.$ 
  (m(xs[ $\mapsto$ ]ys)) x =
  (let xs' = take (size ys) xs
   in if x  $\in$  set xs' then Some(ys ! index xs' x) else m x)
```

```
lemma map-upds-apply-eq-Some:
  ((m(xs[ $\mapsto$ ]ys)) x = Some y) =
  (let xs' = take (size ys) xs
   in if x  $\in$  set xs' then ys ! index xs' x = y else m x = Some y)
```

**lemma** *map-upds-upd-conv-index*:

$\llbracket x \in \text{set } xs; \text{size } xs \leq \text{size } ys \rrbracket$   
 $\implies m(xs[\mapsto]ys, x \mapsto y) = m(xs[\mapsto]ys[\text{index } xs \ x := y])$

**lemma** *image-index*:

$A \subseteq \text{set}(xs @ [x]) \implies \text{index } (xs @ [x]) \text{ ' } A =$   
 $(\text{if } x \in A \text{ then insert } (\text{size } xs) (\text{index } xs \text{ ' } (A - \{x\})) \text{ else index } xs \text{ ' } A)$

**lemma** *index-le-lengthD*:  $\text{index } xs \ x < \text{length } xs \implies x \in \text{set } xs$

**by**(*erule contrapos-pp*)(*simp*)

**lemma** *not-hidden-index-nth*:  $\llbracket i < \text{length } Vs; \neg \text{hidden } Vs \ i \rrbracket \implies \text{index } Vs \ (Vs ! i) = i$

**by**(*induct Vs arbitrary: i*)(*auto split: if-split-asm nat.split-asm simp add: nth-Cons hidden-def*)

**lemma** *hidden-snoc-nth*:

**assumes** *len*:  $i < \text{length } Vs$

**shows**  $\text{hidden } (Vs @ [Vs ! i]) \ i$

**proof**(*cases hidden Vs i*)

**case** *True* **thus** *?thesis* **by** *simp*

**next**

**case** *False*

**with** *len* **have**  $\text{index } Vs \ (Vs ! i) = i$  **by**(*rule not-hidden-index-nth*)

**moreover from** *len* **have**  $\text{hidden } (Vs @ [Vs ! i]) \ (\text{index } Vs \ (Vs ! i))$

**by**(*auto intro: hidden-index*)

**ultimately show** *?thesis* **by** *simp*

**qed**

**lemma** *map-upds-Some-eq-nth-index*:

**assumes**  $[Vs \mapsto] \text{vs} \ V = \text{Some } v \ \text{length } Vs \leq \text{length } \text{vs}$

**shows**  $\text{vs} ! \text{index } Vs \ V = v$

**proof** –

**from**  $\langle [Vs \mapsto] \text{vs} \ V = \text{Some } v \rangle$  **have**  $V \in \text{set } Vs$

**by** –(*rule classical, auto*)

**with**  $\langle [Vs \mapsto] \text{vs} \ V = \text{Some } v \rangle \ \langle \text{length } Vs \leq \text{length } \text{vs} \rangle$  **show** *?thesis*

**proof**(*induct Vs arbitrary: vs*)

**case** *Nil* **thus** *?case* **by** *simp*

**next**

**case** (*Cons x xs ys*)

**note**  $IH = \langle \bigwedge \text{vs}. \llbracket [xs \mapsto] \text{vs} \ V = \text{Some } v; \text{length } xs \leq \text{length } \text{vs}; V \in \text{set } xs \rrbracket \implies \text{vs} ! \text{index } xs \ V = v \rangle$

**from**  $\langle [x \# xs \mapsto] \text{ys} \ V = \text{Some } v \rangle$  **obtain** *y Ys* **where**  $\text{ys} = y \# Ys$  **by**(*cases ys, auto*)

**with**  $\langle \text{length } (x \# xs) \leq \text{length } \text{ys} \rangle$  **have**  $\text{length } xs \leq \text{length } Ys$  **by** *simp*

**show** *?case*

**proof**(*cases V ∈ set xs*)

**case** *True*

**with**  $\langle [x \# xs \mapsto] \text{ys} \ V = \text{Some } v \rangle \ \langle \text{length } xs \leq \text{length } Ys \rangle \ \langle \text{ys} = y \# Ys \rangle$

**have**  $[xs \mapsto] Ys \ V = \text{Some } v$

**apply**(*auto simp add: map-upds-def map-of-eq-None-iff set-zip image-Collect split: if-split-asm*)

**apply**(*clarsimp simp add: in-set-conv-decomp*)

**apply**(*hypsubst-thin*)

**apply**(*erule-tac x=length ys in allE*)

**by**(*simp*)

**with**  $IH[OF \text{ this } \langle \text{length } xs \leq \text{length } Ys \rangle \ \text{True}] \ \langle \text{ys} = y \# Ys \rangle \ \text{True}$

**show** *?thesis* **by**(*simp*)

**next**

```

    case False with ⟨V ∈ set (x # xs)⟩ have x = V by auto
  with False ⟨[x # xs [↦] ys] V = Some v⟩ ⟨ys = y # Ys⟩ have y = v
    by(auto)
  with False ⟨x = V⟩ ⟨ys = y # Ys⟩
  show ?thesis by(simp)
qed
qed
qed
end

```

## 7.21 Compilation Stage 1

**theory** *Compiler1* **imports**

*PCompiler*

*J1State*

*ListIndex*

**begin**

**definition** *fresh-var* :: *vname list*  $\Rightarrow$  *vname*  
**where** *fresh-var* Vs = *sum-list* (STR "V" # Vs)

**lemma** *fresh-var-fresh*: *fresh-var* Vs  $\notin$  *set* Vs

**proof** –

**have**  $V \in \text{set } Vs \implies \text{length } (\text{String.explode } V) < \text{length } (\text{String.explode } (\text{fresh-var } Vs))$  **for** V  
**by** (induction Vs) (auto simp add: *fresh-var-def* *Literal.rep-eq*)  
**then show** ?thesis  
**by** auto

**qed**

Replacing variable names by indices.

**function** *compE1* :: *vname list*  $\Rightarrow$  'addr *expr*  $\Rightarrow$  'addr *expr1*  
**and** *compEs1* :: *vname list*  $\Rightarrow$  'addr *expr list*  $\Rightarrow$  'addr *expr1 list*

**where**

```

  compE1 Vs (new C) = new C
| compE1 Vs (newA T[e]) = newA T[compE1 Vs e]
| compE1 Vs (Cast T e) = Cast T (compE1 Vs e)
| compE1 Vs (e instanceof T) = (compE1 Vs e) instanceof T
| compE1 Vs (Val v) = Val v
| compE1 Vs (Var V) = Var(index Vs V)
| compE1 Vs (e«bop»e') = (compE1 Vs e)«bop»(compE1 Vs e')
| compE1 Vs (V:=e) = (index Vs V):= (compE1 Vs e)
| compE1 Vs (a[i]) = (compE1 Vs a)[compE1 Vs i]
| compE1 Vs (a[i]:=e) = (compE1 Vs a)[compE1 Vs i]:=compE1 Vs e
| compE1 Vs (a.length) = compE1 Vs a.length
| compE1 Vs (e.F{D}) = compE1 Vs e.F{D}
| compE1 Vs (e.F{D}:=e') = compE1 Vs e.F{D}:=compE1 Vs e'
| compE1 Vs (e.compareAndSwap(D.F, e', e'')) = compE1 Vs e.compareAndSwap(D.F, compE1 Vs e', compE1 Vs e'')
| compE1 Vs (e.M(es)) = (compE1 Vs e).M(compEs1 Vs es)
| compE1 Vs {V:T=vo; e} = {(size Vs):T=vo; compE1 (Vs@[V]) e}
| compE1 Vs (syncU (o') e) = synclength Vs (compE1 Vs o') (compE1 (Vs@[fresh-var Vs]) e)
| compE1 Vs (insyncU (a) e) = insynclength Vs (a) (compE1 (Vs@[fresh-var Vs]) e)

```

```
| compE1 Vs (e1;;e2) = (compE1 Vs e1);;(compE1 Vs e2)
| compE1 Vs (if (b) e1 else e2) = (if (compE1 Vs b) (compE1 Vs e1) else (compE1 Vs e2))
| compE1 Vs (while (b) e) = (while (compE1 Vs b) (compE1 Vs e))
| compE1 Vs (throw e) = throw (compE1 Vs e)
| compE1 Vs (try e1 catch (C V) e2) = try(compE1 Vs e1) catch(C (size Vs)) (compE1 (Vs@[V]) e2)
```

```
| compEs1 Vs [] = []
| compEs1 Vs (e#es) = compE1 Vs e # compEs1 Vs es
```

**by** pat-completeness auto

**termination**

**apply**(relation case-sum ( $\lambda p.$  size (snd p)) ( $\lambda p.$  size-list size (snd p)) <\*mlex\*> {})

**apply**(rule wf-mlex[OF wf-empty])

**apply**(rule mlex-less, simp)+

**done**

**lemmas** compE1-compEs1-induct =

compE1-compEs1.induct[case-names New NewArray Cast InstanceOf Val Var BinOp LAss AAcc  
AAss ALen FAcc FAss CAS Call Block Synchronized InSynchronized Seq Cond While throw TryCatch  
Nil Cons]

**lemma** compEs1-conv-map [simp]: compEs1 Vs es = map (compE1 Vs) es

**by**(induct es) simp-all

**lemmas** compEs1-map-Val = compEs1-conv-map

**lemma** compE1-eq-Val [simp]: compE1 Vs e = Val v  $\longleftrightarrow$  e = Val v

**apply**(cases e, auto)

**done**

**lemma** Val-eq-compE1 [simp]: Val v = compE1 Vs e  $\longleftrightarrow$  e = Val v

**apply**(cases e, auto)

**done**

**lemma** compEs1-eq-map-Val [simp]: compEs1 Vs es = map Val vs  $\longleftrightarrow$  es = map Val vs

**apply**(induct es arbitrary: vs)

**apply**(auto, blast)

**done**

**lemma** compE1-eq-Var [simp]: compE1 Vs e = Var V  $\longleftrightarrow$  ( $\exists V'. e = Var V' \wedge V = index\ Vs\ V'$ )

**by**(cases e, auto)

**lemma** compE1-eq-Call [simp]:

compE1 Vs e = obj.M(params)  $\longleftrightarrow$  ( $\exists obj'\ params'. e = obj'.M(params') \wedge compE1\ Vs\ obj' = obj$   
 $\wedge compEs1\ Vs\ params' = params$ )

**by**(cases e, auto)

**lemma** length-compEs2 [simp]:

length (compEs1 Vs es) = length es

**by**(simp add: compEs1-conv-map)

**lemma** fixes e :: 'addr expr and es :: 'addr expr list

**shows** expr-locks-compE1 [simp]: expr-locks (compE1 Vs e) = expr-locks e

**and** expr-lockss-compEs1 [simp]: expr-lockss (compEs1 Vs es) = expr-lockss es

**by**(induct Vs e and Vs es rule: compE1-compEs1.induct)(auto intro: ext)

**lemma fixes**  $e :: 'addr\ expr$  **and**  $es :: 'addr\ expr\ list$   
**shows**  $contains-insync-compE1$  [simp]:  $contains-insync (compE1\ Vs\ e) = contains-insync\ e$   
**and**  $contains-insyncs-compEs1$  [simp]:  $contains-insyncs (compEs1\ Vs\ es) = contains-insyncs\ es$   
**by**( $induct\ Vs\ e$  **and**  $Vs\ es$  *rule: compE1-compEs1.induct*)*simp-all*

**lemma fixes**  $e :: 'addr\ expr$  **and**  $es :: 'addr\ expr\ list$   
**shows**  $max-vars-compE1$ :  $max-vars (compE1\ Vs\ e) = max-vars\ e$   
**and**  $max-varss-compEs1$ :  $max-varss (compEs1\ Vs\ es) = max-varss\ es$   
**apply**( $induct\ Vs\ e$  **and**  $Vs\ es$  *rule: compE1-compEs1.induct*)  
**apply**(*auto*)  
**done**

**lemma fixes**  $e :: 'addr\ expr$  **and**  $es :: 'addr\ expr\ list$   
**shows**  $\mathcal{B}$ :  $size\ Vs = n \implies \mathcal{B} (compE1\ Vs\ e)\ n$   
**and**  $\mathcal{B}s$ :  $size\ Vs = n \implies \mathcal{B}s (compEs1\ Vs\ es)\ n$   
**apply**( $induct\ Vs\ e$  **and**  $Vs\ es$  *arbitrary: n and n rule: compE1-compEs1.induct*)  
**apply** *auto*  
**done**

**lemma fixes**  $e :: 'addr\ expr$  **and**  $es :: 'addr\ expr\ list$   
**shows**  $fv-compE1$ :  $fv\ e \subseteq set\ Vs \implies fv (compE1\ Vs\ e) = (index\ Vs)\ ' (fv\ e)$   
**and**  $fvs-compEs1$ :  $fvs\ es \subseteq set\ Vs \implies fvs (compEs1\ Vs\ es) = (index\ Vs)\ ' (fvs\ es)$   
**proof**( $induct\ Vs\ e$  **and**  $Vs\ es$  *rule: compE1-compEs1.induct*)  
**case** ( $Block\ Vs\ V\ ty\ vo\ exp$ )  
**have**  $IH$ :  $fv\ exp \subseteq set\ (Vs @ [V]) \implies fv (compE1\ (Vs @ [V])\ exp) = index\ (Vs @ [V])\ ' fv\ exp$  **by** *fact*  
**from**  $\langle fv\ \{V:ty=vo;\ exp\} \subseteq set\ Vs \rangle$  **have**  $fv'$ :  $fv\ exp \subseteq set\ (Vs @ [V])$  **by** *auto*  
**from**  $IH[OF\ this]$  **have**  $IH'$ :  $fv (compE1\ (Vs @ [V])\ exp) = index\ (Vs @ [V])\ ' fv\ exp$  .  
**have**  $fv (compE1\ (Vs @ [V])\ exp) - \{length\ Vs\} = index\ Vs\ ' (fv\ exp - \{V\})$   
**proof**(*rule equalityI[OF subsetI subsetI]*)  
**fix**  $x$   
**assume**  $x$ :  $x \in fv (compE1\ (Vs @ [V])\ exp) - \{length\ Vs\}$   
**hence**  $x \neq length\ Vs$  **by** *simp*  
**from**  $x\ IH'$  **have**  $x \in index\ (Vs @ [V])\ ' fv\ exp$  **by** *simp*  
**thus**  $x \in index\ Vs\ ' (fv\ exp - \{V\})$   
**proof**(*rule imageE*)  
**fix**  $y$   
**assume** [simp]:  $x = index\ (Vs @ [V])\ y$   
**and**  $y$ :  $y \in fv\ exp$   
**have**  $y \neq V$   
**proof**  
**assume** [simp]:  $y = V$   
**hence**  $x = length\ Vs$  **by** *simp*  
**with**  $\langle x \neq length\ Vs \rangle$  **show** *False* **by** *contradiction*  
**qed**  
**moreover with**  $fv'\ y$  **have**  $y \in set\ Vs$  **by** *auto*  
**ultimately have**  $index\ (Vs @ [V])\ y = index\ Vs\ y$  **by**(*simp*)  
**thus**  $?thesis$  **using**  $y\ \langle y \neq V \rangle$  **by** *auto*  
**qed**  
**next**  
**fix**  $x$   
**assume**  $x$ :  $x \in index\ Vs\ ' (fv\ exp - \{V\})$   
**thus**  $x \in fv (compE1\ (Vs @ [V])\ exp) - \{length\ Vs\}$

```

proof(rule imageE)
  fix y
  assume [simp]:  $x = \text{index } Vs \ y$ 
  and  $y: y \in \text{fv } \text{exp} - \{V\}$ 
  with  $\text{fv}'$  have  $y \in \text{set } Vs \ y \neq V$  by auto
  hence  $\text{index } Vs \ y = \text{index } (Vs @ [V]) \ y$  by simp
  with  $y$  have  $x \in \text{index } (Vs @ [V]) \ ' \text{fv } \text{exp}$  by auto
  thus ?thesis using  $IH' \langle y \in \text{set } Vs \rangle$  by simp
qed
qed
thus ?case by simp
next
case (Synchronized  $Vs \ V \ \text{exp1} \ \text{exp2}$ )
have  $IH1: \text{fv } \text{exp1} \subseteq \text{set } Vs \implies \text{fv } (\text{compE1 } Vs \ \text{exp1}) = \text{index } Vs \ ' \text{fv } \text{exp1}$ 
  and  $IH2: \text{fv } \text{exp2} \subseteq \text{set } (Vs @ [\text{fresh-var } Vs]) \implies \text{fv } (\text{compE1 } (Vs @ [\text{fresh-var } Vs]) \ \text{exp2}) = \text{index } (Vs @ [\text{fresh-var } Vs]) \ ' \text{fv } \text{exp2}$ 
  by fact+
from  $\langle \text{fv } (\text{sync}_V (\text{exp1}) \ \text{exp2}) \subseteq \text{set } Vs \rangle$  have  $\text{fv1}: \text{fv } \text{exp1} \subseteq \text{set } Vs$ 
  and  $\text{fv2}: \text{fv } \text{exp2} \subseteq \text{set } Vs$  by auto
from  $\text{fv2}$  have  $\text{fv2}': \text{fv } \text{exp2} \subseteq \text{set } (Vs @ [\text{fresh-var } Vs])$  by auto
have  $\text{index } (Vs @ [\text{fresh-var } Vs]) \ ' \text{fv } \text{exp2} = \text{index } Vs \ ' \text{fv } \text{exp2}$ 
proof(rule equalityI[OF subsetI subsetI])
  fix x
  assume  $x: x \in \text{index } (Vs @ [\text{fresh-var } Vs]) \ ' \text{fv } \text{exp2}$ 
  thus  $x \in \text{index } Vs \ ' \text{fv } \text{exp2}$ 
proof(rule imageE)
  fix y
  assume [simp]:  $x = \text{index } (Vs @ [\text{fresh-var } Vs]) \ y$ 
  and  $y: y \in \text{fv } \text{exp2}$ 
  from  $y \ \text{fv2}$  have  $y \in \text{set } Vs$  by auto
  moreover hence  $y \neq (\text{fresh-var } Vs)$  by(auto simp add: fresh-var-fresh)
  ultimately show ?thesis using  $y$  by(auto)
qed
next
fix x
assume  $x: x \in \text{index } Vs \ ' \text{fv } \text{exp2}$ 
thus  $x \in \text{index } (Vs @ [\text{fresh-var } Vs]) \ ' \text{fv } \text{exp2}$ 
proof(rule imageE)
  fix y
  assume [simp]:  $x = \text{index } Vs \ y$ 
  and  $y: y \in \text{fv } \text{exp2}$ 
  from  $y \ \text{fv2}$  have  $y \in \text{set } Vs$  by auto
  moreover hence  $y \neq (\text{fresh-var } Vs)$  by(auto simp add: fresh-var-fresh)
  ultimately have  $\text{index } Vs \ y = \text{index } (Vs @ [\text{fresh-var } Vs]) \ y$  by simp
  thus ?thesis using  $y$  by(auto)
qed
qed
with  $IH1[OF \ \text{fv1}] \ IH2[OF \ \text{fv2}']$  show ?case by(auto)
next
case (InSynchronized  $Vs \ V \ a \ \text{exp}$ )
have  $IH: \text{fv } \text{exp} \subseteq \text{set } (Vs @ [\text{fresh-var } Vs]) \implies \text{fv } (\text{compE1 } (Vs @ [\text{fresh-var } Vs]) \ \text{exp}) = \text{index } (Vs @ [\text{fresh-var } Vs]) \ ' \text{fv } \text{exp}$ 
  by fact
from  $\langle \text{fv } (\text{insync}_V (a) \ \text{exp}) \subseteq \text{set } Vs \rangle$  have  $\text{fv}: \text{fv } \text{exp} \subseteq \text{set } Vs$  by simp

```

```

hence  $fv'$ :  $fv \exp \subseteq set (Vs @ [fresh-var Vs])$  by auto
have  $index (Vs @ [fresh-var Vs]) \text{ ' } fv \exp = index Vs \text{ ' } fv \exp$ 
proof(rule equalityI[OF subsetI subsetI])
  fix  $x$ 
  assume  $x \in index (Vs @ [fresh-var Vs]) \text{ ' } fv \exp$ 
  thus  $x \in index Vs \text{ ' } fv \exp$ 
proof(rule imageE)
  fix  $y$ 
  assume [simp]:  $x = index (Vs @ [fresh-var Vs]) y$ 
  and  $y: y \in fv \exp$ 
  from  $y \text{ } fv$  have  $y \in set Vs$  by auto
  moreover hence  $y \neq (fresh-var Vs)$  by(auto simp add: fresh-var-fresh)
  ultimately have  $index (Vs @ [fresh-var Vs]) y = index Vs y$  by simp
  thus ?thesis using  $y$  by simp
qed
next
fix  $x$ 
assume  $x \in index Vs \text{ ' } fv \exp$ 
thus  $x \in index (Vs @ [fresh-var Vs]) \text{ ' } fv \exp$ 
proof(rule imageE)
  fix  $y$ 
  assume [simp]:  $x = index Vs y$ 
  and  $y: y \in fv \exp$ 
  from  $y \text{ } fv$  have  $y \in set Vs$  by auto
  moreover hence  $y \neq (fresh-var Vs)$  by(auto simp add: fresh-var-fresh)
  ultimately have  $index Vs y = index (Vs @ [fresh-var Vs]) y$  by simp
  thus ?thesis using  $y$  by auto
qed
qed
with  $IH[OF fv']$  show ?case by simp
next
case (TryCatch  $Vs \exp1 C V \exp2$ )
have  $IH1: fv \exp1 \subseteq set Vs \implies fv (compE1 Vs \exp1) = index Vs \text{ ' } fv \exp1$ 
  and  $IH2: fv \exp2 \subseteq set (Vs @ [V]) \implies fv (compE1 (Vs @ [V]) \exp2) = index (Vs @ [V]) \text{ ' } fv \exp2$ 
  by fact+
from  $\langle fv (try \exp1 catch (C V) \exp2) \subseteq set Vs \rangle$  have  $fv1: fv \exp1 \subseteq set Vs$ 
  and  $fv2: fv \exp2 \subseteq set (Vs @ [V])$  by auto
have  $index (Vs @ [V]) \text{ ' } fv \exp2 - \{length Vs\} = index Vs \text{ ' } (fv \exp2 - \{V\})$ 
proof(rule equalityI[OF subsetI subsetI])
  fix  $x$ 
  assume  $x: x \in index (Vs @ [V]) \text{ ' } fv \exp2 - \{length Vs\}$ 
  hence  $x \neq length Vs$  by simp
  from  $x$  have  $x \in index (Vs @ [V]) \text{ ' } fv \exp2$  by auto
  thus  $x \in index Vs \text{ ' } (fv \exp2 - \{V\})$ 
proof(rule imageE)
  fix  $y$ 
  assume [simp]:  $x = index (Vs @ [V]) y$ 
  and  $y: y \in fv \exp2$ 
  have  $y \neq V$ 
  proof
    assume [simp]:  $y = V$ 
    hence  $x = length Vs$  by simp
    with  $\langle x \neq length Vs \rangle$  show False by contradiction

```



```

qed
moreover with fv2 y have y ∈ set Vs by auto
ultimately have index (Vs @ [V]) y = index Vs y by(simp)
thus ?thesis using y ⟨y ≠ V⟩ by auto
qed
next
fix x
assume x: x ∈ index Vs ‘(fv exp2 - {V})’
thus x ∈ index (Vs @ [V]) ‘fv exp2 - {length Vs}’
proof(rule imageE)
  fix y
  assume [simp]: x = index Vs y
  and y: y ∈ fv exp2 - {V}
  with fv2 have y ∈ set Vs y ≠ V by auto
  hence index Vs y = index (Vs @ [V]) y by simp
  with y have x ∈ index (Vs @ [V]) ‘fv exp2’ by auto
  thus ?thesis using ⟨y ∈ set Vs⟩ by simp
qed
qed
with IH1[OF fv1] IH2[OF fv2] show ?case by auto
qed(auto)

lemma fixes e :: ‘addr expr and es :: ‘addr expr list
shows syncvars-compE1: fv e ⊆ set Vs ⇒ syncvars (compE1 Vs e)
and syncvarss-compEs1: fvs es ⊆ set Vs ⇒ syncvarss (compEs1 Vs es)
proof(induct Vs e and Vs es rule: compE1-compEs1-induct)
  case (Block Vs V ty vo exp)
  from ⟨fv {V:ty=vo; exp} ⊆ set Vs⟩ have fv exp ⊆ set (Vs @ [V]) by auto
  from ⟨fv exp ⊆ set (Vs @ [V]) ⇒ syncvars (compE1 (Vs @ [V]) exp)⟩[OF this] show ?case
by(simp)
next
case (Synchronized Vs V exp1 exp2)
note IH1 = ⟨fv exp1 ⊆ set Vs ⇒ syncvars (compE1 Vs exp1)⟩
note IH2 = ⟨fv exp2 ⊆ set (Vs @ [fresh-var Vs]) ⇒ syncvars (compE1 (Vs @ [fresh-var Vs]) exp2)⟩
from ⟨fv (syncV (exp1) exp2) ⊆ set Vs⟩ have fv1: fv exp1 ⊆ set Vs
  and fv2: fv exp2 ⊆ set Vs and fv2’: fv exp2 ⊆ set (Vs @ [fresh-var Vs]) by auto
have length Vs ∉ index (Vs @ [fresh-var Vs]) ‘fv exp2’
proof
  assume length Vs ∈ index (Vs @ [fresh-var Vs]) ‘fv exp2’
  thus False
proof(rule imageE)
  fix x
  assume x: length Vs = index (Vs @ [fresh-var Vs]) x
  and x’: x ∈ fv exp2
  from x’ fv2 have x ∈ set Vs x ≠ (fresh-var Vs) by(auto simp add: fresh-var-fresh)
  with x show ?thesis by(simp)
qed
qed
with IH1[OF fv1] IH2[OF fv2] fv2’ show ?case by(simp add: fv-compE1)
next
case (InSynchronized Vs V a exp)
note IH = ⟨fv exp ⊆ set (Vs @ [fresh-var Vs]) ⇒ syncvars (compE1 (Vs @ [fresh-var Vs]) exp)⟩
from ⟨fv (insyncV (a) exp) ⊆ set Vs⟩ have fv: fv exp ⊆ set Vs
  and fv’: fv exp ⊆ set (Vs @ [fresh-var Vs]) by auto

```

```

have length Vs  $\notin$  index (Vs @ [fresh-var Vs]) ‘fv exp
proof
  assume length Vs  $\in$  index (Vs @ [fresh-var Vs]) ‘fv exp
  thus False
proof(rule imageE)
  fix x
  assume x: length Vs = index (Vs @ [fresh-var Vs]) x
  and x': x  $\in$  fv exp
  from x' fv have x  $\in$  set Vs x  $\neq$  (fresh-var Vs) by(auto simp add: fresh-var-fresh)
  with x show ?thesis by(simp)
qed
qed
with IH[OF fv'] fv' show ?case by(simp add: fv-compE1)
next
case (TryCatch Vs exp1 C V exp2)
note IH1 =  $\langle \text{fv exp1} \subseteq \text{set Vs} \implies \text{syncvars (compE1 Vs exp1)} \rangle$ 
note IH2 =  $\langle \text{fv exp2} \subseteq \text{set (Vs @ [V])} \implies \text{syncvars (compE1 (Vs @ [V]) exp2)} \rangle$ 
from  $\langle \text{fv (try exp1 catch (C V) exp2)} \subseteq \text{set Vs} \rangle$  have fv1: fv exp1  $\subseteq$  set Vs
  and fv2: fv exp2  $\subseteq$  set (Vs @ [V]) by auto
from IH1[OF fv1] IH2[OF fv2] show ?case by auto
qed auto

lemma (in heap-base) synthesized-call-compP [simp]:
  synthesized-call (compP f P) h aMvs = synthesized-call P h aMvs
by(simp add: synthesized-call-def)

```

```

primrec fin1 :: 'addr expr  $\Rightarrow$  'addr expr1
where
  fin1 (Val v) = Val v
  | fin1 (throw e) = throw (fin1 e)

```

```

lemma comp-final: final e  $\implies$  compE1 Vs e = fin1 e
by(erule finalE, simp-all)

```

```

lemma fixes e :: 'addr expr and es :: 'addr expr list
  shows [simp]: max-vars (compE1 Vs e) = max-vars e
  and max-varss (compEs1 Vs es) = max-varss es
by (induct Vs e and Vs es rule: compE1-compEs1-induct)(simp-all)

```

Compiling programs:

```

definition compP1 :: 'addr J-prog  $\Rightarrow$  'addr J1-prog
where
  compP1  $\equiv$  compP ( $\lambda C M Ts T$  (pns,body). compE1 (this#pns) body)

```

```

declare compP1-def[simp]

```

**end**

## 7.22 The bisimulation relation between source and intermediate language

```

theory J0J1Bisim imports

```

```

J1
J1WellForm
Compiler1
../J/JWellForm
J0
begin

```

### 7.22.1 Correctness of program compilation

```

primrec unmod :: 'addr expr1  $\Rightarrow$  nat  $\Rightarrow$  bool
and unmods :: 'addr expr1 list  $\Rightarrow$  nat  $\Rightarrow$  bool
where
  unmod (new C) i = True
| unmod (newA T[e]) i = unmod e i
| unmod (Cast C e) i = unmod e i
| unmod (e instanceof T) i = unmod e i
| unmod (Val v) i = True
| unmod (e1 «bop» e2) i = (unmod e1 i  $\wedge$  unmod e2 i)
| unmod (Var i) j = True
| unmod (i:=e) j = (i  $\neq$  j  $\wedge$  unmod e j)
| unmod (a[i]) j = (unmod a j  $\wedge$  unmod i j)
| unmod (a[i]:=e) j = (unmod a j  $\wedge$  unmod i j  $\wedge$  unmod e j)
| unmod (a.length) j = unmod a j
| unmod (e.F{D}) i = unmod e i
| unmod (e1.F{D}:=e2) i = (unmod e1 i  $\wedge$  unmod e2 i)
| unmod (e1.compareAndSwap(D.F, e2, e3)) i = (unmod e1 i  $\wedge$  unmod e2 i  $\wedge$  unmod e3 i)
| unmod (e.M(es)) i = (unmod e i  $\wedge$  unmods es i)
| unmod {j:T=vo; e} i = ((i = j  $\longrightarrow$  vo = None)  $\wedge$  unmod e i)
| unmod (syncV (o') e) i = (unmod o' i  $\wedge$  unmod e i  $\wedge$  i  $\neq$  V)
| unmod (insyncV (a) e) i = unmod e i
| unmod (e1;;e2) i = (unmod e1 i  $\wedge$  unmod e2 i)
| unmod (if (e) e1 else e2) i = (unmod e i  $\wedge$  unmod e1 i  $\wedge$  unmod e2 i)
| unmod (while (e) c) i = (unmod e i  $\wedge$  unmod c i)
| unmod (throw e) i = unmod e i
| unmod (try e1 catch(C i) e2) j = (unmod e1 j  $\wedge$  (if i=j then False else unmod e2 j))

| unmods ([]) i = True
| unmods (e#es) i = (unmod e i  $\wedge$  unmods es i)

```

```

lemma unmods-map-Val [simp]: unmods (map Val vs) V
by(induct vs) simp-all

```

```

lemma fixes e :: 'addr expr and es :: 'addr expr list
  shows hidden-unmod: hidden Vs i  $\Longrightarrow$  unmod (compE1 Vs e) i
  and hidden-unmods: hidden Vs i  $\Longrightarrow$  unmods (compEs1 Vs es) i
apply(induct Vs e and Vs es rule: compE1-compEs1-induct)
apply (simp-all add:hidden-inacc)
apply(auto simp add:hidden-def)
done

```

```

lemma unmod-extRet2J [simp]: unmod e i  $\Longrightarrow$  unmod (extRet2J e va) i
by(cases va) simp-all

```

```

lemma max-dest: (n :: nat) + max a b  $\leq$  c  $\Longrightarrow$  n + a  $\leq$  c  $\wedge$  n + b  $\leq$  c

```

**apply**(*auto simp add: max-def split: if-split-asm*)  
**done**

**declare** *max-dest* [*dest!*]

**lemma** **fixes** *e* :: '*addr expr* **and** *es* :: '*addr expr list*  
**shows** *fv-unmod-compE1*:  $\llbracket i < \text{length } Vs; Vs ! i \notin \text{fv } e \rrbracket \implies \text{unmod } (\text{compE1 } Vs e) i$   
**and** *fvs-unmods-compEs1*:  $\llbracket i < \text{length } Vs; Vs ! i \notin \text{fvs } es \rrbracket \implies \text{unmods } (\text{compEs1 } Vs es) i$   
**proof**(*induct Vs e and Vs es rule: compE1-compEs1-induct*)  
**case** (*Block Vs V ty vo exp*)  
**note** *IH* =  $\langle \llbracket i < \text{length } (Vs @ [V]); (Vs @ [V]) ! i \notin \text{fv } exp \rrbracket \implies \text{unmod } (\text{compE1 } (Vs @ [V]) exp) \rangle$   
*i*  
**note** *len* =  $\langle i < \text{length } Vs \rangle$   
**hence** *i*:  $i < \text{length } (Vs @ [V])$  **by** *simp*  
**show** ?*case*  
**proof**(*cases Vs ! i = V*)  
**case** *True*  
**from** *len* **have** *hidden* (*Vs* @ [*Vs ! i*]) *i* **by**(*rule hidden-snoc-nth*)  
**with** *len* *True* **show** ?*thesis* **by**(*auto intro: hidden-unmod*)  
**next**  
**case** *False*  
**with**  $\langle Vs ! i \notin \text{fv } \{V:ty=vo; exp\} \rangle$  *len* **have** (*Vs* @ [*V*]) ! *i*  $\notin \text{fv } exp$   
**by**(*auto simp add: nth-append*)  
**from** *IH*[*OF i this*] *len* **show** ?*thesis* **by**(*auto*)  
**qed**  
**next**  
**case** (*TryCatch Vs e1 C V e2*)  
**note** *IH1* =  $\langle \llbracket i < \text{length } Vs; Vs ! i \notin \text{fv } e1 \rrbracket \implies \text{unmod } (\text{compE1 } Vs e1) i \rangle$   
**note** *IH2* =  $\langle \llbracket i < \text{length } (Vs @ [V]); (Vs @ [V]) ! i \notin \text{fv } e2 \rrbracket \implies \text{unmod } (\text{compE1 } (Vs @ [V]) e2) \rangle$   
*i*  
**note** *len* =  $\langle i < \text{length } Vs \rangle$   
**hence** *i*:  $i < \text{length } (Vs @ [V])$  **by** *simp*  
**have** *unmod* (*compE1* (*Vs* @ [*V*]) *e2*) *i*  
**proof**(*cases Vs ! i = V*)  
**case** *True*  
**from** *len* **have** *hidden* (*Vs* @ [*Vs ! i*]) *i* **by**(*rule hidden-snoc-nth*)  
**with** *len* *True* **show** ?*thesis* **by**(*auto intro: hidden-unmod*)  
**next**  
**case** *False*  
**with**  $\langle Vs ! i \notin \text{fv } (\text{try } e1 \text{ catch } (C V) e2) \rangle$  *len* **have** (*Vs* @ [*V*]) ! *i*  $\notin \text{fv } e2$   
**by**(*auto simp add: nth-append*)  
**from** *IH2*[*OF i this*] *len* **show** ?*thesis* **by**(*auto*)  
**qed**  
**with** *IH1*[*OF len*]  $\langle Vs ! i \notin \text{fv } (\text{try } e1 \text{ catch } (C V) e2) \rangle$  *len* **show** ?*case* **by**(*auto*)  
**qed**(*auto dest: index-le-lengthD simp add: nth-append*)

**lemma** *hidden-lengthD*: *hidden Vs i*  $\implies i < \text{length } Vs$   
**by**(*simp add: hidden-def*)

**lemma** **fixes** *e* :: '*addr expr1* **and** *es* :: '*addr expr1 list*  
**shows** *fv-B-unmod*:  $\llbracket V \notin \text{fv } e; \mathcal{B} e n; V < n \rrbracket \implies \text{unmod } e V$   
**and** *fvs-Bs-unmods*:  $\llbracket V \notin \text{fvs } es; \mathcal{B}s es n; V < n \rrbracket \implies \text{unmods } es V$   
**by**(*induct e and es arbitrary: n and n rule: unmod.induct unmods.induct*) *auto*

```

lemma assumes fin: final e'
  shows unmod-inline-call: unmod (inline-call e' e) V  $\longleftrightarrow$  unmod e V
  and unmods-inline-calls: unmods (inline-calls e' es) V  $\longleftrightarrow$  unmods es V
apply(induct e and es rule: unmod.induct unmods.induct)
apply(insert fin)
apply(auto simp add: is-vals-conv)
done

```

## 7.22.2 The delay bisimulation relation

Delay bisimulation for expressions

```

inductive bisim :: vname list  $\Rightarrow$  'addr expr  $\Rightarrow$  'addr expr1  $\Rightarrow$  'addr val list  $\Rightarrow$  bool
  and bisims :: vname list  $\Rightarrow$  'addr expr list  $\Rightarrow$  'addr expr1 list  $\Rightarrow$  'addr val list  $\Rightarrow$  bool
where
  bisimNew: bisim Vs (new C) (new C) xs
| bisimNewArray: bisim Vs e e' xs  $\Longrightarrow$  bisim Vs (newA T[e]) (newA T[e']) xs
| bisimCast: bisim Vs e e' xs  $\Longrightarrow$  bisim Vs (Cast T e) (Cast T e') xs
| bisimInstanceOf: bisim Vs e e' xs  $\Longrightarrow$  bisim Vs (e instanceof T) (e' instanceof T) xs
| bisimVal: bisim Vs (Val v) (Val v) xs
| bisimBinOp1:
   $\llbracket \text{bisim Vs } e \text{ e' xs; } \neg \text{is-val } e; \neg \text{contains-insync } e'' \rrbracket \Longrightarrow \text{bisim Vs } (e \llbracket \text{bop} \rrbracket e'') (e' \llbracket \text{bop} \rrbracket \text{compE1 Vs } e'') \text{ xs}$ 
| bisimBinOp2: bisim Vs e e' xs  $\Longrightarrow$  bisim Vs (Val v  $\llbracket \text{bop} \rrbracket$  e) (Val v  $\llbracket \text{bop} \rrbracket$  e') xs
| bisimVar: bisim Vs (Var V) (Var (index Vs V)) xs
| bisimLAss: bisim Vs e e' xs  $\Longrightarrow$  bisim Vs (V:=e) (index Vs V:=e') xs
| bisimAAcc1:  $\llbracket \text{bisim Vs } a \text{ a' xs; } \neg \text{is-val } a; \neg \text{contains-insync } i \rrbracket \Longrightarrow \text{bisim Vs } (a[i]) (a'[\text{compE1 Vs } i]) \text{ xs}$ 
| bisimAAcc2: bisim Vs i i' xs  $\Longrightarrow$  bisim Vs (Val v[i]) (Val v[i']) xs
| bisimAAss1:
   $\llbracket \text{bisim Vs } a \text{ a' xs; } \neg \text{is-val } a; \neg \text{contains-insync } i; \neg \text{contains-insync } e \rrbracket$ 
   $\Longrightarrow \text{bisim Vs } (a[i]:=e) (a'[\text{compE1 Vs } i] := \text{compE1 Vs } e) \text{ xs}$ 
| bisimAAss2:  $\llbracket \text{bisim Vs } i \text{ i' xs; } \neg \text{is-val } i; \neg \text{contains-insync } e \rrbracket \Longrightarrow \text{bisim Vs } (Val v[i] := e) (Val v[i'] := \text{compE1 Vs } e) \text{ xs}$ 
| bisimAAss3: bisim Vs e e' xs  $\Longrightarrow$  bisim Vs (Val v[Val i] := e) (Val v[Val i'] := e') xs
| bisimALength: bisim Vs a a' xs  $\Longrightarrow$  bisim Vs (a.length) (a'.length) xs
| bisimFAcc: bisim Vs e e' xs  $\Longrightarrow$  bisim Vs (e.F{D}) (e'.F{D}) xs
| bisimFAss1:  $\llbracket \text{bisim Vs } e \text{ e' xs; } \neg \text{is-val } e; \neg \text{contains-insync } e'' \rrbracket \Longrightarrow \text{bisim Vs } (e.F\{D} := e'') (e'.F\{D} := \text{compE1 Vs } e'') \text{ xs}$ 
| bisimFAss2: bisim Vs e e' xs  $\Longrightarrow$  bisim Vs (Val v.F{D} := e) (Val v.F{D} := e') xs
| bisimCAS1:  $\llbracket \text{bisim Vs } e \text{ e' xs; } \neg \text{is-val } e; \neg \text{contains-insync } e2; \neg \text{contains-insync } e3 \rrbracket$ 
   $\Longrightarrow \text{bisim Vs } (e \cdot \text{compareAndSwap}(D.F, e2, e3)) (e' \cdot \text{compareAndSwap}(D.F, \text{compE1 Vs } e2, \text{compE1 Vs } e3)) \text{ xs}$ 
| bisimCAS2:  $\llbracket \text{bisim Vs } e \text{ e' xs; } \neg \text{is-val } e; \neg \text{contains-insync } e3 \rrbracket$ 
   $\Longrightarrow \text{bisim Vs } (Val v \cdot \text{compareAndSwap}(D.F, e, e3)) (Val v \cdot \text{compareAndSwap}(D.F, e', \text{compE1 Vs } e3)) \text{ xs}$ 
| bisimCAS3: bisim Vs e e' xs  $\Longrightarrow$  bisim Vs (Val v  $\cdot$  compareAndSwap(D.F, Val v', e)) (Val v  $\cdot$  compareAndSwap(D.F, Val v', e')) xs
| bisimCallObj:  $\llbracket \text{bisim Vs } e \text{ e' xs; } \neg \text{is-val } e; \neg \text{contains-insyncs } es \rrbracket \Longrightarrow \text{bisim Vs } (e \cdot M(es)) (e' \cdot M(\text{compE1 Vs } es)) \text{ xs}$ 
| bisimCallParams: bisims Vs es es' xs  $\Longrightarrow$  bisim Vs (Val v  $\cdot$  M(es)) (Val v  $\cdot$  M(es')) xs
| bisimBlockNone: bisim (Vs@[V]) e e' xs  $\Longrightarrow$  bisim Vs {V:T=None; e} {(length Vs):T=None; e'} xs
| bisimBlockSome:  $\llbracket \text{bisim } (Vs@[V]) e \text{ e' } (xs[\text{length Vs} := v]) \rrbracket \Longrightarrow \text{bisim Vs } \{V:T=[v]; e\} \{(length Vs):T=[v]; e'\} \text{ xs}$ 
| bisimBlockSomeNone:  $\llbracket \text{bisim } (Vs@[V]) e \text{ e' xs; } xs ! (\text{length Vs}) = v \rrbracket \Longrightarrow \text{bisim Vs } \{V:T=[v]; e\}$ 

```

$\{(length\ Vs):T=None; e'\} xs$   
 $| \text{bisimSynchronized:}$   
 $\llbracket \text{bisim } Vs\ o'\ o''\ xs; \neg \text{contains-insync } e \rrbracket$   
 $\implies \text{bisim } Vs\ (\text{sync}(o')\ e)\ (\text{sync}_{length\ Vs}(o'')\ (\text{compE1}\ (Vs@[fresh-var\ Vs])\ e))\ xs$   
 $| \text{bisimInSynchronized:}$   
 $\llbracket \text{bisim } (Vs@[fresh-var\ Vs])\ e\ e'\ xs; xs!length\ Vs = \text{Addr } a \rrbracket \implies \text{bisim } Vs\ (\text{insync}(a)\ e)\ (\text{insync}_{length\ Vs}(a)\ e')\ xs$   
 $| \text{bisimSeq: } \llbracket \text{bisim } Vs\ e\ e'\ xs; \neg \text{contains-insync } e'' \rrbracket \implies \text{bisim } Vs\ (e;;e'')\ (e';;\text{compE1}\ Vs\ e'')\ xs$   
 $| \text{bisimCond:}$   
 $\llbracket \text{bisim } Vs\ e\ e'\ xs; \neg \text{contains-insync } e1; \neg \text{contains-insync } e2 \rrbracket$   
 $\implies \text{bisim } Vs\ (\text{if } (e)\ e1\ \text{else } e2)\ (\text{if } (e')\ (\text{compE1}\ Vs\ e1)\ \text{else } (\text{compE1}\ Vs\ e2))\ xs$   
 $| \text{bisimWhile:}$   
 $\llbracket \neg \text{contains-insync } b; \neg \text{contains-insync } e \rrbracket \implies \text{bisim } Vs\ (\text{while } (b)\ e)\ (\text{while } (\text{compE1}\ Vs\ b)\ (\text{compE1}\ Vs\ e))\ xs$   
 $| \text{bisimThrow: } \text{bisim } Vs\ e\ e'\ xs \implies \text{bisim } Vs\ (\text{throw } e)\ (\text{throw } e')\ xs$   
 $| \text{bisimTryCatch:}$   
 $\llbracket \text{bisim } Vs\ e\ e'\ xs; \neg \text{contains-insync } e'' \rrbracket$   
 $\implies \text{bisim } Vs\ (\text{try } e\ \text{catch}(C\ V)\ e'')\ (\text{try } e'\ \text{catch}(C\ (length\ Vs))\ \text{compE1}\ (Vs@[V])\ e'')\ xs$   
  
 $| \text{bisimsNil: } \text{bisims } Vs\ []\ []\ xs$   
 $| \text{bisimsCons1: } \llbracket \text{bisim } Vs\ e\ e'\ xs; \neg \text{is-val } e; \neg \text{contains-insyncs } es \rrbracket \implies \text{bisims } Vs\ (e\ \#\ es)\ (e'\ \# \text{compEs1}\ Vs\ es)\ xs$   
 $| \text{bisimsCons2: } \text{bisims } Vs\ es\ es'\ xs \implies \text{bisims } Vs\ (\text{Val } v\ \#\ es)\ (\text{Val } v\ \#\ es')\ xs$

**declare** *bisimNew* [iff]  
**declare** *bisimVal* [iff]  
**declare** *bisimVar* [iff]  
**declare** *bisimWhile* [iff]  
**declare** *bisimsNil* [iff]

**declare** *bisim-bisims.intros* [intro!]  
**declare** *bisimsCons1* [rule del, intro] *bisimsCons2* [rule del, intro]  
*bisimBinOp1* [rule del, intro] *bisimAAcc1* [rule del, intro]  
*bisimAAss1* [rule del, intro] *bisimAAss2* [rule del, intro]  
*bisimFAss1* [rule del, intro]  
*bisimCAS1* [rule del, intro] *bisimCAS2* [rule del, intro]  
*bisimCallObj* [rule del, intro]

**inductive-cases** *bisim-safe-cases* [elim!]:

*bisim*  $Vs\ (\text{new } C)\ e'\ xs$   
*bisim*  $Vs\ (\text{newA } T[e])\ e'\ xs$   
*bisim*  $Vs\ (\text{Cast } T\ e)\ e'\ xs$   
*bisim*  $Vs\ (e\ \text{instanceof } T)\ e'\ xs$   
*bisim*  $Vs\ (\text{Val } v)\ e'\ xs$   
*bisim*  $Vs\ (\text{Var } V)\ e'\ xs$   
*bisim*  $Vs\ (V:=e)\ e'\ xs$   
*bisim*  $Vs\ (\text{Val } v[i])\ e'\ xs$   
*bisim*  $Vs\ (\text{Val } v[\text{Val } v'] := e)\ e'\ xs$   
*bisim*  $Vs\ (\text{Val } v.\text{compareAndSwap}(D.F,\ \text{Val } v',\ e))\ e'\ xs$   
*bisim*  $Vs\ (a.\text{length})\ e'\ xs$   
*bisim*  $Vs\ (e.F\{D\})\ e'\ xs$   
*bisim*  $Vs\ (\text{sync}(o')\ e)\ e'\ xs$   
*bisim*  $Vs\ (\text{insync}(a)\ e)\ e'\ xs$   
*bisim*  $Vs\ (e;;e')\ e''\ xs$

$\text{bisim } Vs \text{ (if (b) e1 else e2) } e' \text{ } xs$   
 $\text{bisim } Vs \text{ (while (b) e) } e' \text{ } xs$   
 $\text{bisim } Vs \text{ (throw e) } e' \text{ } xs$   
 $\text{bisim } Vs \text{ (try e catch (C V) } e') \text{ } e'' \text{ } xs$   
 $\text{bisim } Vs \text{ } e' \text{ (new C) } xs$   
 $\text{bisim } Vs \text{ } e' \text{ (newA T[e]) } xs$   
 $\text{bisim } Vs \text{ } e' \text{ (Cast T e) } xs$   
 $\text{bisim } Vs \text{ } e' \text{ (e instanceof T) } xs$   
 $\text{bisim } Vs \text{ } e' \text{ (Val v) } xs$   
 $\text{bisim } Vs \text{ } e' \text{ (Var V) } xs$   
 $\text{bisim } Vs \text{ } e' \text{ (V:=e) } xs$   
 $\text{bisim } Vs \text{ } e' \text{ (Val v[i]) } xs$   
 $\text{bisim } Vs \text{ } e' \text{ (Val v[Val v'] := e) } xs$   
 $\text{bisim } Vs \text{ } e' \text{ (Val v.compareAndSwap(D.F, Val v', e)) } xs$   
 $\text{bisim } Vs \text{ } e' \text{ (a.length) } xs$   
 $\text{bisim } Vs \text{ } e' \text{ (e.F\{D\}) } xs$   
 $\text{bisim } Vs \text{ } e' \text{ (sync_V (o') e) } xs$   
 $\text{bisim } Vs \text{ } e' \text{ (insync_V (a) e) } xs$   
 $\text{bisim } Vs \text{ } e'' \text{ (e;;e') } xs$   
 $\text{bisim } Vs \text{ } e' \text{ (if (b) e1 else e2) } xs$   
 $\text{bisim } Vs \text{ } e' \text{ (while (b) e) } xs$   
 $\text{bisim } Vs \text{ } e' \text{ (throw e) } xs$   
 $\text{bisim } Vs \text{ } e'' \text{ (try e catch (C V) } e') \text{ } xs$

**inductive-cases** *bisim-cases* [elim]:

$\text{bisim } Vs \text{ (e1 «bop» e2) } e' \text{ } xs$   
 $\text{bisim } Vs \text{ (a[i]) } e' \text{ } xs$   
 $\text{bisim } Vs \text{ (a[i]:=e) } e' \text{ } xs$   
 $\text{bisim } Vs \text{ (e.F\{D\}:=e') } e'' \text{ } xs$   
 $\text{bisim } Vs \text{ (e.compareAndSwap(D.F, e', e'')) } e''' \text{ } xs$   
 $\text{bisim } Vs \text{ (e.M(es)) } e' \text{ } xs$   
 $\text{bisim } Vs \{ V:T=vo; e \} e' \text{ } xs$   
 $\text{bisim } Vs \text{ } e' \text{ (e1 «bop» e2) } xs$   
 $\text{bisim } Vs \text{ } e' \text{ (a[i]) } xs$   
 $\text{bisim } Vs \text{ } e' \text{ (a[i]:=e) } xs$   
 $\text{bisim } Vs \text{ } e'' \text{ (e.F\{D\}:=e') } xs$   
 $\text{bisim } Vs \text{ } e''' \text{ (e.compareAndSwap(D.F, e', e'')) } xs$   
 $\text{bisim } Vs \text{ } e' \text{ (e.M(es)) } xs$   
 $\text{bisim } Vs \text{ } e' \{ V:T=vo; e \} xs$

**inductive-cases** *bisims-safe-cases* [elim!]:

$\text{bisims } Vs \square es \text{ } xs$   
 $\text{bisims } Vs es \square xs$

**inductive-cases** *bisims-cases* [elim]:

$\text{bisims } Vs \text{ (e \# es) } es' \text{ } xs$   
 $\text{bisims } Vs es' \text{ (e \# es) } xs$

Delay bisimulation for call stacks

**inductive** *bisim01* :: 'addr expr  $\Rightarrow$  'addr expr1  $\times$  'addr locals1  $\Rightarrow$  bool

**where**

$\llbracket \text{bisim } \square e \text{ } e' \text{ } xs; \text{fv } e = \{\}; \mathcal{D} \text{ } e \llbracket \{\} \rrbracket; \text{max-vars } e' \leq \text{length } xs; \text{call } e = \llbracket aMvs \rrbracket; \text{call1 } e' = \llbracket aMvs \rrbracket \rrbracket$   
 $\implies \text{bisim01 } e \text{ (e', xs)}$

**inductive** *bisim-list* :: 'addr expr list  $\Rightarrow$  ('addr expr1  $\times$  'addr locals1) list  $\Rightarrow$  bool

**where**

*bisim-listNil*: *bisim-list* [] []  
| *bisim-listCons*:  
[[ *bisim-list* es *exs'*; *bisim* [] *e e' xs*;  
*fv e* = {};  $\mathcal{D} e \lfloor \{\} \rfloor$ ;  
*max-vars e'  $\leq$  length xs*;  
*call e* = [aMvs]; *call1 e' = [aMvs]* ]  
 $\Rightarrow$  *bisim-list* (*e # es*) ((*e'*, *xs*) # *exs'*)

**inductive-cases** *bisim-list-cases* [elim!]:

*bisim-list* [] *exs'*  
*bisim-list* (*ex # exs*) *exs'*  
*bisim-list* *exs* (*ex' # exs'*)

**fun** *bisim-list1* ::

'addr expr  $\times$  'addr expr list  $\Rightarrow$  ('addr expr1  $\times$  'addr locals1)  $\times$  ('addr expr1  $\times$  'addr locals1) list  $\Rightarrow$  bool

**where**

*bisim-list1* (*e*, *es*) ((*e1*, *xs1*), *exs1*)  $\longleftrightarrow$   
*bisim-list* es *exs1*  $\wedge$  *bisim* [] *e e1 xs1*  $\wedge$  *fv e* = {}  $\wedge$   $\mathcal{D} e \lfloor \{\} \rfloor$   $\wedge$  *max-vars e1  $\leq$  length xs1*

**definition** *bisim-red0-Red1* ::

((('addr expr  $\times$  'addr expr list)  $\times$  'heap)  
 $\Rightarrow$  (((('addr expr1  $\times$  'addr locals1)  $\times$  ('addr expr1  $\times$  'addr locals1) list)  $\times$  'heap)  $\Rightarrow$  bool

**where** *bisim-red0-Red1*  $\equiv$  ( $\lambda(es, h)$  (*exs*, *h'*). *bisim-list1* es *exs*  $\wedge$  *h = h'*)

**abbreviation** *ta-bisim01* ::

('addr, 'thread-id, 'heap) *J0-thread-action*  $\Rightarrow$  ('addr, 'thread-id, 'heap) *J1-thread-action*  $\Rightarrow$  bool

**where**

*ta-bisim01*  $\equiv$  *ta-bisim* ( $\lambda t.$  *bisim-red0-Red1*)

**definition** *bisim-wait01* ::

('addr expr  $\times$  'addr expr list)  $\Rightarrow$  ('addr expr1  $\times$  'addr locals1)  $\times$  ('addr expr1  $\times$  'addr locals1) list  $\Rightarrow$  bool

**where** *bisim-wait01*  $\equiv$   $\lambda(e0, es0)$  ((*e1*, *xs1*), *exs1*). *call e0*  $\neq$  None  $\wedge$  *call1 e1*  $\neq$  None

**lemma** *bisim-list1I[intro?]*:

[[ *bisim-list* es *exs1*; *bisim* [] *e e1 xs1*; *fv e* = {};  
 $\mathcal{D} e \lfloor \{\} \rfloor$ ; *max-vars e1  $\leq$  length xs1* ]  
 $\Rightarrow$  *bisim-list1* (*e*, *es*) ((*e1*, *xs1*), *exs1*)

**by** *simp*

**lemma** *bisim-list1E[elim?]*:

**assumes** *bisim-list1* (*e*, *es*) ((*e1*, *xs1*), *exs1*)  
**obtains** *bisim-list* es *exs1* *bisim* [] *e e1 xs1* *fv e* = {}  $\mathcal{D} e \lfloor \{\} \rfloor$  *max-vars e1  $\leq$  length xs1*

**using** *assms* **by** *auto*

**lemma** *bisim-list1-elim*:

**assumes** *bisim-list1* es' *exs*

**obtains** *e es e1 xs1 exs1*

**where** *es'* = (*e*, *es*) *exs* = ((*e1*, *xs1*), *exs1*)

**and** *bisim-list* es *exs1* *bisim* [] *e e1 xs1* *fv e* = {}  $\mathcal{D} e \lfloor \{\} \rfloor$  *max-vars e1  $\leq$  length xs1*

**using** *assms* **by** (cases *es'*) (cases *exs*, *fastforce*)



**declare** *bisim-list1.simps* [*simp del*]

**lemma** *bisims-map-Val-conv* [*simp*]: *bisims Vs (map Val vs) es xs = (es = map Val vs)*  
**apply**(*induct vs arbitrary: es*)  
**apply**(*fastforce*)  
**apply**(*simp*)  
**apply**(*rule iffI*)  
**apply**(*erule bisims-cases, auto*)  
**done**

**declare** *compEs1-conv-map* [*simp del*]

**lemma** *bisim-contains-insync*: *bisim Vs e e' xs  $\implies$  contains-insync e = contains-insync e'*  
**and** *bisims-contains-insyncs*: *bisims Vs es es' xs  $\implies$  contains-insyncs es = contains-insyncs es'*  
**by**(*induct rule: bisim-bisims.inducts*)(*auto*)

**lemma** *bisims-map-Val-Throw*:

*bisims Vs (map Val vs @ Throw a # es) es' xs  $\longleftrightarrow$  es' = map Val vs @ Throw a # compEs1 Vs es*  
 $\wedge \neg$  *contains-insyncs es*  
**apply**(*induct vs arbitrary: es'*)  
**apply**(*simp*)  
**apply**(*fastforce simp add: compEs1-conv-map*)  
**apply**(*fastforce elim!: bisims-cases intro: bisimsCons2*)  
**done**

**lemma** *compE1-bisim* [*intro*]:  $\llbracket fv\ e \subseteq set\ Vs; \neg\ contains-insync\ e \rrbracket \implies bisim\ Vs\ e\ (compE1\ Vs\ e)\ xs$   
**and** *compEs1-bisims* [*intro*]:  $\llbracket fvs\ es \subseteq set\ Vs; \neg\ contains-insyncs\ es \rrbracket \implies bisims\ Vs\ es\ (compEs1\ Vs\ es)\ xs$

**proof**(*induct Vs e and Vs es arbitrary: xs and xs rule: compE1-compEs1-induct*)

**case** (*BinOp Vs exp1 bop exp2 x*)  
**thus** *?case by(cases is-val exp1)(auto)*  
**next**  
**case** (*AAcc Vs exp1 exp2 x*)  
**thus** *?case by(cases is-val exp1)(auto)*  
**next**  
**case** (*AAss Vs exp1 exp2 exp3 x*)  
**thus** *?case by(cases is-val exp1, cases is-val exp2, fastforce+)*  
**next**  
**case** (*FAss Vs exp1 F D exp2 x*)  
**thus** *?case by(cases is-val exp1, auto)*  
**next**  
**case** (*CAS Vs e1 D F e2 e3 x*)  
**thus** *?case by(cases is-val e1, cases is-val e2, fastforce+)*  
**next**  
**case** (*Call Vs obj M params x*)  
**thus** *?case by(cases is-val obj)(auto)*  
**next**  
**case** (*Block Vs V T vo exp xs*)  
**from**  $\langle fv\ \{V:T=vo;\ exp\} \subseteq set\ Vs \rangle$  **have**  $fv\ exp \subseteq set\ (Vs@[V])$  **by**(*auto*)  
**with** *Block* **show** *?case by(cases vo)(auto)*  
**next**  
**case** (*Cons Vs exp list x*)

**thus** ?case **by**(cases is-val exp)(auto intro!: bisimsCons2)  
**qed**(auto)

**lemma** bisim-hidden-unmod:  $\llbracket \text{bisim } Vs \ e \ e' \ xs; \text{hidden } Vs \ i \rrbracket \implies \text{unmod } e' \ i$   
**and** bisims-hidden-unmods:  $\llbracket \text{bisims } Vs \ es \ es' \ xs; \text{hidden } Vs \ i \rrbracket \implies \text{unmods } es' \ i$   
**by**(induct rule: bisim-bisims.inducts)(auto intro: hidden-unmod hidden-unmods dest: hidden-inacc hidden-lengthD)

**lemma** bisim-fv-unmod:  $\llbracket \text{bisim } Vs \ e \ e' \ xs; i < \text{length } Vs; Vs ! i \notin \text{fv } e \rrbracket \implies \text{unmod } e' \ i$   
**and** bisims-fvs-unmods:  $\llbracket \text{bisims } Vs \ es \ es' \ xs; i < \text{length } Vs; Vs ! i \notin \text{fvs } es \rrbracket \implies \text{unmods } es' \ i$

**proof**(induct rule: bisim-bisims.inducts)

**case** (bisimBlockNone Vs V e e' xs T)

**note** len =  $\langle i < \text{length } Vs \rangle$

**have** unmod e' i

**proof**(cases Vs ! i = V)

**case** True

**from** len **have** hidden (Vs @ [Vs ! i]) i **by**(rule hidden-snoc-nth)

**with** len True  $\langle \text{bisim } (Vs @ [V]) \ e \ e' \ xs \rangle$  **show** ?thesis **by**(auto intro: bisim-hidden-unmod)

**next**

**case** False

**with** bisimBlockNone **show** ?thesis **by**(auto simp add: nth-append)

**qed**

**thus** ?case **by** simp

**next**

**case** (bisimBlockSome Vs V e e' xs v T)

**note** len =  $\langle i < \text{length } Vs \rangle$

**show** ?case

**proof**(cases Vs ! i = V)

**case** True

**from** len **have** hidden (Vs @ [Vs ! i]) i **by**(rule hidden-snoc-nth)

**with** len True  $\langle \text{bisim } (Vs @ [V]) \ e \ e' \ (xs[\text{length } Vs := v]) \rangle$

**show** ?thesis **by**(auto intro: bisim-hidden-unmod)

**next**

**case** False

**with** bisimBlockSome **show** ?thesis **by**(auto simp add: nth-append)

**qed**

**next**

**case** (bisimBlockSomeNone Vs V e e' xs v T)

**note** len =  $\langle i < \text{length } Vs \rangle$

**show** ?case

**proof**(cases Vs ! i = V)

**case** True

**from** len **have** hidden (Vs @ [Vs ! i]) i **by**(rule hidden-snoc-nth)

**with** len True  $\langle \text{bisim } (Vs @ [V]) \ e \ e' \ xs \rangle$

**show** ?thesis **by**(auto intro: bisim-hidden-unmod)

**next**

**case** False

**with** bisimBlockSomeNone **show** ?thesis **by**(auto simp add: nth-append)

**qed**

**qed**(fastforce dest: fv-unmod-compE1 fvs-unmods-compEs1 index-le-lengthD simp add: nth-append)+

**lemma** bisim-extRet2J [intro!]:  $\text{bisim } Vs \ e \ e' \ xs \implies \text{bisim } Vs \ (\text{extRet2J } e \ va) \ (\text{extRet2J1 } e' \ va) \ xs$   
**by**(cases va) auto

**lemma** *bisims-map-Val-conv2* [*simp*]: *bisims Vs es (map Val vs) xs = (es = map Val vs)*  
**apply**(*induct vs arbitrary: es*)  
**apply**(*fastforce elim!: bisims-cases*)  
**done**

**lemma** *bisims-map-Val-Throw2*:  
*bisims Vs es' (map Val vs @ Throw a # es) xs  $\longleftrightarrow$*   
*( $\exists es''$ .  $es' = \text{map Val vs @ Throw a \# es''} \wedge es = \text{compEs1 Vs es''} \wedge \neg \text{contains-insyncs es''}$ )*  
**apply**(*induct vs arbitrary: es'*)  
**apply**(*simp*)  
**apply**(*fastforce simp add: compEs1-conv-map*)  
**apply**(*fastforce elim!: bisims-cases*)  
**done**

**lemma** *hidden-bisim-unmod*:  $\llbracket \text{bisim Vs } e \ e' \ xs; \text{hidden Vs } i \rrbracket \implies \text{unmod } e' \ i$   
**and** *hidden-bisims-unmods*:  $\llbracket \text{bisims Vs } es \ es' \ xs; \text{hidden Vs } i \rrbracket \implies \text{unmods } es' \ i$   
**apply**(*induct rule: bisim-bisims.inducts*)  
**apply**(*auto simp add: hidden-inacc intro: hidden-unmod hidden-unmods*)  
**apply**(*auto simp add: hidden-def*)  
**done**

**lemma** *bisim-list-list-all2-conv*:  
*bisim-list es exs'  $\longleftrightarrow$  list-all2 bisim01 es exs'*  
**proof**  
**assume** *bisim-list es exs'*  
**thus** *list-all2 bisim01 es exs'*  
**by** *induct(auto intro!: bisim01.intros)*  
**next**  
**assume** *list-all2 bisim01 es exs'*  
**thus** *bisim-list es exs'*  
**by**(*induct es arbitrary: exs' (auto intro!: bisim-listCons bisim-listNil elim!: bisim01.cases simp add: list-all2-Cons1)*)  
**qed**

**lemma** *bisim-list-extTA2J0-extTA2J1*:  
**assumes** *wf: wf-J-prog P*  
**and** *sees: P  $\vdash$  C sees M:[]  $\rightarrow$  T =  $\lfloor \text{meth} \rfloor$  in D*  
**shows** *bisim-list1 (extNTA2J0 P (C, M, a)) (extNTA2J1 (compP1 P) (C, M, a))*  
**proof** –  
**obtain** *pns body* **where** *meth = (pns, body)* **by**(*cases meth*)  
**with** *sees* **have** *sees: P  $\vdash$  C sees M:[]  $\rightarrow$  T =  $\lfloor (pns, body) \rfloor$  in D* **by** *simp*  
**moreover** **let** *?xs = Addr a # replicate (max-vars body) undefined-value*  
**let** *?e' = {0:Class D=None; compE1 (this # pns) body}*  
**have** *bisim-list1 ({this:Class D= $\lfloor \text{Addr a} \rfloor$ ; body}, []) ((?e', ?xs), [])*  
**proof**  
**show** *bisim-list [] [] ..*  
**from** *sees-wf-mdecl[OF wf-prog-wwf-prog[OF wf] sees]* **have** *fv body  $\subseteq$  set [this] pns = []*  
**by**(*auto simp add: wf-mdecl-def*)  
**thus** *fv ({this:Class D= $\lfloor \text{Addr a} \rfloor$ ; body}) = {}* **by** *simp*  
**from** *sees-wf-mdecl[OF wf sees]* **obtain** *T' where P, [this  $\mapsto$  Class D]  $\vdash$  body :: T' this  $\notin$  set pns*  
**and** *D body  $\lfloor \text{dom [this  $\mapsto$  Addr a]} \rfloor$*  **by**(*auto simp add: wf-mdecl-def*)  
**hence**  $\neg \text{contains-insync body}$  **by**(*auto simp add: contains-insync-conv dest: WT-expr-locks*)  
**with**  $\langle \text{fv body} \subseteq \text{set [this]} \rangle$   
**have** *bisim ([[] @ [this]] body (compE1 (this # pns) body) ?xs)*

**unfolding** *append.simps*  $\langle \text{pns} = [] \rangle$  **by** (*rule compE1-bisim*)  
**hence** *bisim*  $[] \{ \text{this:Class } D = [\text{Addr } a]; \text{body} \} \{ \text{length } ([] :: \text{String.literal list}): \text{Class } D = \text{None};$   
*compE1* (*this* # *pns*) *body*  $\rangle ?xs$   
**by** (*rule bisimBlockSomeNone*) (*simp*)  
**thus** *bisim*  $[] \{ \{ \text{this:Class } D = [\text{Addr } a]; \text{body} \} \} ?e' ?xs$  **by** *simp*  
**from**  $\langle \mathcal{D} \text{ body } [\text{dom } [\text{this} \mapsto \text{Addr } a]] \rangle$  **show**  $\mathcal{D} \{ \{ \text{this:Class } D = [\text{Addr } a]; \text{body} \} \} [\{\}]$  **by** *simp*  
**show** *max-vars*  $?e' \leq \text{length } ?xs$  **by** *simp*  
**qed**  
**ultimately show** *?thesis* **by** (*simp*)  
**qed**

**lemma** *bisim-max-vars*: *bisim*  $Vs \ e \ e' \ xs \implies \text{max-vars } e = \text{max-vars } e'$   
**and** *bisims-max-varss*: *bisims*  $Vs \ es \ es' \ xs \implies \text{max-varss } es = \text{max-varss } es'$   
**apply** (*induct rule: bisim-bisims.inducts*)  
**apply** (*auto simp add: max-vars-compE1 max-varss-compEs1*)  
**done**

**lemma** *bisim-call*: *bisim*  $Vs \ e \ e' \ xs \implies \text{call } e = \text{call } e'$   
**and** *bisims-calls*: *bisims*  $Vs \ es \ es' \ xs \implies \text{calls } es = \text{calls } es'$   
**apply** (*induct rule: bisim-bisims.inducts*)  
**apply** (*auto simp add: is-vals-conv*)  
**done**

**lemma** *bisim-call-None-call1*:  $\llbracket \text{bisim } Vs \ e \ e' \ xs; \text{call } e = \text{None} \rrbracket \implies \text{call1 } e' = \text{None}$   
**and** *bisims-calls-None-calls1*:  $\llbracket \text{bisims } Vs \ es \ es' \ xs; \text{calls } es = \text{None} \rrbracket \implies \text{calls1 } es' = \text{None}$   
**by** (*induct rule: bisim-bisims.inducts*) (*auto simp add: is-vals-conv split: if-split-asm*)

**lemma** *bisim-call1-Some-call*:  
 $\llbracket \text{bisim } Vs \ e \ e' \ xs; \text{call1 } e' = [aMvs] \rrbracket \implies \text{call } e = [aMvs]$

**and** *bisims-calls1-Some-calls*:  
 $\llbracket \text{bisims } Vs \ es \ es' \ xs; \text{calls1 } es' = [aMvs] \rrbracket \implies \text{calls } es = [aMvs]$   
**by** (*induct rule: bisim-bisims.inducts*) (*auto simp add: is-vals-conv split: if-split-asm*)

**lemma** *blocks-bisim*:

**assumes** *bisim*: *bisim* ( $Vs \ @ \ pns$ )  $e \ e' \ xs$   
**and** *length*: *length*  $vs = \text{length } pns \ \text{length } Ts = \text{length } pns$   
**and** *xs*:  $\forall i. i < \text{length } vs \longrightarrow xs ! (i + \text{length } Vs) = vs ! i$   
**shows** *bisim*  $Vs \ (\text{blocks } pns \ Ts \ vs \ e) \ (\text{blocks1 } (\text{length } Vs) \ Ts \ e') \ xs$   
**using** *bisim length xs*  
**proof** (*induct pns Ts vs e arbitrary: e' Vs rule: blocks.induct*)  
**case** ( $1 \ V \ Vs \ T \ Ts \ v \ vs \ e \ e' \ VS$ )  
**note**  $IH = \langle \bigwedge e' \ Vs a. \llbracket \text{bisim } (Vs a \ @ \ Vs) \ e \ e' \ xs; \text{length } vs = \text{length } Vs; \text{length } Ts = \text{length } Vs; \forall i < \text{length } vs. xs ! (i + \text{length } Vs a) = vs ! i \rrbracket$   
 $\implies \text{bisim } Vs a \ (\text{blocks } Vs \ Ts \ vs \ e) \ (\text{blocks1 } (\text{length } Vs a) \ Ts \ e') \ xs \rangle$   
**note**  $xs = \langle \forall i < \text{length } (v \ # \ vs). xs ! (i + \text{length } VS) = (v \ # \ vs) ! i \rangle$   
**hence**  $xs' : \forall i < \text{length } vs. xs ! (i + \text{length } (VS \ @ \ [V])) = vs ! i$  **and**  $v : xs ! \text{length } VS = v$  **by** (*auto*)  
**from**  $\langle \text{bisim } (VS \ @ \ V \ # \ Vs) \ e \ e' \ xs \rangle$  **have** *bisim*  $((VS \ @ \ [V]) \ @ \ Vs) \ e \ e' \ xs$  **by** *simp*  
**from**  $IH[OF \ \text{this} \ - \ - \ xs']$   $\langle \text{length } (v \ # \ vs) = \text{length } (V \ # \ Vs) \rangle \langle \text{length } (T \ # \ Ts) = \text{length } (V \ # \ Vs) \rangle$   
**have** *bisim*  $(VS \ @ \ [V]) \ (\text{blocks } Vs \ Ts \ vs \ e) \ (\text{blocks1 } (\text{length } (VS \ @ \ [V])) \ Ts \ e') \ xs$   
**by** *auto*  
**hence** *bisim*  $VS \ (\{ V : T = [v]; \text{blocks } Vs \ Ts \ vs \ e \}) \{ \text{length } VS : T = \text{None}; \text{blocks1 } (\text{length } (VS \ @ \ [V])) \}$

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Ts e'} xs
  using v by(rule bisimBlockSomeNone)
  thus ?case by simp
qed(auto)

lemma fixes e :: ('a,'b,'addr) exp and es :: ('a,'b,'addr) exp list
  shows inline-call-max-vars: call e = [aMvs]  $\implies$  max-vars (inline-call e' e)  $\leq$  max-vars e + max-vars e'
  and inline-calls-max-varss: calls es = [aMvs]  $\implies$  max-varss (inline-calls e' es)  $\leq$  max-varss es + max-vars e'
  by(induct e and es rule: call.induct calls.induct)(auto)

lemma assumes final E bisim VS E E' xs
  shows inline-call-compE1: call e = [aMvs]  $\implies$  inline-call E' (compE1 Vs e) = compE1 Vs (inline-call E e)
  and inline-calls-compEs1: calls es = [aMvs]  $\implies$  inline-calls E' (compEs1 Vs es) = compEs1 Vs (inline-calls E es)
  proof(induct Vs e and Vs es rule: compE1-compEs1-induct)
    case (Call Vs obj M params)
    note IHobj =  $\langle$ call obj = [aMvs]  $\implies$  inline-call E' (compE1 Vs obj) = compE1 Vs (inline-call E obj) $\rangle$ 
    note IHparams =  $\langle$ calls params = [aMvs]  $\implies$  inline-calls E' (compEs1 Vs params) = compEs1 Vs (inline-calls E params) $\rangle$ 
    obtain a M' vs where [simp]: aMvs = (a, M', vs) by (cases aMvs, auto)
    with  $\langle$ call (obj.M(params)) = [aMvs] $\rangle$  have call (obj.M(params)) = [(a, M', vs)] by simp
    thus ?case
  proof(induct rule: call-callE)
    case CallObj
    with IHobj have inline-call E' (compE1 Vs obj) = compE1 Vs (inline-call E obj) by auto
    with CallObj show ?case by auto
  next
    case (CallParams v)
    with IHparams have inline-calls E' (compEs1 Vs params) = compEs1 Vs (inline-calls E params)
  by auto
  with CallParams show ?case by(auto simp add: is-vals-conv)
  next
    case Call
    with  $\langle$ final E $\rangle$   $\langle$ bisim VS E E' xs $\rangle$  show ?case by(auto simp add: is-vals-conv)
  qed
qed(auto split: if-split-asm)

lemma assumes bisim: bisim VS E E' XS
  and final: final E
  shows bisim-inline-call:
     $\llbracket$  bisim Vs e e' xs; call e = [aMvs]; fv e  $\subseteq$  set Vs  $\rrbracket$ 
     $\implies$  bisim Vs (inline-call E e) (inline-call E' e') xs

  and bisims-inline-calls:
     $\llbracket$  bisims Vs es es' xs; calls es = [aMvs]; fvs es  $\subseteq$  set Vs  $\rrbracket$ 
     $\implies$  bisims Vs (inline-calls E es) (inline-calls E' es') xs
  proof(induct rule: bisim-bisims.inducts)
    case (bisimBinOp1 Vs e e' xs bop e'')
    thus ?case by(cases is-val (inline-call E e))(fastforce)+
  next

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    case (bisimAAcc1 Vs a a' xs i)
    thus ?case by(cases is-val (inline-call E a))(fastforce)+
next
    case (bisimAAss1 Vs a a' xs i e)
    thus ?case by(cases is-val (inline-call E a), cases is-val i)(fastforce)+
next
    case (bisimAAss2 Vs i i' xs a e)
    thus ?case by(cases is-val (inline-call E i))(fastforce)+
next
    case (bisimFAss1 Vs e e' xs F D e'')
    thus ?case by(cases is-val (inline-call E e))(fastforce)+
next
    case (bisimCAS1 Vs e e' xs e2 e3 D F)
    thus ?case
      apply(cases is-val (inline-call E e))
      apply(cases is-val e2)
      apply(fastforce)
      apply clarsimp
      apply(safe; clarsimp?)
      apply auto
    done
next
    case (bisimCAS2 Vs e e' xs e3 v D F)
    thus ?case by(cases is-val (inline-call E e); safe?; clarsimp; fastforce)
next
    case (bisimCallObj Vs e e' xs es M)
    obtain a M' vs where aMvs = (a, M', vs) by(cases aMvs, auto)
    with ⟨call (e.M(es)) = [aMvs]⟩ have call (e.M(es)) = [(a, M', vs)] by simp
    thus ?case
    proof(induct rule: call-callE)
      case CallObj
      with ⟨fv (e.M(es)) ⊆ set Vs⟩ ⟨aMvs = (a, M', vs)⟩
      ⟨[call e = [aMvs]; fv e ⊆ set Vs] ⟹ bisim Vs (inline-call E e) (inline-call E' e') xs⟩
      have IH': bisim Vs (inline-call E e) (inline-call E' e') xs by(auto)
      with ⟨bisim Vs e e' xs⟩ ⟨fv (e.M(es)) ⊆ set Vs⟩ CallObj ⟨¬ contains-insyncs es⟩ show ?thesis
        by(cases is-val (inline-call E e))(fastforce)+
    next
      case (CallParams v)
      hence inline-calls E' (compEs1 Vs es) = compEs1 Vs (inline-calls E es)
        by -(rule inline-calls-compEs1[OF final bisim])
      moreover from ⟨fv (e.M(es)) ⊆ set Vs⟩ final fvs-inline-calls[of E es]
      have fvs (inline-calls E es) ⊆ set Vs by(auto elim!: final.cases)
      moreover note CallParams ⟨bisim Vs e e' xs⟩ ⟨fv (e.M(es)) ⊆ set Vs⟩ ⟨¬ contains-insyncs es⟩
    final
      ultimately show ?case by(auto simp add: is-vals-conv final-iff)
    next
      case Call
      with final bisim ⟨bisim Vs e e' xs⟩ show ?case by(auto simp add: is-vals-conv)
    qed
  next
    case (bisimCallParams Vs es es' xs v M)
    obtain a M' vs where [simp]: aMvs = (a, M', vs) by(cases aMvs, auto)
    with ⟨call (Val v.M(es)) = [aMvs]⟩ have call (Val v.M(es)) = [(a, M', vs)] by simp
    thus ?case

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proof(induct rule: call-callE)
  case CallObj thus ?case by simp
next
  case (CallParams v')
    with  $\langle \llbracket \text{calls } es = \lfloor aMvs \rfloor; fvs\ es \subseteq \text{set } Vs \rrbracket \implies \text{bisims } Vs\ (\text{inline-calls } E\ es)\ (\text{inline-calls } E'\ es') \rangle$ 
     $\langle fv\ (Val\ v \cdot M(es)) \subseteq \text{set } Vs \rangle$ 
    have bisims Vs (inline-calls E es) (inline-calls E' es') xs by(auto)
    with final bisim  $\langle \text{bisims } Vs\ es\ es'\ xs \rangle$  show ?case by(auto simp add: is-vals-conv)
next
  case Call
    with final bisim  $\langle \text{bisims } Vs\ es\ es'\ xs \rangle$  show ?case by(auto)
qed
next
  case (bisimsCons1 Vs e e' xs es)
  thus ?case by(cases is-val (inline-call E e))(fastforce)+
qed(fastforce)+

declare hyperUn-ac [simp del]

lemma sqInt-lem3:  $\llbracket A \sqsubseteq A'; B \sqsubseteq B' \rrbracket \implies A \sqcap B \sqsubseteq A' \sqcap B'$ 
by(auto simp add: hyperset-defs)

lemma sqUn-lem3:  $\llbracket A \sqsubseteq A'; B \sqsubseteq B' \rrbracket \implies A \sqcup B \sqsubseteq A' \sqcup B'$ 
by(auto simp add: hyperset-defs)

lemma A-inline-call: call e =  $\lfloor aMvs \rfloor \implies \mathcal{A}\ e \sqsubseteq \mathcal{A}\ (\text{inline-call } e'\ e)$ 
  and As-inline-calls: calls es =  $\lfloor aMvs \rfloor \implies \mathcal{A}s\ es \sqsubseteq \mathcal{A}s\ (\text{inline-calls } e'\ es)$ 
proof(induct e and es rule: call.induct calls.induct)
  case (Call obj M params)
    obtain a M' vs where [simp]: aMvs = (a, M', vs) by(cases aMvs, auto)
    with  $\langle \text{call } (obj \cdot M(params)) = \lfloor aMvs \rfloor \rangle$  have call (obj·M(params)) =  $\lfloor (a, M', vs) \rfloor$  by simp
    thus ?case
  proof(induct rule: call-callE)
    case CallObj
      with  $\langle \text{call } obj = \lfloor aMvs \rfloor \implies \mathcal{A}\ obj \sqsubseteq \mathcal{A}\ (\text{inline-call } e'\ obj) \rangle$ 
      show ?case by(auto intro: sqUn-lem)
    next
      case CallParams
        with  $\langle \text{calls } params = \lfloor aMvs \rfloor \implies \mathcal{A}s\ params \sqsubseteq \mathcal{A}s\ (\text{inline-calls } e'\ params) \rangle$ 
        show ?case by(auto intro: sqUn-lem)
    next
      case Call
        thus ?case by(auto simp add: hyperset-defs)
    qed
  next
    case Block thus ?case by(fastforce intro: diff-lem)
  next
    case throw thus ?case by(simp add: hyperset-defs)
  next
    case TryCatch thus ?case by(auto intro: sqInt-lem)
qed(fastforce intro: sqUn-lem sqUn-lem2)+

lemma assumes final e'
  shows defass-inline-call:  $\llbracket \text{call } e = \lfloor aMvs \rfloor; \mathcal{D}\ e\ A \rrbracket \implies \mathcal{D}\ (\text{inline-call } e'\ e)\ A$ 

```

**and** *defasss-inline-calls*:  $\llbracket \text{calls } es = \lfloor aMvs \rfloor; \mathcal{D}s \text{ es } A \rrbracket \implies \mathcal{D}s (\text{inline-calls } e' \text{ es}) A$   
**proof**(*induct e and es arbitrary: A and A rule: call.induct calls.induct*)  
**case** (*Call obj M params A*)  
**obtain**  $a \ M' \ vs$  **where** [*simp*]:  $aMvs = (a, M', vs)$  **by**(*cases aMvs, auto*)  
**with**  $\langle \text{call } (obj \cdot M(params)) = \lfloor aMvs \rfloor \rangle$  **have**  $\text{call } (obj \cdot M(params)) = \lfloor (a, M', vs) \rfloor$  **by** *simp*  
**thus** ?*case*  
**proof**(*cases rule: call-callE*)  
**case** *CallObj*  
**with**  $\langle \mathcal{D} (obj \cdot M(params)) A \rangle \langle \llbracket \text{call obj} = \lfloor aMvs \rfloor; \mathcal{D} \text{ obj } A \rrbracket \implies \mathcal{D} (\text{inline-call } e' \text{ obj}) A \rangle$   
**have**  $\mathcal{D} (\text{inline-call } e' \text{ obj}) A$  **by** *simp*  
**moreover from** *A-inline-call[OF CallObj, of e']*  
**have**  $A \sqcup (\mathcal{A} \text{ obj}) \sqsubseteq A \sqcup (\mathcal{A} (\text{inline-call } e' \text{ obj}))$  **by**(*rule sqUn-lem2*)  
**with**  $\langle \mathcal{D} (obj \cdot M(params)) A \rangle$  **have**  $\mathcal{D}s \text{ params } (A \sqcup \mathcal{A} (\text{inline-call } e' \text{ obj}))$  **by**(*auto elim: Ds-mono'*)  
**ultimately show** ?*thesis* **using** *CallObj* **by** *auto*  
**next**  
**case** (*CallParams v*)  
**with**  $\langle \mathcal{D} (obj \cdot M(params)) A \rangle \langle \llbracket \text{calls params} = \lfloor aMvs \rfloor; \mathcal{D}s \text{ params } A \rrbracket \implies \mathcal{D}s (\text{inline-calls } e' \text{ params}) A \rangle$   
**have**  $\mathcal{D}s (\text{inline-calls } e' \text{ params}) A$  **by**(*simp*)  
**with** *CallParams* **show** ?*thesis* **by**(*auto*)  
**next**  
**case** *Call*  
**with**  $\langle \text{final } e' \rangle$  **show** ?*thesis* **by**(*auto elim!: D-mono' simp add: hyperset-defs*)  
**qed**  
**next**  
**case** (*Cons-exp exp exps A*)  
**show** ?*case*  
**proof**(*cases is-val exp*)  
**case** *True*  
**with**  $\langle \mathcal{D}s (\text{exp} \# \text{exps}) A \rangle \langle \llbracket \text{calls exps} = \lfloor aMvs \rfloor; \mathcal{D}s \text{ exps } A \rrbracket \implies \mathcal{D}s (\text{inline-calls } e' \text{ exps}) A \rangle$   
 $\langle \text{calls } (\text{exp} \# \text{exps}) = \lfloor aMvs \rfloor \rangle$   
**have**  $\mathcal{D}s (\text{inline-calls } e' \text{ exps}) A$  **by**(*auto*)  
**with** *True* **show** ?*thesis* **by**(*auto*)  
**next**  
**case** *False*  
**with**  $\langle \llbracket \text{call exp} = \lfloor aMvs \rfloor; \mathcal{D} \text{ exp } A \rrbracket \implies \mathcal{D} (\text{inline-call } e' \text{ exp}) A \rangle \langle \text{calls } (\text{exp} \# \text{exps}) = \lfloor aMvs \rfloor \rangle$   
 $\langle \mathcal{D}s (\text{exp} \# \text{exps}) A \rangle$   
**have**  $\mathcal{D} (\text{inline-call } e' \text{ exp}) A$  **by** *auto*  
**moreover from** *False*  $\langle \text{calls } (\text{exp} \# \text{exps}) = \lfloor aMvs \rfloor \rangle$  **have**  $\mathcal{A} \text{ exp} \sqsubseteq \mathcal{A} (\text{inline-call } e' \text{ exp})$   
**by**(*auto intro: A-inline-call*)  
**hence**  $A \sqcup \mathcal{A} \text{ exp} \sqsubseteq A \sqcup \mathcal{A} (\text{inline-call } e' \text{ exp})$  **by**(*rule sqUn-lem2*)  
**with**  $\langle \mathcal{D}s (\text{exp} \# \text{exps}) A \rangle$  **have**  $\mathcal{D}s \text{ exps } (A \sqcup \mathcal{A} (\text{inline-call } e' \text{ exp}))$   
**by**(*auto intro: Ds-mono'*)  
**ultimately show** ?*thesis* **using** *False* **by**(*auto*)  
**qed**  
**qed**(*fastforce split: if-split-asm elim: D-mono' intro: sqUn-lem2 sqUn-lem A-inline-call*)  
  
**lemma** *bisim-B*:  $\text{bisim } Vs \ e \ E \ xs \implies \mathcal{B} \ E \ (\text{length } Vs)$   
**and** *bisims-Bs*:  $\text{bisims } Vs \ es \ Es \ xs \implies \mathcal{B}s \ Es \ (\text{length } Vs)$   
**apply**(*induct rule: bisim-bisims.inducts*)  
**apply**(*auto intro: B Bs*)  
**done**

**lemma** *bisim-expr-locks-eq*:  $\text{bisim } Vs \ e \ e' \ xs \implies \text{expr-locks } e = \text{expr-locks } e'$



**and** *bisims-expr-lockss-eq*: *bisims*  $\forall s$  *es es' xs*  $\implies$  *expr-lockss* *es* = *expr-lockss* *es'*  
**by**(*induct* rule: *bisim-bisims.inducts*)(*auto* *intro!*: *ext*)

**lemma** *bisim-list-expr-lockss-eq*: *bisim-list* *es* *es'*  $\implies$  *expr-lockss* *es* = *expr-lockss* (*map fst* *es'*)  
**apply**(*induct* rule: *bisim-list.induct*)  
**apply**(*auto* *dest*: *bisim-expr-lockss-eq*)  
**done**

**context** *J1-heap-base* **begin**

**lemma** [*simp*]:  
**fixes** *e* :: ('a, 'b, 'addr) *exp* **and** *es* :: ('a, 'b, 'addr) *exp list*  
**shows**  $\tau\text{move1-comp}P$ :  $\tau\text{move1}$  (*compP* *f* *P*) *h e* =  $\tau\text{move1}$  *P h e*  
**and**  $\tau\text{moves1-comp}P$ :  $\tau\text{moves1}$  (*compP* *f* *P*) *h es* =  $\tau\text{moves1}$  *P h es*  
**by**(*induct* *e* **and** *es* rule:  $\tau\text{move1.induct}$   $\tau\text{moves1.induct}$ ) *auto*

**lemma**  $\tau\text{Move1-comp}P$  [*simp*]:  $\tau\text{Move1}$  (*compP* *f* *P*) =  $\tau\text{Move1}$  *P*  
**by**(*intro* *ext*) *auto*

**lemma** *red1-preserves-unmod*:  
 $\llbracket \text{uf}, P, t \vdash 1 \langle e, s \rangle \text{--}ta\rightarrow \langle e', s' \rangle; \text{unmod } e \ i \rrbracket \implies (\text{lcl } s') ! i = (\text{lcl } s) ! i$   
**and** *reds1-preserves-unmod*:  
 $\llbracket \text{uf}, P, t \vdash 1 \langle es, s \rangle \text{--}[-ta\rightarrow] \langle es', s' \rangle; \text{unmods } es \ i \rrbracket \implies (\text{lcl } s') ! i = (\text{lcl } s) ! i$   
**apply**(*induct* rule: *red1-reds1.inducts*)  
**apply**(*auto* *split*: *if-split-asm*)  
**done**

**lemma** *red1-unmod-preserved*:  
 $\llbracket \text{uf}, P, t \vdash 1 \langle e, s \rangle \text{--}ta\rightarrow \langle e', s' \rangle; \text{unmod } e \ i \rrbracket \implies \text{unmod } e' \ i$   
**and** *reds1-unmods-preserved*:  
 $\llbracket \text{uf}, P, t \vdash 1 \langle es, s \rangle \text{--}[-ta\rightarrow] \langle es', s' \rangle; \text{unmods } es \ i \rrbracket \implies \text{unmods } es' \ i$   
**by**(*induct* rule: *red1-reds1.inducts*)(*auto* *split*: *if-split-asm*)

**lemma**  $\tau\text{red1t-unmod-preserved}$ :  
 $\llbracket \tau\text{red1gt } \text{uf } P \ t \ h \ (e, xs) \ (e', xs'); \text{unmod } e \ i \rrbracket \implies \text{unmod } e' \ i$   
**by**(*induct* rule: *trancpl-induct2*)(*auto* *intro*: *red1-unmod-preserved*)

**lemma**  $\tau\text{red1r-unmod-preserved}$ :  
 $\llbracket \tau\text{red1gr } \text{uf } P \ t \ h \ (e, xs) \ (e', xs'); \text{unmod } e \ i \rrbracket \implies \text{unmod } e' \ i$   
**by**(*induct* rule: *rtrancpl-induct2*)(*auto* *intro*: *red1-unmod-preserved*)

**lemma**  $\tau\text{red1t-preserves-unmod}$ :  
 $\llbracket \tau\text{red1gt } \text{uf } P \ t \ h \ (e, xs) \ (e', xs'); \text{unmod } e \ i; i < \text{length } xs \rrbracket$   
 $\implies xs' ! i = xs ! i$   
**apply**(*induct* rule: *trancpl-induct2*)  
**apply**(*auto* *dest*: *red1-preserves-unmod*)  
**apply**(*drule* *red1-preserves-unmod*)  
**apply**(*erule* (1)  $\tau\text{red1t-unmod-preserved}$ )  
**apply**(*drule*  $\tau\text{red1t-preserves-len}$ )  
**apply** *auto*  
**done**

**lemma**  $\tau\text{red1}'r\text{-preserves-unmod}$ :

```

  [[red1gr uf P t h (e, xs) (e', xs'); unmod e i; i < length xs]]
  ==> xs' ! i = xs ! i
apply(induct rule: converse-rtrancpl-induct2)
apply(auto dest: red1-preserves-unmod red1-unmod-preserved red1-preserves-len)
apply(frule (1) red1-unmod-preserved)
apply(frule red1-preserves-len)
apply(frule (1) red1-preserves-unmod)
apply auto
done

end

context J-heap-base begin

lemma [simp]:
  fixes e :: ('a, 'b, 'addr) exp and es :: ('a, 'b, 'addr) exp list
  shows  $\tau\text{move0-comp}P$ :  $\tau\text{move0 (comp}P f P) h e = \tau\text{move0 } P h e$ 
  and  $\tau\text{moves0-comp}P$ :  $\tau\text{moves0 (comp}P f P) h es = \tau\text{moves0 } P h es$ 
by(induct e and es rule:  $\tau\text{move0.induct } \tau\text{moves0.induct}$  auto)

lemma  $\tau\text{Move0-comp}P$  [simp]:  $\tau\text{Move0 (comp}P f P) = \tau\text{Move0 } P$ 
by(intro ext auto)

end

end

```

## 7.23 Unlocking a sync block never fails

**theory** *Correctness1Threaded imports*

*J0J1Bisim*

*../Framework/FWInitFinLift*

**begin**

**definition** *lock-oks1* ::

('addr, 'thread-id) locks

$\Rightarrow$  ('addr, 'thread-id, (('a, 'b, 'addr) exp  $\times$  'c)  $\times$  (('a, 'b, 'addr) exp  $\times$  'c) list) thread-info  $\Rightarrow$  bool

**where**

$\bigwedge l n. \text{lock-oks1 } ls \ ts \equiv \forall t. (\text{case } (ts \ t) \text{ of } None \Rightarrow (\forall l. \text{has-locks } (ls \ \$ \ l) \ t = 0)$

$\mid [((ex, \text{exs}), \text{ln})] \Rightarrow (\forall l. \text{has-locks } (ls \ \$ \ l) \ t + \text{ln} \ \$ \ l = \text{expr-lockss (map fst (ex \# \text{exs})) } l))$

**primrec** *el-loc-ok* :: 'addr expr1  $\Rightarrow$  'addr locals1  $\Rightarrow$  bool

**and** *els-loc-ok* :: 'addr expr1 list  $\Rightarrow$  'addr locals1  $\Rightarrow$  bool

**where**

*el-loc-ok* (new C) xs  $\longleftrightarrow$  True

| *el-loc-ok* (newA T[e]) xs  $\longleftrightarrow$  *el-loc-ok* e xs

| *el-loc-ok* (Cast T e) xs  $\longleftrightarrow$  *el-loc-ok* e xs

| *el-loc-ok* (e instanceof T) xs  $\longleftrightarrow$  *el-loc-ok* e xs

| *el-loc-ok* (e«bop»e') xs  $\longleftrightarrow$  *el-loc-ok* e xs  $\wedge$  *el-loc-ok* e' xs

| *el-loc-ok* (Var V) xs  $\longleftrightarrow$  True

| *el-loc-ok* (Val v) xs  $\longleftrightarrow$  True

| *el-loc-ok* (V := e) xs  $\longleftrightarrow$  *el-loc-ok* e xs

$| \text{el-loc-ok } (a[i]) \text{ } xs \longleftrightarrow \text{el-loc-ok } a \text{ } xs \wedge \text{el-loc-ok } i \text{ } xs$   
 $| \text{el-loc-ok } (a[i] := e) \text{ } xs \longleftrightarrow \text{el-loc-ok } a \text{ } xs \wedge \text{el-loc-ok } i \text{ } xs \wedge \text{el-loc-ok } e \text{ } xs$   
 $| \text{el-loc-ok } (a.\text{length}) \text{ } xs \longleftrightarrow \text{el-loc-ok } a \text{ } xs$   
 $| \text{el-loc-ok } (e.F\{D\}) \text{ } xs \longleftrightarrow \text{el-loc-ok } e \text{ } xs$   
 $| \text{el-loc-ok } (e.F\{D\} := e') \text{ } xs \longleftrightarrow \text{el-loc-ok } e \text{ } xs \wedge \text{el-loc-ok } e' \text{ } xs$   
 $| \text{el-loc-ok } (e.\text{compareAndSwap}(D.F, e', e'')) \text{ } xs \longleftrightarrow \text{el-loc-ok } e \text{ } xs \wedge \text{el-loc-ok } e' \text{ } xs \wedge \text{el-loc-ok } e'' \text{ } xs$   
 $| \text{el-loc-ok } (e.M(ps)) \text{ } xs \longleftrightarrow \text{el-loc-ok } e \text{ } xs \wedge \text{els-loc-ok } ps \text{ } xs$   
 $| \text{el-loc-ok } \{V:T=vo; e\} \text{ } xs \longleftrightarrow (\text{case } vo \text{ of } \text{None} \Rightarrow \text{el-loc-ok } e \text{ } xs \mid [v] \Rightarrow \text{el-loc-ok } e \text{ } (xs[V := v]))$   
 $| \text{el-loc-ok } (\text{sync}_V(e) \text{ } e') \text{ } xs \longleftrightarrow \text{el-loc-ok } e \text{ } xs \wedge \text{el-loc-ok } e' \text{ } xs \wedge \text{unmod } e' \text{ } V$   
 $| \text{el-loc-ok } (\text{insync}_V(a) \text{ } e) \text{ } xs \longleftrightarrow xs ! V = \text{Addr } a \wedge \text{el-loc-ok } e \text{ } xs \wedge \text{unmod } e \text{ } V$   
 $| \text{el-loc-ok } (e;;e') \text{ } xs \longleftrightarrow \text{el-loc-ok } e \text{ } xs \wedge \text{el-loc-ok } e' \text{ } xs$   
 $| \text{el-loc-ok } (\text{if } (b) \text{ } e \text{ else } e') \text{ } xs \longleftrightarrow \text{el-loc-ok } b \text{ } xs \wedge \text{el-loc-ok } e \text{ } xs \wedge \text{el-loc-ok } e' \text{ } xs$   
 $| \text{el-loc-ok } (\text{while } (b) \text{ } c) \text{ } xs \longleftrightarrow \text{el-loc-ok } b \text{ } xs \wedge \text{el-loc-ok } c \text{ } xs$   
 $| \text{el-loc-ok } (\text{throw } e) \text{ } xs \longleftrightarrow \text{el-loc-ok } e \text{ } xs$   
 $| \text{el-loc-ok } (\text{try } e \text{ catch } (C \text{ } V) \text{ } e') \text{ } xs \longleftrightarrow \text{el-loc-ok } e \text{ } xs \wedge \text{el-loc-ok } e' \text{ } xs$

$| \text{els-loc-ok } [] \text{ } xs \longleftrightarrow \text{True}$   
 $| \text{els-loc-ok } (e \# es) \text{ } xs \longleftrightarrow \text{el-loc-ok } e \text{ } xs \wedge \text{els-loc-ok } es \text{ } xs$

**lemma** *el-loc-okI*:  $\llbracket \neg \text{contains-insync } e; \text{syncvars } e; \mathcal{B} \text{ } e \text{ } n \rrbracket \Longrightarrow \text{el-loc-ok } e \text{ } xs$   
**and** *els-loc-okI*:  $\llbracket \neg \text{contains-insyncs } es; \text{syncvarss } es; \mathcal{B}s \text{ } es \text{ } n \rrbracket \Longrightarrow \text{els-loc-ok } es \text{ } xs$   
**by**(*induct e and es arbitrary: xs n and xs n rule: el-loc-ok.induct els-loc-ok.induct*)(*auto intro: fv-B-unmod*)

**lemma** *el-loc-ok-compE1*:  $\llbracket \neg \text{contains-insync } e; \text{fv } e \subseteq \text{set } Vs \rrbracket \Longrightarrow \text{el-loc-ok } (\text{compE1 } Vs \text{ } e) \text{ } xs$   
**and** *els-loc-ok-compEs1*:  $\llbracket \neg \text{contains-insyncs } es; \text{fvs } es \subseteq \text{set } Vs \rrbracket \Longrightarrow \text{els-loc-ok } (\text{compEs1 } Vs \text{ } es)$   
*xs*  
**by**(*auto intro: el-loc-okI els-loc-okI syncvars-compE1 syncvarss-compEs1 B Bs simp del: compEs1-conv-map*)

**lemma shows** *el-loc-ok-not-contains-insync-local-change*:  
 $\llbracket \neg \text{contains-insync } e; \text{el-loc-ok } e \text{ } xs \rrbracket \Longrightarrow \text{el-loc-ok } e \text{ } xs'$   
**and** *els-loc-ok-not-contains-insyncs-local-change*:  
 $\llbracket \neg \text{contains-insyncs } es; \text{els-loc-ok } es \text{ } xs \rrbracket \Longrightarrow \text{els-loc-ok } es \text{ } xs'$   
**by**(*induct e and es arbitrary: xs xs' and xs xs' rule: el-loc-ok.induct els-loc-ok.induct*)(*fastforce*)+

**lemma** *el-loc-ok-update*:  $\llbracket \mathcal{B} \text{ } e \text{ } n; V < n \rrbracket \Longrightarrow \text{el-loc-ok } e \text{ } (xs[V := v]) = \text{el-loc-ok } e \text{ } xs$   
**and** *els-loc-ok-update*:  $\llbracket \mathcal{B}s \text{ } es \text{ } n; V < n \rrbracket \Longrightarrow \text{els-loc-ok } es \text{ } (xs[V := v]) = \text{els-loc-ok } es \text{ } xs$   
**apply**(*induct e and es arbitrary: n xs and n xs rule: el-loc-ok.induct els-loc-ok.induct*)  
**apply**(*auto simp add: list-update-swap*)  
**done**

**lemma** *els-loc-ok-map-Val [simp]*:  
 $\text{els-loc-ok } (\text{map } Val \text{ } vs) \text{ } xs$   
**by**(*induct vs*) *auto*

**lemma** *els-loc-ok-map-Val-append [simp]*:  
 $\text{els-loc-ok } (\text{map } Val \text{ } vs @ es) \text{ } xs = \text{els-loc-ok } es \text{ } xs$   
**by**(*induct vs*) *auto*

**lemma** *el-loc-ok-extRet2J [simp]*:  
 $\text{el-loc-ok } e \text{ } xs \Longrightarrow \text{el-loc-ok } (\text{extRet2J } e \text{ } va) \text{ } xs$   
**by**(*cases va*) *auto*

**definition** *el-loc-ok1* ::  $((\text{nat}, \text{nat}, 'addr) \text{exp} \times 'addr \text{locals1}) \times ((\text{nat}, \text{nat}, 'addr) \text{exp} \times 'addr \text{locals1})$   
 $\text{list} \Rightarrow \text{bool}$

**where**  $el\text{-}loc\text{-}ok1 = (\lambda((e, xs), exs). el\text{-}loc\text{-}ok\ e\ xs \wedge sync\text{-}ok\ e \wedge (\forall (e, xs) \in set\ exs. el\text{-}loc\text{-}ok\ e\ xs \wedge sync\text{-}ok\ e))$

**lemma**  $el\text{-}loc\text{-}ok1\text{-}simps$ :

$el\text{-}loc\text{-}ok1\ ((e, xs), exs) = (el\text{-}loc\text{-}ok\ e\ xs \wedge sync\text{-}ok\ e \wedge (\forall (e, xs) \in set\ exs. el\text{-}loc\text{-}ok\ e\ xs \wedge sync\text{-}ok\ e))$   
**by** ( $simp\ add: el\text{-}loc\text{-}ok1\text{-}def$ )

**lemma**  $el\text{-}loc\text{-}ok\text{-}blocks1$  [ $simp$ ]:

$el\text{-}loc\text{-}ok\ (blocks1\ n\ Ts\ body)\ xs = el\text{-}loc\text{-}ok\ body\ xs$   
**by** ( $induct\ n\ Ts\ body\ rule: blocks1.induct$ ) *auto*

**lemma**  $sync\text{-}oks\text{-}blocks1$  [ $simp$ ]:  $sync\text{-}ok\ (blocks1\ n\ Ts\ e) = sync\text{-}ok\ e$

**by** ( $induct\ n\ Ts\ e\ rule: blocks1.induct$ ) *auto*

**lemma** **assumes**  $fin$ :  $final\ e'$

**shows**  $el\text{-}loc\text{-}ok\text{-}inline\text{-}call$ :  $el\text{-}loc\text{-}ok\ e\ xs \implies el\text{-}loc\text{-}ok\ (inline\text{-}call\ e'\ e)\ xs$

**and**  $els\text{-}loc\text{-}ok\text{-}inline\text{-}calls$ :  $els\text{-}loc\text{-}ok\ es\ xs \implies els\text{-}loc\text{-}ok\ (inline\text{-}calls\ e'\ es)\ xs$

**apply** ( $induct\ e\ \mathbf{and}\ es\ arbitrary: xs\ \mathbf{and}\ xs\ rule: el\text{-}loc\text{-}ok.induct\ els\text{-}loc\text{-}ok.induct$ )

**apply** ( $insert\ fin$ )

**apply** ( $auto\ simp\ add: unmod\text{-}inline\text{-}call$ )

**done**

**lemma** **assumes**  $sync\text{-}ok\ e'$

**shows**  $sync\text{-}ok\text{-}inline\text{-}call$ :  $sync\text{-}ok\ e \implies sync\text{-}ok\ (inline\text{-}call\ e'\ e)$

**and**  $sync\text{-}oks\text{-}inline\text{-}calls$ :  $sync\text{-}oks\ es \implies sync\text{-}oks\ (inline\text{-}calls\ e'\ es)$

**apply** ( $induct\ e\ \mathbf{and}\ es\ rule: sync\text{-}ok.induct\ sync\text{-}oks.induct$ )

**apply** ( $insert\ \langle sync\text{-}ok\ e' \rangle$ )

**apply** *auto*

**done**

**lemma**  $bisim\text{-}sync\text{-}ok$ :

$bisim\ Vs\ e\ e'\ xs \implies sync\text{-}ok\ e$

$bisim\ Vs\ e\ e'\ xs \implies sync\text{-}ok\ e'$

**and**  $bisims\text{-}sync\text{-}oks$ :

$bisims\ Vs\ es\ es'\ xs \implies sync\text{-}oks\ es$

$bisims\ Vs\ es\ es'\ xs \implies sync\text{-}oks\ es'$

**apply** ( $induct\ rule: bisim\text{-}bisims.inducts$ )

**apply** ( $auto\ intro: not\text{-}contains\text{-}insync\text{-}sync\text{-}ok\ not\text{-}contains\text{-}insyncs\text{-}sync\text{-}oks\ simp\ del: compEs1\text{-}conv\text{-}map$ )

**done**

**lemma** **assumes**  $final\ e'$

**shows**  $expr\text{-}locks\text{-}inline\text{-}call\text{-}final$ :

$expr\text{-}locks\ (inline\text{-}call\ e'\ e) = expr\text{-}locks\ e$

**and**  $expr\text{-}lockss\text{-}inline\text{-}calls\text{-}final$ :

$expr\text{-}lockss\ (inline\text{-}calls\ e'\ es) = expr\text{-}lockss\ es$

**apply** ( $induct\ e\ \mathbf{and}\ es\ rule: expr\text{-}locks.induct\ expr\text{-}lockss.induct$ )

**apply** ( $insert\ \langle final\ e' \rangle$ )

**apply** ( $auto\ simp\ add: is\text{-}vals\text{-}conv\ intro: ext$ )

**done**

**lemma**  $lock\text{-}oks1I$ :

$\llbracket \bigwedge t\ l. ts\ t = None \implies has\text{-}locks\ (ls\ \$\ l)\ t = 0;$

$\bigwedge t\ e\ x\ exs\ ln\ l. ts\ t = \lfloor ((e, x), exs), ln \rfloor \implies has\text{-}locks\ (ls\ \$\ l)\ t + ln\ \$\ l = expr\text{-}locks\ e\ l +$

$\text{expr-lockss } (\text{map fst } \text{exs}) \ l \ ]$   
 $\implies \text{lock-oks1 } \text{ls } \text{ts}$   
**apply**(*fastforce simp add: lock-oks1-def*)  
**done**

**lemma** *lock-oks1E*:

$\llbracket \text{lock-oks1 } \text{ls } \text{ts};$   
 $\quad \forall t. \text{ts } t = \text{None} \longrightarrow (\forall l. \text{has-locks } (\text{ls } \$ l) \ t = 0) \implies Q;$   
 $\quad \forall t \ e \ x \ \text{exs } \text{ln}. \text{ts } t = \llbracket ((e, x), \text{exs}), \text{ln} \rrbracket \longrightarrow (\forall l. \text{has-locks } (\text{ls } \$ l) \ t + \text{ln } \$ l = \text{expr-locks } e \ l +$   
 $\text{expr-lockss } (\text{map fst } \text{exs}) \ l) \implies Q \rrbracket$   
 $\implies Q$   
**by**(*fastforce simp add: lock-oks1-def*)

**lemma** *lock-oks1D1*:

$\llbracket \text{lock-oks1 } \text{ls } \text{ts}; \text{ts } t = \text{None} \rrbracket \implies \forall l. \text{has-locks } (\text{ls } \$ l) \ t = 0$   
**apply**(*simp add: lock-oks1-def*)  
**apply**(*erule-tac x=t in allE*)  
**apply**(*auto*)  
**done**

**lemma** *lock-oks1D2*:

$\bigwedge \text{ln}. \llbracket \text{lock-oks1 } \text{ls } \text{ts}; \text{ts } t = \llbracket ((e, x), \text{exs}), \text{ln} \rrbracket \rrbracket$   
 $\implies \forall l. \text{has-locks } (\text{ls } \$ l) \ t + \text{ln } \$ l = \text{expr-locks } e \ l + \text{expr-lockss } (\text{map fst } \text{exs}) \ l \rrbracket$   
**apply**(*fastforce simp add: lock-oks1-def*)  
**done**

**lemma** *lock-oks1-thr-updI*:

$\bigwedge \text{ln}. \llbracket \text{lock-oks1 } \text{ls } \text{ts}; \text{ts } t = \llbracket ((e, \text{xs}), \text{exs}), \text{ln} \rrbracket;$   
 $\quad \forall l. \text{expr-locks } e \ l + \text{expr-lockss } (\text{map fst } \text{exs}) \ l = \text{expr-locks } e' \ l + \text{expr-lockss } (\text{map fst } \text{exs}') \ l \rrbracket$   
 $\implies \text{lock-oks1 } \text{ls } (\text{ts}(t \mapsto (((e', \text{xs}'), \text{exs}'), \text{ln})))$   
**by**(*rule lock-oks1I*)(*auto split: if-split-asm dest: lock-oks1D2 lock-oks1D1*)

**definition** *mbisim-Red1'-Red1* ::

$((\text{'addr}, \text{'thread-id}, (\text{'addr } \text{expr1} \times \text{'addr } \text{locals1}) \times (\text{'addr } \text{expr1} \times \text{'addr } \text{locals1}) \text{ list}, \text{'heap}, \text{'addr})$   
 $\text{state},$   
 $(\text{'addr}, \text{'thread-id}, (\text{'addr } \text{expr1} \times \text{'addr } \text{locals1}) \times (\text{'addr } \text{expr1} \times \text{'addr } \text{locals1}) \text{ list}, \text{'heap}, \text{'addr})$   
 $\text{state}) \text{ bisim}$

**where**

$\text{mbisim-Red1}'\text{-Red1 } s1 \ s2 =$   
 $(s1 = s2 \wedge \text{lock-oks1 } (\text{locks } s1) \ (\text{thr } s1) \wedge \text{ts-ok } (\lambda t \ \text{exxs } h. \ \text{el-loc-ok1 } \text{exxs}) \ (\text{thr } s1) \ (\text{shr } s1))$

**lemma** *sync-ok-blocks*:

$\llbracket \text{length } \text{vs} = \text{length } \text{pns}; \text{length } \text{Ts} = \text{length } \text{pns} \rrbracket$   
 $\implies \text{sync-ok } (\text{blocks } \text{pns } \text{Ts } \text{vs } \text{body}) = \text{sync-ok } \text{body}$   
**by**(*induct pns Ts vs body rule: blocks.induct*) *auto*

**context** *J1-heap-base* **begin**

**lemma** *red1-True-into-red1-False*:

$\llbracket \text{True}, P, t \vdash 1 \ \langle e, s \rangle \text{ --ta--} \langle e', s' \rangle; \text{el-loc-ok } e \ (\text{lcl } s) \rrbracket$   
 $\implies \text{False}, P, t \vdash 1 \ \langle e, s \rangle \text{ --ta--} \langle e', s' \rangle \vee (\exists l. \text{ta} = \llbracket \text{UnlockFail} \rightarrow l \rrbracket \wedge \text{expr-locks } e \ l > 0)$   
**and** *reds1-True-into-reds1-False*:  
 $\llbracket \text{True}, P, t \vdash 1 \ \langle \text{es}, s \rangle \text{ [-ta-]} \langle \text{es}', s' \rangle; \text{els-loc-ok } \text{es} \ (\text{lcl } s) \rrbracket$

$\Rightarrow \text{False}, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \vee (\exists l. ta = \llbracket \text{UnlockFail} \rightarrow l \rrbracket \wedge \text{expr-lockss } es \ l > 0)$   
**apply**(*induct rule*: *red1-reds1.inducts*)  
**apply**(*auto intro*: *red1-reds1.intros split: if-split-asm*)  
**done**

**lemma** *Red1-True-into-Red1-False*:

**assumes**  $\text{True}, P, t \vdash 1 \langle ex/exs, shr \ s \rangle -ta \rightarrow \langle ex'/exs', m' \rangle$   
**and** *el-loc-ok1* (*ex*, *exs*)  
**shows**  $\text{False}, P, t \vdash 1 \langle ex/exs, shr \ s \rangle -ta \rightarrow \langle ex'/exs', m' \rangle \vee$   
 $(\exists l. ta = \llbracket \text{UnlockFail} \rightarrow l \rrbracket \wedge \text{expr-lockss } (fst \ ex \ \# \ map \ fst \ exs) \ l > 0)$

**using** *assms*

**by**(*cases*)(*auto dest*: *Red1.intros red1-True-into-red1-False simp add*: *el-loc-ok1-def ta-upd-simps*)

**lemma shows** *red1-preserves-el-loc-ok*:

$\llbracket uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{sync-ok } e; \text{el-loc-ok } e \ (lcl \ s) \rrbracket \Rightarrow \text{el-loc-ok } e' \ (lcl \ s')$

**and** *reds1-preserves-els-loc-ok*:

$\llbracket uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{sync-oks } es; \text{els-loc-ok } es \ (lcl \ s) \rrbracket \Rightarrow \text{els-loc-ok } es' \ (lcl \ s')$

**proof**(*induct rule*: *red1-reds1.inducts*)

**case** (*Synchronized1Red2* *e s ta e' s' V a*)

**from**  $\langle \text{el-loc-ok } (\text{insync}_V \ (a) \ e) \ (lcl \ s) \rangle$

**have** *el-loc-ok* *e* (*lcl s*) *unmod* *e V lcl s ! V = Addr a* **by** *auto*

**from**  $\langle \text{sync-ok } (\text{insync}_V \ (a) \ e) \rangle$  **have** *sync-ok* *e* **by** *simp*

**hence** *el-loc-ok* *e'* (*lcl s'*)

**using**  $\langle \text{el-loc-ok } e \ (lcl \ s) \rangle$

**by**(*rule Synchronized1Red2*)

**moreover from**  $\langle uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \rangle \langle \text{unmod } e \ V \rangle$  **have** *unmod* *e' V*

**by**(*rule red1-unmod-preserved*)

**moreover from** *red1-preserves-unmod*[*OF*  $\langle uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \rangle \langle \text{unmod } e \ V \rangle \langle lcl \ s ! V = Addr \ a \rangle$

**have** *lcl s' ! V = Addr a* **by** *simp*

**ultimately show** *?case* **by** *auto*

**qed**(*auto elim*: *el-loc-ok-not-contains-insync-local-change els-loc-ok-not-contains-insyncs-local-change*)

**lemma** *red1-preserves-sync-ok*:  $\llbracket uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{sync-ok } e \rrbracket \Rightarrow \text{sync-ok } e'$

**and** *reds1-preserves-sync-oks*:  $\llbracket uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{sync-oks } es \rrbracket \Rightarrow \text{sync-oks } es'$

**by**(*induct rule*: *red1-reds1.inducts*)(*auto elim*: *not-contains-insync-sync-ok*)

**lemma** *Red1-preserves-el-loc-ok1*:

**assumes** *wf*: *wf-J1-prog P*

**shows**  $\llbracket uf, P, t \vdash 1 \langle ex/exs, m \rangle -ta \rightarrow \langle ex'/exs', m' \rangle; \text{el-loc-ok1 } (ex, exs) \rrbracket \Rightarrow \text{el-loc-ok1 } (ex', exs')$

**apply**(*erule Red1.cases*)

**apply**(*auto simp add*: *el-loc-ok1-def dest*: *red1-preserves-el-loc-ok red1-preserves-sync-ok intro*: *el-loc-ok-inline-call sync-ok-inline-call*)

**apply**(*fastforce dest*!: *sees-wf-mdecl*[*OF* *wf*] *simp add*: *wf-mdecl-def intro*!: *el-loc-okI dest*: *WT1-not-contains-insync*  
*intro*: *not-contains-insync-sync-ok*)**+**

**done**

**lemma assumes** *wf*: *wf-J1-prog P*

**shows** *red1-el-loc-ok1-new-thread*:

$\llbracket uf, P, t \vdash 1 \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{NewThread } t' \ (C, M, a) \ h \in \text{set } \llbracket ta \rrbracket_t \rrbracket$

$\Rightarrow \text{el-loc-ok1 } ((\{0:\text{Class } (fst \ (method \ P \ C \ M)) = \text{None}; \text{the } (snd \ (snd \ (snd \ (method \ P \ C \ M))))\},$

*xs*),  $\llbracket \rrbracket$ )

**and** *reds1-el-loc-ok1-new-thread*:  
 $\llbracket uf, P, t \vdash 1 \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; NewThread\ t' (C, M, a) \ h \in set\ \{\{ta\}_t\} \rrbracket$   
 $\implies el\text{-}loc\text{-}ok1\ ((\{0:Class\ (fst\ (method\ P\ C\ M))=None; the\ (snd\ (snd\ (snd\ (method\ P\ C\ M))))\},$   
 $xs), \llbracket \rrbracket)$   
**proof**(*induct rule: red1-reds1.inducts*)  
**case** *Red1CallExternal* **thus** ?*case*  
**apply**(*auto dest!:: red-external-new-thread-sees[OF wf] simp add: el-loc-ok1-simps*)  
**apply**(*auto dest!:: sees-wf-mdecl[OF wf] WT1-not-contains-insync simp add: wf-mdecl-def intro!:*  
*el-loc-ok1 not-contains-insync-sync-ok*)  
**done**  
**qed** *auto*

**lemma** *Red1-el-loc-ok1-new-thread*:

**assumes** *wf: wf-J1-prog P*  
**shows**  $\llbracket uf, P, t \vdash 1 \langle ex/exs, m \rangle -ta \rightarrow \langle ex'/exs', m' \rangle; NewThread\ t'\ exexs\ m' \in set\ \{\{ta\}_t\} \rrbracket$   
 $\implies el\text{-}loc\text{-}ok1\ exexs$   
**by**(*erule Red1.cases*)(*fastforce elim: red1-el-loc-ok1-new-thread[OF wf] simp add: ta-upd-simps*)+

**lemma** *Red1-el-loc-ok*:

**assumes** *wf: wf-J1-prog P*  
**shows** *lifting-wf final-expr1 (mred1g uf P) ( $\lambda t\ exexs\ h. el\text{-}loc\text{-}ok1\ exexs$ )*  
**by**(*unfold-locales*)(*auto elim: Red1-preserves-el-loc-ok1[OF wf] Red1-el-loc-ok1-new-thread[OF wf]*)

**lemma** *mred1-eq-mred1'*:

**assumes** *lok: lock-oks1 (locks s) (thr s)*  
**and** *elo: ts-ok ( $\lambda t\ exexs\ h. el\text{-}loc\text{-}ok1\ exexs$ ) (thr s) (shr s)*  
**and** *tst: thr s t = [(exexs, no-wait-locks)]*  
**and** *aoe: Red1-mthr.actions-ok s t ta*  
**shows** *mred1 P t (exexs, shr s) ta = mred1' P t (exexs, shr s) ta*  
**proof**(*intro ext iffI*)  
**fix** *xm'*  
**assume** *mred1 P t (exexs, shr s) ta xm'*  
**moreover obtain** *ex exs where exexs [simp]: exexs = (ex, exs) by(cases exexs)*  
**moreover obtain** *ex' exs' m' where xm' [simp]: xm' = ((ex', exs'), m') by(cases xm') auto*  
**ultimately have** *red: True, P, t  $\vdash 1 \langle ex/exs, shr s \rangle -ta \rightarrow \langle ex'/exs', m' \rangle$  by simp*  
**from** *elo tst have el-loc-ok1 (ex, exs) by(auto dest: ts-okD)*  
**from** *Red1-True-into-Red1-False[OF red this]*  
**have** *False, P, t  $\vdash 1 \langle ex/exs, shr s \rangle -ta \rightarrow \langle ex'/exs', m' \rangle$*   
**proof**

**assume**  $\exists l. ta = \{\{UnlockFail \rightarrow l\} \wedge 0 < expr\text{-}lockss\ (fst\ ex\ \# \ map\ fst\ exs)\ l$   
**then obtain** *l where ta: ta =  $\{\{UnlockFail \rightarrow l\}$*   
**and** *el: expr-lockss (fst ex  $\# \ map\ fst\ exs$ ) l > 0 by blast*  
**from** *aoe have lock-actions-ok (locks s  $\$ \ l$ ) t ( $\{\{ta\}_l\ \$ \ l$ )*  
**by**(*auto simp add: lock-ok-las-def*)  
**with** *ta have has-locks (locks s  $\$ \ l$ ) t = 0 by simp*  
**with** *lok tst have expr-lockss (map fst (ex  $\# \ exs$ )) l = 0*  
**by**(*cases ex*)(*auto  $\& \ 6$  simp add: lock-oks1-def*)  
**with** *el have False by simp*  
**thus** ?*thesis* ..

**qed**  
**thus** *mred1' P t (exexs, shr s) ta xm' by simp*  
**next**  
**fix** *xm'*  
**assume** *mred1' P t (exexs, shr s) ta xm'*

**thus**  $mred1\ P\ t\ (exes, shr\ s)\ ta\ xm'$   
**by**(cases  $xm'$ )(auto simp add: split-beta intro: Red1-False-into-Red1-True)  
**qed**

**lemma** *Red1-mthr-eq-Red1-mthr'*:

**assumes** *lok*:  $lock-oks1\ (locks\ s)\ (thr\ s)$

**and** *elo*:  $ts-ok\ (\lambda t\ exes\ h.\ el-loc-ok1\ exes)\ (thr\ s)\ (shr\ s)$

**shows**  $Red1-mthr.redT\ True\ P\ s = Red1-mthr.redT\ False\ P\ s$

**proof**(intro ext)

**fix**  $tta\ s'$

**show**  $Red1-mthr.redT\ True\ P\ s\ tta\ s' = Red1-mthr.redT\ False\ P\ s\ tta\ s'$  (**is** ?lhs = ?rhs)

**proof**

**assume** ?lhs **thus** ?rhs

**proof** cases

**case** ( $redT-normal\ t\ x\ ta\ x'\ m'$ )

**from**  $\langle mred1\ P\ t\ (x, shr\ s)\ ta\ (x', m') \rangle$  **have**  $mred1'\ P\ t\ (x, shr\ s)\ ta\ (x', m')$

**unfolding**  $mred1-eq-mred1'$  [OF *lok elo*  $\langle thr\ s\ t = [(x, no-wait-locks)] \rangle$   $\langle Red1-mthr.actions-ok\ s\ t\ ta \rangle$ ].

**thus** ?thesis **using**  $redT-normal(3-)$  **unfolding**  $\langle tta = (t, ta) \rangle$  ..

**next**

**case** ( $redT-acquire\ t\ x\ ln\ n$ )

**from**  $this(2-)$  **show** ?thesis **unfolding**  $redT-acquire(1)$  ..

**qed**

**next**

**assume** ?rhs **thus** ?lhs

**proof**(cases)

**case** ( $redT-normal\ t\ x\ ta\ x'\ m'$ )

**from**  $\langle mred1'\ P\ t\ (x, shr\ s)\ ta\ (x', m') \rangle$  **have**  $mred1\ P\ t\ (x, shr\ s)\ ta\ (x', m')$

**unfolding**  $mred1-eq-mred1'$  [OF *lok elo*  $\langle thr\ s\ t = [(x, no-wait-locks)] \rangle$   $\langle Red1-mthr.actions-ok\ s\ t\ ta \rangle$ ].

**thus** ?thesis **using**  $redT-normal(3-)$  **unfolding**  $\langle tta = (t, ta) \rangle$  ..

**next**

**case** ( $redT-acquire\ t\ x\ ln\ n$ )

**from**  $this(2-)$  **show** ?thesis **unfolding**  $redT-acquire(1)$  ..

**qed**

**qed**

**qed**

**lemma** **assumes** *wf*:  $wf-J1-prog\ P$

**shows** *expr-locks-new-thread1*:

$\llbracket wf, P, t \vdash 1\ \langle e, s \rangle - TA \rightarrow \langle e', s' \rangle; NewThread\ t' (ex, exs)\ h \in set\ (map\ (convert-new-thread-action\ (extNTA2J1\ P))\ \{TA\}_t) \rrbracket$

$\implies expr-lockss\ (map\ fst\ (ex\ \# \ exs)) = (\lambda ad.\ 0)$

**and** *expr-lockss-new-thread1*:

$\llbracket wf, P, t \vdash 1\ \langle es, s \rangle [-TA \rightarrow] \langle es', s' \rangle; NewThread\ t' (ex, exs)\ h \in set\ (map\ (convert-new-thread-action\ (extNTA2J1\ P))\ \{TA\}_t) \rrbracket$

$\implies expr-lockss\ (map\ fst\ (ex\ \# \ exs)) = (\lambda ad.\ 0)$

**proof**(induct rule:  $red1-reds1.inducts$ )

**case** ( $Red1CallExternal\ s\ a\ T\ M\ vs\ ta\ va\ h'\ e'\ s'$ )

**then obtain**  $C\ fs\ ad$  **where**  $subThread: P \vdash C \preceq^* Thread$  **and**  $ext: extNTA2J1\ P\ (C, run, ad) = (ex, exs)$

**by**(fastforce dest:  $red-external-new-thread-sub-thread$ )

**from**  $sub-Thread-sees-run[OF\ wf\ subThread]$  **obtain**  $D\ body$

**where**  $sees: P \vdash C\ sees\ run: [] \rightarrow Void = [body]\ in\ D$  **by** auto



**from** *sees-wf-mdecl*[*OF wf this*] **obtain** *T* **where**  $P, [\text{Class } D] \vdash 1 \text{ body} :: T$   
**by**(*auto simp add: wf-mdecl-def*)  
**hence**  $\neg \text{contains-insync body}$  **by**(*rule WT1-not-contains-insync*)  
**hence**  $\text{expr-locks body} = (\lambda ad. 0)$  **by**(*auto simp add: contains-insync-conv fun-eq-iff*)  
**with** *sees ext* **show** *?case* **by**(*auto*)  
**qed** *auto*

**lemma** *assumes wf: wf-J1-prog P*

**shows** *red1-update-expr-locks*:

$\llbracket \text{False}, P, t \vdash 1 \langle e, s \rangle \rightarrow -ta \rightarrow \langle e', s' \rangle; \text{sync-ok } e; \text{el-loc-ok } e \text{ (lcl } s) \rrbracket$   
 $\implies \text{upd-expr-locks (int o expr-locks } e) \llbracket ta \rrbracket_l = \text{int o expr-locks } e'$

**and** *reds1-update-expr-lockss*:

$\llbracket \text{False}, P, t \vdash 1 \langle es, s \rangle \rightarrow -ta \rightarrow \langle es', s' \rangle; \text{sync-oks } es; \text{els-loc-ok } es \text{ (lcl } s) \rrbracket$   
 $\implies \text{upd-expr-locks (int o expr-lockss } es) \llbracket ta \rrbracket_l = \text{int o expr-lockss } es'$

**proof** –

**have**  $\llbracket \text{False}, P, t \vdash 1 \langle e, s \rangle \rightarrow -ta \rightarrow \langle e', s' \rangle; \text{sync-ok } e; \text{el-loc-ok } e \text{ (lcl } s) \rrbracket$   
 $\implies \text{upd-expr-locks } (\lambda ad. 0) \llbracket ta \rrbracket_l = (\lambda ad. (\text{int o expr-locks } e') ad - (\text{int o expr-locks } e) ad)$   
**and**  $\llbracket \text{False}, P, t \vdash 1 \langle es, s \rangle \rightarrow -ta \rightarrow \langle es', s' \rangle; \text{sync-oks } es; \text{els-loc-ok } es \text{ (lcl } s) \rrbracket$   
 $\implies \text{upd-expr-locks } (\lambda ad. 0) \llbracket ta \rrbracket_l = (\lambda ad. (\text{int o expr-lockss } es') ad - (\text{int o expr-lockss } es) ad)$

**proof**(*induct rule: red1-reds1.inducts*)

**case** *Red1CallExternal thus ?case*

**by**(*auto simp add: fun-eq-iff contains-insync-conv contains-insyncs-conv finfun-upd-apply elim!*:  
*red-external.cases*)

**qed**(*fastforce simp add: fun-eq-iff contains-insync-conv contains-insyncs-conv finfun-upd-apply*) +

**hence**  $\llbracket \text{False}, P, t \vdash 1 \langle e, s \rangle \rightarrow -ta \rightarrow \langle e', s' \rangle; \text{sync-ok } e; \text{el-loc-ok } e \text{ (lcl } s) \rrbracket$   
 $\implies \text{upd-expr-locks } (\lambda ad. 0 + (\text{int o expr-locks } e) ad) \llbracket ta \rrbracket_l = \text{int o expr-locks } e'$

**and**  $\llbracket \text{False}, P, t \vdash 1 \langle es, s \rangle \rightarrow -ta \rightarrow \langle es', s' \rangle; \text{sync-oks } es; \text{els-loc-ok } es \text{ (lcl } s) \rrbracket$   
 $\implies \text{upd-expr-locks } (\lambda ad. 0 + (\text{int o expr-lockss } es) ad) \llbracket ta \rrbracket_l = \text{int o expr-lockss } es'$

**by**(*fastforce simp only: upd-expr-locks-add*) +

**thus**  $\llbracket \text{False}, P, t \vdash 1 \langle e, s \rangle \rightarrow -ta \rightarrow \langle e', s' \rangle; \text{sync-ok } e; \text{el-loc-ok } e \text{ (lcl } s) \rrbracket$   
 $\implies \text{upd-expr-locks (int o expr-locks } e) \llbracket ta \rrbracket_l = \text{int o expr-locks } e'$

**and**  $\llbracket \text{False}, P, t \vdash 1 \langle es, s \rangle \rightarrow -ta \rightarrow \langle es', s' \rangle; \text{sync-oks } es; \text{els-loc-ok } es \text{ (lcl } s) \rrbracket$   
 $\implies \text{upd-expr-locks (int o expr-lockss } es) \llbracket ta \rrbracket_l = \text{int o expr-lockss } es'$

**by**(*auto simp add: o-def*)

**qed**

**lemma** *Red1'-preserves-lock-oks*:

**assumes** *wf: wf-J1-prog P*

**and** *Red: Red1-mthr.redT False P s1 ta1 s1'*

**and** *locks: lock-oks1 (locks s1) (thr s1)*

**and** *sync: ts-ok* ( $\lambda t \text{ exes } h. \text{el-loc-ok1 exes}$ ) (*thr s1*) (*shr s1*)

**shows** *lock-oks1 (locks s1') (thr s1')*

**using** *Red*

**proof**(*cases rule: Red1-mthr.redT.cases*)

**case** (*redT-normal t x ta x' m'*)

**note** [*simp*] =  $\langle ta1 = (t, ta) \rangle$

**obtain** *ex exs* **where**  $x: x = (ex, exs)$  **by** (*cases x*)

**obtain** *ex' exs'* **where**  $x': x' = (ex', exs')$  **by** (*cases x'*)

**note** *thrst* =  $\langle \text{thr } s1 \text{ } t = \lfloor (x, \text{no-wait-locks}) \rfloor \rangle$

**note** *aoe* =  $\langle \text{Red1-mthr.actions-ok } s1 \text{ } t \text{ } ta \rangle$

**from**  $\langle \text{mred1}' P \text{ } t \text{ } (x, \text{shr } s1) \text{ } ta \text{ } (x', m') \rangle$

**have** *red: False, P, t*  $\vdash 1 \langle ex/exs, \text{shr } s1 \rangle \rightarrow -ta \rightarrow \langle ex'/exs', m' \rangle$

**unfolding**  $x \text{ } x'$  **by** *simp-all*

```

note  $s1' = \langle redT\text{-}upd\ s1\ t\ ta\ x'\ m'\ s1' \rangle$ 
moreover from  $red$ 
have  $lock\text{-}oks1\ (locks\ s1')\ (thr\ s1')$ 
proof cases
  case  $(red1Red\ e\ x\ TA\ e'\ x')$ 
    note  $[simp] = \langle ex = (e, x) \rangle \langle ta = extTA2J1\ P\ TA \rangle \langle ex' = (e', x') \rangle \langle exs' = exs \rangle$ 
    and  $red = \langle False, P, t \vdash 1\ \langle e, (shr\ s1, x) \rangle - TA \rightarrow \langle e', (m', x') \rangle \rangle$ 
    { fix  $t'$ 
      assume  $None: ((redT\text{-}updTs\ (thr\ s1)\ (map\ (convert\text{-}new\text{-}thread\text{-}action\ (extNTA2J1\ P))\ \{\!\!\{TA\}\!\!\}_t))(t$ 
 $\mapsto (((e', x'), exs), redT\text{-}updLns\ (locks\ s1)\ t\ (snd\ (the\ (thr\ s1\ t)))\ \{\!\!\{TA\}\!\!\}_l))\ t' = None$ 
      { fix  $l$ 
        from  $aoe$  have  $lock\text{-}actions\text{-}ok\ (locks\ s1\ \$\ l)\ t\ (\{\!\!\{ta\}\!\!\}_l\ \$\ l)$  by  $(auto\ simp\ add: lock\text{-}ok\text{-}las\text{-}def)$ 
        with  $None$  have  $has\text{-}locks\ ((redT\text{-}updLs\ (locks\ s1)\ t\ \{\!\!\{ta\}\!\!\}_l)\ \$\ l)\ t' = has\text{-}locks\ (locks\ s1\ \$\ l)\ t'$ 
by  $(auto\ split: if\text{-}split\text{-}asm)$ 
        also from  $loks\ None$  have  $has\text{-}locks\ (locks\ s1\ \$\ l)\ t' = 0$  unfolding  $lock\text{-}oks1\text{-}def$ 
by  $(force\ split: if\text{-}split\text{-}asm\ dest!: redT\text{-}updTs\text{-}None)$ 
        finally have  $has\text{-}locks\ (upd\text{-}locks\ (locks\ s1\ \$\ l)\ t\ (\{\!\!\{TA\}\!\!\}_l\ \$\ l))\ t' = 0$  by  $simp$  }
        hence  $\forall l. has\text{-}locks\ (upd\text{-}locks\ (locks\ s1\ \$\ l)\ t\ (\{\!\!\{TA\}\!\!\}_l\ \$\ l))\ t' = 0 \ .. \}$ 
      }
      moreover {
        fix  $t'\ eX\ eXS\ LN$ 
        assume  $Some: ((redT\text{-}updTs\ (thr\ s1)\ (map\ (convert\text{-}new\text{-}thread\text{-}action\ (extNTA2J1\ P))\ \{\!\!\{TA\}\!\!\}_t))(t$ 
 $\mapsto (((e', x'), exs), redT\text{-}updLns\ (locks\ s1)\ t\ (snd\ (the\ (thr\ s1\ t)))\ \{\!\!\{TA\}\!\!\}_l))\ t' = [(eX, eXS), LN]$ 
        { fix  $l$ 
          from  $aoe$  have  $lao: lock\text{-}actions\text{-}ok\ (locks\ s1\ \$\ l)\ t\ (\{\!\!\{ta\}\!\!\}_l\ \$\ l)$  by  $(auto\ simp\ add: lock\text{-}ok\text{-}las\text{-}def)$ 
          have  $has\text{-}locks\ ((redT\text{-}updLs\ (locks\ s1)\ t\ \{\!\!\{ta\}\!\!\}_l)\ \$\ l)\ t' + LN\ \$\ l = expr\text{-}lockss\ (map\ fst\ (eX\ \#$ 
 $eXS))\ l$ 
          proof  $(cases\ t = t')$ 
            case  $True$ 
            from  $loks\ thrst\ x$ 
            have  $has\text{-}locks\ (locks\ s1\ \$\ l)\ t = expr\text{-}locks\ e\ l + expr\text{-}lockss\ (map\ fst\ exs)\ l$ 
by  $(force\ simp\ add: lock\text{-}oks1\text{-}def)$ 
            hence  $lock\text{-}expr\text{-}locks\text{-}ok\ (locks\ s1\ \$\ l)\ t\ 0\ (int\ (expr\text{-}locks\ e\ l + expr\text{-}lockss\ (map\ fst\ exs)\ l))$ 
by  $(simp\ add: lock\text{-}expr\text{-}locks\text{-}ok\text{-}def)$ 
            with  $lao$  have  $lock\text{-}expr\text{-}locks\text{-}ok\ (upd\text{-}locks\ (locks\ s1\ \$\ l)\ t\ (\{\!\!\{ta\}\!\!\}_l\ \$\ l))\ t\ (upd\text{-}threadRs\ 0$ 
 $(locks\ s1\ \$\ l)\ t\ (\{\!\!\{ta\}\!\!\}_l\ \$\ l))$ 
             $(upd\text{-}expr\text{-}lock\text{-}actions\ (int\ (expr\text{-}locks\ e\ l + expr\text{-}lockss\ (map\ fst\ exs)\ l))\ (\{\!\!\{ta\}\!\!\}_l\ \$\ l))$ 
by  $(rule\ upd\text{-}locks\text{-}upd\text{-}expr\text{-}lock\text{-}preserve\text{-}lock\text{-}expr\text{-}locks\text{-}ok)$ 
            moreover from  $sync\ thrst\ x$  have  $sync\text{-}ok\ e\ el\text{-}loc\text{-}ok\ e\ x$ 
unfolding  $el\text{-}loc\text{-}ok1\text{-}def$  by  $(auto\ dest: ts\text{-}okD)$ 
            with  $red1\text{-}update\text{-}expr\text{-}locks[OF\ wf\ red]$ 
            have  $upd\text{-}expr\text{-}locks\ (int\ \circ\ expr\text{-}locks\ e)\ \{\!\!\{TA\}\!\!\}_l = int\ \circ\ expr\text{-}locks\ e'$  by  $(simp)$ 
            hence  $upd\text{-}expr\text{-}lock\text{-}actions\ (int\ (expr\text{-}locks\ e\ l))\ (\{\!\!\{TA\}\!\!\}_l\ \$\ l) = int\ (expr\text{-}locks\ e'\ l)$ 
by  $(simp\ add: upd\text{-}expr\text{-}locks\text{-}def\ fun\text{-}eq\text{-}iff)$ 
            ultimately show  $?thesis$  using  $lao\ Some\ thrst\ x\ True$ 
by  $(auto\ simp\ add: lock\text{-}expr\text{-}locks\text{-}ok\text{-}def\ upd\text{-}expr\text{-}locks\text{-}def)$ 
          }
        }
      }
    }
  }
  next
  case  $False$ 
  from  $aoe$  have  $tok: thread\text{-}oks\ (thr\ s1)\ \{\!\!\{ta\}\!\!\}_t$  by  $auto$ 
  show  $?thesis$ 
  proof  $(cases\ thr\ s1\ t' = None)$ 
    case  $True$ 
    with  $Some\ tok\ False$  obtain  $m$ 
    where  $nt: NewThread\ t'\ (eX, eXS)\ m \in set\ (map\ (convert\text{-}new\text{-}thread\text{-}action\ (extNTA2J1$ 
 $P))\ \{\!\!\{TA\}\!\!\}_t)$ 

```

```

    and [simp]: LN = no-wait-locks by(auto dest: redT-updTs-new-thread)
    note expr-locks-new-thread1[OF wf red nt]
    moreover from loks True have has-locks (locks s1 $ l) t' = 0
      by(force simp add: lock-oks1-def)
    ultimately show ?thesis using lao False by simp
  next
    case False
    with Some ⟨t ≠ t'⟩ tok
    have thr s1 t' = [((eX, eXS), LN)] by(fastforce dest: redT-updTs-Some[OF - tok])
    with loks tok lao ⟨t ≠ t'⟩ show ?thesis by(cases eX)(auto simp add: lock-oks1-def)
  qed
qed }
  hence ∀ l. has-locks ((redT-updLs (locks s1) t {ta}_l) $ l) t' + LN $ l = expr-lockss (map fst (eX
# eXS)) l .. }
  ultimately show ?thesis using s1' unfolding lock-oks1-def x' by(clarsimp simp del: fun-upd-apply)
next
  case (red1Call e a M vs U Ts T body D x)
  from wf ⟨P ⊢ class-type-of U sees M: Ts → T = [body] in D⟩
  obtain T' where P, Class D # Ts ⊢ 1 body :: T'
    by(auto simp add: wf-mdecl-def dest!: sees-wf-mdecl)
  hence expr-locks (blocks1 0 (Class D # Ts) body) = (λ l. 0)
  by(auto simp add: expr-locks-blocks1 contains-insync-conv fun-eq-iff dest!: WT1-not-contains-insync)
  thus ?thesis using red1Call thrst loks s1'
    unfolding lock-oks1-def x' x
    by auto force+
next
  case (red1Return e' x' e x)
  thus ?thesis using thrst loks s1'
    unfolding lock-oks1-def x' x
    apply(auto simp add: redT-updWs-def elim!: rtrancl3p-cases)
    apply(erule-tac x=t in allE)
    apply(erule conjE)
    apply(erule disjE)
    apply(force simp add: expr-locks-inline-call-final ac-simps)
    apply(fastforce simp add: expr-locks-inline-call-final)
    apply hypsubst-thin
    apply(erule-tac x=ta in allE)
    apply fastforce
  done
qed
  moreover from sync ⟨mred1' P t (x, shr s1) ta (x', m')⟩ thrst aoe s1'
  have ts-ok (λ t exxs h. el-loc-ok1 exxs) (thr s1') (shr s1')
    by(auto intro: lifting-wf.redT-updTs-preserves[OF Red1-el-loc-ok[OF wf]])
  ultimately show ?thesis by simp
next
  case (redT-acquire t x n ln)
  thus ?thesis using loks unfolding lock-oks1-def
    apply auto
    apply force
    apply(case-tac ln $ l::nat)
    apply simp
    apply(erule allE)
    apply(erule conjE)
    apply(erule allE)+

```

```

    apply(erule (1) impE)
    apply(erule-tac x=l in allE)
    apply fastforce
    apply(erule may-acquire-allE)
    apply(erule allE)
    apply(erule-tac x=l in allE)
    apply(erule impE)
    apply simp
    apply(simp only: has-locks-acquire-locks-conv)
    apply(erule conjE)
    apply(erule allE)+
    apply(erule (1) impE)
    apply(erule-tac x=l in allE)
    apply simp
  done
qed

lemma Red1'-Red1-bisimulation:
  assumes wf: wf-J1-prog P
  shows bisimulation (Red1-mthr.redT False P) (Red1-mthr.redT True P) mbisim-Red1'-Red1 (=)
proof
  fix s1 s2 tl1 s1'
  assume mbisim-Red1'-Red1 s1 s2 and Red1-mthr.redT False P s1 tl1 s1'
  thus  $\exists s2' tl2. \text{Red1-mthr.redT True P } s2 \text{ } tl2 \text{ } s2' \wedge \text{mbisim-Red1'-Red1 } s1' \text{ } s2' \wedge tl1 = tl2$ 
  by(cases tl1)(auto simp add: mbisim-Red1'-Red1-def Red1-mthr-eq-Red1-mthr' simp del: split-paired-Ex
    elim: Red1'-preserves-lock-oks[OF wf] lifting-wf.redT-preserves[OF Red1-el-loc-ok, OF wf])
  next
    fix s1 s2 tl2 s2'
    assume mbisim-Red1'-Red1 s1 s2 Red1-mthr.redT True P s2 tl2 s2'
    thus  $\exists s1' tl1. \text{Red1-mthr.redT False P } s1 \text{ } tl1 \text{ } s1' \wedge \text{mbisim-Red1'-Red1 } s1' \text{ } s2' \wedge tl1 = tl2$ 
    by(cases tl2)(auto simp add: mbisim-Red1'-Red1-def Red1-mthr-eq-Red1-mthr' simp del: split-paired-Ex
      elim: Red1'-preserves-lock-oks[OF wf] lifting-wf.redT-preserves[OF Red1-el-loc-ok, OF wf])
  qed

lemma Red1'-Red1-bisimulation-final:
  wf-J1-prog P
   $\implies$  bisimulation-final (Red1-mthr.redT False P) (Red1-mthr.redT True P)
    mbisim-Red1'-Red1 (=) Red1-mthr.mfinal Red1-mthr.mfinal
  apply(intro-locales)
  apply(erule Red1'-Red1-bisimulation)
  apply(unfold-locales)
  apply(auto simp add: mbisim-Red1'-Red1-def)
  done

lemma bisim-J1-J1-start:
  assumes wf: wf-J1-prog P
  and wf-start: wf-start-state P C M vs
  shows mbisim-Red1'-Red1 (J1-start-state P C M vs) (J1-start-state P C M vs)
proof -
  from wf-start obtain Ts T body D
  where sees:  $P \vdash C \text{ sees } M:Ts \rightarrow T = [body]$  in D
  and conf:  $P, \text{start-heap} \vdash vs [;\leq] Ts$ 
  by cases
  let ?e = blocks1 0 (Class C#Ts) body

```

```

let ?xs = Null # vs @ replicate (max-vars body) undefined-value

from sees-wf-mdecl[OF wf sees] obtain T'
  where B:  $\mathcal{B}$  body (Suc (length Ts))
  and wt: P, Class D # Ts  $\vdash 1$  body :: T'
  and da:  $\mathcal{D}$  body [ $\{\dots\}$ length Ts]
  and sv: syncvars body
  by(auto simp add: wf-mdecl-def)

from wt have expr-locks ?e = ( $\lambda$ -. 0) by(auto intro: WT1-expr-locks)
thus ?thesis using da sees sv B
  unfolding start-state-def
  by(fastforce simp add: mbisim-Red1'-Red1-def lock-oks1-def el-loc-ok1-def contains-insync-conv
intro!: ts-okI expr-locks-sync-ok split: if-split-asm intro: el-loc-okI)
qed

lemma Red1'-Red1-bisim-into-weak:
  assumes wf: wf-J1-prog P
  shows bisimulation-into-delay (Red1-mthr.redT False P) (Red1-mthr.redT True P) mbisim-Red1'-Red1
  (=) (Red1-mthr.m $\tau$ move P) (Red1-mthr.m $\tau$ move P)
proof -
  interpret b: bisimulation Red1-mthr.redT False P Red1-mthr.redT True P mbisim-Red1'-Red1 (=)
  by(rule Red1'-Red1-bisimulation[OF wf])
  show ?thesis by(unfold-locales)(simp add: mbisim-Red1'-Red1-def)
qed

end

sublocale J1-heap-base < Red1-mthr:
  if- $\tau$ multithreaded-wf
  final-expr1
  mred1g uf P
  convert-RA
   $\tau$ MOVE1 P
  for uf P
by(unfold-locales)

context J1-heap-base begin

abbreviation if-lock-oks1 ::
  ('addr, 'thread-id) locks
   $\Rightarrow$  ('addr, 'thread-id, (status  $\times$  (('a, 'b, 'addr) exp  $\times$  'c)  $\times$  (('a, 'b, 'addr) exp  $\times$  'c) list)) thread-info
   $\Rightarrow$  bool
where
  if-lock-oks1 ls ts  $\equiv$  lock-oks1 ls (init-fin-descend-thr ts)

definition if-mbisim-Red1'-Red1 ::
  (('addr, 'thread-id, status  $\times$  (('addr expr1  $\times$  'addr locals1)  $\times$  ('addr expr1  $\times$  'addr locals1) list), 'heap, 'addr)
  state,
  ('addr, 'thread-id, status  $\times$  (('addr expr1  $\times$  'addr locals1)  $\times$  ('addr expr1  $\times$  'addr locals1) list), 'heap, 'addr)
  state) bisim
where
  if-mbisim-Red1'-Red1 s1 s2  $\longleftrightarrow$ 
  s1 = s2  $\wedge$  if-lock-oks1 (locks s1) (thr s1)  $\wedge$  ts-ok (init-fin-lift ( $\lambda$ t exers h. el-loc-ok1 exers)) (thr s1)

```

(shr s1)

**lemma** *if-mbisim-Red1'-Red1-imp-mbisim-Red1'-Red1:*

*if-mbisim-Red1'-Red1 s1 s2  $\implies$  mbisim-Red1'-Red1 (init-fin-descend-state s1) (init-fin-descend-state s2)*

**by**(auto simp add: mbisim-Red1'-Red1-def if-mbisim-Red1'-Red1-def ts-ok-init-fin-descend-state)

**lemma** *if-Red1-mthr-imp-if-Red1-mthr':*

**assumes** lok: *if-lock-oks1* (locks s) (thr s)

**and** elo: *ts-ok* (init-fin-lift ( $\lambda t$  exes h. el-loc-ok1 exes)) (thr s) (shr s)

**and** Red: *Red1-mthr.if.redT* uf P s tta s'

**shows** *Red1-mthr.if.redT* ( $\neg$  uf) P s tta s'

**using** Red

**proof**(cases)

**case** (*redT-acquire* t x ln n)

**from** *this*(2-) **show** ?thesis **unfolding** *redT-acquire*(1) ..

**next**

**case** (*redT-normal* t x ta x' m')

**note** aok =  $\langle \text{Red1-mthr.if.actions-ok } s \text{ } t \text{ } ta \rangle$

**and** tst =  $\langle \text{thr } s \text{ } t = \lfloor (x, \text{no-wait-locks}) \rfloor \rangle$

**from**  $\langle \text{Red1-mthr.init-fin uf } P \text{ } t \text{ } (x, \text{shr } s) \text{ } ta \text{ } (x', m') \rangle$

**have** *Red1-mthr.init-fin* ( $\neg$  uf) P t (x, shr s) ta (x', m')

**proof**(cases)

**case** *InitialThreadAction* **show** ?thesis **unfolding** *InitialThreadAction* ..

**next**

**case** (*ThreadFinishAction* exes)

**from**  $\langle \text{final-expr1 } exes \rangle$  **show** ?thesis **unfolding** *ThreadFinishAction* ..

**next**

**case** (*NormalAction* exes ta' exes')

**let** ?s = *init-fin-descend-state* s

**from** lok **have** *lock-oks1* (locks ?s) (thr ?s) **by**(simp)

**moreover from** elo **have** elo: *ts-ok* ( $\lambda t$  exes h. el-loc-ok1 exes) (thr ?s) (shr ?s)

**by**(simp add: *ts-ok-init-fin-descend-state*)

**moreover from** tst  $\langle x = (\text{Running}, exes) \rangle$

**have** thr ?s t =  $\lfloor (exes, \text{no-wait-locks}) \rfloor$  **by** simp

**moreover from** aok **have** *Red1-mthr.actions-ok* ?s t ta'

**using**  $\langle ta = \text{convert-TA-initial } (\text{convert-obs-initial } ta') \rangle$  **by** auto

**ultimately have** *mred1* P t (exes, shr ?s) ta' = *mred1'* P t (exes, shr ?s) ta'

**by**(rule *mred1-eq-mred1'*)

**with**  $\langle \text{mred1g uf } P \text{ } t \text{ } (exes, \text{shr } s) \text{ } ta' \text{ } (exes', m') \rangle$

**have** *mred1g* ( $\neg$  uf) P t (exes, shr s) ta' (exes', m')

**by**(cases uf) simp-all

**thus** ?thesis **unfolding** *NormalAction*(1-3) **by**(rule *Red1-mthr.init-fin.NormalAction*)

**qed**

**thus** ?thesis **using** tst aok  $\langle \text{redT-upd } s \text{ } t \text{ } ta \text{ } x' \text{ } m' \text{ } s' \rangle$  **unfolding**  $\langle tta = (t, ta) \rangle$  ..

**qed**

**lemma** *if-Red1-mthr-eq-if-Red1-mthr':*

**assumes** lok: *if-lock-oks1* (locks s) (thr s)

**and** elo: *ts-ok* (init-fin-lift ( $\lambda t$  exes h. el-loc-ok1 exes)) (thr s) (shr s)

**shows** *Red1-mthr.if.redT* True P s = *Red1-mthr.if.redT* False P s

**using** *if-Red1-mthr-imp-if-Red1-mthr'*[OF assms, of True P, simplified]

*if-Red1-mthr-imp-if-Red1-mthr'*[OF assms, of False P, simplified]

by(*blast del: equalityI*)

**lemma** *if-Red1-el-loc-ok:*

**assumes** *wf: wf-J1-prog P*

**shows** *lifting-wf Red1-mthr.init-fin-final (Red1-mthr.init-fin uf P) (init-fin-lift ( $\lambda t$  exes h. el-loc-ok1 exes))*

by(*rule lifting-wf.lifting-wf-init-fin-lift*)(*rule Red1-el-loc-ok[OF wf]*)

**lemma** *if-Red1'-preserves-if-lock-oks:*

**assumes** *wf: wf-J1-prog P*

**and** *Red: Red1-mthr.if.redT False P s1 ta1 s1'*

**and** *loks: if-lock-oks1 (locks s1) (thr s1)*

**and** *sync: ts-ok (init-fin-lift ( $\lambda t$  exes h. el-loc-ok1 exes)) (thr s1) (shr s1)*

**shows** *if-lock-oks1 (locks s1') (thr s1')*

**proof** –

**let** *?s1 = init-fin-descend-state s1*

**let** *?s1' = init-fin-descend-state s1'*

**from** *loks have* *loks': lock-oks1 (locks ?s1) (thr ?s1)* **by** *simp*

**from** *sync have* *sync': ts-ok ( $\lambda t$  exes h. el-loc-ok1 exes) (thr ?s1) (shr ?s1)*

**by**(*simp add: ts-ok-init-fin-descend-state*)

**from** *Red show* *?thesis*

**proof**(*cases*)

**case** (*redT-acquire t x n ln*)

**hence** *Red1-mthr.redT False P ?s1 (t, K\$ [], [], [], [], convert-RA ln) ?s1'*

**by**(*cases x*)(*auto intro!: Red1-mthr.redT.redT-acquire simp add: init-fin-descend-thr-def*)

**with** *wf have* *lock-oks1 (locks ?s1') (thr ?s1')* **using** *loks' sync'* **by**(*rule Red1'-preserves-lock-oks*)

**thus** *?thesis* **by** *simp*

**next**

**case** (*redT-normal t sx ta sx' m'*)

**note** *tst = ⟨thr s1 t = [(sx, no-wait-locks)]⟩*

**from** *⟨Red1-mthr.init-fin False P t (sx, shr s1) ta (sx', m')⟩*

**show** *?thesis*

**proof**(*cases*)

**case** (*InitialThreadAction x*) **thus** *?thesis* **using** *redT-normal loks*

**by**(*cases x*)(*auto 4 3 simp add: init-fin-descend-thr-def redT-updLns-def expand-finfun-eq fun-eq-iff*)

*intro: lock-oks1-thr-updI*)

**next**

**case** (*ThreadFinishAction x*) **thus** *?thesis* **using** *redT-normal loks*

**by**(*cases x*)(*auto 4 3 simp add: init-fin-descend-thr-def redT-updLns-def expand-finfun-eq fun-eq-iff*)

*intro: lock-oks1-thr-updI*)

**next**

**case** (*NormalAction x ta' x'*)

**note** *ta = ⟨ta = convert-TA-initial (convert-obs-initial ta')⟩*

**from** *⟨mred1' P t (x, shr s1) ta' (x', m')⟩*

**have** *mred1' P t (x, shr ?s1) ta' (x', m')* **by** *simp*

**moreover** **have** *tst': thr ?s1 t = [(x, no-wait-locks)]*

**using** *tst ⟨sx = (Running, x)⟩* **by** *simp*

**moreover** **have** *Red1-mthr.actions-ok ?s1 t ta'*

**using** *ta ⟨Red1-mthr.if.actions-ok s1 t ta⟩* **by** *simp*

**moreover** **from** *⟨redT-upd s1 t ta sx' m' s1'⟩* *tst tst' ta ⟨sx' = (Running, x')⟩*

**have** *redT-upd ?s1 t ta' x' m' ?s1'* **by** *auto*

**ultimately** **have** *Red1-mthr.redT False P ?s1 (t, ta') ?s1' ..*

**with** *wf have* *lock-oks1 (locks ?s1') (thr ?s1')* **using** *loks' sync'* **by**(*rule Red1'-preserves-lock-oks*)

**thus** *?thesis* **by** *simp*

qed  
 qed  
 qed

**lemma** *Red1'-Red1-if-bisimulation*:

**assumes** *wf*: *wf-J1-prog P*

**shows** *bisimulation (Red1-mthr.if.redT False P) (Red1-mthr.if.redT True P) if-mbisim-Red1'-Red1 (=)*

**proof**

**fix** *s1 s2 tl1 s1'*

**assume** *if-mbisim-Red1'-Red1 s1 s2 and Red1-mthr.if.redT False P s1 tl1 s1'*

**thus**  $\exists s2' tl2. Red1-mthr.if.redT True P s2 tl2 s2' \wedge if-mbisim-Red1'-Red1 s1' s2' \wedge tl1 = tl2$

**by**(*cases tl1*)(*auto simp add: if-mbisim-Red1'-Red1-def if-Red1-mthr-eq-if-Red1-mthr' simp del: split-paired-Ex elim: if-Red1'-preserves-if-lock-oks[OF wf] lifting-wf.redT-preserves[OF if-Red1-el-loc-ok, OF wf]*)

**next**

**fix** *s1 s2 tl2 s2'*

**assume** *if-mbisim-Red1'-Red1 s1 s2 Red1-mthr.if.redT True P s2 tl2 s2'*

**thus**  $\exists s1' tl1. Red1-mthr.if.redT False P s1 tl1 s1' \wedge if-mbisim-Red1'-Red1 s1' s2' \wedge tl1 = tl2$

**by**(*cases tl2*)(*auto simp add: if-mbisim-Red1'-Red1-def if-Red1-mthr-eq-if-Red1-mthr' simp del: split-paired-Ex elim: if-Red1'-preserves-if-lock-oks[OF wf] lifting-wf.redT-preserves[OF if-Red1-el-loc-ok, OF wf]*)

**qed**

**lemma** *if-bisim-J1-J1-start*:

**assumes** *wf*: *wf-J1-prog P*

**and** *wf-start*: *wf-start-state P C M vs*

**shows** *if-mbisim-Red1'-Red1 (init-fin-lift-state status (J1-start-state P C M vs)) (init-fin-lift-state status (J1-start-state P C M vs))*

**proof** –

**from** *assms have mbisim-Red1'-Red1 (J1-start-state P C M vs) (J1-start-state P C M vs) by(rule bisim-J1-J1-start)*

**thus** *?thesis*

**by**(*simp add: if-mbisim-Red1'-Red1-def mbisim-Red1'-Red1-def*)(*simp add: init-fin-lift-state-conv-simps init-fin-descend-thr-def thr-init-fin-list-state' o-def map-option.compositionality map-option.identity split-beta*)

**qed**

**lemma** *if-Red1'-Red1-bisim-into-weak*:

**assumes** *wf*: *wf-J1-prog P*

**shows** *bisimulation-into-delay (Red1-mthr.if.redT False P) (Red1-mthr.if.redT True P) if-mbisim-Red1'-Red1 (=) (Red1-mthr.if.m $\tau$ move P) (Red1-mthr.if.m $\tau$ move P)*

**proof** –

**interpret** *b*: *bisimulation Red1-mthr.if.redT False P Red1-mthr.if.redT True P if-mbisim-Red1'-Red1 (=)*

**by**(*rule Red1'-Red1-if-bisimulation[OF wf]*)

**show** *?thesis by(unfold-locales)(simp add: if-mbisim-Red1'-Red1-def)*

**qed**

**lemma** *if-Red1'-Red1-bisimulation-final*:

*wf-J1-prog P*

$\implies$  *bisimulation-final (Red1-mthr.if.redT False P) (Red1-mthr.if.redT True P)*

*if-mbisim-Red1'-Red1 (=) Red1-mthr.if.mfinal Red1-mthr.if.mfinal*

**apply**(*intro-locales*)

**apply**(*erule Red1'-Red1-if-bisimulation*)



```

apply(unfold-locales)
apply(auto simp add: if-mbisim-Red1'-Red1-def)
done

```

```

end

```

```

end

```

## 7.24 Semantic Correctness of Stage 1

```

theory Correctness1 imports

```

```

  J0J1Bisim

```

```

  ../J/DefAssPreservation

```

```

begin

```

```

lemma finals-map-Val [simp]: finals (map Val vs)
by(simp add: finals-iff)

```

```

context J-heap-base begin

```

```

lemma τred0r-preserves-defass:

```

```

  assumes wf: wf-J-prog P

```

```

  shows  $\llbracket \tau\text{red0r extTA } P \text{ t h } (e, xs) (e', xs'); \mathcal{D} e \llbracket \text{dom } xs \rrbracket \rrbracket \implies \mathcal{D} e' \llbracket \text{dom } xs' \rrbracket$ 
by(induct rule: rtrancp-induct2)(auto dest: red-preserves-defass[OF wf])

```

```

lemma τred0t-preserves-defass:

```

```

  assumes wf: wf-J-prog P

```

```

  shows  $\llbracket \tau\text{red0t extTA } P \text{ t h } (e, xs) (e', xs'); \mathcal{D} e \llbracket \text{dom } xs \rrbracket \rrbracket \implies \mathcal{D} e' \llbracket \text{dom } xs' \rrbracket$ 
by(rule τred0r-preserves-defass[OF wf])(rule trancp-into-rtrancp)

```

```

end

```

```

lemma LAss-lem:

```

```

   $\llbracket x \in \text{set } xs; \text{size } xs \leq \text{size } ys \rrbracket$ 

```

```

   $\implies m1 \subseteq_m m2(xs[\mapsto]ys) \implies m1(x \mapsto y) \subseteq_m m2(xs[\mapsto]ys[\text{index } xs \ x := y])$ 

```

```

apply(simp add:map-le-def)

```

```

apply(simp add:fun-upds-apply index-less-aux eq-sym-conv)

```

```

done

```

```

lemma Block-lem:

```

```

fixes l :: 'a  $\rightarrow$  'b

```

```

assumes 0:  $l \subseteq_m [Vs [\mapsto] ls]$ 

```

```

  and 1:  $l' \subseteq_m [Vs [\mapsto] ls', V \mapsto v]$ 

```

```

  and hidden:  $V \in \text{set } Vs \implies ls ! \text{index } Vs \ V = ls' ! \text{index } Vs \ V$ 

```

```

  and size:  $\text{size } ls = \text{size } ls' \quad \text{size } Vs < \text{size } ls'$ 

```

```

shows  $l'(V := l \ V) \subseteq_m [Vs [\mapsto] ls']$ 

```

```

proof –

```

```

  have  $l'(V := l \ V) \subseteq_m [Vs [\mapsto] ls', V \mapsto v](V := l \ V)$ 

```

```

    using 1 by(rule map-le-upd)

```

```

  also have  $\dots = [Vs [\mapsto] ls'](V := l \ V)$  by simp

```

```

  also have  $\dots \subseteq_m [Vs [\mapsto] ls']$ 

```

```

  proof (cases l V)

```

```

    case None thus ?thesis by simp

```

```

next
  case (Some w)
  hence [Vs [↦] ls] V = Some w
    using 0 by (force simp add: map-le-def split-if-splits)
  hence VinVs: V ∈ set Vs and w: w = ls ! index Vs V
    using size by (auto simp add: fun-upds-apply split-if-splits)
  hence w = ls' ! index Vs V using hidden[OF VinVs] by simp
  hence [Vs [↦] ls'](V := l V) = [Vs [↦] ls']
    using Some size VinVs by (simp add: index-less-aux map-upds-upd-conv-index)
  thus ?thesis by simp
qed
finally show ?thesis .
qed

```

### 7.24.1 Correctness proof

```

locale J0-J1-heap-base =
  J?: J-heap-base +
  J1?: J1-heap-base +
  constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
  and thread-id2addr :: 'thread-id ⇒ 'addr
  and empty-heap :: 'heap
  and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
  and typeof-addr :: 'heap ⇒ 'addr → htype
  and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
  and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
begin

lemma ta-bisim01-extTA2J0-extTA2J1:
  assumes wf: wf-J-prog P
  and nt: ∧n T C M a h. [ n < length {ta}_t; {ta}_t ! n = NewThread T (C, M, a) h ]
    ⇒ typeof-addr h a = [Class-type C] ∧ (∃ T meth D. P ⊢ C sees M:[] → T = [meth] in D)
  shows ta-bisim01 (extTA2J0 P ta) (extTA2J1 (compP1 P) ta)
  apply (simp add: ta-bisim-def ta-upd-simps)
  apply (auto intro!: list-all2-all-nthI)
  apply (case-tac {ta}_t ! n)
  apply (auto simp add: bisim-red0-Red1-def)
  apply (drule (1) nt)
  apply (clarify)
  apply (erule bisim-list-extTA2J0-extTA2J1 [OF wf, simplified])
done

lemma red-external-ta-bisim01:
  [ wf-J-prog P; P, t ⊢ ⟨a • M(vs), h⟩ -ta→ext ⟨va, h'⟩ ] ⇒ ta-bisim01 (extTA2J0 P ta) (extTA2J1
    (compP1 P) ta)
  apply (rule ta-bisim01-extTA2J0-extTA2J1, assumption)
  apply (drule (1) red-external-new-thread-sees, auto simp add: in-set-conv-nth)
  apply (drule red-ext-new-thread-heap, auto simp add: in-set-conv-nth)
done

lemmas τred1t-expr =
  NewArray-τred1t-xt Cast-τred1t-xt InstanceOf-τred1t-xt BinOp-τred1t-xt1 BinOp-τred1t-xt2 LAss-τred1t
  AAcc-τred1t-xt1 AAcc-τred1t-xt2 AAss-τred1t-xt1 AAss-τred1t-xt2 AAss-τred1t-xt3
  ALength-τred1t-xt FAcc-τred1t-xt FAss-τred1t-xt1 FAss-τred1t-xt2

```

*CAS- $\tau$ red1t-xt1 CAS- $\tau$ red1t-xt2 CAS- $\tau$ red1t-xt3 Call- $\tau$ red1t-obj*  
*Call- $\tau$ red1t-param Block-None- $\tau$ red1t-xt Block- $\tau$ red1t-Some Sync- $\tau$ red1t-xt InSync- $\tau$ red1t-xt*  
*Seq- $\tau$ red1t-xt Cond- $\tau$ red1t-xt Throw- $\tau$ red1t-xt Try- $\tau$ red1t-xt*

**lemmas**  $\tau$ red1r-expr =

*NewArray- $\tau$ red1r-xt Cast- $\tau$ red1r-xt InstanceOf- $\tau$ red1r-xt BinOp- $\tau$ red1r-xt1 BinOp- $\tau$ red1r-xt2 LAss- $\tau$ red1r*  
*AAcc- $\tau$ red1r-xt1 AAcc- $\tau$ red1r-xt2 AAss- $\tau$ red1r-xt1 AAss- $\tau$ red1r-xt2 AAss- $\tau$ red1r-xt3*  
*ALength- $\tau$ red1r-xt FAcc- $\tau$ red1r-xt FAss- $\tau$ red1r-xt1 FAss- $\tau$ red1r-xt2*  
*CAS- $\tau$ red1r-xt1 CAS- $\tau$ red1r-xt2 CAS- $\tau$ red1r-xt3 Call- $\tau$ red1r-obj*  
*Call- $\tau$ red1r-param Block-None- $\tau$ red1r-xt Block- $\tau$ red1r-Some Sync- $\tau$ red1r-xt InSync- $\tau$ red1r-xt*  
*Seq- $\tau$ red1r-xt Cond- $\tau$ red1r-xt Throw- $\tau$ red1r-xt Try- $\tau$ red1r-xt*

**definition** *sim-move01* ::

*'addr J1-prog  $\Rightarrow$  'thread-id  $\Rightarrow$  ('addr, 'thread-id, 'heap) J0-thread-action  $\Rightarrow$  'addr expr  $\Rightarrow$  'addr expr1*  
 *$\Rightarrow$  'heap*  
 *$\Rightarrow$  'addr locals1  $\Rightarrow$  ('addr, 'thread-id, 'heap) external-thread-action  $\Rightarrow$  'addr expr1  $\Rightarrow$  'heap  $\Rightarrow$  'addr*  
*locals1  $\Rightarrow$  bool*

**where**

*sim-move01 P t ta0 e0 e h xs ta e' h' xs'  $\longleftrightarrow$   $\neg$  final e0  $\wedge$*   
*(if  $\tau$ move0 P h e0 then  $h' = h \wedge ta0 = \varepsilon \wedge ta = \varepsilon \wedge \tau$ red1't P t h (e, xs) (e', xs')*  
*else ta-bisim01 ta0 (extTA2J1 P ta)  $\wedge$*   
*(if call e0 = None  $\vee$  call1 e = None*  
*then  $(\exists e'' xs''. \tau$ red1'r P t h (e, xs) (e'', xs'')  $\wedge$  False, P, t  $\vdash$  1  $\langle e'', (h, xs'') \rangle -ta \rightarrow \langle e', (h', xs') \rangle$ )*  
 $\wedge$   
 $\neg \tau$ move1 P h e'')  
*else False, P, t  $\vdash$  1  $\langle e, (h, xs) \rangle -ta \rightarrow \langle e', (h', xs') \rangle \wedge \neg \tau$ move1 P h e))*

**definition** *sim-moves01* ::

*'addr J1-prog  $\Rightarrow$  'thread-id  $\Rightarrow$  ('addr, 'thread-id, 'heap) J0-thread-action  $\Rightarrow$  'addr expr list  $\Rightarrow$  'addr*  
*expr1 list  $\Rightarrow$  'heap*  
 *$\Rightarrow$  'addr locals1  $\Rightarrow$  ('addr, 'thread-id, 'heap) external-thread-action  $\Rightarrow$  'addr expr1 list  $\Rightarrow$  'heap  $\Rightarrow$*   
*'addr locals1  $\Rightarrow$  bool*

**where**

*sim-moves01 P t ta0 es0 es h xs ta es' h' xs'  $\longleftrightarrow$   $\neg$  finals es0  $\wedge$*   
*(if  $\tau$ moves0 P h es0 then  $h' = h \wedge ta0 = \varepsilon \wedge ta = \varepsilon \wedge \tau$ reds1't P t h (es, xs) (es', xs')*  
*else ta-bisim01 ta0 (extTA2J1 P ta)  $\wedge$*   
*(if calls es0 = None  $\vee$  calls1 es = None*  
*then  $(\exists es'' xs''. \tau$ reds1'r P t h (es, xs) (es'', xs'')  $\wedge$  False, P, t  $\vdash$  1  $\langle es'', (h, xs'') \rangle [-ta \rightarrow] \langle es', (h',$*   
*xs') \rangle \wedge*  
 $\neg \tau$ moves1 P h es'')  
*else False, P, t  $\vdash$  1  $\langle es, (h, xs) \rangle [-ta \rightarrow] \langle es', (h', xs') \rangle \wedge \neg \tau$ moves1 P h es))*

**declare**  $\tau$ red1t-expr [elim!]  $\tau$ red1r-expr[elim!]

**lemma** *sim-move01-expr*:

**assumes** *sim-move01 P t ta0 e0 e h xs ta e' h' xs'*

**shows**

*sim-move01 P t ta0 (newA T[e0]) (newA T[e]) h xs ta (newA T[e']) h' xs'*  
*sim-move01 P t ta0 (Cast T e0) (Cast T e) h xs ta (Cast T e') h' xs'*  
*sim-move01 P t ta0 (e0 instanceof T) (e instanceof T) h xs ta (e' instanceof T) h' xs'*  
*sim-move01 P t ta0 (e0 «bop» e2) (e «bop» e2') h xs ta (e' «bop» e2') h' xs'*  
*sim-move01 P t ta0 (Val v «bop» e0) (Val v «bop» e) h xs ta (Val v «bop» e') h' xs'*  
*sim-move01 P t ta0 (V := e0) (V := e) h xs ta (V := e') h' xs'*  
*sim-move01 P t ta0 (e0[e2]) (e[e2']) h xs ta (e'[e2']) h' xs'*

$\text{sim-move01 } P \ t \ ta0 \ (Val \ v[e0]) \ (Val \ v[e]) \ h \ xs \ ta \ (Val \ v[e']) \ h' \ xs'$   
 $\text{sim-move01 } P \ t \ ta0 \ (e0[e2] := e3) \ (e[e2'] := e3') \ h \ xs \ ta \ (e'[e2'] := e3') \ h' \ xs'$   
 $\text{sim-move01 } P \ t \ ta0 \ (Val \ v[e0] := e3) \ (Val \ v[e] := e3') \ h \ xs \ ta \ (Val \ v[e'] := e3') \ h' \ xs'$   
 $\text{sim-move01 } P \ t \ ta0 \ (AAss \ (Val \ v) \ (Val \ v') \ e0) \ (AAss \ (Val \ v) \ (Val \ v') \ e) \ h \ xs \ ta \ (AAss \ (Val \ v) \ (Val \ v') \ e') \ h' \ xs'$   
 $\text{sim-move01 } P \ t \ ta0 \ (e0.length) \ (e.length) \ h \ xs \ ta \ (e'.length) \ h' \ xs'$   
 $\text{sim-move01 } P \ t \ ta0 \ (e0.F\{D\}) \ (e.F'\{D'\}) \ h \ xs \ ta \ (e'.F'\{D'\}) \ h' \ xs'$   
 $\text{sim-move01 } P \ t \ ta0 \ (FAss \ e0 \ F \ D \ e2) \ (FAss \ e \ F' \ D' \ e2') \ h \ xs \ ta \ (FAss \ e' \ F' \ D' \ e2') \ h' \ xs'$   
 $\text{sim-move01 } P \ t \ ta0 \ (FAss \ (Val \ v) \ F \ D \ e0) \ (FAss \ (Val \ v) \ F' \ D' \ e) \ h \ xs \ ta \ (FAss \ (Val \ v) \ F' \ D' \ e') \ h' \ xs'$   
 $\text{sim-move01 } P \ t \ ta0 \ (CompareAndSwap \ e0 \ D \ F \ e2 \ e3) \ (CompareAndSwap \ e \ D \ F \ e2' \ e3') \ h \ xs \ ta \ (CompareAndSwap \ e' \ D \ F \ e2' \ e3') \ h' \ xs'$   
 $\text{sim-move01 } P \ t \ ta0 \ (CompareAndSwap \ (Val \ v) \ D \ F \ e0 \ e3) \ (CompareAndSwap \ (Val \ v) \ D \ F \ e \ e3') \ h \ xs \ ta \ (CompareAndSwap \ (Val \ v) \ D \ F \ e' \ e3') \ h' \ xs'$   
 $\text{sim-move01 } P \ t \ ta0 \ (CompareAndSwap \ (Val \ v) \ D \ F \ (Val \ v') \ e0) \ (CompareAndSwap \ (Val \ v) \ D \ F \ (Val \ v') \ e) \ h \ xs \ ta \ (CompareAndSwap \ (Val \ v) \ D \ F \ (Val \ v') \ e') \ h' \ xs'$   
 $\text{sim-move01 } P \ t \ ta0 \ (e0.M(es)) \ (e.M(es')) \ h \ xs \ ta \ (e'.M(es')) \ h' \ xs'$   
 $\text{sim-move01 } P \ t \ ta0 \ (\{V:T=vo; e0\}) \ (\{V':T=None; e\}) \ h \ xs \ ta \ (\{V':T=None; e'\}) \ h' \ xs'$   
 $\text{sim-move01 } P \ t \ ta0 \ (sync(e0) \ e2) \ (sync_{V'}(e) \ e2') \ h \ xs \ ta \ (sync_{V'}(e') \ e2') \ h' \ xs'$   
 $\text{sim-move01 } P \ t \ ta0 \ (insync(a) \ e0) \ (insync_{V'}(a') \ e) \ h \ xs \ ta \ (insync_{V'}(a') \ e') \ h' \ xs'$   
 $\text{sim-move01 } P \ t \ ta0 \ (e0;;e2) \ (e;;e2') \ h \ xs \ ta \ (e';;e2') \ h' \ xs'$   
 $\text{sim-move01 } P \ t \ ta0 \ (if \ (e0) \ e2 \ else \ e3) \ (if \ (e) \ e2' \ else \ e3') \ h \ xs \ ta \ (if \ (e') \ e2' \ else \ e3') \ h' \ xs'$   
 $\text{sim-move01 } P \ t \ ta0 \ (throw \ e0) \ (throw \ e) \ h \ xs \ ta \ (throw \ e') \ h' \ xs'$   
 $\text{sim-move01 } P \ t \ ta0 \ (try \ e0 \ catch(C \ V) \ e2) \ (try \ e \ catch(C' \ V') \ e2') \ h \ xs \ ta \ (try \ e' \ catch(C' \ V') \ e2') \ h' \ xs'$

**using** *assms*

**apply**(*simp-all add: sim-move01-def final-iff  $\tau_{red1r}$ -Val  $\tau_{red1t}$ -Val split: if-split-asm split del: if-split*)

**apply**(*fastforce simp add: final-iff  $\tau_{red1r}$ -Val  $\tau_{red1t}$ -Val split!: if-splits intro: red1-reds1.intros*)+  
**done**

**lemma** *sim-moves01-expr:*

$\text{sim-move01 } P \ t \ ta0 \ e0 \ e \ h \ xs \ ta \ e' \ h' \ xs' \implies \text{sim-moves01 } P \ t \ ta0 \ (e0 \ \# \ es2) \ (e \ \# \ es2') \ h \ xs \ ta \ (e' \ \# \ es2') \ h' \ xs'$

$\text{sim-moves01 } P \ t \ ta0 \ es0 \ es \ h \ xs \ ta \ es' \ h' \ xs' \implies \text{sim-moves01 } P \ t \ ta0 \ (Val \ v \ \# \ es0) \ (Val \ v \ \# \ es) \ h \ xs \ ta \ (Val \ v \ \# \ es') \ h' \ xs'$

**apply**(*simp-all add: sim-move01-def sim-moves01-def final-iff finals-iff Cons-eq-append-conv  $\tau_{red1t}$ -Val  $\tau_{red1r}$ -Val split: if-split-asm split del: if-split*)

**apply**(*auto simp add: Cons-eq-append-conv  $\tau_{red1t}$ -Val  $\tau_{red1r}$ -Val split!: if-splits intro: List1Red1 List1Red2  $\tau_{red1t}$ -inj- $\tau_{reds1t}$   $\tau_{red1r}$ -inj- $\tau_{reds1r}$   $\tau_{reds1t}$ -cons- $\tau_{reds1t}$   $\tau_{reds1r}$ -cons- $\tau_{reds1r}$* )

**apply**(*force elim!:  $\tau_{red1r}$ -inj- $\tau_{reds1r}$  List1Red1*)

**apply**(*force elim!:  $\tau_{red1r}$ -inj- $\tau_{reds1r}$  List1Red1*)

**apply**(*force elim!:  $\tau_{red1r}$ -inj- $\tau_{reds1r}$  List1Red1*)

**apply**(*force elim!:  $\tau_{red1r}$ -inj- $\tau_{reds1r}$  List1Red1*)

**apply**(*force elim!:  $\tau_{reds1r}$ -cons- $\tau_{reds1r}$  intro!: List1Red2*)

**apply**(*force elim!:  $\tau_{reds1r}$ -cons- $\tau_{reds1r}$  intro!: List1Red2*)

**done**

**lemma** *sim-move01-CallParams:*

$\text{sim-moves01 } P \ t \ ta0 \ es0 \ es \ h \ xs \ ta \ es' \ h' \ xs'$

$\implies \text{sim-move01 } P \ t \ ta0 \ (Val \ v.M(es0)) \ (Val \ v.M(es)) \ h \ xs \ ta \ (Val \ v.M(es')) \ h' \ xs'$

**apply**(*clarsimp simp add: sim-move01-def sim-moves01-def  $\tau_{reds1r}$ -map-Val  $\tau_{reds1t}$ -map-Val is-vals-conv split: if-split-asm split del: if-split*)

**apply**(*fastforce simp add: sim-move01-def sim-moves01-def  $\tau_{reds1r}$ -map-Val  $\tau_{reds1t}$ -map-Val intro: Call- $\tau_{red1r}$ -param Call1Params*)

```

apply(rule conjI, fastforce)
apply(split if-split)
apply(rule conjI)
  apply(clarsimp simp add: finals-iff)
apply(clarify)
apply(split if-split)
apply(rule conjI)
  apply(simp del: call.simps calls.simps call1.simps calls1.simps)
apply(fastforce simp add: sim-move01-def sim-moves01-def  $\tau$ red1r-Val  $\tau$ red1t-Val  $\tau$ reds1r-map-Val-Throw
intro: Call- $\tau$ red1r-param Call1Params split: if-split-asm)
apply(fastforce split: if-split-asm simp add: is-vals-conv  $\tau$ reds1r-map-Val  $\tau$ reds1r-map-Val-Throw)
apply(rule conjI, fastforce)
apply(fastforce simp add: sim-move01-def sim-moves01-def  $\tau$ red1r-Val  $\tau$ red1t-Val  $\tau$ reds1t-map-Val
 $\tau$ reds1r-map-Val is-vals-conv intro: Call- $\tau$ red1r-param Call1Params split: if-split-asm)
done

```

**lemma** *sim-move01-reds*:

```

 $\llbracket (h', a) \in \text{allocate } h \text{ (Class-type } C); ta0 = \llbracket \text{NewHeapElem } a \text{ (Class-type } C) \rrbracket; ta = \llbracket \text{NewHeapElem } a \text{ (Class-type } C) \rrbracket \rrbracket$ 
 $\implies \text{sim-move01 } P \ t \ ta0 \ (new \ C) \ (new \ C) \ h \ xs \ ta \ (addr \ a) \ h' \ xs$ 
 $\text{allocate } h \text{ (Class-type } C) = \{\} \implies \text{sim-move01 } P \ t \ \varepsilon \ (new \ C) \ (new \ C) \ h \ xs \ \varepsilon \ (THROW \text{ OutOfMemory}) \ h \ xs$ 
 $\llbracket (h', a) \in \text{allocate } h \text{ (Array-type } T \text{ (nat (sint } i)))}; 0 \leq s \ i;$ 
 $ta0 = \llbracket \text{NewHeapElem } a \text{ (Array-type } T \text{ (nat (sint } i))) \rrbracket; ta = \llbracket \text{NewHeapElem } a \text{ (Array-type } T \text{ (nat (sint } i))) \rrbracket \rrbracket$ 
 $\implies \text{sim-move01 } P \ t \ ta0 \ (newA \ T \llbracket \text{Val (Intg } i) \rrbracket) \ (newA \ T \llbracket \text{Val (Intg } i) \rrbracket) \ h \ xs \ ta \ (addr \ a) \ h' \ xs$ 
 $i < s \ 0 \implies \text{sim-move01 } P \ t \ \varepsilon \ (newA \ T \llbracket \text{Val (Intg } i) \rrbracket) \ (newA \ T \llbracket \text{Val (Intg } i) \rrbracket) \ h \ xs \ \varepsilon \ (THROW \text{ NegativeArraySize}) \ h \ xs$ 
 $\llbracket \text{allocate } h \text{ (Array-type } T \text{ (nat (sint } i))) = \{\}; 0 \leq s \ i \rrbracket$ 
 $\implies \text{sim-move01 } P \ t \ \varepsilon \ (newA \ T \llbracket \text{Val (Intg } i) \rrbracket) \ (newA \ T \llbracket \text{Val (Intg } i) \rrbracket) \ h \ xs \ \varepsilon \ (THROW \text{ OutOfMemory}) \ h \ xs$ 
 $\llbracket \text{typeof}_h \ v = \llbracket U \rrbracket; P \vdash U \leq T \rrbracket$ 
 $\implies \text{sim-move01 } P \ t \ \varepsilon \ (Cast \ T \ (Val \ v)) \ (Cast \ T \ (Val \ v)) \ h \ xs \ \varepsilon \ (Val \ v) \ h \ xs$ 
 $\llbracket \text{typeof}_h \ v = \llbracket U \rrbracket; \neg P \vdash U \leq T \rrbracket$ 
 $\implies \text{sim-move01 } P \ t \ \varepsilon \ (Cast \ T \ (Val \ v)) \ (Cast \ T \ (Val \ v)) \ h \ xs \ \varepsilon \ (THROW \text{ ClassCast}) \ h \ xs$ 
 $\llbracket \text{typeof}_h \ v = \llbracket U \rrbracket; b \longleftrightarrow v \neq \text{Null} \wedge P \vdash U \leq T \rrbracket$ 
 $\implies \text{sim-move01 } P \ t \ \varepsilon \ ((Val \ v) \text{ instanceof } T) \ ((Val \ v) \text{ instanceof } T) \ h \ xs \ \varepsilon \ (Val \ (Bool \ b)) \ h \ xs$ 
 $\text{binop } bop \ v1 \ v2 = \text{Some } (Inl \ v) \implies \text{sim-move01 } P \ t \ \varepsilon \ ((Val \ v1) \llbracket bop \rrbracket (Val \ v2)) \ (Val \ v1 \llbracket bop \rrbracket Val \ v2) \ h \ xs \ \varepsilon \ (Val \ v) \ h \ xs$ 
 $\text{binop } bop \ v1 \ v2 = \text{Some } (Inr \ a) \implies \text{sim-move01 } P \ t \ \varepsilon \ ((Val \ v1) \llbracket bop \rrbracket (Val \ v2)) \ (Val \ v1 \llbracket bop \rrbracket Val \ v2) \ h \ xs \ \varepsilon \ (Throw \ a) \ h \ xs$ 
 $\llbracket xs!V = v; V < \text{size } xs \rrbracket \implies \text{sim-move01 } P \ t \ \varepsilon \ (Var \ V') \ (Var \ V) \ h \ xs \ \varepsilon \ (Val \ v) \ h \ xs$ 
 $V < \text{length } xs \implies \text{sim-move01 } P \ t \ \varepsilon \ (V' := Val \ v) \ (V := Val \ v) \ h \ xs \ \varepsilon \ \text{unit } h \ (xs[V := v])$ 
 $\text{sim-move01 } P \ t \ \varepsilon \ (\text{null} \llbracket Val \ v \rrbracket) \ (\text{null} \llbracket Val \ v \rrbracket) \ h \ xs \ \varepsilon \ (THROW \text{ NullPointer}) \ h \ xs$ 
 $\llbracket \text{typeof-addr } h \ a = \llbracket \text{Array-type } T \ n \rrbracket; i < s \ 0 \vee \text{sint } i \geq \text{int } n \rrbracket$ 
 $\implies \text{sim-move01 } P \ t \ \varepsilon \ (\text{addr } a \llbracket Val \ (Intg \ i) \rrbracket) \ ((\text{addr } a) \llbracket Val \ (Intg \ i) \rrbracket) \ h \ xs \ \varepsilon \ (THROW \text{ ArrayIndexOutOfBounds}) \ h \ xs$ 
 $\llbracket \text{typeof-addr } h \ a = \llbracket \text{Array-type } T \ n \rrbracket; 0 \leq s \ i; \text{sint } i < \text{int } n;$ 
 $\text{heap-read } h \ a \ (ACell \ (\text{nat } (\text{sint } i))) \ v;$ 
 $ta0 = \llbracket \text{ReadMem } a \ (ACell \ (\text{nat } (\text{sint } i))) \ v \rrbracket;$ 
 $ta = \llbracket \text{ReadMem } a \ (ACell \ (\text{nat } (\text{sint } i))) \ v \rrbracket \rrbracket$ 
 $\implies \text{sim-move01 } P \ t \ ta0 \ (\text{addr } a \llbracket Val \ (Intg \ i) \rrbracket) \ ((\text{addr } a) \llbracket Val \ (Intg \ i) \rrbracket) \ h \ xs \ ta \ (Val \ v) \ h \ xs$ 
 $\text{sim-move01 } P \ t \ \varepsilon \ (\text{null} \llbracket Val \ v \rrbracket := Val \ v') \ (\text{null} \llbracket Val \ v \rrbracket := Val \ v') \ h \ xs \ \varepsilon \ (THROW \text{ NullPointer}) \ h \ xs$ 
 $\llbracket \text{typeof-addr } h \ a = \llbracket \text{Array-type } T \ n \rrbracket; i < s \ 0 \vee \text{sint } i \geq \text{int } n \rrbracket$ 

```

$\Rightarrow \text{sim-move01 } P \ t \ \varepsilon \ (AAss \ (\text{addr } a) \ (\text{Val } (\text{Intg } i)) \ (\text{Val } v)) \ (AAss \ (\text{addr } a) \ (\text{Val } (\text{Intg } i)) \ (\text{Val } v))$   
 $h \ xs \ \varepsilon \ (\text{THROW } \text{ArrayIndexOutOfBounds}) \ h \ xs$   
 $\llbracket \text{typeof-addr } h \ a = \lfloor \text{Array-type } T \ n \rfloor; 0 \leq i; \text{sint } i < \text{int } n; \text{typeof}_h v = \lfloor U \rfloor; \neg (P \vdash U \leq T) \rrbracket$   
 $\Rightarrow \text{sim-move01 } P \ t \ \varepsilon \ (AAss \ (\text{addr } a) \ (\text{Val } (\text{Intg } i)) \ (\text{Val } v)) \ (AAss \ (\text{addr } a) \ (\text{Val } (\text{Intg } i)) \ (\text{Val } v))$   
 $h \ xs \ \varepsilon \ (\text{THROW } \text{ArrayStore}) \ h \ xs$   
 $\llbracket \text{typeof-addr } h \ a = \lfloor \text{Array-type } T \ n \rfloor; 0 \leq i; \text{sint } i < \text{int } n; \text{typeof}_h v = \text{Some } U; P \vdash U \leq T;$   
 $\text{heap-write } h \ a \ (\text{ACell } (\text{nat } (\text{sint } i))) \ v \ h';$   
 $\text{ta0} = \llbracket \text{WriteMem } a \ (\text{ACell } (\text{nat } (\text{sint } i))) \ v \rrbracket; \text{ta} = \llbracket \text{WriteMem } a \ (\text{ACell } (\text{nat } (\text{sint } i))) \ v \rrbracket \rrbracket$   
 $\Rightarrow \text{sim-move01 } P \ t \ \text{ta0} \ (AAss \ (\text{addr } a) \ (\text{Val } (\text{Intg } i)) \ (\text{Val } v)) \ (AAss \ (\text{addr } a) \ (\text{Val } (\text{Intg } i)) \ (\text{Val } v))$   
 $h \ xs \ \text{ta} \ \text{unit } h' \ xs$   
 $\text{typeof-addr } h \ a = \lfloor \text{Array-type } T \ n \rfloor \Rightarrow \text{sim-move01 } P \ t \ \varepsilon \ (\text{addr } a \cdot \text{length}) \ (\text{addr } a \cdot \text{length}) \ h \ xs \ \varepsilon$   
 $(\text{Val } (\text{Intg } (\text{word-of-int } (\text{int } n)))) \ h \ xs$   
 $\text{sim-move01 } P \ t \ \varepsilon \ (\text{null} \cdot \text{length}) \ (\text{null} \cdot \text{length}) \ h \ xs \ \varepsilon \ (\text{THROW } \text{NullPointer}) \ h \ xs$

$\llbracket \text{heap-read } h \ a \ (\text{CField } D \ F) \ v; \text{ta0} = \llbracket \text{ReadMem } a \ (\text{CField } D \ F) \ v \rrbracket; \text{ta} = \llbracket \text{ReadMem } a \ (\text{CField } D \ F) \ v \rrbracket \rrbracket$   
 $\Rightarrow \text{sim-move01 } P \ t \ \text{ta0} \ (\text{addr } a \cdot F\{D\}) \ (\text{addr } a \cdot F\{D\}) \ h \ xs \ \text{ta} \ (\text{Val } v) \ h \ xs$   
 $\text{sim-move01 } P \ t \ \varepsilon \ (\text{null} \cdot F\{D\}) \ (\text{null} \cdot F\{D\}) \ h \ xs \ \varepsilon \ (\text{THROW } \text{NullPointer}) \ h \ xs$   
 $\llbracket \text{heap-write } h \ a \ (\text{CField } D \ F) \ v \ h'; \text{ta0} = \llbracket \text{WriteMem } a \ (\text{CField } D \ F) \ v \rrbracket; \text{ta} = \llbracket \text{WriteMem } a \ (\text{CField } D \ F) \ v \rrbracket \rrbracket$   
 $\Rightarrow \text{sim-move01 } P \ t \ \text{ta0} \ (\text{addr } a \cdot F\{D\} := \text{Val } v) \ (\text{addr } a \cdot F\{D\} := \text{Val } v) \ h \ xs \ \text{ta} \ \text{unit } h' \ xs$   
 $\text{sim-move01 } P \ t \ \varepsilon \ (\text{null} \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v, \text{Val } v')) \ (\text{null} \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v, \text{Val } v')) \ h \ xs \ \varepsilon \ (\text{THROW } \text{NullPointer}) \ h \ xs$   
 $\llbracket \text{heap-read } h \ a \ (\text{CField } D \ F) \ v''; \text{heap-write } h \ a \ (\text{CField } D \ F) \ v' \ h'; v'' = v;$   
 $\text{ta0} = \llbracket \text{ReadMem } a \ (\text{CField } D \ F) \ v'', \text{WriteMem } a \ (\text{CField } D \ F) \ v' \rrbracket; \text{ta} = \llbracket \text{ReadMem } a \ (\text{CField } D \ F) \ v'', \text{WriteMem } a \ (\text{CField } D \ F) \ v' \rrbracket \rrbracket$   
 $\Rightarrow \text{sim-move01 } P \ t \ \text{ta0} \ (\text{addr } a \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v, \text{Val } v')) \ (\text{addr } a \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v, \text{Val } v')) \ h \ xs \ \text{ta} \ \text{true } h' \ xs$   
 $\llbracket \text{heap-read } h \ a \ (\text{CField } D \ F) \ v''; v'' \neq v;$   
 $\text{ta0} = \llbracket \text{ReadMem } a \ (\text{CField } D \ F) \ v'' \rrbracket; \text{ta} = \llbracket \text{ReadMem } a \ (\text{CField } D \ F) \ v'' \rrbracket \rrbracket$   
 $\Rightarrow \text{sim-move01 } P \ t \ \text{ta0} \ (\text{addr } a \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v, \text{Val } v')) \ (\text{addr } a \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v, \text{Val } v')) \ h \ xs \ \text{ta} \ \text{false } h' \ xs$   
 $\text{sim-move01 } P \ t \ \varepsilon \ (\text{null} \cdot F\{D\} := \text{Val } v) \ (\text{null} \cdot F\{D\} := \text{Val } v) \ h \ xs \ \varepsilon \ (\text{THROW } \text{NullPointer}) \ h \ xs$   
 $\text{sim-move01 } P \ t \ \varepsilon \ (\{V':T=\text{vo}; \text{Val } u\}) \ (\{V':T=\text{None}; \text{Val } u\}) \ h \ xs \ \varepsilon \ (\text{Val } u) \ h \ xs$   
 $V < \text{length } xs \Rightarrow \text{sim-move01 } P \ t \ \varepsilon \ (\text{sync}(\text{null}) \ e0) \ (\text{sync}_V(\text{null}) \ e1) \ h \ xs \ \varepsilon \ (\text{THROW } \text{NullPointer})$   
 $h \ (xs[V := \text{Null}])$   
 $\text{sim-move01 } P \ t \ \varepsilon \ (\text{Val } v;; e0) \ (\text{Val } v;; e1) \ h \ xs \ \varepsilon \ e1 \ h \ xs$   
 $\text{sim-move01 } P \ t \ \varepsilon \ (\text{if } (\text{true}) \ e0 \ \text{else } e0') \ (\text{if } (\text{true}) \ e1 \ \text{else } e1') \ h \ xs \ \varepsilon \ e1 \ h \ xs$   
 $\text{sim-move01 } P \ t \ \varepsilon \ (\text{if } (\text{false}) \ e0 \ \text{else } e0') \ (\text{if } (\text{false}) \ e1 \ \text{else } e1') \ h \ xs \ \varepsilon \ e1' \ h \ xs$   
 $\text{sim-move01 } P \ t \ \varepsilon \ (\text{throw } \text{null}) \ (\text{throw } \text{null}) \ h \ xs \ \varepsilon \ (\text{THROW } \text{NullPointer}) \ h \ xs$   
 $\text{sim-move01 } P \ t \ \varepsilon \ (\text{try } (\text{Val } v) \ \text{catch}(C \ V') \ e0) \ (\text{try } (\text{Val } v) \ \text{catch}(C \ V) \ e1) \ h \ xs \ \varepsilon \ (\text{Val } v) \ h \ xs$   
 $\llbracket \text{typeof-addr } h \ a = \lfloor \text{Class-type } D \rfloor; P \vdash D \preceq^* C; V < \text{length } xs \rrbracket$   
 $\Rightarrow \text{sim-move01 } P \ t \ \varepsilon \ (\text{try } (\text{Throw } a) \ \text{catch}(C \ V') \ e0) \ (\text{try } (\text{Throw } a) \ \text{catch}(C \ V) \ e1) \ h \ xs \ \varepsilon$   
 $(\{V:\text{Class } C=\text{None}; e1\}) \ h \ (xs[V := \text{Addr } a])$   
 $\text{sim-move01 } P \ t \ \varepsilon \ (\text{newA } T[\text{Throw } a]) \ (\text{newA } T[\text{Throw } a]) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$   
 $\text{sim-move01 } P \ t \ \varepsilon \ (\text{Cast } T \ (\text{Throw } a)) \ (\text{Cast } T \ (\text{Throw } a)) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$   
 $\text{sim-move01 } P \ t \ \varepsilon \ ((\text{Throw } a) \ \text{instanceof } T) \ ((\text{Throw } a) \ \text{instanceof } T) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$   
 $\text{sim-move01 } P \ t \ \varepsilon \ ((\text{Throw } a) \ \llbracket \text{bop} \rrbracket \ e0) \ ((\text{Throw } a) \ \llbracket \text{bop} \rrbracket \ e1) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$   
 $\text{sim-move01 } P \ t \ \varepsilon \ (\text{Val } v \ \llbracket \text{bop} \rrbracket \ (\text{Throw } a)) \ (\text{Val } v \ \llbracket \text{bop} \rrbracket \ (\text{Throw } a)) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$   
 $\text{sim-move01 } P \ t \ \varepsilon \ (V' := \text{Throw } a) \ (V := \text{Throw } a) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$   
 $\text{sim-move01 } P \ t \ \varepsilon \ (\text{Throw } a[e0]) \ (\text{Throw } a[e1]) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$   
 $\text{sim-move01 } P \ t \ \varepsilon \ (\text{Val } v[\text{Throw } a]) \ (\text{Val } v[\text{Throw } a]) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$   
 $\text{sim-move01 } P \ t \ \varepsilon \ (\text{Throw } a[e0] := e0') \ (\text{Throw } a[e1] := e1') \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$   
 $\text{sim-move01 } P \ t \ \varepsilon \ (\text{Val } v[\text{Throw } a] := e0) \ (\text{Val } v[\text{Throw } a] := e1) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$

$\text{sim-move01 } P \ t \ \varepsilon \ (\text{Val } v \lfloor \text{Val } v' \rfloor := \text{Throw } a) \ (\text{Val } v \lfloor \text{Val } v' \rfloor := \text{Throw } a) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$   
 $\text{sim-move01 } P \ t \ \varepsilon \ (\text{Throw } a \cdot \text{length}) \ (\text{Throw } a \cdot \text{length}) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$   
 $\text{sim-move01 } P \ t \ \varepsilon \ (\text{Throw } a \cdot F\{D\}) \ (\text{Throw } a \cdot F\{D\}) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$   
 $\text{sim-move01 } P \ t \ \varepsilon \ (\text{Throw } a \cdot F\{D\} := e0) \ (\text{Throw } a \cdot F\{D\} := e1) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$   
 $\text{sim-move01 } P \ t \ \varepsilon \ (\text{Val } v \cdot F\{D\} := \text{Throw } a) \ (\text{Val } v \cdot F\{D\} := \text{Throw } a) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$   
 $\text{sim-move01 } P \ t \ \varepsilon \ (\text{Throw } a \cdot \text{compareAndSwap}(D \cdot F, e2, e3)) \ (\text{Throw } a \cdot \text{compareAndSwap}(D \cdot F, e2', e3')) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$   
 $\text{sim-move01 } P \ t \ \varepsilon \ (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Throw } a, e3)) \ (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Throw } a, e3')) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$   
 $\text{sim-move01 } P \ t \ \varepsilon \ (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', \text{Throw } a)) \ (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', \text{Throw } a)) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$   
 $\text{sim-move01 } P \ t \ \varepsilon \ (\text{Throw } a \cdot M(es0)) \ (\text{Throw } a \cdot M(es1)) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$   
 $\text{sim-move01 } P \ t \ \varepsilon \ (\{V': T=vo; \text{Throw } a\}) \ (\{V': T=None; \text{Throw } a\}) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$   
 $\text{sim-move01 } P \ t \ \varepsilon \ (\text{sync}(\text{Throw } a) \ e0) \ (\text{sync}_V(\text{Throw } a) \ e1) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$   
 $\text{sim-move01 } P \ t \ \varepsilon \ (\text{Throw } a;;e0) \ (\text{Throw } a;;e1) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$   
 $\text{sim-move01 } P \ t \ \varepsilon \ (\text{if } (\text{Throw } a) \ e0 \text{ else } e0') \ (\text{if } (\text{Throw } a) \ e1 \text{ else } e1') \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$   
 $\text{sim-move01 } P \ t \ \varepsilon \ (\text{throw } (\text{Throw } a)) \ (\text{throw } (\text{Throw } a)) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$   
**apply**(simp-all add: sim-move01-def ta-bisim-def split: if-split-asm split del: if-split)  
**apply**(fastforce intro: red1-reds1.intros)+  
**done**

**lemma** *sim-move01-ThrowParams:*

$\text{sim-move01 } P \ t \ \varepsilon \ (\text{Val } v \cdot M(\text{map } \text{Val } vs \ @ \ \text{Throw } a \ \# \ es0)) \ (\text{Val } v \cdot M(\text{map } \text{Val } vs \ @ \ \text{Throw } a \ \# \ es1)) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$   
**apply**(simp add: sim-move01-def split del: if-split)  
**apply**(rule conjI, fastforce)  
**apply**(split if-split)  
**apply**(rule conjI)  
**apply**(fastforce intro: red1-reds1.intros)  
**apply**(fastforce simp add: sim-move01-def intro: red1-reds1.intros)  
**done**

**lemma** *sim-move01-CallNull:*

$\text{sim-move01 } P \ t \ \varepsilon \ (\text{null} \cdot M(\text{map } \text{Val } vs)) \ (\text{null} \cdot M(\text{map } \text{Val } vs)) \ h \ xs \ \varepsilon \ (\text{THROW } \text{NullPointer}) \ h \ xs$   
**by**(fastforce simp add: sim-move01-def map-eq-append-conv intro: red1-reds1.intros)

**lemma** *sim-move01-SyncLocks:*

$\llbracket V < \text{length } xs; \text{ta0} = \llbracket \text{Lock} \rightarrow a, \text{SyncLock } a \rrbracket; \text{ta} = \llbracket \text{Lock} \rightarrow a, \text{SyncLock } a \rrbracket \rrbracket$   
 $\implies \text{sim-move01 } P \ t \ \text{ta0} \ (\text{sync}(\text{addr } a) \ e0) \ (\text{sync}_V(\text{addr } a) \ e1) \ h \ xs \ \text{ta} \ (\text{insync}_V(a) \ e1) \ h \ (xs[V := \text{Addr } a])$   
 $\llbracket xs ! V = \text{Addr } a'; V < \text{length } xs; \text{ta0} = \llbracket \text{Unlock} \rightarrow a', \text{SyncUnlock } a' \rrbracket; \text{ta} = \llbracket \text{Unlock} \rightarrow a', \text{SyncUnlock } a' \rrbracket \rrbracket$   
 $\implies \text{sim-move01 } P \ t \ \text{ta0} \ (\text{insync}(a') \ (\text{Val } v)) \ (\text{insync}_V(a) \ (\text{Val } v)) \ h \ xs \ \text{ta} \ (\text{Val } v) \ h \ xs$   
 $\llbracket xs ! V = \text{Addr } a'; V < \text{length } xs; \text{ta0} = \llbracket \text{Unlock} \rightarrow a', \text{SyncUnlock } a' \rrbracket; \text{ta} = \llbracket \text{Unlock} \rightarrow a', \text{SyncUnlock } a' \rrbracket \rrbracket$   
 $\implies \text{sim-move01 } P \ t \ \text{ta0} \ (\text{insync}(a') \ (\text{Throw } a'')) \ (\text{insync}_V(a) \ (\text{Throw } a'')) \ h \ xs \ \text{ta} \ (\text{Throw } a'') \ h \ xs$   
**by**(fastforce simp add: sim-move01-def ta-bisim-def expand-funfun-eq fun-eq-iff finfun-upd-apply ta-upd-simps intro: red1-reds1.intros[simplified] split: if-split-asm)+

**lemma** *sim-move01-TryFail:*

$\llbracket \text{typeof-addr } h \ a = \lfloor \text{Class-type } D \rfloor; \neg P \vdash D \preceq^* C \rrbracket$   
 $\implies \text{sim-move01 } P \ t \ \varepsilon \ (\text{try } (\text{Throw } a) \ \text{catch}(C \ V') \ e0) \ (\text{try } (\text{Throw } a) \ \text{catch}(C \ V) \ e1) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ xs$   
**by**(auto simp add: sim-move01-def intro!: Red1TryFail)

**lemma** *sim-move01-BlockSome*:

$\llbracket \text{sim-move01 } P \ t \ ta0 \ e0 \ e \ h \ (xs[V := v]) \ ta \ e' \ h' \ xs'; \ V < \text{length } xs \rrbracket$   
 $\implies \text{sim-move01 } P \ t \ ta0 \ (\{V':T=[v]; \ e0\}) \ (\{V:T=[v]; \ e\}) \ h \ xs \ ta \ (\{V:T=None; \ e'\}) \ h' \ xs'$   
 $V < \text{length } xs \implies \text{sim-move01 } P \ t \ \varepsilon \ (\{V':T=[v]; \ \text{Val } u\}) \ (\{V:T=[v]; \ \text{Val } u\}) \ h \ xs \ \varepsilon \ (\text{Val } u) \ h$   
 $(xs[V := v])$   
 $V < \text{length } xs \implies \text{sim-move01 } P \ t \ \varepsilon \ (\{V':T=[v]; \ \text{Throw } a\}) \ (\{V:T=[v]; \ \text{Throw } a\}) \ h \ xs \ \varepsilon \ (\text{Throw } a) \ h \ (xs[V := v])$   
**apply**(*auto simp add: sim-move01-def*)  
**apply**(*split if-split-asm*)  
**apply**(*fastforce intro: intro: converse-rtrancpl-into-rtrancpl Block1Some Block1Red Block- $\tau$ red1r-Some*)  
**apply**(*fastforce intro: intro: converse-rtrancpl-into-rtrancpl Block1Some Block1Red Block- $\tau$ red1r-Some*)  
**apply**(*fastforce simp add: sim-move01-def intro!:  $\tau$ red1t-2step[OF Block1Some]  $\tau$ red1r-1step[OF Block1Some]*  
*Red1Block Block1Throw*)  
**done**

**lemmas** *sim-move01-intros* =

*sim-move01-expr sim-move01-reds sim-move01-ThrowParams sim-move01-CallNull sim-move01-TryFail*  
*sim-move01-BlockSome sim-move01-CallParams*

**declare** *sim-move01-intros*[*intro*]

**lemma** *sim-move01-preserves-len*: *sim-move01*  $P \ t \ ta0 \ e0 \ e \ h \ xs \ ta \ e' \ h' \ xs' \implies \text{length } xs' = \text{length } xs$   
**by**(*fastforce simp add: sim-move01-def split: if-split-asm dest:  $\tau$ red1r-preserves-len  $\tau$ red1t-preserves-len red1-preserves-len*)

**lemma** *sim-move01-preserves-unmod*:

$\llbracket \text{sim-move01 } P \ t \ ta0 \ e0 \ e \ h \ xs \ ta \ e' \ h' \ xs'; \ \text{unmod } e \ i; \ i < \text{length } xs \rrbracket \implies xs' ! i = xs ! i$   
**apply**(*auto simp add: sim-move01-def split: if-split-asm dest:  $\tau$ red1t-preserves-unmod*)  
**apply**(*frule (2)  $\tau$ red1'r-preserves-unmod*)  
**apply**(*frule (1)  $\tau$ red1r-unmod-preserved*)  
**apply**(*frule  $\tau$ red1r-preserves-len*)  
**apply**(*auto dest: red1-preserves-unmod*)  
**apply**(*frule (2)  $\tau$ red1'r-preserves-unmod*)  
**apply**(*frule (1)  $\tau$ red1r-unmod-preserved*)  
**apply**(*frule  $\tau$ red1r-preserves-len*)  
**apply**(*auto dest: red1-preserves-unmod*)  
**done**

**lemma** **assumes** *wf: wf-J-prog P*

**shows** *red1-simulates-red-aux*:

$\llbracket \text{extTA2J0 } P, P, t \vdash \langle e1, S \rangle -TA \rightarrow \langle e1', S' \rangle; \text{bisim } vs \ e1 \ e2 \ XS; \text{fv } e1 \subseteq \text{set } vs;$   
 $\text{lcl } S \subseteq_m [vs \mapsto] XS; \text{length } vs + \text{max-vars } e1 \leq \text{length } XS;$   
 $\forall aMvs. \text{call } e1 = [aMvs] \longrightarrow \text{synthesized-call } P \ (hp \ S) \ aMvs \rrbracket$   
 $\implies \exists ta \ e2' \ XS'. \text{sim-move01 } (\text{compP1 } P) \ t \ TA \ e1 \ e2 \ (hp \ S) \ XS \ ta \ e2' \ (hp \ S') \ XS' \wedge \text{bisim } vs \ e1'$   
 $e2' \ XS' \wedge \text{lcl } S' \subseteq_m [vs \mapsto] XS']$   
 $(\text{is } \llbracket -; -; -; -; ?\text{synth } e1 \ S \rrbracket \implies ?\text{concl } e1 \ e2 \ S \ XS \ e1' \ S' \ TA \ vs)$

**and** *reds1-simulates-reds-aux*:

$\llbracket \text{extTA2J0 } P, P, t \vdash \langle es1, S \rangle [-TA \rightarrow] \langle es1', S' \rangle; \text{bisims } vs \ es1 \ es2 \ XS; \text{fvs } es1 \subseteq \text{set } vs;$   
 $\text{lcl } S \subseteq_m [vs \mapsto] XS; \text{length } vs + \text{max-varss } es1 \leq \text{length } XS;$   
 $\forall aMvs. \text{calls } es1 = [aMvs] \longrightarrow \text{synthesized-call } P \ (hp \ S) \ aMvs \rrbracket$   
 $\implies \exists ta \ es2' \ xs'. \text{sim-moves01 } (\text{compP1 } P) \ t \ TA \ es1 \ es2 \ (hp \ S) \ XS \ ta \ es2' \ (hp \ S') \ xs' \wedge \text{bisims } vs$   
 $es1' \ es2' \ xs' \wedge \text{lcl } S' \subseteq_m [vs \mapsto] xs']$



$(\text{is } \llbracket -; -; -; -; ?\text{synths } es1 \ S \rrbracket \implies ?\text{concls } es1 \ es2 \ S \ XS \ es1' \ S' \ TA \ vs)$   
**proof**(*induct arbitrary: vs e2 XS and vs es2 XS rule: red-reds.inducts*)  
**case** (*BinOpRed1 e s ta e' s' bop e2 Vs E2 xs*)  
**note**  $IH = \langle \bigwedge vs \ e2 \ XS. \llbracket \text{bisim } vs \ e \ e2 \ XS; fv \ e \subseteq \text{set } vs; lcl \ s \subseteq_m [vs \mapsto] XS \rrbracket; \text{length } vs + \text{max-vars } e \leq \text{length } XS;$   
 $\quad ?\text{synth } e \ s \rrbracket \implies ?\text{concl } e \ e2 \ s \ XS \ e' \ s' \ ta \ vs \rangle$   
**from**  $\langle \text{extTA2J0 } P, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \rangle$  **have**  $\neg \text{is-val } e$  **by** *auto*  
**with**  $\langle \text{bisim } Vs \ (e \llbracket \text{bop} \rrbracket e2) \ E2 \ xs \rangle$  **obtain**  $E$   
**where**  $E2 = E \llbracket \text{bop} \rrbracket \text{compE1 } Vs \ e2$  **and**  $\text{bisim } Vs \ e \ E \ xs$  **and**  $\neg \text{contains-insync } e2$  **by** *auto*  
**with**  $IH[\text{of } Vs \ E \ xs] \langle fv \ (e \llbracket \text{bop} \rrbracket e2) \subseteq \text{set } Vs \rangle \langle lcl \ s \subseteq_m [Vs \mapsto] xs \rangle \langle \neg \text{is-val } e \rangle$   
 $\langle \text{length } Vs + \text{max-vars } (e \llbracket \text{bop} \rrbracket e2) \leq \text{length } xs \rangle \langle ?\text{synth } (e \llbracket \text{bop} \rrbracket e2) \ s \rangle \langle \text{extTA2J0 } P, P, t \vdash \langle e, s \rangle$   
 $-ta \rightarrow \langle e', s' \rangle \rangle$   
**show**  $?case \text{ by}(\text{cases is-val } e^{\wedge})(\text{fastforce elim!}: \text{sim-move01-expr})+$   
**next**  
**case** (*BinOpRed2 e s ta e' s' v bop Vs E2 xs*)  
**note**  $IH = \langle \bigwedge vs \ e2 \ XS. \llbracket \text{bisim } vs \ e \ e2 \ XS; fv \ e \subseteq \text{set } vs; lcl \ s \subseteq_m [vs \mapsto] XS \rrbracket; \text{length } vs + \text{max-vars } e \leq \text{length } XS;$   
 $\quad ?\text{synth } e \ s \rrbracket \implies ?\text{concl } e \ e2 \ s \ XS \ e' \ s' \ ta \ vs \rangle$   
**from**  $\langle \text{bisim } Vs \ (Val \ v \llbracket \text{bop} \rrbracket e) \ E2 \ xs \rangle$  **obtain**  $E$   
**where**  $E2 = Val \ v \llbracket \text{bop} \rrbracket E$  **and**  $\text{bisim } Vs \ e \ E \ xs$  **by** *auto*  
**with**  $IH[\text{of } Vs \ E \ xs] \langle fv \ (Val \ v \llbracket \text{bop} \rrbracket e) \subseteq \text{set } Vs \rangle \langle lcl \ s \subseteq_m [Vs \mapsto] xs \rangle$   
 $\langle \text{length } Vs + \text{max-vars } (Val \ v \llbracket \text{bop} \rrbracket e) \leq \text{length } xs \rangle \langle ?\text{synth } (Val \ v \llbracket \text{bop} \rrbracket e) \ s \rangle \langle \text{extTA2J0 } P, P, t$   
 $\vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \rangle$   
**show**  $?case \text{ by}(\text{fastforce elim!}: \text{sim-move01-expr})$   
**next**  
**case** *RedVar thus ?case*  
**by**(*fastforce simp add: index-less-aux map-le-def fun-upds-apply intro!: exI dest: bspec*)  
**next**  
**case** *RedLAss thus ?case*  
**by**(*fastforce intro: index-less-aux LAss-lem intro!: exI simp del: fun-upd-apply*)  
**next**  
**case** (*AAccRed1 a s ta a' s' i Vs E2 xs*)  
**note**  $IH = \langle \bigwedge vs \ e2 \ XS. \llbracket \text{bisim } vs \ a \ e2 \ XS; fv \ a \subseteq \text{set } vs; lcl \ s \subseteq_m [vs \mapsto] XS \rrbracket; \text{length } vs + \text{max-vars } a \leq \text{length } XS;$   
 $\quad ?\text{synth } a \ s \rrbracket \implies ?\text{concl } a \ e2 \ s \ XS \ a' \ s' \ ta \ vs \rangle$   
**from**  $\langle \text{extTA2J0 } P, P, t \vdash \langle a, s \rangle -ta \rightarrow \langle a', s' \rangle \rangle$  **have**  $\neg \text{is-val } a$  **by** *auto*  
**with**  $\langle \text{bisim } Vs \ (a[i]) \ E2 \ xs \rangle$  **obtain**  $E$   
**where**  $E2 = E[\text{compE1 } Vs \ i]$  **and**  $\text{bisim } Vs \ a \ E \ xs$  **and**  $\neg \text{contains-insync } i$  **by** *auto*  
**with**  $IH[\text{of } Vs \ E \ xs] \langle fv \ (a[i]) \subseteq \text{set } Vs \rangle \langle lcl \ s \subseteq_m [Vs \mapsto] xs \rangle \langle \neg \text{is-val } a \rangle$   
 $\langle \text{length } Vs + \text{max-vars } (a[i]) \leq \text{length } xs \rangle \langle ?\text{synth } (a[i]) \ s \rangle \langle \text{extTA2J0 } P, P, t \vdash \langle a, s \rangle -ta \rightarrow \langle a', s' \rangle \rangle$   
**show**  $?case \text{ by}(\text{cases is-val } a^{\wedge})(\text{fastforce elim!}: \text{sim-move01-expr})+$   
**next**  
**case** (*AAccRed2 i s ta i' s' a Vs E2 xs*)  
**note**  $IH = \langle \bigwedge vs \ e2 \ XS. \llbracket \text{bisim } vs \ i \ e2 \ XS; fv \ i \subseteq \text{set } vs; lcl \ s \subseteq_m [vs \mapsto] XS \rrbracket; \text{length } vs + \text{max-vars } i \leq \text{length } XS;$   
 $\quad ?\text{synth } i \ s \rrbracket \implies ?\text{concl } i \ e2 \ s \ XS \ i' \ s' \ ta \ vs \rangle$   
**from**  $\langle \text{bisim } Vs \ (Val \ a[i]) \ E2 \ xs \rangle$  **obtain**  $E$   
**where**  $E2 = Val \ a[E]$  **and**  $\text{bisim } Vs \ i \ E \ xs$  **by** *auto*  
**with**  $IH[\text{of } Vs \ E \ xs] \langle fv \ (Val \ a[i]) \subseteq \text{set } Vs \rangle \langle lcl \ s \subseteq_m [Vs \mapsto] xs \rangle$   
 $\langle \text{length } Vs + \text{max-vars } (Val \ a[i]) \leq \text{length } xs \rangle \langle ?\text{synth } (Val \ a[i]) \ s \rangle \langle \text{extTA2J0 } P, P, t \vdash \langle i, s \rangle -ta \rightarrow$   
 $\langle i', s' \rangle \rangle$   
**show**  $?case \text{ by}(\text{fastforce elim!}: \text{sim-move01-expr})$   
**next**  
**case** *RedAAcc thus ?case by(auto simp del: split-paired-Ex)*

next

**case** (*AAssRed1* *a s ta a' s' i e Vs E2 xs*)  
**note**  $IH = \langle \bigwedge vs \ e2 \ XS. \llbracket bisim \ vs \ a \ e2 \ XS; \ fv \ a \subseteq set \ vs; \ lcl \ s \subseteq_m [vs \mapsto] \ XS \rrbracket; \ length \ vs + \ max\text{-}vars \ a \leq \ length \ XS;$   
 $\text{?synth } a \ s \rrbracket \implies \text{?concl } a \ e2 \ s \ XS \ a' \ s' \ ta \ vs \rangle$   
**from**  $\langle extTA2J0 \ P, P, t \vdash \langle a, s \rangle -ta \rightarrow \langle a', s' \rangle \rangle$  **have**  $\neg is\text{-}val \ a$  **by** *auto*  
**with**  $\langle bisim \ Vs \ (a[i] := e) \ E2 \ xs \rangle$  **obtain** *E*  
**where**  $E2: E2 = E[compE1 \ Vs \ i] := compE1 \ Vs \ e$  **and**  $bisim \ Vs \ a \ E \ xs$   
**and**  $sync: \neg contains\text{-}insync \ i \neg contains\text{-}insync \ e$  **by** *auto*  
**with**  $IH[of \ Vs \ E \ xs] \langle fv \ (a[i] := e) \subseteq set \ Vs \rangle \langle lcl \ s \subseteq_m [Vs \mapsto] \ xs \rangle \langle \neg is\text{-}val \ a \rangle \langle extTA2J0 \ P, P, t \vdash \langle a, s \rangle -ta \rightarrow \langle a', s' \rangle \rangle$   
 $\langle length \ Vs + \ max\text{-}vars \ (a[i] := e) \leq \ length \ xs \rangle \langle \text{?synth } (a[i] := e) \ s \rangle$   
**obtain**  $ta' \ e2' \ xs'$   
**where**  $IH': sim\text{-}move01 \ (compP1 \ P) \ t \ ta \ a \ E \ (hp \ s) \ xs \ ta' \ e2' \ (hp \ s') \ xs'$   
 $bisim \ Vs \ a' \ e2' \ xs' \ lcl \ s' \subseteq_m [Vs \mapsto] \ xs'$   
**by** *auto*  
**show** *?case*  
**proof**(*cases is-val a'*)  
**case** *True*  
**from**  $\langle fv \ (a[i] := e) \subseteq set \ Vs \rangle \ sync$   
**have**  $bisim \ Vs \ i \ (compE1 \ Vs \ i) \ xs' \ bisim \ Vs \ e \ (compE1 \ Vs \ e) \ xs'$  **by** *auto*  
**with**  $IH' \ E2 \ True \ sync \ \langle \neg is\text{-}val \ a \rangle \langle extTA2J0 \ P, P, t \vdash \langle a, s \rangle -ta \rightarrow \langle a', s' \rangle \rangle$  **show** *?thesis*  
**by**(*cases is-val i*)(*fastforce elim!:: sim-move01-expr*)+  
next  
**case** *False* **with**  $IH' \ E2 \ sync \ \langle \neg is\text{-}val \ a \rangle \langle extTA2J0 \ P, P, t \vdash \langle a, s \rangle -ta \rightarrow \langle a', s' \rangle \rangle$   
**show** *?thesis* **by**(*fastforce elim!:: sim-move01-expr*)  
**qed**  
next  
**case** (*AAssRed2* *i s ta i' s' a e Vs E2 xs*)  
**note**  $IH = \langle \bigwedge vs \ e2 \ XS. \llbracket bisim \ vs \ i \ e2 \ XS; \ fv \ i \subseteq set \ vs; \ lcl \ s \subseteq_m [vs \mapsto] \ XS \rrbracket; \ length \ vs + \ max\text{-}vars \ i \leq \ length \ XS;$   
 $\text{?synth } i \ s \rrbracket \implies \text{?concl } i \ e2 \ s \ XS \ i' \ s' \ ta \ vs \rangle$   
**from**  $\langle extTA2J0 \ P, P, t \vdash \langle i, s \rangle -ta \rightarrow \langle i', s' \rangle \rangle$  **have**  $\neg is\text{-}val \ i$  **by** *auto*  
**with**  $\langle bisim \ Vs \ (Val \ a[i] := e) \ E2 \ xs \rangle$  **obtain** *E*  
**where**  $E2 = Val \ a[E] := compE1 \ Vs \ e$  **and**  $bisim \ Vs \ i \ E \ xs$  **and**  $\neg contains\text{-}insync \ e$  **by** *auto*  
**with**  $IH[of \ Vs \ E \ xs] \langle fv \ (Val \ a[i] := e) \subseteq set \ Vs \rangle \langle lcl \ s \subseteq_m [Vs \mapsto] \ xs \rangle \langle \neg is\text{-}val \ i \rangle \langle extTA2J0 \ P, P, t \vdash \langle i, s \rangle -ta \rightarrow \langle i', s' \rangle \rangle$   
 $\langle length \ Vs + \ max\text{-}vars \ (Val \ a[i] := e) \leq \ length \ xs \rangle \langle \text{?synth } (Val \ a[i] := e) \ s \rangle$   
**show** *?case* **by**(*cases is-val i'*)(*fastforce elim!:: sim-move01-expr*)+  
next  
**case** (*AAssRed3* *e s ta e' s' a i Vs E2 xs*)  
**note**  $IH = \langle \bigwedge vs \ e2 \ XS. \llbracket bisim \ vs \ e \ e2 \ XS; \ fv \ e \subseteq set \ vs; \ lcl \ s \subseteq_m [vs \mapsto] \ XS \rrbracket; \ length \ vs + \ max\text{-}vars \ e \leq \ length \ XS;$   
 $\text{?synth } e \ s \rrbracket \implies \text{?concl } e \ e2 \ s \ XS \ e' \ s' \ ta \ vs \rangle$   
**from**  $\langle bisim \ Vs \ (Val \ a[Val \ i] := e) \ E2 \ xs \rangle$  **obtain** *E*  
**where**  $E2 = Val \ a[Val \ i] := E$  **and**  $bisim \ Vs \ e \ E \ xs$  **by** *auto*  
**with**  $IH[of \ Vs \ E \ xs] \langle fv \ (Val \ a[Val \ i] := e) \subseteq set \ Vs \rangle \langle lcl \ s \subseteq_m [Vs \mapsto] \ xs \rangle \langle extTA2J0 \ P, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \rangle$   
 $\langle length \ Vs + \ max\text{-}vars \ (Val \ a[Val \ i] := e) \leq \ length \ xs \rangle \langle \text{?synth } (Val \ a[Val \ i] := e) \ s \rangle$   
**show** *?case* **by**(*fastforce elim!:: sim-move01-expr*)  
next  
**case** *RedAAssStore* **thus** *?case* **by**(*auto intro!:: exI*)  
next  
**case** (*FAssRed1* *e s ta e' s' F D e2 Vs E2 xs*)

**note**  $IH = \langle \bigwedge vs\ e2\ XS. \llbracket bisim\ vs\ e\ e2\ XS; fv\ e \subseteq set\ vs; lcl\ s \subseteq_m [vs\ [\mapsto]\ XS]; length\ vs + max\ vars\ e \leq length\ XS; \rrbracket$

$?synth\ e\ s \rrbracket \implies ?concl\ e\ e2\ s\ XS\ e'\ s'\ ta\ vs \rangle$

**from**  $\langle extTA2J0\ P, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \rangle$  **have**  $\neg is\ val\ e$  **by** *auto*

**with**  $\langle bisim\ Vs\ (e \cdot F\{D\} := e2)\ E2\ xs \rangle$  **obtain**  $E$

**where**  $E2 = E \cdot F\{D\} := compE1\ Vs\ e2$  **and**  $bisim\ Vs\ e\ E\ xs$  **and**  $\neg contains\ insync\ e2$  **by** *auto*

**with**  $IH[of\ Vs\ E\ xs]\ \langle fv\ (e \cdot F\{D\} := e2) \subseteq set\ Vs \rangle \langle lcl\ s \subseteq_m [Vs\ [\mapsto]\ xs] \rangle \langle \neg is\ val\ e \rangle \langle extTA2J0\ P, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \rangle$

$\langle length\ Vs + max\ vars\ (e \cdot F\{D\} := e2) \leq length\ xs \rangle \langle ?synth\ (e \cdot F\{D\} := e2)\ s \rangle$

**show**  $?case\ by(cases\ is\ val\ e)(fastforce\ elim!:\ sim\ move01\ expr) +$

**next**

**case**  $(FAssRed2\ e\ s\ ta\ e'\ s'\ v\ F\ D\ Vs\ E2\ xs)$

**note**  $IH = \langle \bigwedge vs\ e2\ XS. \llbracket bisim\ vs\ e\ e2\ XS; fv\ e \subseteq set\ vs; lcl\ s \subseteq_m [vs\ [\mapsto]\ XS]; length\ vs + max\ vars\ e \leq length\ XS; \rrbracket$

$?synth\ e\ s \rrbracket \implies ?concl\ e\ e2\ s\ XS\ e'\ s'\ ta\ vs \rangle$

**from**  $\langle bisim\ Vs\ (Val\ v \cdot F\{D\} := e)\ E2\ xs \rangle$  **obtain**  $E$

**where**  $E2 = Val\ v \cdot F\{D\} := E$  **and**  $bisim\ Vs\ e\ E\ xs$  **by** *auto*

**with**  $IH[of\ Vs\ E\ xs]\ \langle fv\ (Val\ v \cdot F\{D\} := e) \subseteq set\ Vs \rangle \langle lcl\ s \subseteq_m [Vs\ [\mapsto]\ xs] \rangle \langle extTA2J0\ P, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \rangle$

$\langle length\ Vs + max\ vars\ (Val\ v \cdot F\{D\} := e) \leq length\ xs \rangle \langle ?synth\ (Val\ v \cdot F\{D\} := e)\ s \rangle$

**show**  $?case\ by(fastforce\ elim!:\ sim\ move01\ expr)$

**next**

**case**  $(CASRed1\ e\ s\ ta\ e'\ s'\ D\ F\ e2\ e3\ Vs\ E2\ xs)$

**note**  $IH = \langle \bigwedge vs\ e2\ XS. \llbracket bisim\ vs\ e\ e2\ XS; fv\ e \subseteq set\ vs; lcl\ s \subseteq_m [vs\ [\mapsto]\ XS]; length\ vs + max\ vars\ e \leq length\ XS; \rrbracket$

$?synth\ e\ s \rrbracket \implies ?concl\ e\ e2\ s\ XS\ e'\ s'\ ta\ vs \rangle$

**from**  $\langle extTA2J0\ P, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \rangle$  **have**  $\neg is\ val\ e$  **by** *auto*

**with**  $\langle bisim\ Vs\ -\ E2\ xs \rangle$  **obtain**  $E$

**where**  $E2: E2 = E \cdot compareAndSwap(D \cdot F, compE1\ Vs\ e2, compE1\ Vs\ e3)$  **and**  $bisim\ Vs\ e\ E\ xs$

**and**  $sync: \neg contains\ insync\ e2 \neg contains\ insync\ e3$  **by** *(auto)*

**with**  $IH[of\ Vs\ E\ xs]\ \langle fv\ - \subseteq set\ Vs \rangle \langle lcl\ s \subseteq_m [Vs\ [\mapsto]\ xs] \rangle \langle \neg is\ val\ e \rangle \langle extTA2J0\ P, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \rangle$

$\langle length\ Vs + max\ vars\ - \leq length\ xs \rangle \langle ?synth\ -\ s \rangle$

**obtain**  $ta'\ e2'\ xs'$

**where**  $IH': sim\ move01\ (compP1\ P)\ t\ ta\ e\ E\ (hp\ s)\ xs\ ta'\ e2'\ (hp\ s')\ xs'$

$bisim\ Vs\ e'\ e2'\ xs'\ lcl\ s' \subseteq_m [Vs\ [\mapsto]\ xs']$

**by** *auto*

**show**  $?case$

**proof**  $(cases\ is\ val\ e')$

**case** *True*

**from**  $\langle fv\ - \subseteq set\ Vs \rangle\ sync$

**have**  $bisim\ Vs\ e2\ (compE1\ Vs\ e2)\ xs'\ bisim\ Vs\ e3\ (compE1\ Vs\ e3)\ xs'$  **by** *auto*

**with**  $IH'\ E2\ True\ sync\ \langle \neg is\ val\ e \rangle \langle extTA2J0\ P, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \rangle$  **show**  $?thesis$

**by**  $(cases\ is\ val\ e2)(fastforce\ elim!:\ sim\ move01\ expr) +$

**next**

**case** *False* **with**  $IH'\ E2\ sync\ \langle \neg is\ val\ e \rangle \langle extTA2J0\ P, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \rangle$

**show**  $?thesis\ by(fastforce\ elim!:\ sim\ move01\ expr)$

**qed**

**next**

**case**  $(CASRed2\ e\ s\ ta\ e'\ s'\ v\ D\ F\ e3\ Vs\ E2\ xs)$

**note**  $IH = \langle \bigwedge vs\ e2\ XS. \llbracket bisim\ vs\ e\ e2\ XS; fv\ e \subseteq set\ vs; lcl\ s \subseteq_m [vs\ [\mapsto]\ XS]; length\ vs + max\ vars\ e \leq length\ XS; \rrbracket$

$?synth\ e\ s \rrbracket \implies ?concl\ e\ e2\ s\ XS\ e'\ s'\ ta\ vs \rangle$

**from**  $\langle extTA2J0\ P, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \rangle$  **have**  $\neg is\ val\ e$  **by** *auto*

**with**  $\langle \text{bisim } Vs - E2 \text{ } xs \rangle$  **obtain**  $E$   
**where**  $E2 = \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, E, \text{compE1 } Vs \text{ } e3)$  **and**  $\text{bisim } Vs \text{ } e \text{ } E \text{ } xs$  **and**  $\neg$   
 $\text{contains-insync } e3$  **by**  $(\text{auto})$   
**with**  $IH[\text{of } Vs \text{ } E \text{ } xs] \langle fv - \subseteq \text{set } Vs \rangle \langle \text{lcl } s \subseteq_m [Vs \mapsto] xs \rangle \langle \neg \text{is-val } e \rangle \langle \text{extTA2J0 } P, P, t \vdash \langle e, s \rangle$   
 $-ta \rightarrow \langle e', s' \rangle \rangle$   
 $\langle \text{length } Vs + \text{max-vars} - \leq \text{length } xs \rangle \langle ?\text{synth} - s \rangle$   
**show**  $?case \text{ by}(\text{cases is-val } e^\wedge)(\text{fastforce elim!}: \text{sim-move01-expr}) +$   
**next**  
**case**  $(\text{CASRed3 } e \text{ } s \text{ } ta \text{ } e' \text{ } s' \text{ } v \text{ } D \text{ } F \text{ } v' \text{ } Vs \text{ } E2 \text{ } xs)$   
**note**  $IH = \langle \bigwedge vs \text{ } e2 \text{ } XS. \llbracket \text{bisim } vs \text{ } e \text{ } e2 \text{ } XS; fv \text{ } e \subseteq \text{set } vs; \text{lcl } s \subseteq_m [vs \mapsto] XS; \text{length } vs + \text{max-vars}$   
 $e \leq \text{length } XS;$   
 $?synth \text{ } e \text{ } s \rrbracket \implies ?concl \text{ } e \text{ } e2 \text{ } s \text{ } XS \text{ } e' \text{ } s' \text{ } ta \text{ } vs \rangle$   
**from**  $\langle \text{bisim } Vs - E2 \text{ } xs \rangle$  **obtain**  $E$   
**where**  $E2 = \text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', E)$  **and**  $\text{bisim } Vs \text{ } e \text{ } E \text{ } xs$  **by**  $\text{auto}$   
**with**  $IH[\text{of } Vs \text{ } E \text{ } xs] \langle fv - \subseteq \text{set } Vs \rangle \langle \text{lcl } s \subseteq_m [Vs \mapsto] xs \rangle \langle \text{extTA2J0 } P, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle$   
 $\langle \text{length } Vs + \text{max-vars} - \leq \text{length } xs \rangle \langle ?\text{synth} - s \rangle$   
**show**  $?case \text{ by}(\text{fastforce elim!}: \text{sim-move01-expr})$   
**next**  
**case**  $(\text{CallObj } e \text{ } s \text{ } ta \text{ } e' \text{ } s' \text{ } M \text{ } es \text{ } Vs \text{ } E2 \text{ } xs)$   
**note**  $IH = \langle \bigwedge vs \text{ } e2 \text{ } XS. \llbracket \text{bisim } vs \text{ } e \text{ } e2 \text{ } XS; fv \text{ } e \subseteq \text{set } vs; \text{lcl } s \subseteq_m [vs \mapsto] XS; \text{length } vs + \text{max-vars}$   
 $e \leq \text{length } XS;$   
 $?synth \text{ } e \text{ } s \rrbracket \implies ?concl \text{ } e \text{ } e2 \text{ } s \text{ } XS \text{ } e' \text{ } s' \text{ } ta \text{ } vs \rangle$   
**from**  $\langle \text{extTA2J0 } P, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \rangle$  **have**  $\neg \text{is-val } e$  **by**  $\text{auto}$   
**with**  $\langle \text{bisim } Vs \text{ } (e \cdot M(es)) \text{ } E2 \text{ } xs \rangle$  **obtain**  $E$   
**where**  $E2 = E \cdot M(\text{compEs1 } Vs \text{ } es)$  **and**  $\text{bisim } Vs \text{ } e \text{ } E \text{ } xs$  **and**  $\neg \text{contains-insyncs } es$   
**by**  $(\text{auto simp add: compEs1-conv-map})$   
**with**  $IH[\text{of } Vs \text{ } E \text{ } xs] \langle fv \text{ } (e \cdot M(es)) \subseteq \text{set } Vs \rangle \langle \text{lcl } s \subseteq_m [Vs \mapsto] xs \rangle$   
 $\langle \text{length } Vs + \text{max-vars } (e \cdot M(es)) \leq \text{length } xs \rangle \langle ?\text{synth } (e \cdot M(es)) \text{ } s \rangle$   
**show**  $?case \text{ by}(\text{cases is-val } e^\wedge)(\text{fastforce elim!}: \text{sim-move01-expr split: if-split-asm}) +$   
**next**  
**case**  $(\text{CallParams } es \text{ } s \text{ } ta \text{ } es' \text{ } s' \text{ } v \text{ } M \text{ } Vs \text{ } E2 \text{ } xs)$   
**note**  $IH = \langle \bigwedge vs \text{ } es2 \text{ } XS. \llbracket \text{bisims } vs \text{ } es \text{ } es2 \text{ } XS; fvs } es \subseteq \text{set } vs; \text{lcl } s \subseteq_m [vs \mapsto] XS; \text{length } vs +$   
 $\text{max-varss } es \leq \text{length } XS;$   
 $?synths \text{ } es \text{ } s \rrbracket \implies ?concls \text{ } es \text{ } es2 \text{ } s \text{ } XS \text{ } es' \text{ } s' \text{ } ta \text{ } vs \rangle$   
**from**  $\langle \text{bisim } Vs \text{ } (\text{Val } v \cdot M(es)) \text{ } E2 \text{ } xs \rangle$  **obtain**  $Es$   
**where**  $E2 = \text{Val } v \cdot M(Es)$  **and**  $\text{bisims } Vs \text{ } es \text{ } Es \text{ } xs$  **by**  $\text{auto}$   
**moreover from**  $\langle \text{extTA2J0 } P, P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \rangle$  **have**  $\neg \text{is-vals } es$  **by**  $\text{auto}$   
**with**  $\langle ?\text{synth } (\text{Val } v \cdot M(es)) \text{ } s \rangle$  **have**  $?synths \text{ } es \text{ } s$  **by**  $(\text{auto})$   
**moreover note**  $IH[\text{of } Vs \text{ } Es \text{ } xs] \langle fv \text{ } (\text{Val } v \cdot M(es)) \subseteq \text{set } Vs \rangle \langle \text{lcl } s \subseteq_m [Vs \mapsto] xs \rangle$   
 $\langle \text{length } Vs + \text{max-vars } (\text{Val } v \cdot M(es)) \leq \text{length } xs \rangle$   
**ultimately show**  $?case \text{ by}(\text{fastforce elim!}: \text{sim-move01-CallParams})$   
**next**  
**case**  $(\text{RedCall } s \text{ } a \text{ } U \text{ } M \text{ } Ts \text{ } T \text{ } pns \text{ } body \text{ } D \text{ } vs \text{ } Vs \text{ } E2 \text{ } xs)$   
**from**  $\langle \text{typeof-addr } (hp \text{ } s) \text{ } a = \lfloor U \rfloor \rangle$   
**have**  $\text{call } (\text{addr } a \cdot M(\text{map } \text{Val } vs)) = \lfloor (a, M, vs) \rfloor$  **by**  $\text{auto}$   
**with**  $\langle ?\text{synth } (\text{addr } a \cdot M(\text{map } \text{Val } vs)) \text{ } s \rangle$  **have**  $\text{synthesized-call } P \text{ } (hp \text{ } s) \text{ } (a, M, vs)$  **by**  $\text{auto}$   
**with**  $\langle \text{typeof-addr } (hp \text{ } s) \text{ } a = \lfloor U \rfloor \rangle \langle P \vdash \text{class-type-of } U \text{ sees } M: Ts \rightarrow T = \lfloor (pns, body) \rfloor \text{ in } D \rangle$   
**have**  $\text{False}$  **by**  $(\text{auto simp add: synthesized-call-conv dest: sees-method-fun})$   
**thus**  $?case \dots$   
**next**  
**case**  $(\text{RedCallExternal } s \text{ } a \text{ } T \text{ } M \text{ } Ts \text{ } Tr \text{ } D \text{ } vs \text{ } ta \text{ } va \text{ } h' \text{ } ta' \text{ } e' \text{ } s' \text{ } Vs \text{ } E2 \text{ } xs)$   
**from**  $\langle \text{bisim } Vs \text{ } (\text{addr } a \cdot M(\text{map } \text{Val } vs)) \text{ } E2 \text{ } xs \rangle$  **have**  $E2 = \text{addr } a \cdot M(\text{map } \text{Val } vs)$  **by**  $\text{auto}$   
**moreover note**  $\langle P \vdash \text{class-type-of } T \text{ sees } M: Ts \rightarrow Tr = \text{Native in } D \rangle \langle \text{typeof-addr } (hp \text{ } s) \text{ } a = \lfloor T \rfloor \rangle$   
 $\langle ta' = \text{extTA2J0 } P \text{ } ta \rangle$

$\langle e' = \text{extRet2J} (\text{addr } a \cdot M(\text{map Val } vs)) \text{ va} \rangle \langle s' = (h', \text{lcl } s) \rangle \langle P, t \vdash \langle a \cdot M(vs), hp \ s \rangle -ta \rightarrow \text{ext} \langle va, h' \rangle \rangle$   
 $\langle \text{lcl } s \subseteq_m [Vs \mapsto] xs \rangle$   
**moreover from**  $wf \langle P, t \vdash \langle a \cdot M(vs), hp \ s \rangle -ta \rightarrow \text{ext} \langle va, h' \rangle \rangle$   
**have**  $ta\text{-bisim01} (\text{extTA2J0 } P \text{ ta}) (\text{extTA2J1} (\text{compP1 } P) \text{ ta})$  **by** (rule  $\text{red-external-ta-bisim01}$ )  
**moreover from**  $\langle P, t \vdash \langle a \cdot M(vs), hp \ s \rangle -ta \rightarrow \text{ext} \langle va, h' \rangle \rangle \langle \text{typeof-addr } (hp \ s) \ a = \lfloor T \rfloor \rangle$   
 $\langle P \vdash \text{class-type-of } T \text{ sees } M: Ts \rightarrow Tr = \text{Native in } D \rangle$   
**have**  $\tau\text{external-defs } D \ M \implies h' = hp \ s \wedge ta = \varepsilon$   
**by** (fastforce dest:  $\tau\text{external}'\text{-red-external-heap-unchanged } \tau\text{external}'\text{-red-external-TA-empty simp}$   
 $\text{add: } \tau\text{external}'\text{-def } \tau\text{external-def}$ )  
**ultimately show** ?case  
**by** (cases va) (fastforce intro!:  $\text{exI}[\text{where } x=ta]$  intro:  $\text{Red1CallExternal simp add: map-eq-append-conv}$   
 $\text{sim-move01-def dest: sees-method-fun simp del: split-paired-Ex}) +$   
**next**  
**case** ( $\text{BlockRed } e \ h \ x \ V \ vo \ ta \ e' \ h' \ x' \ T \ Vs \ E2 \ xs$ )  
**note**  $IH = \langle \bigwedge vs \ e2 \ XS. \llbracket \text{bisim } vs \ e \ e2 \ XS; \text{fv } e \subseteq \text{set } vs; \text{lcl } (h, x(V := vo)) \subseteq_m [vs \mapsto] XS \rrbracket$   
 $\implies \text{length } vs + \text{max-vars } e \leq \text{length } XS; \text{?synth } e \ (h, (x(V := vo))) \rrbracket$   
 $\implies \text{?concl } e \ e2 \ (h, x(V := vo)) \ XS \ e' \ (h', x') \ ta \ vs \rangle$   
**note**  $\text{red} = \langle \text{extTA2J0 } P, P, t \vdash \langle e, (h, x(V := vo)) \rangle -ta \rightarrow \langle e', (h', x') \rangle \rangle$   
**note**  $\text{len} = \langle \text{length } Vs + \text{max-vars } \{V:T=vo; e\} \leq \text{length } xs \rangle$   
**from**  $\langle \text{fv } \{V:T=vo; e\} \subseteq \text{set } Vs \rangle$  **have**  $\text{fv: } \text{fv } e \subseteq \text{set } (Vs @ [V])$  **by** auto  
**from**  $\langle \text{bisim } Vs \ \{V:T=vo; e\} \ E2 \ xs \rangle$  **show** ?case  
**proof** (cases rule:  $\text{bisim-cases}(7)[\text{consumes } 1, \text{case-names } \text{BlockNone } \text{BlockSome } \text{BlockSomeNone}]$ )  
**case** ( $\text{BlockNone } E'$ )  
**with**  $\text{red } IH[\text{of } Vs @ [V] \ E' \ xs] \text{fv } \langle \text{lcl } (h, x) \subseteq_m [Vs \mapsto] xs \rangle$   
 $\langle \text{length } Vs + \text{max-vars } \{V:T=vo; e\} \leq \text{length } xs \rangle \langle \text{?synth } \{V:T=vo; e\} \ (h, x) \rangle$   
**obtain**  $TA' \ e2' \ xs'$  **where**  $\text{red}': \text{sim-move01} (\text{compP1 } P) \ t \ ta \ e \ E' \ h \ xs \ TA' \ e2' \ h' \ xs'$   
**and**  $\text{bisim}': \text{bisim } (Vs @ [V]) \ e' \ e2' \ xs' \ x' \subseteq_m [Vs @ [V] \mapsto] xs'$  **by** auto  
**from**  $\text{red}' \langle \text{length } Vs + \text{max-vars } \{V:T=vo; e\} \leq \text{length } xs \rangle$   
**have**  $\text{length } (Vs @ [V]) + \text{max-vars } e \leq \text{length } xs'$   
**by** (fastforce simp add:  $\text{sim-move01-def dest: red1-preserves-len } \tau\text{red1t-preserves-len } \tau\text{red1r-preserves-len}$   
 $\text{split: if-split-asm}$ )  
**with**  $\langle x' \subseteq_m [Vs @ [V] \mapsto] xs' \rangle$  **have**  $x' \subseteq_m [Vs \mapsto] xs', V \mapsto xs' ! \text{length } Vs$  **by** (simp)  
**moreover**  
**{** **assume**  $V \in \text{set } Vs$   
**hence**  $\text{hidden } (Vs @ [V]) \ (\text{index } Vs \ V)$  **by** (rule  $\text{hidden-index}$ )  
**with**  $\langle \text{bisim } (Vs @ [V]) \ e \ E' \ xs \rangle$  **have**  $\text{unmod } E' \ (\text{index } Vs \ V)$   
**by**  $-(\text{rule } \text{hidden-bisim-unmod})$   
**moreover from**  $\langle \text{length } Vs + \text{max-vars } \{V:T=vo; e\} \leq \text{length } xs \rangle \langle V \in \text{set } Vs \rangle$   
**have**  $\text{index } Vs \ V < \text{length } xs$  **by** (auto intro:  $\text{index-less-aux}$ )  
**ultimately have**  $xs ! \text{index } Vs \ V = xs' ! \text{index } Vs \ V$   
**using**  $\text{sim-move01-preserves-unmod}[OF \ \text{red}']$  **by** (simp) **}**  
**moreover from**  $\text{red}'$  **have**  $\text{length } xs = \text{length } xs'$  **by** (rule  $\text{sim-move01-preserves-len}[\text{symmetric}]$ )  
**ultimately have**  $\text{rel: } x'(V := x \ V) \subseteq_m [Vs \mapsto] xs'$   
**using**  $\langle \text{lcl } (h, x) \subseteq_m [Vs \mapsto] xs \rangle \langle \text{length } Vs + \text{max-vars } \{V:T=vo; e\} \leq \text{length } xs \rangle$   
**by** (auto intro:  $\text{Block-lem}$ )  
**show** ?thesis  
**proof** (cases  $x' \ V$ )  
**case** None  
**with**  $\text{red}' \text{bisim}' \text{BlockNone } \text{len}$   
**show** ?thesis **by** (fastforce simp del:  $\text{split-paired-Ex fun-upd-apply intro: rel}$ )  
**next**  
**case** ( $\text{Some } v$ )  
**moreover**

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with  $\langle x' \subseteq_m [Vs @ [V] \mapsto xs] \rangle$  have  $[Vs @ [V] \mapsto xs] \ V = [v]$ 
by(auto simp add: map-le-def dest: bspec)
moreover
from  $\langle \text{length } Vs + \text{max-vars } \{V:T=vo; e\} \leq \text{length } xs \rangle$  have  $\text{length } Vs < \text{length } xs$  by auto
ultimately have  $xs' ! \text{length } Vs = v$  using  $\langle \text{length } xs = \text{length } xs' \rangle$  by(simp)
with red' bisim' BlockNone Some len
show ?thesis by(fastforce simp del: fun-upd-apply intro: rel)
qed
next
case (BlockSome E' v)
with red IH[of Vs@[V] E' xs[length Vs := v]] fv lcl (h, x)  $\subseteq_m [Vs \mapsto xs]$ 
 $\langle \text{length } Vs + \text{max-vars } \{V:T=vo; e\} \leq \text{length } xs \rangle \langle ?\text{synth } \{V:T=vo; e\} (h, x) \rangle$ 
obtain  $TA' e2' xs'$  where red': sim-move01 (compP1 P) t ta e E' h (xs[length Vs := v]) TA' e2'
h' xs'
and bisim': bisim (Vs @ [V]) e' e2' xs' x'  $\subseteq_m [Vs @ [V] \mapsto xs]$  by auto
from red'  $\langle \text{length } Vs + \text{max-vars } \{V:T=vo; e\} \leq \text{length } xs \rangle$ 
have  $\text{length } (Vs@[V]) + \text{max-vars } e \leq \text{length } xs'$  by(auto dest: sim-move01-preserves-len)
with  $\langle x' \subseteq_m [Vs @ [V] \mapsto xs] \rangle$  have  $x' \subseteq_m [Vs \mapsto xs', V \mapsto xs' ! \text{length } Vs]$  by(simp)
moreover
{ assume  $V \in \text{set } Vs$ 
hence hidden (Vs @ [V]) (index Vs V) by(rule hidden-index)
with  $\langle \text{bisim } (Vs @ [V]) e E' (xs[\text{length } Vs := v]) \rangle$  have unmod E' (index Vs V)
by  $\neg(\text{rule hidden-bisim-unmod})$ 
moreover from  $\langle \text{length } Vs + \text{max-vars } \{V:T=vo; e\} \leq \text{length } xs \rangle \langle V \in \text{set } Vs \rangle$ 
have  $\text{index } Vs V < \text{length } xs$  by(auto intro: index-less-aux)
moreover from  $\langle \text{length } Vs + \text{max-vars } \{V:T=vo; e\} \leq \text{length } xs \rangle \langle V \in \text{set } Vs \rangle$ 
have  $(xs[\text{length } Vs := v]) ! \text{index } Vs V = xs ! \text{index } Vs V$  by(simp)
ultimately have  $xs ! \text{index } Vs V = xs' ! \text{index } Vs V$ 
using sim-move01-preserves-unmod[OF red', of index Vs V] by(simp) }
moreover from red' have length xs = length xs' by(auto dest: sim-move01-preserves-len)
ultimately have rel: x'(V := x V)  $\subseteq_m [Vs \mapsto xs]$ 
using  $\langle \text{lcl } (h, x) \subseteq_m [Vs \mapsto xs] \rangle \langle \text{length } Vs + \text{max-vars } \{V:T=vo; e\} \leq \text{length } xs \rangle$ 
by(auto intro: Block-lem)
from BlockSome red obtain v' where Some: x' V = [v'] by(auto dest!: red-lcl-incr)
with  $\langle x' \subseteq_m [Vs @ [V] \mapsto xs] \rangle$  have  $[Vs @ [V] \mapsto xs] \ V = [v']$ 
by(auto simp add: map-le-def dest: bspec)
moreover
from  $\langle \text{length } Vs + \text{max-vars } \{V:T=vo; e\} \leq \text{length } xs \rangle$  have  $\text{length } Vs < \text{length } xs$  by auto
ultimately have  $xs' ! \text{length } Vs = v'$  using  $\langle \text{length } xs = \text{length } xs' \rangle$  by(simp)
with red' bisim' BlockSome Some  $\langle \text{length } Vs < \text{length } xs \rangle$ 
show ?thesis by(fastforce simp del: fun-upd-apply intro: rel)
next
case (BlockSomeNone E')
with red IH[of Vs@[V] E' xs] fv lcl (h, x)  $\subseteq_m [Vs \mapsto xs]$ 
 $\langle \text{length } Vs + \text{max-vars } \{V:T=vo; e\} \leq \text{length } xs \rangle \langle ?\text{synth } \{V:T=vo; e\} (h, x) \rangle$ 
obtain  $TA' e2' xs'$  where red': sim-move01 (compP1 P) t ta e E' h xs TA' e2' h' xs'
and IH': bisim (Vs @ [V]) e' e2' xs' x'  $\subseteq_m [Vs @ [V] \mapsto xs]$  by auto
from red'  $\langle \text{length } Vs + \text{max-vars } \{V:T=vo; e\} \leq \text{length } xs \rangle$ 
have  $\text{length } (Vs@[V]) + \text{max-vars } e \leq \text{length } xs'$  by(auto dest: sim-move01-preserves-len)
with  $\langle x' \subseteq_m [Vs @ [V] \mapsto xs] \rangle$  have  $x' \subseteq_m [Vs \mapsto xs', V \mapsto xs' ! \text{length } Vs]$  by(simp)
moreover
{ assume  $V \in \text{set } Vs$ 
hence hidden (Vs @ [V]) (index Vs V) by(rule hidden-index)
with  $\langle \text{bisim } (Vs @ [V]) e E' xs \rangle$  have unmod E' (index Vs V)

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    by  $\neg$ (rule hidden-bisim-unmod)
  moreover from  $\langle \text{length } Vs + \text{max-vars } \{V:T=vo; e\} \leq \text{length } xs \rangle \langle V \in \text{set } Vs \rangle$ 
  have index  $Vs$   $V < \text{length } xs$  by(auto intro: index-less-aux)
  moreover from  $\langle \text{length } Vs + \text{max-vars } \{V:T=vo; e\} \leq \text{length } xs \rangle \langle V \in \text{set } Vs \rangle$ 
  have  $(xs[\text{length } Vs := v]) ! \text{index } Vs$   $V = xs ! \text{index } Vs$   $V$  by(simp)
  ultimately have  $xs ! \text{index } Vs$   $V = xs' ! \text{index } Vs$   $V$ 
    using sim-move01-preserves-unmod[OF red', of index  $Vs$   $V$ ] by(simp) }
  moreover from red' have  $\text{length } xs = \text{length } xs'$  by(auto dest: sim-move01-preserves-len)
  ultimately have rel:  $x'(V := x \ V) \subseteq_m [Vs \mapsto] xs'$ 
    using  $\langle \text{lcl } (h, x) \subseteq_m [Vs \mapsto] xs \rangle \langle \text{length } Vs + \text{max-vars } \{V:T=vo; e\} \leq \text{length } xs \rangle$ 
    by(auto intro: Block-lem)
  from BlockSomeNone red obtain  $v'$  where Some:  $x' \ V = [v']$  by(auto dest!: red-lcl-incr)
  with  $\langle x' \subseteq_m [Vs @ [V] \mapsto] xs' \rangle$  have  $[Vs @ [V] \mapsto] xs'$   $V = [v']$ 
    by(auto simp add: map-le-def dest: bspec)
  moreover
  from  $\langle \text{length } Vs + \text{max-vars } \{V:T=vo; e\} \leq \text{length } xs \rangle$  have  $\text{length } Vs < \text{length } xs$  by auto
  ultimately have  $xs' ! \text{length } Vs = v'$  using  $\langle \text{length } xs = \text{length } xs' \rangle$  by(simp)
  with red' IH' BlockSomeNone Some  $\langle \text{length } Vs < \text{length } xs \rangle$ 
  show ?thesis by(fastforce simp del: fun-upd-apply intro: rel)
qed
next
case (RedBlock  $V \ T \ vo \ u \ s \ Vs \ E2 \ xs$ )
from  $\langle \text{bisim } Vs \ \{V:T=vo; \text{Val } u\} \ E2 \ xs \rangle$  obtain  $vo'$ 
  where [simp]:  $E2 = \{\text{length } Vs:T=vo'; \text{Val } u\}$  by auto
from RedBlock show ?case
proof(cases vo)
  case (Some v)
  with  $\langle \text{bisim } Vs \ \{V:T=vo; \text{Val } u\} \ E2 \ xs \rangle$ 
  have  $vo'$ : case  $vo'$  of None  $\Rightarrow xs ! \text{length } Vs = v \mid$  Some  $v' \Rightarrow v = v'$  by auto
  have sim-move01 (compP1  $P$ )  $t \in \{V:T=vo; \text{Val } u\} \ E2 \ (hp \ s) \ xs \in (\text{Val } u) \ (hp \ s) \ (xs[\text{length } Vs$ 
:= v])
  proof(cases vo')
    case None with vo'
    have  $xs[\text{length } Vs := v] = xs$  by auto
    thus ?thesis using Some None by auto
  next
    case Some
    from  $\langle \text{length } Vs + \text{max-vars } \{V:T=vo; \text{Val } u\} \leq \text{length } xs \rangle$  have  $\text{length } Vs < \text{length } xs$  by simp
    with vo' Some show ?thesis using  $\langle vo = \text{Some } v \rangle$  by auto
  qed
  thus ?thesis using RedBlock by fastforce
qed fastforce
next
case SynchronizedNull thus ?case by fastforce
next
case (LockSynchronized  $a \ e \ s \ Vs \ E2 \ xs$ )
from  $\langle \text{bisim } Vs \ (\text{sync}(\text{addr } a) \ e) \ E2 \ xs \rangle$ 
have E2:  $E2 = \text{sync}_{\text{length } Vs} (\text{addr } a) (\text{compE1 } (Vs@[fresh-var \ Vs]) \ e)$ 
  and sync:  $\neg \text{contains-insync } e$  by auto
moreover have fresh-var  $Vs \notin \text{set } Vs$  by(rule fresh-var-fresh)
with  $\langle \text{fv } (\text{sync}(\text{addr } a) \ e) \subseteq \text{set } Vs \rangle$  have fresh-var  $Vs \notin \text{fv } e$  by auto
from E2  $\langle \text{fv } (\text{sync}(\text{addr } a) \ e) \subseteq \text{set } Vs \rangle$  sync
have bisim  $(Vs@[fresh-var \ Vs]) \ e \ (\text{compE1 } (Vs@[fresh-var \ Vs]) \ e) \ (xs[\text{length } Vs := \text{Addr } a])$ 
  by(auto intro!: compE1-bisim)

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hence  $\text{bisim } Vs \text{ (insync}(a) \text{ } e) \text{ (insync}_{\text{length } Vs} (a) \text{ (compE1 (Vs@[fresh-var Vs]) } e)) \text{ (xs[length Vs := Addr a])}$   
 using  $\langle \text{fresh-var } Vs \notin \text{fv } e \rangle \langle \text{length } Vs + \text{max-vars (sync(addr } a) \text{ } e) \leq \text{length } xs \rangle$  by auto  
 moreover from  $\langle \text{length } Vs + \text{max-vars (sync(addr } a) \text{ } e) \leq \text{length } xs \rangle$   
 have  $\text{False, compP1 } P, t \vdash 1 \langle \text{sync}_{\text{length } Vs} (addr \ a) \text{ (compE1 (Vs@[fresh-var Vs]) } e), (hp \ s, \ xs) \rangle$   
 $\quad - \llbracket \text{Lock} \rightarrow a, \text{ SyncLock } a \rrbracket \rightarrow$   
 $\quad \langle \text{insync}_{\text{length } Vs} (a) \text{ (compE1 (Vs@[fresh-var Vs]) } e), (hp \ s, \ xs[\text{length } Vs := \text{Addr } a]) \rangle$   
 by  $-(\text{rule Lock1Synchronized, auto})$   
 hence  $\text{sim-move01 (compP1 } P) \ t \ \llbracket \text{Lock} \rightarrow a, \text{ SyncLock } a \rrbracket \text{ (sync(addr } a) \text{ } e) \ E2 \ (hp \ s) \ xs \ \llbracket \text{Lock} \rightarrow a, \text{ SyncLock } a \rrbracket \text{ (insync}_{\text{length } Vs} (a) \text{ (compE1 (Vs@[fresh-var Vs]) } e)) \ (hp \ s) \ (xs[\text{length } Vs := \text{Addr } a])$   
 using  $E2$  by  $(\text{fastforce simp add: sim-move01-def ta-bisim-def})$   
 moreover have  $\text{zip } Vs \text{ (xs[length Vs := Addr a])} = (\text{zip } Vs \ xs)[\text{length } Vs := (\text{arbitrary, Addr } a)]$   
 by  $(\text{rule sym})(\text{simp add: update-zip})$   
 hence  $\text{zip } Vs \text{ (xs[length Vs := Addr a])} = \text{zip } Vs \ xs$  by simp  
 with  $\langle \text{lcl } s \subseteq_m [Vs \mapsto xs] \rangle$  have  $\text{lcl } s \subseteq_m [Vs \mapsto xs[\text{length } Vs := \text{Addr } a]]$   
 by  $(\text{auto simp add: map-le-def map-upds-def})$   
 ultimately show  $?case$  using  $\langle \text{lcl } s \subseteq_m [Vs \mapsto xs] \rangle$  by fastforce  
 next  
 case  $(\text{SynchronizedRed2 } e \ s \ ta \ e' \ s' \ a \ Vs \ E2 \ xs)$   
 note  $IH = \langle \bigwedge vs \ e2 \ XS. \llbracket \text{bisim } vs \ e \ e2 \ XS; \text{fv } e \subseteq \text{set } vs; \text{lcl } s \subseteq_m [vs \mapsto XS]; \text{length } vs + \text{max-vars } e \leq \text{length } XS; \text{?synth } e \ s \rrbracket \implies \text{?concl } e \ e2 \ s \ XS \ e' \ s' \ ta \ vs \rangle$   
 from  $\langle \text{bisim } Vs \text{ (insync}(a) \text{ } e) \ E2 \ xs \rangle$  obtain  $E$   
 where  $E2: E2 = \text{insync}_{\text{length } Vs} (a) \ E$  and  $\text{bisim: bisim (Vs@[fresh-var Vs]) } e \ E \ xs$   
 and  $\text{xs!length Vs = Addr } a$  by auto  
 from  $\langle \text{fv (insync}(a) \text{ } e) \subseteq \text{set } Vs \rangle$  fresh-var-fresh[ $of \ Vs$ ] have  $\text{fv: fresh-var } Vs \notin \text{fv } e$  by auto  
 from  $\langle \text{length } Vs + \text{max-vars (insync}(a) \text{ } e) \leq \text{length } xs \rangle$  have  $\text{length } Vs < \text{length } xs$  by simp  
 { assume  $\text{lcl } s \text{ (fresh-var } Vs) \neq \text{None}$   
 then obtain  $v$  where  $\text{lcl } s \text{ (fresh-var } Vs) = [v]$  by auto  
 with  $\langle \text{lcl } s \subseteq_m [Vs \mapsto xs] \rangle$  have  $[Vs \mapsto xs] \text{ (fresh-var } Vs) = [v]$   
 by  $(\text{auto simp add: map-le-def dest: bspec})$   
 hence  $\text{fresh-var } Vs \in \text{set } Vs$   
 by  $(\text{auto simp add: map-upds-def set-zip dest!: map-of-SomeD})$   
 moreover have  $\text{fresh-var } Vs \notin \text{set } Vs$  by  $(\text{rule fresh-var-fresh})$   
 ultimately have  $\text{False}$  by contradiction }  
 hence  $\text{lcl } s \text{ (fresh-var } Vs) = \text{None}$  by  $(\text{cases lcl } s \text{ (fresh-var } Vs), \text{auto})$   
 hence  $(\text{lcl } s) \text{ (fresh-var } Vs := \text{None}) = \text{lcl } s$  by  $(\text{auto intro: ext})$   
 moreover from  $\langle \text{lcl } s \subseteq_m [Vs \mapsto xs] \rangle$   
 have  $(\text{lcl } s) \text{ (fresh-var } Vs := \text{None}) \subseteq_m [Vs \mapsto xs, \text{fresh-var } Vs \mapsto xs ! \text{length } Vs]$  by  $(\text{simp})$   
 ultimately have  $\text{lcl } s \subseteq_m [Vs @ [\text{fresh-var } Vs] \mapsto xs]$   
 using  $\langle \text{length } Vs < \text{length } xs \rangle$  by  $(\text{auto})$   
 with  $IH[of \ Vs @ [\text{fresh-var } Vs] \ E \ xs] \langle \text{fv (insync}(a) \text{ } e) \subseteq \text{set } Vs \rangle$  bisim  
 $\langle \text{length } Vs + \text{max-vars (insync}(a) \text{ } e) \leq \text{length } xs \rangle \langle \text{?synth (insync}(a) \text{ } e) \ s \rangle$   
 obtain  $TA' \ e2' \ xs'$  where  $IH': \text{sim-move01 (compP1 } P) \ t \ ta \ e \ E \ (hp \ s) \ xs \ TA' \ e2' \ (hp \ s') \ xs'$   
 $\text{bisim (Vs @ [fresh-var Vs]) } e' \ e2' \ xs' \ \text{lcl } s' \subseteq_m [Vs @ [\text{fresh-var } Vs] \mapsto xs']$  by auto  
 from  $\langle \text{extTA2J0 } P, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \rangle$  have  $\text{dom (lcl } s') \subseteq \text{dom (lcl } s) \cup \text{fv } e$  by  $(\text{auto dest: red-dom-lcl})$   
 with  $\text{fv } \langle \text{lcl } s \text{ (fresh-var } Vs) = \text{None} \rangle$  have  $(\text{fresh-var } Vs) \notin \text{dom (lcl } s')$  by auto  
 hence  $\text{lcl } s' \text{ (fresh-var } Vs) = \text{None}$  by auto  
 moreover from  $IH'$  have  $\text{length } xs = \text{length } xs'$  by  $(\text{auto dest: sim-move01-preserves-len})$   
 moreover note  $\langle \text{lcl } s' \subseteq_m [Vs @ [\text{fresh-var } Vs] \mapsto xs'] \rangle \langle \text{length } Vs < \text{length } xs \rangle$   
 ultimately have  $\text{lcl } s' \subseteq_m [Vs \mapsto xs']$  by  $(\text{auto simp add: map-le-def dest: bspec})$   
 moreover from bisim fv have unmod  $E \text{ (length } Vs)$  by  $(\text{auto intro: bisim-fv-unmod})$   
 with  $IH' \langle \text{length } Vs < \text{length } xs \rangle$  have  $xs ! \text{length } Vs = xs' ! \text{length } Vs$



```

  by(auto dest!: sim-move01-preserves-unmod)
with xsa have xs' ! length Vs = Addr a by simp
ultimately show ?case using IH' E2 by(fastforce)
next
case (UnlockSynchronized a v s Vs E2 xs)
from ⟨bisim Vs (insync(a) Val v) E2 xs⟩ have E2: E2 = insynclength Vs (a) Val v
  and xsa: xs ! length Vs = Addr a by auto
moreover from ⟨length Vs + max-vars (insync(a) Val v) ≤ length xs⟩ xsa
have False, compP1 P, t ⊢ 1 ⟨insynclength Vs (a) (Val v), (hp s, xs)⟩ -⊥ Unlock → a, SyncUnlock a ⊥ →
⟨Val v, (hp s, xs)⟩
  by-(rule Unlock1Synchronized, auto)
hence sim-move01 (compP1 P) t ⊥ Unlock → a, SyncUnlock a ⊥ (insync(a) Val v) (insynclength Vs (a)
Val v) (hp s) xs ⊥ Unlock → a, SyncUnlock a ⊥ (Val v) (hp s) xs
  by(fastforce simp add: sim-move01-def ta-bisim-def)
ultimately show ?case using ⟨lcl s ⊆m [Vs [↦] xs]⟩ by fastforce
next
case (RedWhile b c s Vs E2 xs)
from ⟨bisim Vs (while (b) c) E2 xs⟩ have E2 = while (compE1 Vs b) (compE1 Vs c)
  and sync: ¬ contains-insync b ¬ contains-insync c by auto
moreover have False, compP1 P, t ⊢ 1 ⟨while(compE1 Vs b) (compE1 Vs c), (hp s, xs)⟩
  -ε → ⟨if (compE1 Vs b) (compE1 Vs c;; while(compE1 Vs b) (compE1 Vs c)) else unit,
(hp s, xs)⟩
  by(rule Red1While)
hence sim-move01 (compP1 P) t ε (while (b) c) (while (compE1 Vs b) (compE1 Vs c)) (hp s) xs ε
(if (compE1 Vs b) (compE1 Vs c;; while(compE1 Vs b) (compE1 Vs c)) else unit) (hp s) xs
  by(auto simp add: sim-move01-def)
moreover from ⟨fv (while (b) c) ⊆ set Vs⟩ sync
have bisim Vs (if (b) (c;; while (b) c) else unit)
  (if (compE1 Vs b) (compE1 Vs (c;; while(b) c)) else (compE1 Vs unit)) xs
  by -(rule bisimCond, auto)
ultimately show ?case using ⟨lcl s ⊆m [Vs [↦] xs]⟩
  by(simp)((rule exI)+, erule conjI, auto)
next
case (RedTryCatch s a D C V e2 Vs E2 xs)
thus ?case by(auto intro!: exI)(auto intro!: compE1-bisim)
next
case RedTryFail thus ?case by(auto intro!: exI)
next
case (ListRed1 e s ta e' s' es Vs ES2 xs)
note IH = ⟨∧ vs e2 XS. ⊥ bisim vs e e2 XS; fv e ⊆ set vs; lcl s ⊆m [vs [↦] XS]; length vs + max-vars
e ≤ length XS;
  ?synth e s ⊥ ⇒ ?concl e e2 s XS e' s' ta vs⟩
from ⟨extTA2J0 P, P, t ⊢ ⟨e, s⟩ -ta→ ⟨e', s'⟩⟩ have ¬ is-val e by auto
with ⟨bisims Vs (e # es) ES2 xs⟩ obtain E'
  where bisim Vs e E' xs and ES2: ES2 = E' # compEs1 Vs es
  and sync: ¬ contains-insyncs es by(auto simp add: compEs1-conv-map)
with IH[of Vs E' xs] ⟨fvs (e # es) ⊆ set Vs⟩ ⟨lcl s ⊆m [Vs [↦] xs]⟩ ⟨extTA2J0 P, P, t ⊢ ⟨e, s⟩ -ta→
⟨e', s'⟩⟩
  ⟨length Vs + max-varss (e # es) ≤ length xs⟩ ⟨?synths (e # es) s⟩ ⟨¬ is-val e⟩
show ?case by(cases is-val e)(fastforce elim!: sim-moves01-expr split: if-split-asm)+
next
case (ListRed2 es s ta es' s' v Vs ES2 xs)
thus ?case by(fastforce elim!: bisims-cases elim!: sim-moves01-expr)
next

```

```

case CallThrowParams thus ?case
  by(fastforce elim!::bisim-cases simp add: bisims-map-Val-Throw)
next
  case (BlockThrow V T vo a s Vs e2 xs) thus ?case
    by(cases vo)(fastforce elim!:: bisim-cases)+
next
  case (SynchronizedThrow2 a ad s Vs E2 xs)
    from  $\langle \text{bisim } Vs \text{ (insync}(a) \text{ Throw ad)} E2 \text{ xs} \rangle$  have  $xs \text{ ! length } Vs = \text{Addr } a$  by auto
    with  $\langle \text{length } Vs + \text{max-vars (insync}(a) \text{ Throw ad)} \leq \text{length } xs \rangle$ 
    have False, compP1 P, t  $\vdash 1 \langle \text{insync}_{\text{length } Vs} (a) \text{ Throw ad, (hp s, xs)} \rangle - \llbracket \text{Unlock} \rightarrow a, \text{SyncUnlock } a \rrbracket \rightarrow \langle \text{Throw ad, (hp s, xs)} \rangle$ 
    by-(rule Synchronized1Throw2, auto)
    hence sim-move01 (compP1 P) t  $\llbracket \text{Unlock} \rightarrow a, \text{SyncUnlock } a \rrbracket$  (insync(a) Throw ad) (insynclength Vs (a) Throw ad) (hp s) xs  $\llbracket \text{Unlock} \rightarrow a, \text{SyncUnlock } a \rrbracket$  (Throw ad) (hp s) xs
    by(fastforce simp add: sim-move01-def ta-bisim-def fun-eq-iff expand-funfun-eq finfun-upd-apply ta-upd-simps split: if-split-asm)
    moreover note  $\langle \text{lcl } s \subseteq_m [Vs \mapsto] xs \rangle \langle \text{bisim } Vs \text{ (insync}(a) \text{ Throw ad)} E2 \text{ xs} \rangle$ 
    ultimately show ?case by(fastforce)
next
  case InstanceOfRed thus ?case by(fastforce)
next
  case RedInstanceOf thus ?case by(auto intro!:: exI)
next
  case InstanceOfThrow thus ?case by fastforce
qed(fastforce simp del: fun-upd-apply split: if-split-asm)+

end

declare max-dest [iff del]

declare split-paired-Ex [simp del]

primrec countInitBlock :: ('a, 'b, 'addr) exp  $\Rightarrow$  nat
and countInitBlocks :: ('a, 'b, 'addr) exp list  $\Rightarrow$  nat
where
  countInitBlock (new C) = 0
  | countInitBlock (newA T [e]) = countInitBlock e
  | countInitBlock (Cast T e) = countInitBlock e
  | countInitBlock (e instanceof T) = countInitBlock e
  | countInitBlock (Val v) = 0
  | countInitBlock (Var V) = 0
  | countInitBlock (V := e) = countInitBlock e
  | countInitBlock (e «bop» e') = countInitBlock e + countInitBlock e'
  | countInitBlock (a [i]) = countInitBlock a + countInitBlock i
  | countInitBlock (AAss a i e) = countInitBlock a + countInitBlock i + countInitBlock e
  | countInitBlock (a.length) = countInitBlock a
  | countInitBlock (e.F{D}) = countInitBlock e
  | countInitBlock (FAss e F D e') = countInitBlock e + countInitBlock e'
  | countInitBlock (e.compareAndSwap(D.F, e', e'')) =
    countInitBlock e + countInitBlock e' + countInitBlock e''
  | countInitBlock (e.M(es)) = countInitBlock e + countInitBlocks es
  | countInitBlock ({V:T=vo; e}) = (case vo of None  $\Rightarrow$  0 | Some v  $\Rightarrow$  1) + countInitBlock e
  | countInitBlock (syncV' (e) e') = countInitBlock e + countInitBlock e'
  | countInitBlock (insyncV' (ad) e) = countInitBlock e

```

```

| countInitBlock (e;;e') = countInitBlock e + countInitBlock e'
| countInitBlock (if (e) e1 else e2) = countInitBlock e + countInitBlock e1 + countInitBlock e2
| countInitBlock (while(b) e) = countInitBlock b + countInitBlock e
| countInitBlock (throw e) = countInitBlock e
| countInitBlock (try e catch(C V) e') = countInitBlock e + countInitBlock e'

| countInitBlocks [] = 0
| countInitBlocks (e # es) = countInitBlock e + countInitBlocks es

```

**context** *J0-J1-heap-base* **begin**

**lemmas**  $\tau\text{red0r-expr} =$

*NewArray- $\tau\text{red0r-xt}$  Cast- $\tau\text{red0r-xt}$  InstanceOf- $\tau\text{red0r-xt}$  BinOp- $\tau\text{red0r-xt1}$  BinOp- $\tau\text{red0r-xt2}$  LAss- $\tau\text{red0r}$   
 AAcc- $\tau\text{red0r-xt1}$  AAcc- $\tau\text{red0r-xt2}$  AAss- $\tau\text{red0r-xt1}$  AAss- $\tau\text{red0r-xt2}$  AAss- $\tau\text{red0r-xt3}$   
 ALength- $\tau\text{red0r-xt}$  FAcc- $\tau\text{red0r-xt}$  FAss- $\tau\text{red0r-xt1}$  FAss- $\tau\text{red0r-xt2}$   
 CAS- $\tau\text{red0r-xt1}$  CAS- $\tau\text{red0r-xt2}$  CAS- $\tau\text{red0r-xt3}$  Call- $\tau\text{red0r-obj}$   
 Call- $\tau\text{red0r-param}$  Block- $\tau\text{red0r-xt}$  Sync- $\tau\text{red0r-xt}$  InSync- $\tau\text{red0r-xt}$   
 Seq- $\tau\text{red0r-xt}$  Cond- $\tau\text{red0r-xt}$  Throw- $\tau\text{red0r-xt}$  Try- $\tau\text{red0r-xt}$*

**lemmas**  $\tau\text{red0t-expr} =$

*NewArray- $\tau\text{red0t-xt}$  Cast- $\tau\text{red0t-xt}$  InstanceOf- $\tau\text{red0t-xt}$  BinOp- $\tau\text{red0t-xt1}$  BinOp- $\tau\text{red0t-xt2}$  LAss- $\tau\text{red0t}$   
 AAcc- $\tau\text{red0t-xt1}$  AAcc- $\tau\text{red0t-xt2}$  AAss- $\tau\text{red0t-xt1}$  AAss- $\tau\text{red0t-xt2}$  AAss- $\tau\text{red0t-xt3}$   
 ALength- $\tau\text{red0t-xt}$  FAcc- $\tau\text{red0t-xt}$  FAss- $\tau\text{red0t-xt1}$  FAss- $\tau\text{red0t-xt2}$   
 CAS- $\tau\text{red0t-xt1}$  CAS- $\tau\text{red0t-xt2}$  CAS- $\tau\text{red0t-xt3}$  Call- $\tau\text{red0t-obj}$   
 Call- $\tau\text{red0t-param}$  Block- $\tau\text{red0t-xt}$  Sync- $\tau\text{red0t-xt}$  InSync- $\tau\text{red0t-xt}$   
 Seq- $\tau\text{red0t-xt}$  Cond- $\tau\text{red0t-xt}$  Throw- $\tau\text{red0t-xt}$  Try- $\tau\text{red0t-xt}$*

**declare**  $\tau\text{red0r-expr}$  [elim!]

**declare**  $\tau\text{red0t-expr}$  [elim!]

**definition** *sim-move10* ::

*'addr J-prog  $\Rightarrow$  'thread-id  $\Rightarrow$  ('addr, 'thread-id, 'heap) external-thread-action  $\Rightarrow$  'addr expr1  $\Rightarrow$  'addr  
 expr1  $\Rightarrow$  'addr expr  
 $\Rightarrow$  'heap  $\Rightarrow$  'addr locals  $\Rightarrow$  ('addr, 'thread-id, 'heap) J0-thread-action  $\Rightarrow$  'addr expr  $\Rightarrow$  'heap  $\Rightarrow$  'addr  
 locals  $\Rightarrow$  bool*

**where**

*sim-move10 P t ta1 e1 e1' e h xs ta e' h' xs'  $\longleftrightarrow$   $\neg$  final e1  $\wedge$   
 (if  $\tau\text{move1}$  P h e1 then ( $\tau\text{red0t}$  (extTA2J0 P) P t h (e, xs) (e', xs')  $\vee$  countInitBlock e1' < coun-  
 tInitBlock e1  $\wedge$  e' = e  $\wedge$  xs' = xs)  $\wedge$  h' = h  $\wedge$  ta1 =  $\varepsilon$   $\wedge$  ta =  $\varepsilon$   
 else ta-bisim01 ta (extTA2J1 (compP1 P) ta1)  $\wedge$   
 (if call e = None  $\vee$  call1 e1 = None  
 then ( $\exists$  e'' xs''.  $\tau\text{red0r}$  (extTA2J0 P) P t h (e, xs) (e'', xs'')  $\wedge$  extTA2J0 P,P,t  $\vdash$   $\langle$ e'', (h, xs'') $\rangle$   
 $-ta \rightarrow \langle$ e', (h', xs') $\rangle$   $\wedge$  no-call P h e''  $\wedge$   $\neg$   $\tau\text{move0}$  P h e'')  
 else extTA2J0 P,P,t  $\vdash$   $\langle$ e, (h, xs) $\rangle$   $-ta \rightarrow \langle$ e', (h', xs') $\rangle$   $\wedge$  no-call P h e  $\wedge$   $\neg$   $\tau\text{move0}$  P h e))*

**definition** *sim-moves10* ::

*'addr J-prog  $\Rightarrow$  'thread-id  $\Rightarrow$  ('addr, 'thread-id, 'heap) external-thread-action  $\Rightarrow$  'addr expr1 list  $\Rightarrow$   
 'addr expr1 list  
 $\Rightarrow$  'addr expr list  $\Rightarrow$  'heap  $\Rightarrow$  'addr locals  $\Rightarrow$  ('addr, 'thread-id, 'heap) J0-thread-action  $\Rightarrow$  'addr expr  
 list  $\Rightarrow$  'heap  
 $\Rightarrow$  'addr locals  $\Rightarrow$  bool*

**where**

*sim-moves10 P t ta1 es1 es1' es h xs ta es' h' xs'  $\longleftrightarrow$   $\neg$  finals es1  $\wedge$   
 (if  $\tau\text{moves1}$  P h es1 then ( $\tau\text{reds0t}$  (extTA2J0 P) P t h (es, xs) (es', xs')  $\vee$  countInitBlocks es1' <*

$\text{countInitBlocks } es1 \wedge es' = es \wedge xs' = xs) \wedge h' = h \wedge ta1 = \varepsilon \wedge ta = \varepsilon$   
 $\text{else } ta\text{-bisim01 } ta \text{ (extTA2J1 (compP1 } P) ta1) \wedge$   
 $(\text{if calls } es = \text{None} \vee \text{calls1 } es1 = \text{None}$   
 $\text{then } (\exists es'' xs''. \tau\text{reds0r (extTA2J0 } P) P t h (es, xs) (es'', xs'') \wedge \text{extTA2J0 } P, P, t \vdash \langle es'', (h,$   
 $xs'') \rangle [-ta \rightarrow] \langle es', (h', xs') \rangle \wedge \text{no-calls } P h es'' \wedge \neg \tau\text{moves0 } P h es'')$   
 $\text{else extTA2J0 } P, P, t \vdash \langle es, (h, xs) \rangle [-ta \rightarrow] \langle es', (h', xs') \rangle \wedge \text{no-calls } P h es \wedge \neg \tau\text{moves0 } P h$   
 $es))$

**lemma** *sim-move10-expr*:

**assumes** *sim-move10*  $P t ta1 e1 e1' e h xs ta e' h' xs'$

**shows**

$\text{sim-move10 } P t ta1 (\text{newA } T[e1]) (\text{newA } T[e1']) (\text{newA } T[e]) h xs ta (\text{newA } T[e']) h' xs'$   
 $\text{sim-move10 } P t ta1 (\text{Cast } T e1) (\text{Cast } T e1') (\text{Cast } T e) h xs ta (\text{Cast } T e') h' xs'$   
 $\text{sim-move10 } P t ta1 (e1 \text{ instanceof } T) (e1' \text{ instanceof } T) (e \text{ instanceof } T) h xs ta (e' \text{ instanceof } T)$   
 $h' xs'$   
 $\text{sim-move10 } P t ta1 (e1 \ll bop \gg e2) (e1' \ll bop \gg e2) (e \ll bop \gg e2') h xs ta (e' \ll bop \gg e2') h' xs'$   
 $\text{sim-move10 } P t ta1 (\text{Val } v \ll bop \gg e1) (\text{Val } v \ll bop \gg e1') (\text{Val } v \ll bop \gg e) h xs ta (\text{Val } v \ll bop \gg e') h'$   
 $xs'$   
 $\text{sim-move10 } P t ta1 (V := e1) (V := e1') (V' := e) h xs ta (V' := e') h' xs'$   
 $\text{sim-move10 } P t ta1 (e1[e2]) (e1'[e2]) (e[e2]) h xs ta (e'[e2]) h' xs'$   
 $\text{sim-move10 } P t ta1 (\text{Val } v[e1]) (\text{Val } v[e1']) (\text{Val } v[e]) h xs ta (\text{Val } v[e']) h' xs'$   
 $\text{sim-move10 } P t ta1 (e1[e2] := e3) (e1'[e2] := e3) (e[e2] := e3') h xs ta (e'[e2] := e3') h' xs'$   
 $\text{sim-move10 } P t ta1 (\text{Val } v[e1] := e3) (\text{Val } v[e1'] := e3) (\text{Val } v[e] := e3') h xs ta (\text{Val } v[e'] :=$   
 $e3') h' xs'$   
 $\text{sim-move10 } P t ta1 (\text{AAss } (\text{Val } v) (\text{Val } v') e1) (\text{AAss } (\text{Val } v) (\text{Val } v') e1') (\text{AAss } (\text{Val } v) (\text{Val } v')$   
 $e) h xs ta (\text{AAss } (\text{Val } v) (\text{Val } v') e') h' xs'$   
 $\text{sim-move10 } P t ta1 (e1 \cdot \text{length}) (e1' \cdot \text{length}) (e \cdot \text{length}) h xs ta (e' \cdot \text{length}) h' xs'$   
 $\text{sim-move10 } P t ta1 (e1 \cdot F\{D\}) (e1' \cdot F\{D\}) (e \cdot F\{D\}) h xs ta (e' \cdot F\{D\}) h' xs'$   
 $\text{sim-move10 } P t ta1 (\text{FAss } e1 F D e2) (\text{FAss } e1' F D e2) (\text{FAss } e F' D' e2') h xs ta (\text{FAss } e' F' D'$   
 $e2') h' xs'$   
 $\text{sim-move10 } P t ta1 (\text{FAss } (\text{Val } v) F D e1) (\text{FAss } (\text{Val } v) F D e1') (\text{FAss } (\text{Val } v) F' D' e) h xs ta$   
 $(\text{FAss } (\text{Val } v) F' D' e') h' xs'$   
 $\text{sim-move10 } P t ta1 (\text{CompareAndSwap } e1 F D e2 e3) (\text{CompareAndSwap } e1' F D e2 e3) (\text{CompareAndSwap}$   
 $e F' D' e2' e3') h xs ta (\text{CompareAndSwap } e' F' D' e2' e3') h' xs'$   
 $\text{sim-move10 } P t ta1 (\text{CompareAndSwap } (\text{Val } v) F D e1 e3) (\text{CompareAndSwap } (\text{Val } v) F D e1' e3)$   
 $(\text{CompareAndSwap } (\text{Val } v) F' D' e e3') h xs ta (\text{CompareAndSwap } (\text{Val } v) F' D' e' e3') h' xs'$   
 $\text{sim-move10 } P t ta1 (\text{CompareAndSwap } (\text{Val } v) F D (\text{Val } v') e1) (\text{CompareAndSwap } (\text{Val } v) F D$   
 $(\text{Val } v') e1') (\text{CompareAndSwap } (\text{Val } v) F' D' (\text{Val } v') e) h xs ta (\text{CompareAndSwap } (\text{Val } v) F' D'$   
 $(\text{Val } v') e') h' xs'$   
 $\text{sim-move10 } P t ta1 (e1 \cdot M(es)) (e1' \cdot M(es)) (e \cdot M(es')) h xs ta (e' \cdot M(es')) h' xs'$   
 $\text{sim-move10 } P t ta1 (\text{sync}_V(e1) e2) (\text{sync}_V(e1') e2) (\text{sync}(e) e2') h xs ta (\text{sync}(e') e2') h' xs'$   
 $\text{sim-move10 } P t ta1 (\text{insync}_V(a) e1) (\text{insync}_V(a) e1') (\text{insync}(a') e) h xs ta (\text{insync}(a') e') h' xs'$   
 $\text{sim-move10 } P t ta1 (e1;;e2) (e1';;e2) (e;;e2') h xs ta (e';;e2') h' xs'$   
 $\text{sim-move10 } P t ta1 (\text{if } (e1) e2 \text{ else } e3) (\text{if } (e1') e2 \text{ else } e3) (\text{if } (e) e2' \text{ else } e3') h xs ta (\text{if } (e') e2'$   
 $\text{else } e3') h' xs'$   
 $\text{sim-move10 } P t ta1 (\text{throw } e1) (\text{throw } e1') (\text{throw } e) h xs ta (\text{throw } e') h' xs'$   
 $\text{sim-move10 } P t ta1 (\text{try } e1 \text{ catch}(C V) e2) (\text{try } e1' \text{ catch}(C V) e2) (\text{try } e \text{ catch}(C' V') e2') h xs$   
 $ta (\text{try } e' \text{ catch}(C' V') e2') h' xs'$   
**using** *assms*  
**apply**(*simp-all add: sim-move10-def final-iff split del: if-split split: if-split-asm*)  
**apply**(*fastforce simp:  $\tau\text{red0t-Val } \tau\text{red0r-Val intro: red-reds.intros split!: if-splits}$* )  
**done**

**lemma** *sim-moves10-expr*:

$\text{sim-move10 } P \ t \ ta1 \ e1 \ e1' \ e \ h \ xs \ ta \ e' \ h' \ xs' \Longrightarrow \text{sim-moves10 } P \ t \ ta1 \ (e1 \ \# \ es2) \ (e1' \ \# \ es2) \ (e \ \# \ es2') \ h \ xs \ ta \ (e' \ \# \ es2') \ h' \ xs'$

$\text{sim-moves10 } P \ t \ ta1 \ es1 \ es1' \ es \ h \ xs \ ta \ es' \ h' \ xs' \Longrightarrow \text{sim-moves10 } P \ t \ ta1 \ (\text{Val } v \ \# \ es1) \ (\text{Val } v \ \# \ es1') \ (\text{Val } v \ \# \ es) \ h \ xs \ ta \ (\text{Val } v \ \# \ es') \ h' \ xs'$

**unfolding** *sim-moves10-def sim-move10-def final-iff finals-iff*

**apply**(*simp-all add: Cons-eq-append-conv split del: if-split split: if-split-asm*)

**apply**(*safe intro!: if-split*)

**apply**(*fastforce simp add: is-vals-conv  $\tau\text{reds0t-map-Val}$   $\tau\text{reds0r-map-Val}$   $\tau\text{red0t-Val}$   $\tau\text{red0r-Val}$  *intro!:  $\tau\text{red0r-inj-}\tau\text{reds0r}$   $\tau\text{reds0r-cons-}\tau\text{reds0r}$   $\tau\text{red0t-inj-}\tau\text{reds0t}$   $\tau\text{reds0t-cons-}\tau\text{reds0t}$  ListRed1 ListRed2 split: if-split-asm}*)+*

**done**

**lemma** *sim-move10-CallParams:*

$\text{sim-moves10 } P \ t \ ta1 \ es1 \ es1' \ es \ h \ xs \ ta \ es' \ h' \ xs' \Longrightarrow \text{sim-move10 } P \ t \ ta1 \ (\text{Val } v \cdot M(es1)) \ (\text{Val } v \cdot M(es1')) \ (\text{Val } v \cdot M(es)) \ h \ xs \ ta \ (\text{Val } v \cdot M(es')) \ h' \ xs'$

**unfolding** *sim-move10-def sim-moves10-def*

**apply**(*simp split: if-split-asm split del: if-split add: is-vals-conv*)

**apply**(*fastforce simp add:  $\tau\text{red0t-Val}$   $\tau\text{red0r-Val}$   $\tau\text{reds0t-map-Val}$   $\tau\text{reds0r-map-Val}$  *intro: Call- $\tau\text{red0r-param}$  Call- $\tau\text{red0t-param}$  CallParams split: if-split-asm split del: if-split intro!: if-split*)*

**apply**(*rule conjI*)

**apply** *fastforce*

**apply**(*rule if-intro*)

**apply** *fastforce*

**apply**(*clarsimp split del: if-split*)

**apply**(*rule if-intro*)

**apply**(*clarsimp split: if-split-asm simp add: is-vals-conv*)

**apply**(*rule exI conjI*)+

**apply**(*erule Call- $\tau\text{red0r-param}$* )

**apply**(*fastforce intro: CallParams*)

**apply**(*rule exI conjI*)+

**apply**(*erule Call- $\tau\text{red0r-param}$* )

**apply**(*fastforce intro!: CallParams*)

**apply**(*clarsimp split del: if-split split: if-split-asm simp add: is-vals-conv  $\tau\text{reds0r-map-Val}$* )

**apply** *fastforce*

**apply**(*rule conjI*)

**apply** *fastforce*

**apply**(*rule if-intro*)

**apply** *fastforce*

**apply**(*rule conjI*)

**apply** *fastforce*

**apply**(*rule if-intro*)

**apply**(*clarsimp split: if-split-asm*)

**apply**(*clarsimp split: if-split-asm split del: if-split simp add: is-vals-conv*)

**apply**(*fastforce intro: CallParams*)

**done**

**lemma** *sim-move10-Block:*

$\text{sim-move10 } P \ t \ ta1 \ e1 \ e1' \ e \ h \ (xs(V' := vo)) \ ta \ e' \ h' \ xs' \Longrightarrow \text{sim-move10 } P \ t \ ta1 \ (\{V:T=None; e1\}) \ (\{V:T=None; e1'\}) \ (\{V':T=vo; e\}) \ h \ xs \ ta \ (\{V':T=xs' \ V'; e'\}) \ h' \ (xs'(V' := xs \ V'))$

**proof** –

**assume** *sim-move10*  $P \ t \ ta1 \ e1 \ e1' \ e \ h \ (xs(V' := vo)) \ ta \ e' \ h' \ xs'$

**moreover** {

**fix**  $e'' \ xs''$

**assume**  $\text{extTA2J0 } P, P, t \vdash \langle e'', (h, xs'') \rangle -ta \rightarrow \langle e', (h', xs') \rangle$   
**hence**  $\text{extTA2J0 } P, P, t \vdash \langle e'', (h, xs''(V' := xs \ V', \ V' := xs'' \ V')) \rangle -ta \rightarrow \langle e', (h', xs') \rangle$  **by** *simp*  
**from** *BlockRed[OF this, of T]*  
**have**  $\text{extTA2J0 } P, P, t \vdash \langle \{ V':T=xs'' \ V'; e'' \}, (h, xs''(V' := xs \ V')) \rangle -ta \rightarrow \langle \{ V':T=xs' \ V'; e' \}, (h', xs'(V' := xs \ V')) \rangle$   
**by** *simp* }  
**ultimately show** *?thesis*  
**by**(*fastforce simp add: sim-move10-def final-iff split: if-split-asm*)  
**qed**

**lemma** *sim-move10-reds:*

$\llbracket (h', a) \in \text{allocate } h \text{ (Class-type } C); ta1 = \llbracket \text{NewHeapElem } a \text{ (Class-type } C) \rrbracket; ta = \llbracket \text{NewHeapElem } a \text{ (Class-type } C) \rrbracket \rrbracket$   
 $\implies \text{sim-move10 } P \ t \ ta1 \ (new \ C) \ e1' \ (new \ C) \ h \ xs \ ta \ (addr \ a) \ h' \ xs$   
 $\text{allocate } h \text{ (Class-type } C) = \{\} \implies \text{sim-move10 } P \ t \ \varepsilon \ (new \ C) \ e1' \ (new \ C) \ h \ xs \ \varepsilon \ (THROW \text{ OutOfMemory}) \ h \ xs$   
 $\llbracket (h', a) \in \text{allocate } h \text{ (Array-type } T \text{ (nat (sint } i))); 0 \leq i; ta1 = \llbracket \text{NewHeapElem } a \text{ (Array-type } T \text{ (nat (sint } i)) \rrbracket; ta = \llbracket \text{NewHeapElem } a \text{ (Array-type } T \text{ (nat (sint } i)) \rrbracket \rrbracket$   
 $\implies \text{sim-move10 } P \ t \ ta1 \ (newA \ T[Val \ (Intg \ i)]) \ e1' \ (newA \ T[Val \ (Intg \ i)]) \ h \ xs \ ta \ (addr \ a) \ h' \ xs$   
 $i < s \ 0 \implies \text{sim-move10 } P \ t \ \varepsilon \ (newA \ T[Val \ (Intg \ i)]) \ e1' \ (newA \ T[Val \ (Intg \ i)]) \ h \ xs \ \varepsilon \ (THROW \text{ NegativeArraySize}) \ h \ xs$   
 $\llbracket \text{allocate } h \text{ (Array-type } T \text{ (nat (sint } i))) = \{\}; 0 \leq i \rrbracket$   
 $\implies \text{sim-move10 } P \ t \ \varepsilon \ (newA \ T[Val \ (Intg \ i)]) \ e1' \ (newA \ T[Val \ (Intg \ i)]) \ h \ xs \ \varepsilon \ (THROW \text{ OutOfMemory}) \ h \ xs$   
 $\llbracket \text{typeof}_h \ v = \lfloor U \rfloor; P \vdash U \leq T \rrbracket$   
 $\implies \text{sim-move10 } P \ t \ \varepsilon \ (Cast \ T \ (Val \ v)) \ e1' \ (Cast \ T \ (Val \ v)) \ h \ xs \ \varepsilon \ (Val \ v) \ h \ xs$   
 $\llbracket \text{typeof}_h \ v = \lfloor U \rfloor; \neg P \vdash U \leq T \rrbracket$   
 $\implies \text{sim-move10 } P \ t \ \varepsilon \ (Cast \ T \ (Val \ v)) \ e1' \ (Cast \ T \ (Val \ v)) \ h \ xs \ \varepsilon \ (THROW \text{ ClassCast}) \ h \ xs$   
 $\llbracket \text{typeof}_h \ v = \lfloor U \rfloor; b \longleftrightarrow v \neq \text{Null} \wedge P \vdash U \leq T \rrbracket$   
 $\implies \text{sim-move10 } P \ t \ \varepsilon \ ((Val \ v) \text{ instanceof } T) \ e1' \ ((Val \ v) \text{ instanceof } T) \ h \ xs \ \varepsilon \ (Val \ (Bool \ b)) \ h \ xs$   
 $\text{binop } bop \ v1 \ v2 = \text{Some } (Inl \ v) \implies \text{sim-move10 } P \ t \ \varepsilon \ ((Val \ v1) \llbracket bop \rrbracket (Val \ v2)) \ e1' \ (Val \ v1 \llbracket bop \rrbracket Val \ v2) \ h \ xs \ \varepsilon \ (Val \ v) \ h \ xs$   
 $\text{binop } bop \ v1 \ v2 = \text{Some } (Inr \ a) \implies \text{sim-move10 } P \ t \ \varepsilon \ ((Val \ v1) \llbracket bop \rrbracket (Val \ v2)) \ e1' \ (Val \ v1 \llbracket bop \rrbracket Val \ v2) \ h \ xs \ \varepsilon \ (Throw \ a) \ h \ xs$   
 $xs \ V = \lfloor v \rfloor \implies \text{sim-move10 } P \ t \ \varepsilon \ (Var \ V') \ e1' \ (Var \ V) \ h \ xs \ \varepsilon \ (Val \ v) \ h \ xs$   
 $\text{sim-move10 } P \ t \ \varepsilon \ (V' := Val \ v) \ e1' \ (V := Val \ v) \ h \ xs \ \varepsilon \ \text{unit } h \ (xs(V \mapsto v))$   
 $\text{sim-move10 } P \ t \ \varepsilon \ (\text{null} \lfloor Val \ v \rfloor) \ e1' \ (\text{null} \lfloor Val \ v \rfloor) \ h \ xs \ \varepsilon \ (THROW \text{ NullPointer}) \ h \ xs$   
 $\llbracket \text{typeof-addr } h \ a = \lfloor \text{Array-type } T \ n \rfloor; i < s \ 0 \vee \text{sint } i \geq \text{int } n \rrbracket$   
 $\implies \text{sim-move10 } P \ t \ \varepsilon \ (addr \ a \lfloor Val \ (Intg \ i) \rfloor) \ e1' \ ((addr \ a) \lfloor Val \ (Intg \ i) \rfloor) \ h \ xs \ \varepsilon \ (THROW \text{ ArrayIndexOutOfBounds}) \ h \ xs$   
 $\llbracket \text{typeof-addr } h \ a = \lfloor \text{Array-type } T \ n \rfloor; 0 \leq i; \text{sint } i < \text{int } n; \text{heap-read } h \ a \ (ACell \ (nat \ (\text{sint } i))) \ v; ta1 = \llbracket \text{ReadMem } a \ (ACell \ (nat \ (\text{sint } i))) \ v \rrbracket; ta = \llbracket \text{ReadMem } a \ (ACell \ (nat \ (\text{sint } i))) \ v \rrbracket \rrbracket$   
 $\implies \text{sim-move10 } P \ t \ ta1 \ (addr \ a \lfloor Val \ (Intg \ i) \rfloor) \ e1' \ ((addr \ a) \lfloor Val \ (Intg \ i) \rfloor) \ h \ xs \ ta \ (Val \ v) \ h \ xs$   
 $\text{sim-move10 } P \ t \ \varepsilon \ (\text{null} \lfloor Val \ v \rfloor := Val \ v') \ e1' \ (\text{null} \lfloor Val \ v \rfloor := Val \ v') \ h \ xs \ \varepsilon \ (THROW \text{ NullPointer}) \ h \ xs$   
 $\llbracket \text{typeof-addr } h \ a = \lfloor \text{Array-type } T \ n \rfloor; i < s \ 0 \vee \text{sint } i \geq \text{int } n \rrbracket$   
 $\implies \text{sim-move10 } P \ t \ \varepsilon \ (AAss \ (addr \ a) \ (Val \ (Intg \ i)) \ (Val \ v)) \ e1' \ (AAss \ (addr \ a) \ (Val \ (Intg \ i)) \ (Val \ v)) \ h \ xs \ \varepsilon \ (THROW \text{ ArrayIndexOutOfBounds}) \ h \ xs$   
 $\llbracket \text{typeof-addr } h \ a = \lfloor \text{Array-type } T \ n \rfloor; 0 \leq i; \text{sint } i < \text{int } n; \text{typeof}_h \ v = \lfloor U \rfloor; \neg (P \vdash U \leq T) \rrbracket$   
 $\implies \text{sim-move10 } P \ t \ \varepsilon \ (AAss \ (addr \ a) \ (Val \ (Intg \ i)) \ (Val \ v)) \ e1' \ (AAss \ (addr \ a) \ (Val \ (Intg \ i)) \ (Val \ v)) \ h \ xs \ \varepsilon \ (THROW \text{ ArrayStore}) \ h \ xs$   
 $\llbracket \text{typeof-addr } h \ a = \lfloor \text{Array-type } T \ n \rfloor; 0 \leq i; \text{sint } i < \text{int } n; \text{typeof}_h \ v = \text{Some } U; P \vdash U \leq T; \rrbracket$

$heap\text{-}write\ h\ a\ (ACell\ (nat\ (sint\ i)))\ v\ h';$   
 $ta1 = \llbracket WriteMem\ a\ (ACell\ (nat\ (sint\ i)))\ v \rrbracket; ta = \llbracket WriteMem\ a\ (ACell\ (nat\ (sint\ i)))\ v \rrbracket \parallel$   
 $\implies sim\text{-}move10\ P\ t\ ta1\ (AAss\ (addr\ a)\ (Val\ (Intg\ i))\ (Val\ v))\ e1'\ (AAss\ (addr\ a)\ (Val\ (Intg\ i))\ (Val\ v))\ h\ xs\ ta\ unit\ h'\ xs$   
 $typeof\text{-}addr\ h\ a = \lfloor Array\text{-}type\ T\ n \rfloor \implies sim\text{-}move10\ P\ t\ \varepsilon\ (addr\ a \cdot length)\ e1'\ (addr\ a \cdot length)\ h\ xs$   
 $\varepsilon\ (Val\ (Intg\ (word\text{-}of\text{-}nat\ n)))\ h\ xs$   
 $sim\text{-}move10\ P\ t\ \varepsilon\ (null \cdot length)\ e1'\ (null \cdot length)\ h\ xs\ \varepsilon\ (THROW\ NullPointer)\ h\ xs$   
 $\llbracket heap\text{-}read\ h\ a\ (CField\ D\ F)\ v; ta1 = \llbracket ReadMem\ a\ (CField\ D\ F)\ v \rrbracket; ta = \llbracket ReadMem\ a\ (CField\ D\ F)\ v \rrbracket \parallel$   
 $\implies sim\text{-}move10\ P\ t\ ta1\ (addr\ a \cdot F\{D\})\ e1'\ (addr\ a \cdot F\{D\})\ h\ xs\ ta\ (Val\ v)\ h\ xs$   
 $sim\text{-}move10\ P\ t\ \varepsilon\ (null \cdot F\{D\})\ e1'\ (null \cdot F\{D\})\ h\ xs\ \varepsilon\ (THROW\ NullPointer)\ h\ xs$   
 $\llbracket heap\text{-}write\ h\ a\ (CField\ D\ F)\ v\ h'; ta1 = \llbracket WriteMem\ a\ (CField\ D\ F)\ v \rrbracket; ta = \llbracket WriteMem\ a\ (CField\ D\ F)\ v \rrbracket \parallel$   
 $\implies sim\text{-}move10\ P\ t\ ta1\ (addr\ a \cdot F\{D\} := Val\ v)\ e1'\ (addr\ a \cdot F\{D\} := Val\ v)\ h\ xs\ ta\ unit\ h'\ xs$   
 $sim\text{-}move10\ P\ t\ \varepsilon\ (null \cdot compareAndSwap(D \cdot F, Val\ v, Val\ v'))\ e1'\ (null \cdot compareAndSwap(D \cdot F, Val\ v, Val\ v'))\ h\ xs\ \varepsilon\ (THROW\ NullPointer)\ h\ xs$   
 $\llbracket heap\text{-}read\ h\ a\ (CField\ D\ F)\ v''; heap\text{-}write\ h\ a\ (CField\ D\ F)\ v'\ h'; v'' = v;$   
 $ta1 = \llbracket ReadMem\ a\ (CField\ D\ F)\ v'', WriteMem\ a\ (CField\ D\ F)\ v' \rrbracket; ta = \llbracket ReadMem\ a\ (CField\ D\ F)\ v'', WriteMem\ a\ (CField\ D\ F)\ v' \rrbracket \parallel$   
 $\implies sim\text{-}move10\ P\ t\ ta1\ (addr\ a \cdot compareAndSwap(D \cdot F, Val\ v, Val\ v'))\ e1'\ (addr\ a \cdot compareAndSwap(D \cdot F, Val\ v, Val\ v'))\ h\ xs\ ta\ true\ h'\ xs$   
 $\llbracket heap\text{-}read\ h\ a\ (CField\ D\ F)\ v''; v'' \neq v;$   
 $ta1 = \llbracket ReadMem\ a\ (CField\ D\ F)\ v'' \rrbracket; ta = \llbracket ReadMem\ a\ (CField\ D\ F)\ v'' \rrbracket \parallel$   
 $\implies sim\text{-}move10\ P\ t\ ta1\ (addr\ a \cdot compareAndSwap(D \cdot F, Val\ v, Val\ v'))\ e1'\ (addr\ a \cdot compareAndSwap(D \cdot F, Val\ v, Val\ v'))\ h\ xs\ ta\ false\ h\ xs$

$sim\text{-}move10\ P\ t\ \varepsilon\ (null \cdot F\{D\} := Val\ v)\ e1'\ (null \cdot F\{D\} := Val\ v)\ h\ xs\ \varepsilon\ (THROW\ NullPointer)\ h\ xs$

$sim\text{-}move10\ P\ t\ \varepsilon\ (\{V':T=None; Val\ u\})\ e1'\ (\{V:T=vo; Val\ u\})\ h\ xs\ \varepsilon\ (Val\ u)\ h\ xs$   
 $sim\text{-}move10\ P\ t\ \varepsilon\ (\{V':T=[v]; e\})\ (\{V':T=None; e\})\ (\{V:T=vo; e'\})\ h\ xs\ \varepsilon\ (\{V:T=vo; e'\})\ h\ xs$

$sim\text{-}move10\ P\ t\ \varepsilon\ (sync_{V'}(null)\ e0)\ e1'\ (sync(null)\ e1)\ h\ xs\ \varepsilon\ (THROW\ NullPointer)\ h\ xs$   
 $sim\text{-}move10\ P\ t\ \varepsilon\ (Val\ v;;e0)\ e1'\ (Val\ v;;e1)\ h\ xs\ \varepsilon\ e1\ h\ xs$   
 $sim\text{-}move10\ P\ t\ \varepsilon\ (if\ (true)\ e0\ else\ e0')\ e1'\ (if\ (true)\ e1\ else\ e2)\ h\ xs\ \varepsilon\ e1\ h\ xs$   
 $sim\text{-}move10\ P\ t\ \varepsilon\ (if\ (false)\ e0\ else\ e0')\ e1'\ (if\ (false)\ e1\ else\ e2)\ h\ xs\ \varepsilon\ e2\ h\ xs$   
 $sim\text{-}move10\ P\ t\ \varepsilon\ (throw\ null)\ e1'\ (throw\ null)\ h\ xs\ \varepsilon\ (THROW\ NullPointer)\ h\ xs$   
 $sim\text{-}move10\ P\ t\ \varepsilon\ (try\ (Val\ v)\ catch\ (C\ V')\ e0)\ e1'\ (try\ (Val\ v)\ catch\ (C\ V)\ e1)\ h\ xs\ \varepsilon\ (Val\ v)\ h\ xs$   
 $\llbracket typeof\text{-}addr\ h\ a = \lfloor Class\text{-}type\ D \rfloor; P \vdash D \leq^* C \rrbracket$   
 $\implies sim\text{-}move10\ P\ t\ \varepsilon\ (try\ (Throw\ a)\ catch\ (C\ V')\ e0)\ e1'\ (try\ (Throw\ a)\ catch\ (C\ V)\ e1)\ h\ xs\ \varepsilon\ (\{V:Class\ C=\lfloor Addr\ a \rfloor; e1\})\ h\ xs$

$sim\text{-}move10\ P\ t\ \varepsilon\ (newA\ T\ \lfloor Throw\ a \rfloor)\ e1'\ (newA\ T\ \lfloor Throw\ a \rfloor)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$   
 $sim\text{-}move10\ P\ t\ \varepsilon\ (Cast\ T\ (Throw\ a))\ e1'\ (Cast\ T\ (Throw\ a))\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$   
 $sim\text{-}move10\ P\ t\ \varepsilon\ ((Throw\ a)\ instanceof\ T)\ e1'\ ((Throw\ a)\ instanceof\ T)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$   
 $sim\text{-}move10\ P\ t\ \varepsilon\ ((Throw\ a)\ \llbracket bop \rrbracket\ e0)\ e1'\ ((Throw\ a)\ \llbracket bop \rrbracket\ e1)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$   
 $sim\text{-}move10\ P\ t\ \varepsilon\ (Val\ v\ \llbracket bop \rrbracket\ (Throw\ a))\ e1'\ (Val\ v\ \llbracket bop \rrbracket\ (Throw\ a))\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$   
 $sim\text{-}move10\ P\ t\ \varepsilon\ (V' := Throw\ a)\ e1'\ (V := Throw\ a)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$   
 $sim\text{-}move10\ P\ t\ \varepsilon\ (Throw\ a\ \lfloor e0 \rfloor)\ e1'\ (Throw\ a\ \lfloor e1 \rfloor)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$   
 $sim\text{-}move10\ P\ t\ \varepsilon\ (Val\ v\ \lfloor Throw\ a \rfloor)\ e1'\ (Val\ v\ \lfloor Throw\ a \rfloor)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$   
 $sim\text{-}move10\ P\ t\ \varepsilon\ (Throw\ a\ \lfloor e0 \rfloor := e0')\ e1'\ (Throw\ a\ \lfloor e1 \rfloor := e2)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$   
 $sim\text{-}move10\ P\ t\ \varepsilon\ (Val\ v\ \lfloor Throw\ a \rfloor := e0)\ e1'\ (Val\ v\ \lfloor Throw\ a \rfloor := e1)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$   
 $sim\text{-}move10\ P\ t\ \varepsilon\ (Val\ v\ \lfloor Val\ v' \rfloor := Throw\ a)\ e1'\ (Val\ v\ \lfloor Val\ v' \rfloor := Throw\ a)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$   
 $sim\text{-}move10\ P\ t\ \varepsilon\ (Throw\ a \cdot length)\ e1'\ (Throw\ a \cdot length)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$   
 $sim\text{-}move10\ P\ t\ \varepsilon\ (Throw\ a \cdot F\{D\})\ e1'\ (Throw\ a \cdot F\{D\})\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$   
 $sim\text{-}move10\ P\ t\ \varepsilon\ (Throw\ a \cdot F\{D\} := e0)\ e1'\ (Throw\ a \cdot F\{D\} := e1)\ h\ xs\ \varepsilon\ (Throw\ a)\ h\ xs$

$\text{sim-move10 } P \ t \in (\text{Val } v \cdot F\{D\} := \text{Throw } a) \ e1' \ (\text{Val } v \cdot F\{D\} := \text{Throw } a) \ h \ xs \in (\text{Throw } a) \ h \ xs$   
 $\text{sim-move10 } P \ t \in (\text{CompareAndSwap } (\text{Throw } a) \ D \ F \ e0 \ e0') \ e1' \ (\text{Throw } a \cdot \text{compareAndSwap}(D \cdot F, e1'', e1''')) \ h \ xs \in (\text{Throw } a) \ h \ xs$   
 $\text{sim-move10 } P \ t \in (\text{CompareAndSwap } (\text{Val } v) \ D \ F \ (\text{Throw } a) \ e0') \ e1' \ (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Throw } a, e1'')) \ h \ xs \in (\text{Throw } a) \ h \ xs$   
 $\text{sim-move10 } P \ t \in (\text{CompareAndSwap } (\text{Val } v) \ D \ F \ (\text{Val } v') \ (\text{Throw } a)) \ e1' \ (\text{Val } v \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v', \text{Throw } a)) \ h \ xs \in (\text{Throw } a) \ h \ xs$   
 $\text{sim-move10 } P \ t \in (\text{Throw } a \cdot M(es0)) \ e1' \ (\text{Throw } a \cdot M(es1)) \ h \ xs \in (\text{Throw } a) \ h \ xs$   
 $\text{sim-move10 } P \ t \in (\text{Val } v \cdot M(\text{map } \text{Val } vs \ @ \ \text{Throw } a \ \# \ es0)) \ e1' \ (\text{Val } v \cdot M(\text{map } \text{Val } vs \ @ \ \text{Throw } a \ \# \ es1)) \ h \ xs \in (\text{Throw } a) \ h \ xs$   
 $\text{sim-move10 } P \ t \in (\{V':T=None; \text{Throw } a\}) \ e1' \ (\{V:T=vo; \text{Throw } a\}) \ h \ xs \in (\text{Throw } a) \ h \ xs$   
 $\text{sim-move10 } P \ t \in (\text{sync}_{V'}(\text{Throw } a) \ e0) \ e1' \ (\text{sync}(\text{Throw } a) \ e1) \ h \ xs \in (\text{Throw } a) \ h \ xs$   
 $\text{sim-move10 } P \ t \in (\text{Throw } a;;e0) \ e1' \ (\text{Throw } a;;e1) \ h \ xs \in (\text{Throw } a) \ h \ xs$   
 $\text{sim-move10 } P \ t \in (\text{if } (\text{Throw } a) \ e0 \text{ else } e0') \ e1' \ (\text{if } (\text{Throw } a) \ e1 \text{ else } e2) \ h \ xs \in (\text{Throw } a) \ h \ xs$   
 $\text{sim-move10 } P \ t \in (\text{throw } (\text{Throw } a)) \ e1' \ (\text{throw } (\text{Throw } a)) \ h \ xs \in (\text{Throw } a) \ h \ xs$   
**apply**(fastforce simp add: sim-move10-def no-calls-def no-call-def ta-bisim-def intro: red-reds.intros)+  
**done**

**lemma** *sim-move10-CallNull*:

$\text{sim-move10 } P \ t \in (\text{null} \cdot M(\text{map } \text{Val } vs)) \ e1' \ (\text{null} \cdot M(\text{map } \text{Val } vs)) \ h \ xs \in (\text{THROW } \text{NullPointer}) \ h \ xs$   
**by**(fastforce simp add: sim-move10-def map-eq-append-conv intro: RedCallNull)

**lemma** *sim-move10-SyncLocks*:

$\llbracket \text{ta1} = \{\text{Lock} \rightarrow a, \text{SyncLock } a\}; \text{ta} = \{\text{Lock} \rightarrow a, \text{SyncLock } a\} \rrbracket$   
 $\implies \text{sim-move10 } P \ t \ \text{ta1} \ (\text{sync}_{V'}(\text{addr } a) \ e0) \ e1' \ (\text{sync}(\text{addr } a) \ e1) \ h \ xs \ \text{ta} \ (\text{insync}(a) \ e1) \ h \ xs$   
 $\llbracket \text{ta1} = \{\text{Unlock} \rightarrow a, \text{SyncUnlock } a\}; \text{ta} = \{\text{Unlock} \rightarrow a, \text{SyncUnlock } a\} \rrbracket$   
 $\implies \text{sim-move10 } P \ t \ \text{ta1} \ (\text{insync}_{V'}(a') \ (\text{Val } v)) \ e1' \ (\text{insync}(a) \ (\text{Val } v)) \ h \ xs \ \text{ta} \ (\text{Val } v) \ h \ xs$   
 $\llbracket \text{ta1} = \{\text{Unlock} \rightarrow a, \text{SyncUnlock } a\}; \text{ta} = \{\text{Unlock} \rightarrow a, \text{SyncUnlock } a\} \rrbracket$   
 $\implies \text{sim-move10 } P \ t \ \text{ta1} \ (\text{insync}_{V'}(a') \ (\text{Throw } a'')) \ e1' \ (\text{insync}(a) \ (\text{Throw } a'')) \ h \ xs \ \text{ta} \ (\text{Throw } a'') \ h \ xs$   
**by**(fastforce simp add: sim-move10-def ta-bisim-def ta-upd-simps intro: red-reds.intros[simplified])+

**lemma** *sim-move10-TryFail*:

$\llbracket \text{typeof-addr } h \ a = [\text{Class-type } D]; \neg P \vdash D \preceq^* C \rrbracket$   
 $\implies \text{sim-move10 } P \ t \in (\text{try } (\text{Throw } a) \ \text{catch}(C \ V') \ e0) \ e1' \ (\text{try } (\text{Throw } a) \ \text{catch}(C \ V) \ e1) \ h \ xs \in (\text{Throw } a) \ h \ xs$   
**by**(auto simp add: sim-move10-def intro!: RedTryFail)

**lemmas** *sim-move10-intros* =

*sim-move10-expr sim-move10-reds sim-move10-CallNull sim-move10-TryFail sim-move10-Block sim-move10-CallNull*

**lemma** *sim-move10-preserves-defass*:

**assumes** *wf: wf-J-prog P*  
**shows**  $\llbracket \text{sim-move10 } P \ t \ \text{ta1} \ e1 \ e1' \ e \ h \ xs \ \text{ta} \ e' \ h' \ xs'; \mathcal{D} \ e \ [\text{dom } xs] \rrbracket \implies \mathcal{D} \ e' \ [\text{dom } xs']$   
**by**(auto simp add: sim-move10-def split: if-split-asm dest!:  $\tau_{\text{red0r}}$ -preserves-defass[OF wf]  $\tau_{\text{red0t}}$ -preserves-defass[OF wf] red-preserves-defass[OF wf])

**declare** *sim-move10-intros*[intro]

**lemma** **assumes** *wf: wf-J-prog P*

**shows** *red-simulates-red1-aux*:

$\llbracket \text{False}, \text{compP1 } P, t \vdash 1 \ \langle e1, S \rangle - \text{TA} \rightarrow \langle e1', S' \rangle; \text{bisim } vs \ e2 \ e1 \ (\text{lcl } S); \text{fv } e2 \subseteq \text{set } vs;$   
 $x \subseteq_m [vs \mapsto] \text{lcl } S; \text{length } vs + \text{max-vars } e1 \leq \text{length } (\text{lcl } S);$



$\mathcal{D} \ e2 \ [dom \ x] \ ]$   
 $\implies \exists ta \ e2' \ x'. \ sim-move10 \ P \ t \ TA \ e1 \ e1' \ e2 \ (hp \ S) \ x \ ta \ e2' \ (hp \ S') \ x' \wedge bisim \ vs \ e2' \ e1' \ (lcl \ S')$   
 $\wedge x' \subseteq_m [vs \ [\mapsto] \ lcl \ S']$   
 $(is \ [ \ -; \ -; \ -; \ -; \ - ] \implies ?concl \ e1 \ e1' \ e2 \ S \ x \ TA \ S' \ e1' \ vs)$

**and** *reds-simulates-reds1-aux*:

$[ \ False, compP1 \ P, t \vdash 1 \ \langle es1, S \rangle \ [-TA \rightarrow] \ \langle es1', S' \rangle; bisims \ vs \ es2 \ es1 \ (lcl \ S); fvs \ es2 \subseteq set \ vs;$   
 $x \subseteq_m [vs \ [\mapsto] \ lcl \ S]; length \ vs + max-varss \ es1 \leq length \ (lcl \ S);$   
 $\mathcal{D} \ s \ es2 \ [dom \ x] \ ]$   
 $\implies \exists ta \ es2' \ x'. \ sim-moves10 \ P \ t \ TA \ es1 \ es1' \ es2 \ (hp \ S) \ x \ ta \ es2' \ (hp \ S') \ x' \wedge bisims \ vs \ es2' \ es1'$   
 $(lcl \ S') \wedge x' \subseteq_m [vs \ [\mapsto] \ lcl \ S']$

$(is \ [ \ -; \ -; \ -; \ -; \ - ] \implies ?concls \ es1 \ es1' \ es2 \ S \ x \ TA \ S' \ es1' \ vs)$

**proof**(*induct arbitrary: vs e2 x and vs es2 x rule: red1-reds1.inducts*)

**case** (*Bin1OpRed1 e s ta e' s' bop e2 Vs E2 X*)

**note**  $IH = \langle \bigwedge vs \ e2 \ x. \ [ \ bisim \ vs \ e2 \ e \ (lcl \ s); fv \ e2 \subseteq set \ vs;$

$x \subseteq_m [vs \ [\mapsto] \ lcl \ s]; length \ vs + max-vars \ e \leq length \ (lcl \ s); \mathcal{D} \ e2 \ [dom \ x] ]$

$\implies ?concl \ e \ e' \ e2 \ s \ x \ ta \ s' \ e' \ vs \rangle$

**from**  $\langle False, compP1 \ P, t \vdash 1 \ \langle e, s \rangle \ [-ta \rightarrow] \ \langle e', s' \rangle \rangle$  **have**  $\neg is-val \ e$  **by** *auto*

**with**  $\langle bisim \ Vs \ E2 \ (e \ \langle bop \rangle \ e2) \ (lcl \ s) \rangle$  **obtain**  $E \ E2'$

**where**  $E2: E2 = E \ \langle bop \rangle \ E2' \ e2 = compE1 \ Vs \ E2' \ \text{and} \ bisim \ Vs \ E \ e \ (lcl \ s)$

**and** *sync*:  $\neg contains-insync \ E2'$

**by**(*auto elim!: bisim-cases*)

**moreover note**  $IH[of \ Vs \ E \ X] \ \langle fv \ E2 \subseteq set \ Vs \rangle \ \langle X \subseteq_m [Vs \ [\mapsto] \ lcl \ s] \rangle$

$\langle length \ Vs + max-vars \ (e \ \langle bop \rangle \ e2) \leq length \ (lcl \ s) \rangle \ \langle \mathcal{D} \ E2 \ [dom \ X] \rangle$

**ultimately obtain**  $TA' \ e2' \ x' \ \text{where} \ sim-move10 \ P \ t \ ta \ e \ e' \ E \ (hp \ s) \ X \ TA' \ e2' \ (hp \ s') \ x'$

$bisim \ Vs \ e2' \ e' \ (lcl \ s') \ x' \subseteq_m [Vs \ [\mapsto] \ lcl \ s'] \ \text{by}(\text{auto})$

**with**  $E2 \ \langle fv \ E2 \subseteq set \ Vs \rangle \ \text{sync} \ \text{show} \ ?case \ \text{by}(\text{cases } is-val \ e2')(\text{auto intro: BinOpRed1})$

**next**

**case** (*Bin1OpRed2 e s ta e' s' v bop Vs E2 X*)

**note**  $IH = \langle \bigwedge vs \ e2 \ x. \ [ \ bisim \ vs \ e2 \ e \ (lcl \ s); fv \ e2 \subseteq set \ vs;$

$x \subseteq_m [vs \ [\mapsto] \ lcl \ s]; length \ vs + max-vars \ e \leq length \ (lcl \ s); \mathcal{D} \ e2 \ [dom \ x] ]$

$\implies ?concl \ e \ e' \ e2 \ s \ x \ ta \ s' \ e' \ vs \rangle$

**from**  $\langle bisim \ Vs \ E2 \ (Val \ v \ \langle bop \rangle \ e) \ (lcl \ s) \rangle$  **obtain**  $E$

**where**  $E2: E2 = Val \ v \ \langle bop \rangle \ E \ \text{and} \ bisim \ Vs \ E \ e \ (lcl \ s) \ \text{by}(\text{auto})$

**moreover note**  $IH[of \ Vs \ E \ X] \ \langle fv \ E2 \subseteq set \ Vs \rangle \ \langle X \subseteq_m [Vs \ [\mapsto] \ lcl \ s] \rangle$

$\langle length \ Vs + max-vars \ (Val \ v \ \langle bop \rangle \ e) \leq length \ (lcl \ s) \rangle \ \langle \mathcal{D} \ E2 \ [dom \ X] \rangle \ E2$

**ultimately show**  $?case \ \text{by}(\text{auto intro: BinOpRed2})$

**next**

**case** (*Red1Var s V v Vs E2 X*)

**from**  $\langle bisim \ Vs \ E2 \ (Var \ V) \ (lcl \ s) \rangle \ \langle fv \ E2 \subseteq set \ Vs \rangle$

**obtain**  $V'$  **where**  $E2 = Var \ V' \ V' = Vs ! V \ V = index \ Vs \ V' \ \text{by}(\text{clarify, simp})$

**from**  $\langle E2 = Var \ V' \rangle \ \langle \mathcal{D} \ E2 \ [dom \ X] \rangle$

**obtain**  $v'$  **where**  $X \ V' = [v'] \ \text{by}(\text{auto simp add: hyperset-defs})$

**with**  $\langle X \subseteq_m [Vs \ [\mapsto] \ lcl \ s] \rangle$  **have**  $[Vs \ [\mapsto] \ lcl \ s] \ V' = [v'] \ \text{by}(\text{rule map-le-SomeD})$

**with**  $\langle length \ Vs + max-vars \ (Var \ V) \leq length \ (lcl \ s) \rangle$

**have**  $lcl \ s ! (index \ Vs \ V') = v' \ \text{by}(\text{auto intro: map-upds-Some-eq-nth-index})$

**with**  $\langle V = index \ Vs \ V' \rangle \ \langle lcl \ s ! V = v \rangle$  **have**  $v = v' \ \text{by} \ \text{simp}$

**with**  $\langle X \ V' = [v'] \rangle \ \langle E2 = Var \ V' \rangle \ \langle X \subseteq_m [Vs \ [\mapsto] \ lcl \ s] \rangle$

**show**  $?case \ \text{by}(\text{fastforce intro: RedVar})$

**next**

**case** (*LAss1Red e s ta e' s' V Vs E2 X*)

**note**  $IH = \langle \bigwedge vs \ e2 \ x. \ [ \ bisim \ vs \ e2 \ e \ (lcl \ s); fv \ e2 \subseteq set \ vs;$

$x \subseteq_m [vs \ [\mapsto] \ lcl \ s]; length \ vs + max-vars \ e \leq length \ (lcl \ s); \mathcal{D} \ e2 \ [dom \ x] ]$

$\implies ?concl \ e \ e' \ e2 \ s \ x \ ta \ s' \ e' \ vs \rangle$

**from**  $\langle \text{bisim } Vs \ E2 \ (V := e) \ (lcl \ s) \rangle$  **obtain**  $E \ V'$   
**where**  $E2: E2 = (V' := E) \ V = \text{index } Vs \ V' \text{ and } \text{bisim } Vs \ E \ e \ (lcl \ s) \text{ by } \text{auto}$   
**with**  $IH[\text{of } Vs \ E \ X] \langle \text{fv } E2 \subseteq \text{set } Vs \rangle \langle X \subseteq_m [Vs \mapsto] \text{ lcl } s \rangle$   
 $\langle \text{length } Vs + \text{max-vars } (V := e) \leq \text{length } (lcl \ s) \rangle \langle \mathcal{D} \ E2 \ [dom \ X] \rangle$   
 $E2 \text{ show } ?\text{case by}(\text{auto intro: } LAssRed)$   
**next**  
**case**  $(Red1LAss \ V \ l \ v \ h \ Vs \ E2 \ X)$   
**from**  $\langle \text{bisim } Vs \ E2 \ (V := Val \ v) \ (lcl \ (h, l)) \rangle$  **obtain**  $V'$  **where**  $E2 = (V' := Val \ v) \ V = \text{index } Vs \ V' \text{ by}(\text{auto})$   
**moreover with**  $\langle \text{fv } E2 \subseteq \text{set } Vs \rangle \langle X \subseteq_m [Vs \mapsto] \text{ lcl } (h, l) \rangle \langle \text{length } Vs + \text{max-vars } (V := Val \ v) \leq \text{length } (lcl \ (h, l)) \rangle$   
**have**  $X(V' \mapsto v) \subseteq_m [Vs \mapsto] \text{ lcl } [index \ Vs \ V' := v] \text{ by}(\text{auto intro: } LAss\text{-lem})$   
**ultimately show**  $?\text{case by}(\text{auto intro: } RedLAss \text{ simp del: fun-upd-apply})$   
**next**  
**case**  $(AAcc1Red1 \ a \ s \ ta \ a' \ s' \ i \ Vs \ E2 \ X)$   
**note**  $IH = \langle \bigwedge vs \ e2 \ x. \llbracket \text{bisim } vs \ e2 \ a \ (lcl \ s); \text{fv } e2 \subseteq \text{set } vs; \text{ } x \subseteq_m [vs \mapsto] \text{ lcl } s; \text{length } vs + \text{max-vars } a \leq \text{length } (lcl \ s); \mathcal{D} \ e2 \ [dom \ x] \rrbracket \implies ?\text{concl } a \ a' \ e2 \ s \ x \ ta \ s' \ a' \ vs \rangle$   
**from**  $\langle \text{False, compP1 } P, t \vdash 1 \ \langle a, s \rangle \rightarrow \langle a', s' \rangle \rangle$  **have**  $\neg \text{is-val } a \text{ by } \text{auto}$   
**with**  $\langle \text{bisim } Vs \ E2 \ (a[i]) \ (lcl \ s) \rangle$  **obtain**  $E \ E2'$   
**where**  $E2: E2 = E[E2'] \ i = \text{compE1 } Vs \ E2' \text{ and } \text{bisim } Vs \ E \ a \ (lcl \ s)$   
**and**  $\text{sync: } \neg \text{contains-insync } E2' \text{ by}(\text{fastforce})$   
**moreover note**  $IH[\text{of } Vs \ E \ X] \langle \text{fv } E2 \subseteq \text{set } Vs \rangle \langle X \subseteq_m [Vs \mapsto] \text{ lcl } s \rangle$   
 $\langle \text{length } Vs + \text{max-vars } (a[i]) \leq \text{length } (lcl \ s) \rangle \langle \mathcal{D} \ E2 \ [dom \ X] \rangle$   
**ultimately obtain**  $TA' \ e2' \ x' \text{ where } \text{sim-move10 } P \ t \ ta \ a \ a' \ E \ (hp \ s) \ X \ TA' \ e2' \ (hp \ s') \ x'$   
 $\text{bisim } Vs \ e2' \ a' \ (lcl \ s') \ x' \subseteq_m [Vs \mapsto] \text{ lcl } s' \text{ by}(\text{auto})$   
**with**  $E2 \langle \text{fv } E2 \subseteq \text{set } Vs \rangle \text{ sync show } ?\text{case}$   
**by**  $(\text{cases is-val } e2')(\text{auto intro: } AAccRed1)$   
**next**  
**case**  $(AAcc1Red2 \ i \ s \ ta \ i' \ s' \ a \ Vs \ E2 \ X)$   
**note**  $IH = \langle \bigwedge vs \ e2 \ x. \llbracket \text{bisim } vs \ e2 \ i \ (lcl \ s); \text{fv } e2 \subseteq \text{set } vs; \text{ } x \subseteq_m [vs \mapsto] \text{ lcl } s; \text{length } vs + \text{max-vars } i \leq \text{length } (lcl \ s); \mathcal{D} \ e2 \ [dom \ x] \rrbracket \implies ?\text{concl } i \ i' \ e2 \ s \ x \ ta \ s' \ i' \ vs \rangle$   
**from**  $\langle \text{bisim } Vs \ E2 \ (Val \ a[i]) \ (lcl \ s) \rangle$  **obtain**  $E$   
**where**  $E2: E2 = Val \ a[E] \text{ and } \text{bisim } Vs \ E \ i \ (lcl \ s) \text{ by}(\text{auto})$   
**moreover note**  $IH[\text{of } Vs \ E \ X] \langle \text{fv } E2 \subseteq \text{set } Vs \rangle \langle X \subseteq_m [Vs \mapsto] \text{ lcl } s \rangle \ E2$   
 $\langle \text{length } Vs + \text{max-vars } (Val \ a[i]) \leq \text{length } (lcl \ s) \rangle \langle \mathcal{D} \ E2 \ [dom \ X] \rangle$   
**ultimately show**  $?\text{case by}(\text{auto intro: } AAccRed2)$   
**next**  
**case**  $Red1AAcc \text{ thus } ?\text{case by}(\text{fastforce intro: } RedAAcc \text{ simp del: fun-upd-apply})$   
**next**  
**case**  $(AAss1Red1 \ a \ s \ ta \ a' \ s' \ i \ e \ Vs \ E2 \ X)$   
**note**  $IH = \langle \bigwedge vs \ e2 \ x. \llbracket \text{bisim } vs \ e2 \ a \ (lcl \ s); \text{fv } e2 \subseteq \text{set } vs; \text{ } x \subseteq_m [vs \mapsto] \text{ lcl } s; \text{length } vs + \text{max-vars } a \leq \text{length } (lcl \ s); \mathcal{D} \ e2 \ [dom \ x] \rrbracket \implies ?\text{concl } a \ a' \ e2 \ s \ x \ ta \ s' \ a' \ vs \rangle$   
**from**  $\langle \text{False, compP1 } P, t \vdash 1 \ \langle a, s \rangle \rightarrow \langle a', s' \rangle \rangle$  **have**  $\neg \text{is-val } a \text{ by } \text{auto}$   
**with**  $\langle \text{bisim } Vs \ E2 \ (a[i] := e) \ (lcl \ s) \rangle$  **obtain**  $E \ E2' \ E2''$   
**where**  $E2: E2 = E[E2'] := E2'' \ i = \text{compE1 } Vs \ E2' \ e = \text{compE1 } Vs \ E2'' \text{ and } \text{bisim } Vs \ E \ a \ (lcl \ s)$   
**and**  $\text{sync: } \neg \text{contains-insync } E2' \neg \text{contains-insync } E2'' \text{ by}(\text{fastforce})$   
**moreover note**  $IH[\text{of } Vs \ E \ X] \langle \text{fv } E2 \subseteq \text{set } Vs \rangle \langle X \subseteq_m [Vs \mapsto] \text{ lcl } s \rangle$   
 $\langle \text{length } Vs + \text{max-vars } (a[i] := e) \leq \text{length } (lcl \ s) \rangle \langle \mathcal{D} \ E2 \ [dom \ X] \rangle$   
**ultimately obtain**  $TA' \ e2' \ x' \text{ where } IH': \text{sim-move10 } P \ t \ ta \ a \ a' \ E \ (hp \ s) \ X \ TA' \ e2' \ (hp \ s') \ x'$   
 $\text{bisim } Vs \ e2' \ a' \ (lcl \ s') \ x' \subseteq_m [Vs \mapsto] \text{ lcl } s' \text{ by}(\text{auto})$   
**show**  $?\text{case}$

```

proof(cases is-val e2')
  case True
    from E2  $\langle \text{fv } E2 \subseteq \text{set } Vs \rangle \text{ sync}$  have bisim Vs E2' i (lcl s') bisim Vs E2'' e (lcl s') by auto
    with IH' E2 True sync show ?thesis
      by(cases is-val E2')(fastforce intro: AAssRed1)+
  next
    case False with IH' E2 sync show ?thesis by(fastforce intro: AAssRed1)
qed
next
  case (AAss1Red2 i s ta i' s' a e Vs E2 X)
  note IH =  $\langle \bigwedge vs \ e2 \ x. \llbracket \text{bisim } vs \ e2 \ i \ (lcl \ s); \text{fv } e2 \subseteq \text{set } vs; \\ x \subseteq_m [vs \mapsto] \text{ lcl } s; \text{length } vs + \text{max-vars } i \leq \text{length } (lcl \ s); \mathcal{D} \ e2 \ [dom \ x] \rrbracket \\ \implies ?concl \ i \ i' \ e2 \ s \ x \text{ ta } s' \ i' \ vs \rangle$ 
  from  $\langle \text{False}, \text{compP1 } P, t \vdash 1 \ \langle i, s \rangle \rightarrow \langle i', s' \rangle \rangle$  have  $\neg \text{is-val } i$  by auto
  with  $\langle \text{bisim } Vs \ E2 \ (\text{Val } a[i] := e) \ (lcl \ s) \rangle$  obtain E E2'
    where E2: E2 = Val a[E] := E2' e = compE1 Vs E2' and bisim Vs E i (lcl s)
    and sync:  $\neg \text{contains-insync } E2'$  by(fastforce)
  moreover note IH[of Vs E X]  $\langle \text{fv } E2 \subseteq \text{set } Vs \rangle \langle X \subseteq_m [Vs \mapsto] \text{ lcl } s \rangle$ 
   $\langle \text{length } Vs + \text{max-vars } (\text{Val } a[i] := e) \leq \text{length } (lcl \ s) \rangle \langle \mathcal{D} \ E2 \ [dom \ X] \rangle$ 
  ultimately obtain TA' e2' x' where sim-move10 P t ta i i' E (hp s) X TA' e2' (hp s') x'
    bisim Vs e2' i' (lcl s') x'  $\subseteq_m [Vs \mapsto] \text{ lcl } s'$  by(auto)
  with E2  $\langle \text{fv } E2 \subseteq \text{set } Vs \rangle \text{ sync}$  show ?case
    by(cases is-val e2')(fastforce intro: AAssRed2)+
next
  case (AAss1Red3 e s ta e' s' a i Vs E2 X)
  note IH =  $\langle \bigwedge vs \ e2 \ x. \llbracket \text{bisim } vs \ e2 \ e \ (lcl \ s); \text{fv } e2 \subseteq \text{set } vs; \\ x \subseteq_m [vs \mapsto] \text{ lcl } s; \text{length } vs + \text{max-vars } e \leq \text{length } (lcl \ s); \mathcal{D} \ e2 \ [dom \ x] \rrbracket \\ \implies ?concl \ e \ e' \ e2 \ s \ x \text{ ta } s' \ e' \ vs \rangle$ 
  from  $\langle \text{bisim } Vs \ E2 \ (\text{Val } a[\text{Val } i] := e) \ (lcl \ s) \rangle$  obtain E
    where E2: E2 = Val a[Val i] := E and bisim Vs E e (lcl s) by(fastforce)
  moreover note IH[of Vs E X]  $\langle \text{fv } E2 \subseteq \text{set } Vs \rangle \langle X \subseteq_m [Vs \mapsto] \text{ lcl } s \rangle \ E2$ 
   $\langle \text{length } Vs + \text{max-vars } (\text{Val } a[\text{Val } i] := e) \leq \text{length } (lcl \ s) \rangle \langle \mathcal{D} \ E2 \ [dom \ X] \rangle$ 
  ultimately show ?case by(fastforce intro: AAssRed3)
next
  case Red1AAssStore thus ?case by(auto)((rule exI conjI)+, auto)
next
  case Red1AAss thus ?case
    by(fastforce simp del: fun-upd-apply)
next
  case (FAss1Red1 e s ta e' s' F D e2 Vs E2 X)
  note IH =  $\langle \bigwedge vs \ e2 \ x. \llbracket \text{bisim } vs \ e2 \ e \ (lcl \ s); \text{fv } e2 \subseteq \text{set } vs; \\ x \subseteq_m [vs \mapsto] \text{ lcl } s; \text{length } vs + \text{max-vars } e \leq \text{length } (lcl \ s); \mathcal{D} \ e2 \ [dom \ x] \rrbracket \\ \implies ?concl \ e \ e' \ e2 \ s \ x \text{ ta } s' \ e' \ vs \rangle$ 
  from  $\langle \text{False}, \text{compP1 } P, t \vdash 1 \ \langle e, s \rangle \rightarrow \langle e', s' \rangle \rangle$  have  $\neg \text{is-val } e$  by auto
  with  $\langle \text{bisim } Vs \ E2 \ (e \cdot F\{D\} := e2) \ (lcl \ s) \rangle$  obtain E E2'
    where E2: E2 = E · F{D} := E2' e2 = compE1 Vs E2' and bisim Vs E e (lcl s)
    and sync:  $\neg \text{contains-insync } E2'$  by(fastforce)
  with IH[of Vs E X]  $\langle \text{fv } E2 \subseteq \text{set } Vs \rangle \langle X \subseteq_m [Vs \mapsto] \text{ lcl } s \rangle$ 
   $\langle \text{length } Vs + \text{max-vars } (e \cdot F\{D\} := e2) \leq \text{length } (lcl \ s) \rangle \langle \mathcal{D} \ E2 \ [dom \ X] \rangle$ 
  obtain TA' e2' x' where sim-move10 P t ta e e' E (hp s) X TA' e2' (hp s') x'
    bisim Vs e2' e' (lcl s') x'  $\subseteq_m [Vs \mapsto] \text{ lcl } s'$  by(fastforce)
  with E2  $\langle \text{fv } E2 \subseteq \text{set } Vs \rangle \text{ sync}$  show ?case by(cases is-val e2')(auto intro: FAssRed1)
next
  case (FAss1Red2 e s ta e' s' v F D Vs E2 X)

```

**note**  $IH = \langle \bigwedge vs \ e2 \ x. \llbracket bisim \ vs \ e2 \ e \ (lcl \ s); \ fv \ e2 \subseteq set \ vs; \ x \subseteq_m [vs \mapsto] \ lcl \ s; \ length \ vs + max\text{-}vars \ e \leq length \ (lcl \ s); \mathcal{D} \ e2 \ [dom \ x] \rrbracket \Rightarrow ?concl \ e \ e' \ e2 \ s \ x \ ta \ s' \ e' \ vs \rangle$   
**from**  $\langle bisim \ Vs \ E2 \ (Val \ v \cdot F\{D\} := e) \ (lcl \ s) \rangle$  **obtain**  $E$   
**where**  $E2: E2 = (Val \ v \cdot F\{D\} := E)$  **and**  $bisim \ Vs \ E \ e \ (lcl \ s)$  **by**  $(fastforce)$   
**with**  $IH[of \ Vs \ E \ X] \langle fv \ E2 \subseteq set \ Vs \rangle \langle X \subseteq_m [Vs \mapsto] \ lcl \ s \rangle$   
 $\langle length \ Vs + max\text{-}vars \ (Val \ v \cdot F\{D\} := e) \leq length \ (lcl \ s) \rangle \langle \mathcal{D} \ E2 \ [dom \ X] \rangle$   
 $E2$  **show**  $?case$  **by**  $(fastforce \ intro: \ FAssRed2)$   
**next**  
**case**  $(CAS1Red1 \ e \ s \ ta \ e' \ s' \ D \ F \ e2 \ e3 \ Vs \ E2 \ X)$   
**note**  $IH = \langle \bigwedge vs \ e2 \ x. \llbracket bisim \ vs \ e2 \ e \ (lcl \ s); \ fv \ e2 \subseteq set \ vs; \ x \subseteq_m [vs \mapsto] \ lcl \ s; \ length \ vs + max\text{-}vars \ e \leq length \ (lcl \ s); \mathcal{D} \ e2 \ [dom \ x] \rrbracket \Rightarrow ?concl \ e \ e' \ e2 \ s \ x \ ta \ s' \ e' \ vs \rangle$   
**from**  $\langle False, compP1 \ P, t \vdash 1 \ \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \rangle$  **have**  $\neg is\text{-}val \ e$  **by**  $auto$   
**with**  $\langle bisim \ Vs \ E2 - (lcl \ s) \rangle$  **obtain**  $E \ E2' \ E2''$   
**where**  $E2: E2 = E \cdot compareAndSwap(D \cdot F, \ E2', \ E2'') \ e2 = compE1 \ Vs \ E2' \ e3 = compE1 \ Vs \ E2''$  **and**  $bisim \ Vs \ E \ e \ (lcl \ s)$   
**and**  $sync: \neg contains\text{-}insync \ E2' \neg contains\text{-}insync \ E2''$  **by**  $(fastforce)$   
**moreover note**  $IH[of \ Vs \ E \ X] \langle fv \ E2 \subseteq set \ Vs \rangle \langle X \subseteq_m [Vs \mapsto] \ lcl \ s \rangle$   
 $\langle length \ Vs + max\text{-}vars - \leq length \ (lcl \ s) \rangle \langle \mathcal{D} \ E2 \ [dom \ X] \rangle$   
**ultimately obtain**  $TA' \ e2' \ x'$  **where**  $IH': sim\text{-}move10 \ P \ t \ ta \ e \ e' \ E \ (hp \ s) \ X \ TA' \ e2' \ (hp \ s') \ x'$   
 $bisim \ Vs \ e2' \ e' \ (lcl \ s') \ x' \subseteq_m [Vs \mapsto] \ lcl \ s' \rangle$  **by**  $(auto)$   
**show**  $?case$   
**proof**  $(cases \ is\text{-}val \ e2')$   
**case**  $True$   
**from**  $E2 \langle fv \ E2 \subseteq set \ Vs \rangle sync$  **have**  $bisim \ Vs \ E2' \ e2 \ (lcl \ s') \ bisim \ Vs \ E2'' \ e3 \ (lcl \ s')$  **by**  $auto$   
**with**  $IH' \ E2 \ True \ sync$  **show**  $?thesis$  **by**  $(cases \ is\text{-}val \ E2') (fastforce) +$   
**next**  
**case**  $False$  **with**  $IH' \ E2 \ sync$  **show**  $?thesis$  **by**  $(fastforce)$   
**qed**  
**next**  
**case**  $(CAS1Red2 \ e \ s \ ta \ e' \ s' \ v \ D \ F \ e3 \ Vs \ E2 \ X)$   
**note**  $IH = \langle \bigwedge vs \ e2 \ x. \llbracket bisim \ vs \ e2 \ e \ (lcl \ s); \ fv \ e2 \subseteq set \ vs; \ x \subseteq_m [vs \mapsto] \ lcl \ s; \ length \ vs + max\text{-}vars \ e \leq length \ (lcl \ s); \mathcal{D} \ e2 \ [dom \ x] \rrbracket \Rightarrow ?concl \ e \ e' \ e2 \ s \ x \ ta \ s' \ e' \ vs \rangle$   
**from**  $\langle False, compP1 \ P, t \vdash 1 \ \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \rangle$  **have**  $\neg is\text{-}val \ e$  **by**  $auto$   
**with**  $\langle bisim \ Vs \ E2 - (lcl \ s) \rangle$  **obtain**  $E \ E2'$   
**where**  $E2: E2 = (Val \ v \cdot compareAndSwap(D \cdot F, \ E, \ E2')) \ e3 = compE1 \ Vs \ E2'$  **and**  $bisim \ Vs \ E \ e \ (lcl \ s)$   
**and**  $sync: \neg contains\text{-}insync \ E2'$  **by**  $(auto)$   
**moreover note**  $IH[of \ Vs \ E \ X] \langle fv \ E2 \subseteq set \ Vs \rangle \langle X \subseteq_m [Vs \mapsto] \ lcl \ s \rangle$   
 $\langle length \ Vs + max\text{-}vars - \leq length \ (lcl \ s) \rangle \langle \mathcal{D} \ E2 \ [dom \ X] \rangle$   
**ultimately obtain**  $TA' \ e2' \ x'$  **where**  $sim\text{-}move10 \ P \ t \ ta \ e \ e' \ E \ (hp \ s) \ X \ TA' \ e2' \ (hp \ s') \ x'$   
 $bisim \ Vs \ e2' \ e' \ (lcl \ s') \ x' \subseteq_m [Vs \mapsto] \ lcl \ s' \rangle$  **by**  $(auto)$   
**with**  $E2 \langle fv \ E2 \subseteq set \ Vs \rangle sync$  **show**  $?case$   
**by**  $(cases \ is\text{-}val \ e2') (fastforce) +$   
**next**  
**case**  $(CAS1Red3 \ e \ s \ ta \ e' \ s' \ v \ D \ F \ v' \ Vs \ E2 \ X)$   
**note**  $IH = \langle \bigwedge vs \ e2 \ x. \llbracket bisim \ vs \ e2 \ e \ (lcl \ s); \ fv \ e2 \subseteq set \ vs; \ x \subseteq_m [vs \mapsto] \ lcl \ s; \ length \ vs + max\text{-}vars \ e \leq length \ (lcl \ s); \mathcal{D} \ e2 \ [dom \ x] \rrbracket \Rightarrow ?concl \ e \ e' \ e2 \ s \ x \ ta \ s' \ e' \ vs \rangle$   
**from**  $\langle bisim \ Vs \ E2 - (lcl \ s) \rangle$  **obtain**  $E$   
**where**  $E2: E2 = (Val \ v \cdot compareAndSwap(D \cdot F, \ Val \ v', \ E))$  **and**  $bisim \ Vs \ E \ e \ (lcl \ s)$  **by**  $(fastforce)$   
**moreover note**  $IH[of \ Vs \ E \ X] \langle fv \ E2 \subseteq set \ Vs \rangle \langle X \subseteq_m [Vs \mapsto] \ lcl \ s \rangle \ E2$

$\langle \text{length } Vs + \text{max-vars} - \leq \text{length } (lcl\ s) \rangle \langle \mathcal{D}\ E2\ [dom\ X] \rangle$   
**ultimately show** ?case **by**(fastforce)

**next**

**case** (Call1Obj  $e\ s\ ta\ e'\ s'\ M\ es\ Vs\ E2\ X$ )  
**note**  $IH = \langle \bigwedge vs\ e2\ x. \llbracket bisim\ vs\ e2\ e\ (lcl\ s); fv\ e2 \subseteq set\ vs; \$   
 $x \subseteq_m [vs\ [\mapsto] lcl\ s]; \text{length } vs + \text{max-vars } e \leq \text{length } (lcl\ s);$   
 $\mathcal{D}\ e2\ [dom\ x] \rrbracket \implies ?concl\ e\ e'\ e2\ s\ x\ ta\ s'\ e'\ vs \rangle$   
**from**  $\langle False, compP1\ P, t \vdash 1\ \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \rangle \langle bisim\ Vs\ E2\ (e \cdot M(es))\ (lcl\ s) \rangle$   
**obtain**  $E\ es'$  **where**  $E2: E2 = E \cdot M(es')$   $es = compEs1\ Vs\ es'$  **and**  $bisim\ Vs\ E\ e\ (lcl\ s)$   
**and**  $sync: \neg \text{contains-insyncs } es'$  **by**(auto elim!: bisim-cases simp add: compEs1-conv-map)  
**with**  $IH[of\ Vs\ E\ X] \langle fv\ E2 \subseteq set\ Vs \rangle \langle X \subseteq_m [Vs\ [\mapsto] lcl\ s] \rangle$   
 $\langle \text{length } Vs + \text{max-vars } (e \cdot M(es)) \leq \text{length } (lcl\ s) \rangle \langle \mathcal{D}\ E2\ [dom\ X] \rangle$   
**obtain**  $TA'\ e2'\ x'$  **where**  $IH': sim-move10\ P\ t\ ta\ e\ e'\ E\ (hp\ s)\ X\ TA'\ e2'\ (hp\ s')\ x'$   
 $bisim\ Vs\ e2'\ e'\ (lcl\ s')\ x' \subseteq_m [Vs\ [\mapsto] lcl\ s']$  **by**(fastforce)  
**with**  $E2 \langle fv\ E2 \subseteq set\ Vs \rangle \langle E2 = E \cdot M(es') \rangle$  **sync show** ?case  
**by**(cases is-val  $e2'$ )(auto intro: CallObj)

**next**

**case** (Call1Params  $es\ s\ ta\ es'\ s'\ v\ M\ Vs\ E2\ X$ )  
**note**  $IH = \langle \bigwedge vs\ es2\ x. \llbracket bisims\ vs\ es2\ es\ (lcl\ s); fvs\ es2 \subseteq set\ vs; \$   
 $x \subseteq_m [vs\ [\mapsto] lcl\ s]; \text{length } vs + \text{max-varss } es \leq \text{length } (lcl\ s); \mathcal{D}s\ es2\ [dom\ x] \rrbracket$   
 $\implies ?concls\ es\ es'\ es2\ s\ x\ ta\ s'\ es'\ vs \rangle$   
**from**  $\langle bisim\ Vs\ E2\ (Val\ v \cdot M(es))\ (lcl\ s) \rangle$  **obtain**  $Es$   
**where**  $E2 = Val\ v \cdot M(Es)$   $bisims\ Vs\ Es\ es\ (lcl\ s)$  **by**(auto)  
**with**  $IH[of\ Vs\ Es\ X] \langle fv\ E2 \subseteq set\ Vs \rangle \langle X \subseteq_m [Vs\ [\mapsto] lcl\ s] \rangle$   
 $\langle \text{length } Vs + \text{max-vars } (Val\ v \cdot M(es)) \leq \text{length } (lcl\ s) \rangle \langle \mathcal{D}\ E2\ [dom\ X] \rangle$   
 $\langle E2 = Val\ v \cdot M(Es) \rangle$  **show** ?case **by**(fastforce intro: CallParams)

**next**

**case** (Red1CallExternal  $s\ a\ T\ M\ Ts\ Tr\ D\ vs\ ta\ va\ h'\ e'\ s'\ Vs\ E2\ X$ )  
**from**  $\langle bisim\ Vs\ E2\ (addr\ a \cdot M(\text{map } Val\ vs))\ (lcl\ s) \rangle$  **have**  $E2: E2 = addr\ a \cdot M(\text{map } Val\ vs)$  **by** auto  
**moreover from**  $\langle compP1\ P \vdash \text{class-type-of } T \text{ sees } M: Ts \rightarrow Tr = \text{Native in } D \rangle$   
**have**  $P \vdash \text{class-type-of } T \text{ sees } M: Ts \rightarrow Tr = \text{Native in } D$  **by** simp  
**moreover from**  $\langle compP1\ P, t \vdash \langle a \cdot M(vs), hp\ s \rangle -ta \rightarrow ext\ \langle va, h' \rangle \rangle$   
**have**  $P, t \vdash \langle a \cdot M(vs), hp\ s \rangle -ta \rightarrow ext\ \langle va, h' \rangle$  **by** simp  
**moreover from**  $wf\ \langle P, t \vdash \langle a \cdot M(vs), hp\ s \rangle -ta \rightarrow ext\ \langle va, h' \rangle \rangle$   
**have**  $ta\text{-bisim}01\ (extTA2J0\ P\ ta)\ (extTA2J1\ (compP1\ P)\ ta)$   
**by**(rule red-external-ta-bisim01)  
**moreover note**  $\langle \text{typeof-addr } (hp\ s)\ a = \lfloor T \rfloor \rangle \langle e' = extRet2J1\ (addr\ a \cdot M(\text{map } Val\ vs))\ va \rangle \langle s' =$   
 $(h', lcl\ s) \rangle$   
**moreover from**  $\langle \text{typeof-addr } (hp\ s)\ a = \lfloor T \rfloor \rangle \langle P, t \vdash \langle a \cdot M(vs), hp\ s \rangle -ta \rightarrow ext\ \langle va, h' \rangle \rangle$   
 $\langle P \vdash \text{class-type-of } T \text{ sees } M: Ts \rightarrow Tr = \text{Native in } D \rangle$   
**have**  $\tau\text{external-defs } D\ M \implies ta = \varepsilon \wedge h' = hp\ s$   
**by**(fastforce dest:  $\tau\text{external}'\text{-red-external-heap-unchanged } \tau\text{external}'\text{-red-external-TA-empty simp}$   
 $add: \tau\text{external}'\text{-def } \tau\text{external-def}$ )  
**ultimately show** ?case **using**  $\langle X \subseteq_m [Vs\ [\mapsto] lcl\ s] \rangle$   
**by**(fastforce intro!: exI[**where**  $x = extTA2J0\ P\ ta$ ] intro: RedCallExternal simp add: map-eq-append-conv  
 $sim-move10\text{-def synthesized-call-def dest: sees-method-fun del: disjCI intro: disjI1 disjI2}$ )

**next**

**case** (Block1Red  $e\ h\ x\ ta\ e'\ h'\ x'\ V\ T\ Vs\ E2\ X$ )  
**note**  $IH = \langle \bigwedge vs\ e2\ xa. \llbracket bisim\ vs\ e2\ e\ (lcl\ (h, x)); fv\ e2 \subseteq set\ vs; xa \subseteq_m [vs\ [\mapsto] lcl\ (h, x)]; \$   
 $\text{length } vs + \text{max-vars } e \leq \text{length } (lcl\ (h, x)); \mathcal{D}\ e2\ [dom\ xa] \rrbracket$   
 $\implies ?concl\ e\ e'\ e2\ (h, x)\ xa\ ta\ (h', x')\ e'\ vs \rangle$   
**from**  $\langle False, compP1\ P, t \vdash 1\ \langle e, (h, x) \rangle -ta \rightarrow \langle e', (h', x') \rangle \rangle$   
**have**  $\text{length } x = \text{length } x'$  **by**(auto dest: red1-preserves-len)  
**with**  $\langle \text{length } Vs + \text{max-vars } \{V:T=None; e\} \leq \text{length } (lcl\ (h, x)) \rangle$

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have  $\text{length } Vs < \text{length } x'$  by simp
from  $\langle \text{bisim } Vs \ E2 \ \{V:T=None; e\} \ (lcl \ (h, x)) \rangle$ 
show ?case
proof(cases rule: bisim-cases(14)[consumes 1, case-names BlockNone BlockSome BlockSomeNone])
  case (BlockNone  $V' \ E$ )
    with  $\langle \text{fv } E2 \subseteq \text{set } Vs \rangle$  have  $\text{fv } E \subseteq \text{set } (Vs @ [V'])$  by auto
    with  $\text{IH}[of \ Vs @ [V'] \ E \ X(V' := None)]$  BlockNone  $\langle \text{fv } E2 \subseteq \text{set } Vs \rangle$   $\langle X \subseteq_m [Vs \mapsto] \text{ lcl } (h, x) \rangle$ 
       $\langle \text{length } Vs + \text{max-vars } \{V:T=None; e\} \leq \text{length } (lcl \ (h, x)) \rangle$   $\langle \mathcal{D} \ E2 \ \lfloor \text{dom } X \rfloor \rangle$ 
    obtain  $TA' \ e2' \ X'$  where  $\text{IH}': \text{sim-move10 } P \ t \ ta \ e \ e' \ E \ h \ (X(V' := None)) \ TA' \ e2' \ h' \ X'$ 
       $\text{bisim } (Vs @ [V']) \ e2' \ e' \ x' \ X' \subseteq_m [Vs @ [V'] \mapsto] \ x'$ 
      by(fastforce simp del: fun-upd-apply)
    { assume  $V' \in \text{set } Vs$ 
      hence hidden  $(Vs @ [V']) \ (index \ Vs \ V')$  by(rule hidden-index)
      with  $\langle \text{bisim } (Vs @ [V']) \ E \ e \ (lcl \ (h, x)) \rangle$  have unmod  $e \ (index \ Vs \ V')$ 
        by(auto intro: hidden-bisim-unmod)
      moreover from  $\langle \text{length } Vs + \text{max-vars } \{V:T=None; e\} \leq \text{length } (lcl \ (h, x)) \rangle$   $\langle V' \in \text{set } Vs \rangle$ 
        have  $index \ Vs \ V' < \text{length } x$  by(auto intro: index-less-aux)
      ultimately have  $x \neq index \ Vs \ V' = x' \neq index \ Vs \ V'$ 
        using red1-preserves-unmod[OF  $\langle \text{False}, \text{compP1 } P, t \vdash 1 \ \langle e, (h, x) \rangle \rightarrow \langle e', (h', x') \rangle \rangle$ ]
        by(simp) }
    with  $\langle \text{length } Vs + \text{max-vars } \{V:T=None; e\} \leq \text{length } (lcl \ (h, x)) \rangle$ 
       $\langle X' \subseteq_m [Vs @ [V'] \mapsto] \ x' \rangle$   $\langle \text{length } x = \text{length } x' \rangle$   $\langle X \subseteq_m [Vs \mapsto] \text{ lcl } (h, x) \rangle$ 
    have  $\text{rel: } X'(V' := X \ V') \subseteq_m [Vs \mapsto] \ x'$  by(auto intro: Block-lem)

  show ?thesis
  proof(cases  $X' \ V'$ )
    case None
      with BlockNone  $\text{IH}' \ \text{rel}$  show ?thesis by(fastforce intro: BlockRed)
    next
      case (Some  $v$ )
        with  $\langle X' \subseteq_m [Vs @ [V'] \mapsto] \ x' \rangle$   $\langle \text{length } Vs < \text{length } x' \rangle$ 
        have  $x' \neq \text{length } Vs = v$  by(auto dest: map-le-SomeD)
        with BlockNone  $\text{IH}' \ \text{rel} \ \text{Some}$  show ?thesis by(fastforce intro: BlockRed)
      qed
    next
      case BlockSome thus ?thesis by simp
    next
      case (BlockSomeNone  $V' \ E$ )
        with  $\langle \text{fv } E2 \subseteq \text{set } Vs \rangle$  have  $\text{fv } E \subseteq \text{set } (Vs @ [V'])$  by auto
        with  $\text{IH}[of \ Vs @ [V'] \ E \ X(V' \mapsto x \neq \text{length } Vs)]$  BlockSomeNone  $\langle \text{fv } E2 \subseteq \text{set } Vs \rangle$   $\langle X \subseteq_m [Vs \mapsto] \text{ lcl } (h, x) \rangle$ 
           $\langle \text{length } Vs + \text{max-vars } \{V:T=None; e\} \leq \text{length } (lcl \ (h, x)) \rangle$   $\langle \mathcal{D} \ E2 \ \lfloor \text{dom } X \rfloor \rangle$ 
        obtain  $TA' \ e2' \ X'$  where  $\text{IH}': \text{sim-move10 } P \ t \ ta \ e \ e' \ E \ h \ (X(V' \mapsto x \neq \text{length } Vs)) \ TA' \ e2' \ h' \ X'$ 
           $\text{bisim } (Vs @ [V']) \ e2' \ e' \ x' \ X' \subseteq_m [Vs @ [V'] \mapsto] \ x'$ 
          by(fastforce simp del: fun-upd-apply)
        { assume  $V' \in \text{set } Vs$ 
          hence hidden  $(Vs @ [V']) \ (index \ Vs \ V')$  by(rule hidden-index)
          with  $\langle \text{bisim } (Vs @ [V']) \ E \ e \ (lcl \ (h, x)) \rangle$  have unmod  $e \ (index \ Vs \ V')$ 
            by(auto intro: hidden-bisim-unmod)
          moreover from  $\langle \text{length } Vs + \text{max-vars } \{V:T=None; e\} \leq \text{length } (lcl \ (h, x)) \rangle$   $\langle V' \in \text{set } Vs \rangle$ 
            have  $index \ Vs \ V' < \text{length } x$  by(auto intro: index-less-aux)
          ultimately have  $x \neq index \ Vs \ V' = x' \neq index \ Vs \ V'$ 
            using red1-preserves-unmod[OF  $\langle \text{False}, \text{compP1 } P, t \vdash 1 \ \langle e, (h, x) \rangle \rightarrow \langle e', (h', x') \rangle \rangle$ ]
            by(simp) }
        }

```

**with**  $\langle \text{length } Vs + \text{max-vars } \{V:T=\text{None}; e\} \leq \text{length } (\text{lcl } (h, x)) \rangle$   
 $\langle X' \subseteq_m [Vs @ [V'] \mapsto x'] \rangle \langle \text{length } x = \text{length } x' \rangle \langle X \subseteq_m [Vs \mapsto] \text{lcl } (h, x) \rangle$   
**have**  $\text{rel}: X'(V' := X V') \subseteq_m [Vs \mapsto] x'$  **by** (*auto intro: Block-lem*)  
**from**  $\langle \text{sim-move10 } P \text{ } t \text{ } ta \text{ } e \text{ } e' \text{ } E \text{ } h \text{ } (X(V' \mapsto x) ! \text{length } Vs) \rangle TA' \text{ } e2' \text{ } h' \text{ } X'$   
**obtain**  $v'$  **where**  $X' V' = \lfloor v' \rfloor$   
**by** (*auto simp: sim-move10-def split: if-split-asm dest!:  $\tau\text{red0t-lcl-incr } \tau\text{red0r-lcl-incr red-lcl-incr subsetD}$* )  
**with**  $\langle X' \subseteq_m [Vs @ [V'] \mapsto] x' \rangle \langle \text{length } Vs < \text{length } x' \rangle$   
**have**  $x' ! \text{length } Vs = v'$  **by** (*auto dest: map-le-SomeD*)  
**with** *BlockSomeNone IH' rel*  $\langle X' V' = \lfloor v' \rfloor \rangle$   
**show**  $?thesis$  **by** (*fastforce intro: BlockRed*)  
**qed**  
**next**  
**case** (*Block1Some V xs T v e h*)  
**from**  $\langle \text{bisim } vs \text{ } e2 \text{ } \{V:T=\lfloor v \rfloor; e\} (\text{lcl } (h, xs)) \rangle$  **obtain**  $e' V'$  **where**  $e2 = \{V':T=\lfloor v \rfloor; e'\}$   
**and**  $V = \text{length } vs \text{ bisim } (vs @ [V']) e' e (xs[\text{length } vs := v])$  **by** (*fastforce*)  
**moreover have** *sim-move10*  $P \text{ } t \in \{\text{length } vs:T=\lfloor v \rfloor; e\} \{\text{length } vs:T=\text{None}; e\} \{V':T=\lfloor v \rfloor; e'\}$   $h$   
 $x \in \{V':T=\lfloor v \rfloor; e'\} h \text{ } x$   
**by** (*auto*)  
**moreover from**  $\langle \text{bisim } (vs @ [V']) e' e (xs[\text{length } vs := v]) \rangle$   
 $\langle \text{length } vs + \text{max-vars } \{V:T=\lfloor v \rfloor; e\} \leq \text{length } (\text{lcl } (h, xs)) \rangle$   
**have**  $\text{bisim } vs \{V':T=\lfloor v \rfloor; e'\} \{\text{length } vs:T=\text{None}; e\} (xs[\text{length } vs := v])$  **by** *auto*  
**moreover from**  $\langle x \subseteq_m [vs \mapsto] \text{lcl } (h, xs) \rangle \langle \text{length } vs + \text{max-vars } \{V:T=\lfloor v \rfloor; e\} \leq \text{length } (\text{lcl } (h, xs)) \rangle$   
**have**  $x \subseteq_m [vs \mapsto] xs[\text{length } vs := v]$  **by** *auto*  
**ultimately show**  $?case$  **by** *auto*  
**next**  
**case** (*Lock1Synchronized V xs a e h Vs E2 X*)  
**note**  $\text{len} = \langle \text{length } Vs + \text{max-vars } (\text{sync}_V (\text{addr } a) \text{ } e) \leq \text{length } (\text{lcl } (h, xs)) \rangle$   
**from**  $\langle \text{bisim } Vs \text{ } E2 \text{ } (\text{sync}_V (\text{addr } a) \text{ } e) (\text{lcl } (h, xs)) \rangle$  **obtain**  $E$   
**where**  $E2: E2 = \text{sync}(\text{addr } a) \text{ } E \text{ } e = \text{compE1 } (Vs @ [\text{fresh-var } Vs]) \text{ } E$   
**and**  $\text{sync}: \neg \text{contains-insync } E$  **and**  $[\text{simp}]: V = \text{length } Vs$  **by** *auto*  
**moreover**  
**have** *extTA2J0*  $P, P, t \vdash \langle \text{sync}(\text{addr } a) \text{ } E, (h, X) \rangle - \{\text{Lock} \rightarrow a, \text{SyncLock } a\} \rightarrow \langle \text{insync}(a) \text{ } E, (h, X) \rangle$   
**by** (*rule LockSynchronized*)  
**moreover from**  $\langle \text{fv } E2 \subseteq \text{set } Vs \rangle \text{fresh-var-fresh}[of \text{ } Vs] \text{sync len}$   
**have**  $\text{bisim } Vs (\text{insync}(a) \text{ } E) (\text{insync}_{\text{length } Vs} (a) \text{ } e) (xs[\text{length } Vs := \text{Addr } a])$   
**unfolding**  $\langle e = \text{compE1 } (Vs @ [\text{fresh-var } Vs]) \text{ } E \rangle \langle E2 = \text{sync}(\text{addr } a) \text{ } E \rangle$   
**by**  $-(\text{rule bisimInSynchronized, rule compE1-bisim, auto})$   
**moreover have**  $\text{zip } Vs (xs[\text{length } Vs := \text{Addr } a]) = (\text{zip } Vs \text{ } xs)[\text{length } Vs := (\text{arbitrary}, \text{Addr } a)]$   
**by** (*rule sym*) (*simp add: update-zip*)  
**hence**  $\text{zip } Vs (xs[\text{length } Vs := \text{Addr } a]) = \text{zip } Vs \text{ } xs$  **by** *simp*  
**with**  $\langle X \subseteq_m [Vs \mapsto] (\text{lcl } (h, xs)) \rangle$  **have**  $X \subseteq_m [Vs \mapsto] xs[\text{length } Vs := \text{Addr } a]$   
**by** (*auto simp add: map-le-def map-upds-def*)  
**ultimately show**  $?case$  **by** (*fastforce intro: sim-move10-SyncLocks*)  
**next**  
**case** (*Synchronized1Red2 e s ta e' s' V a Vs E2 X*)  
**note**  $IH = \langle \bigwedge vs \text{ } e2 \text{ } x. \llbracket \text{bisim } vs \text{ } e2 \text{ } e (\text{lcl } s); \text{fv } e2 \subseteq \text{set } vs; x \subseteq_m [vs \mapsto] \text{lcl } s; \text{length } vs + \text{max-vars } e \leq \text{length } (\text{lcl } s); \mathcal{D} \text{ } e2 \text{ } [\text{dom } x] \rrbracket \implies ?concl \text{ } e \text{ } e' \text{ } e2 \text{ } s \text{ } x \text{ } ta \text{ } s' \text{ } e' \text{ } vs \rangle$   
**from**  $\langle \text{bisim } Vs \text{ } E2 \text{ } (\text{insync}_V (a) \text{ } e) (\text{lcl } s) \rangle$  **obtain**  $E$   
**where**  $E2: E2 = \text{insync}(a) \text{ } E$  **and**  $\text{bisim}: \text{bisim } (Vs @ [\text{fresh-var } Vs]) \text{ } E \text{ } e (\text{lcl } s)$   
**and**  $\text{xs}a: \text{lcl } s ! \text{length } Vs = \text{Addr } a$  **and**  $[\text{simp}]: V = \text{length } Vs$  **by** *auto*  
**with**  $\langle \text{fv } E2 \subseteq \text{set } Vs \rangle \text{fresh-var-fresh}[of \text{ } Vs]$  **have**  $\text{fv}: (\text{fresh-var } Vs) \notin \text{fv } E$  **by** *auto*

**from**  $\langle \text{length } Vs + \text{max-vars } (\text{insync}_V(a) \ e) \leq \text{length } (\text{lcl } s) \rangle$  **have**  $\text{length } Vs < \text{length } (\text{lcl } s)$  **by** *simp*  
**{** **assume**  $X (\text{fresh-var } Vs) \neq \text{None}$   
**then obtain**  $v$  **where**  $X (\text{fresh-var } Vs) = \lfloor v \rfloor$  **by** *auto*  
**with**  $\langle X \subseteq_m [Vs \mapsto] \text{lcl } s \rangle$  **have**  $[Vs \mapsto] \text{lcl } s (\text{fresh-var } Vs) = \lfloor v \rfloor$   
**by**(*auto simp add: map-le-def dest: bspec*)  
**hence**  $(\text{fresh-var } Vs) \in \text{set } Vs$   
**by**(*auto simp add: map-upds-def set-zip dest!: map-of-SomeD*)  
**moreover have**  $(\text{fresh-var } Vs) \notin \text{set } Vs$  **by**(*rule fresh-var-fresh*)  
**ultimately have** *False* **by** *contradiction* **}**  
**hence**  $X (\text{fresh-var } Vs) = \text{None}$  **by**(*cases X (fresh-var Vs), auto*)  
**hence**  $X(\text{fresh-var } Vs := \text{None}) = X$  **by**(*auto intro: ext*)  
**moreover from**  $\langle X \subseteq_m [Vs \mapsto] \text{lcl } s \rangle$   
**have**  $X(\text{fresh-var } Vs := \text{None}) \subseteq_m [Vs \mapsto] \text{lcl } s, (\text{fresh-var } Vs) \mapsto (\text{lcl } s) ! \text{length } Vs$  **by**(*simp*)  
**ultimately have**  $X \subseteq_m [Vs @ [\text{fresh-var } Vs] \mapsto] \text{lcl } s$   
**using**  $\langle \text{length } Vs < \text{length } (\text{lcl } s) \rangle$  **by**(*auto*)  
**moreover note**  $IH[\text{of } Vs @ [\text{fresh-var } Vs] \ E \ X] \text{ bisim } E2 \ \langle \text{fv } E2 \subseteq \text{set } Vs \rangle \ \langle X \subseteq_m [Vs \mapsto] \text{lcl } s \rangle$   
 $\langle \text{length } Vs + \text{max-vars } (\text{insync}_V(a) \ e) \leq \text{length } (\text{lcl } s) \rangle \ \langle \mathcal{D} \ E2 \ \lfloor \text{dom } X \rfloor \rangle$   
**ultimately obtain**  $TA' \ e2' \ x'$  **where**  $IH': \text{sim-move10 } P \ t \ ta \ e \ e' \ E \ (hp \ s) \ X \ TA' \ e2' \ (hp \ s') \ x'$   
 $\text{bisim } (Vs @ [\text{fresh-var } Vs]) \ e2' \ e' \ (\text{lcl } s') \ x' \subseteq_m [Vs @ [\text{fresh-var } Vs] \mapsto] \text{lcl } s'$  **by** *auto*  
**hence**  $\text{dom } x' \subseteq \text{dom } X \cup \text{fv } E$   
**by**(*fastforce iff del: domIff simp add: sim-move10-def dest: red-dom-lcl  $\tau$  red0r-dom-lcl [OF wf-prog-wwf-prog [OF wf]]  $\tau$  red0t-dom-lcl [OF wf-prog-wwf-prog [OF wf]]  $\tau$  red0r-fv-subset [OF wf-prog-wwf-prog [OF wf]] split: if-split-asm*)  
**with**  $\text{fv } \langle X (\text{fresh-var } Vs) = \text{None} \rangle$  **have**  $(\text{fresh-var } Vs) \notin \text{dom } x'$  **by** *auto*  
**hence**  $x' (\text{fresh-var } Vs) = \text{None}$  **by** *auto*  
**moreover from**  $\langle \text{False}, \text{compP1 } P, t \vdash 1 \ \langle e, s \rangle - ta \rightarrow \langle e', s' \rangle \rangle$   
**have**  $\text{length } (\text{lcl } s) = \text{length } (\text{lcl } s')$  **by**(*auto dest: red1-preserves-len*)  
**moreover note**  $\langle x' \subseteq_m [Vs @ [\text{fresh-var } Vs] \mapsto] \text{lcl } s' \rangle \ \langle \text{length } Vs < \text{length } (\text{lcl } s) \rangle$   
**ultimately have**  $x' \subseteq_m [Vs \mapsto] \text{lcl } s'$  **by**(*auto simp add: map-le-def dest: bspec*)  
**moreover from** *bisim fv* **have** *unmod e*  $(\text{length } Vs)$  **by**(*auto intro: bisim-fv-unmod*)  
**with**  $\langle \text{False}, \text{compP1 } P, t \vdash 1 \ \langle e, s \rangle - ta \rightarrow \langle e', s' \rangle \rangle \ \langle \text{length } Vs < \text{length } (\text{lcl } s) \rangle$   
**have**  $\text{lcl } s ! \text{length } Vs = \text{lcl } s' ! \text{length } Vs$   
**by**(*auto dest!: red1-preserves-unmod*)  
**with** *xsa* **have**  $\text{lcl } s' ! \text{length } Vs = \text{Addr } a$  **by** *simp*  
**ultimately show** *?case* **using**  $IH' \ E2$  **by**(*auto intro: SynchronizedRed2*)  
**next**  
**case** (*Unlock1Synchronized xs V a' a v h Vs E2 X*)  
**from**  $\langle \text{bisim } Vs \ E2 \ (\text{insync}_V(a) \ \text{Val } v) \ (\text{lcl } (h, xs)) \rangle$   
**have**  $E2: E2 = \text{insync}(a) \ \text{Val } v \ V = \text{length } Vs \ xs ! \text{length } Vs = \text{Addr } a$  **by** *auto*  
**moreover with**  $\langle xs ! V = \text{Addr } a' \rangle$  **have**  $[simp]: a' = a$  **by** *simp*  
**have**  $\text{extTA2J0 } P, P, t \vdash \langle \text{insync}(a) \ (\text{Val } v), (h, X) \rangle - \llbracket \text{Unlock} \rightarrow a, \text{SyncUnlock } a \rrbracket \rightarrow \langle \text{Val } v, (h, X) \rangle$   
**by**(*rule UnlockSynchronized*)  
**ultimately show** *?case* **using**  $\langle X \subseteq_m [Vs \mapsto] \text{lcl } (h, xs) \rangle$  **by**(*fastforce intro: sim-move10-SyncLocks*)  
**next**  
**case** (*Unlock1SynchronizedNull xs V a v h Vs E2 X*)  
**from**  $\langle \text{bisim } Vs \ E2 \ (\text{insync}_V(a) \ \text{Val } v) \ (\text{lcl } (h, xs)) \rangle$   
**have**  $V = \text{length } Vs \ xs ! \text{length } Vs = \text{Addr } a$  **by**(*auto*)  
**with**  $\langle xs ! V = \text{Null} \rangle$  **have** *False* **by** *simp*  
**thus** *?case* **..**  
**next**  
**case** (*Unlock1SynchronizedFail xs V A' a' v h Vs E2 X*)  
**from**  $\langle \text{False} \rangle$  **show** *?case* **..**  
**next**



**case** (*Red1While*  $b\ c\ s\ Vs\ E2\ X$ )  
**from**  $\langle bisim\ Vs\ E2\ (while\ (b)\ c)\ (lcl\ s) \rangle$  **obtain**  $B\ C$   
**where**  $E2: E2 = while\ (B)\ C\ b = compE1\ Vs\ B\ c = compE1\ Vs\ C$   
**and**  $sync: \neg\ contains-insync\ B\ \neg\ contains-insync\ C$  **by** *auto*  
**moreover have**  $extTA2J0\ P, P, t \vdash \langle while\ (B)\ C,\ (hp\ s,\ X) \rangle \rightarrow \langle if\ (B)\ (C;; while\ (B)\ C)\ else\ unit,\ (hp\ s,\ X) \rangle$   
**by**(*rule RedWhile*)  
**hence**  $sim-move10\ P\ t \in (while\ (compE1\ Vs\ B)\ (compE1\ Vs\ C))\ (if\ (compE1\ Vs\ B)\ (compE1\ Vs\ C;; while\ (compE1\ Vs\ B)\ (compE1\ Vs\ C))\ else\ unit)\ (while\ (B)\ C)\ (hp\ s)\ X \in (if\ (B)\ (C;; while\ (B)\ C)\ else\ unit)\ (hp\ s)\ X$   
**by**(*auto simp add: sim-move10-def*)  
**moreover from**  $\langle fv\ E2 \subseteq set\ Vs \rangle\ E2\ sync$   
**have**  $bisim\ Vs\ (if\ (B)\ (C;; while\ (B)\ C)\ else\ unit)\ (if\ (compE1\ Vs\ B)\ (compE1\ Vs\ (C;; while\ (B)\ C))\ else\ (compE1\ Vs\ unit))\ (lcl\ s)$   
**by**  $-(rule\ bisimCond,\ auto)$   
**ultimately show**  $?case\ using\ \langle X \subseteq_m [Vs\ \mapsto]\ lcl\ s \rangle$   
**by**(*simp*)(*rule exI, rule exI, rule exI, erule conjI, auto*)  
**next**  
**case** (*Red1TryCatch*  $h\ a\ D\ C\ V\ x\ e2\ Vs\ E2\ X$ )  
**from**  $\langle bisim\ Vs\ E2\ (try\ Throw\ a\ catch(C\ V)\ e2)\ (lcl\ (h,\ x)) \rangle$   
**obtain**  $E2'\ V'$  **where**  $E2 = try\ Throw\ a\ catch(C\ V')\ E2'\ V = length\ Vs\ e2 = compE1\ (Vs\ @\ [V'])\ E2'$   
**and**  $sync: \neg\ contains-insync\ E2'$  **by**(*auto*)  
**with**  $\langle fv\ E2 \subseteq set\ Vs \rangle$  **have**  $fv\ E2' \subseteq set\ (Vs\ @\ [V'])$  **by** *auto*  
**with**  $\langle e2 = compE1\ (Vs\ @\ [V'])\ E2' \rangle$   $sync$  **have**  $bisim\ (Vs\ @\ [V'])\ E2'\ e2\ (x[V := Addr\ a])$   
**by**(*auto intro!: compE1-bisim*)  
**with**  $\langle V = length\ Vs \rangle\ \langle length\ Vs + max-vars\ (try\ Throw\ a\ catch(C\ V)\ e2) \leq length\ (lcl\ (h,\ x)) \rangle$   
**have**  $bisim\ Vs\ \{V':Class\ C=[Addr\ a];\ E2'\}\ \{length\ Vs:Class\ C=None;\ e2'\}\ (x[V := Addr\ a])$   
**by**(*auto intro: bisimBlockSomeNone*)  
**moreover from**  $\langle length\ Vs + max-vars\ (try\ Throw\ a\ catch(C\ V)\ e2) \leq length\ (lcl\ (h,\ x)) \rangle$   
**have**  $[Vs\ \mapsto]\ x[length\ Vs := Addr\ a] = [Vs\ \mapsto]\ x$  **by** *simp*  
**with**  $\langle X \subseteq_m [Vs\ \mapsto]\ lcl\ (h,\ x) \rangle$  **have**  $X \subseteq_m [Vs\ \mapsto]\ x[length\ Vs := Addr\ a]$  **by** *simp*  
**moreover note**  $\langle e2 = compE1\ (Vs\ @\ [V'])\ E2' \rangle\ \langle E2 = try\ Throw\ a\ catch(C\ V')\ E2' \rangle$   
 $\langle typeof-addr\ h\ a = [Class-type\ D] \rangle\ \langle compP1\ P \vdash D \preceq^* C \rangle\ \langle V = length\ Vs \rangle$   
**ultimately show**  $?case\ by\ (auto\ intro!: exI)$   
**next**  
**case** *Red1TryFail* **thus**  $?case\ by\ (auto\ intro!: exI\ sim-move10-TryFail)$   
**next**  
**case** (*List1Red1*  $e\ s\ ta\ e'\ s'\ es\ Vs\ ES2\ X$ )  
**note**  $IH = \langle \bigwedge vs\ e2\ x. \llbracket bisim\ vs\ e2\ e\ (lcl\ s); fv\ e2 \subseteq set\ vs; x \subseteq_m [vs\ \mapsto]\ lcl\ s; length\ vs + max-vars\ e \leq length\ (lcl\ s); \mathcal{D}\ e2\ [dom\ x] \rrbracket \implies \exists TA'\ e2'\ x'. sim-move10\ P\ t\ ta\ e\ e'\ e2\ (hp\ s)\ x\ TA'\ e2'\ (hp\ s')\ x' \wedge bisim\ vs\ e2'\ e'\ (lcl\ s') \wedge x' \subseteq_m [vs\ \mapsto]\ lcl\ s' \rangle$   
**from**  $\langle bisims\ Vs\ ES2\ (e\ \# es)\ (lcl\ s) \rangle\ \langle False, compP1\ P, t \vdash 1\ \langle e, s \rangle - ta \rightarrow \langle e', s' \rangle \rangle$   
**obtain**  $E\ ES$  **where**  $ES2 = E\ \# ES\ \neg\ is-val\ E\ es = compEs1\ Vs\ ES\ bisim\ Vs\ E\ e\ (lcl\ s)$   
**and**  $sync: \neg\ contains-insyncs\ ES$  **by**(*auto elim!: bisims-cases simp add: compEs1-conv-map*)  
**with**  $IH[of\ Vs\ E\ X]\ \langle fvs\ ES2 \subseteq set\ Vs \rangle\ \langle X \subseteq_m [Vs\ \mapsto]\ lcl\ s \rangle$   
 $\langle length\ Vs + max-varss\ (e\ \# es) \leq length\ (lcl\ s) \rangle\ \langle \mathcal{D}s\ ES2\ [dom\ X] \rangle$   
**obtain**  $TA'\ e2'\ x'$  **where**  $IH': sim-move10\ P\ t\ ta\ e\ e'\ E\ (hp\ s)\ X\ TA'\ e2'\ (hp\ s')\ x'$   
 $bisim\ Vs\ e2'\ e'\ (lcl\ s')\ x' \subseteq_m [Vs\ \mapsto]\ lcl\ s'$  **by** *fastforce*  
**show**  $?case$   
**proof**(*cases is-val e2'*)  
**case** *False*  
**with**  $IH'\ \langle ES2 = E\ \# ES \rangle\ \langle es = compEs1\ Vs\ ES \rangle\ sync$  **show**  $?thesis\ by\ (auto\ intro: sim-moves10-expr)$

```

next
  case True
    from  $\langle fvs\ ES2 \subseteq set\ Vs \rangle \langle ES2 = E \# ES \rangle \langle es = compEs1\ Vs\ ES \rangle sync$ 
    have  $bisims\ Vs\ ES\ es\ (lcl\ s')$  by(auto intro: compEs1-bisims)
    with  $IH'\ True\ \langle ES2 = E \# ES \rangle \langle es = compEs1\ Vs\ ES \rangle$  show ?thesis by(auto intro: sim-moves10-expr)
  qed
next
  case (List1Red2 es s ta es' s' v Vs ES2 X)
  note  $IH = \langle \bigwedge vs\ es2\ x. [\![bisims\ vs\ es2\ es\ (lcl\ s); fvs\ es2 \subseteq set\ vs;$ 
     $x \subseteq_m [vs \mapsto] lcl\ s]; length\ vs + max-varss\ es \leq length\ (lcl\ s); \mathcal{D}s\ es2 \downarrow dom\ x]\!\!]$ 
     $\implies \exists TA'\ es2'\ x'. sim-moves10\ P\ t\ ta\ es\ es'\ es2\ (hp\ s)\ x\ TA'\ es2'\ (hp\ s')\ x' \wedge bisims\ vs\ es2'\$ 
 $es'\ (lcl\ s') \wedge x' \subseteq_m [vs \mapsto] lcl\ s'] \rangle$ 
  from  $\langle bisims\ Vs\ ES2\ (Val\ v \# es)\ (lcl\ s) \rangle$  obtain  $ES$  where  $ES2 = Val\ v \# ES\ bisims\ Vs\ ES\ es$ 
 $(lcl\ s)$ 
  by(auto elim!: bisims-cases)
  with  $IH[of\ Vs\ ES\ X]\ \langle fvs\ ES2 \subseteq set\ Vs \rangle \langle X \subseteq_m [Vs \mapsto] lcl\ s \rangle$ 
   $\langle length\ Vs + max-varss\ (Val\ v \# es) \leq length\ (lcl\ s) \rangle \langle \mathcal{D}s\ ES2 \downarrow dom\ X \rangle$ 
   $\langle ES2 = Val\ v \# ES \rangle$  show ?case by(fastforce intro: sim-moves10-expr)
next
  case Call1ThrowParams
  thus ?case by(fastforce intro: CallThrowParams elim!: bisim-cases simp add: bisims-map-Val-Throw2)
next
  case (Synchronized1Throw2 xs V a' a ad h Vs E2 X)
  from  $\langle bisim\ Vs\ E2\ (insync_V\ (a)\ Throw\ ad)\ (lcl\ (h, xs)) \rangle$ 
  have  $xs ! length\ Vs = Addr\ a$  and  $V = length\ Vs$  by auto
  with  $\langle xs ! V = Addr\ a' \rangle$  have [simp]:  $a' = a$  by simp
  have  $extTA2J0\ P, P, t \vdash \langle insync(a)\ Throw\ ad, (h, X) \rangle \dashv\!\{Unlock \rightarrow a, SyncUnlock\ a\} \rightarrow \langle Throw\ ad,$ 
 $(h, X) \rangle$ 
  by(rule SynchronizedThrow2)
  with  $\langle X \subseteq_m [Vs \mapsto] lcl\ (h, xs) \rangle \langle bisim\ Vs\ E2\ (insync_V\ (a)\ Throw\ ad)\ (lcl\ (h, xs)) \rangle$ 
  show ?case by(auto intro: sim-move10-SyncLocks intro!: exI)
next
  case (Synchronized1Throw2Null xs V a a' h Vs E2 X)
  from  $\langle bisim\ Vs\ E2\ (insync_V\ (a)\ Throw\ a')\ (lcl\ (h, xs)) \rangle$ 
  have  $V = length\ Vs\ xs ! length\ Vs = Addr\ a$  by(auto)
  with  $\langle xs ! V = Null \rangle$  have False by simp
  thus ?case ..
next
  case (Synchronized1Throw2Fail xs V A' a' a h Vs E2 X)
  from  $\langle False \rangle$  show ?case ..
next
  case InstanceOf1Red thus ?case by auto(blast)
next
  case Red1InstanceOf thus ?case by hypsubst-thin auto
next
  case InstanceOf1Throw thus ?case by auto
next
  case CAS1Throw thus ?case by fastforce
next
  case CAS1Throw2 thus ?case by fastforce
next
  case CAS1Throw3 thus ?case by fastforce
qed(simp-all del: fun-upd-apply, (fastforce intro: red-reds.intros simp del: fun-upd-apply simp add:
finfun-upd-apply)+)

```

**lemma** *bisim-call-Some-call1*:

$\llbracket \text{bisim } Vs \ e \ e' \ xs; \text{call } e = \lfloor aMvs \rfloor; \text{length } Vs + \text{max-vars } e' \leq \text{length } xs \rrbracket$   
 $\implies \exists e'' \ xs'. \tau\text{red1}'r \ P \ t \ h \ (e', \ xs) \ (e'', \ xs') \wedge \text{call1 } e'' = \lfloor aMvs \rfloor \wedge$   
 $\text{bisim } Vs \ e \ e'' \ xs' \wedge \text{take } (\text{length } Vs) \ xs = \text{take } (\text{length } Vs) \ xs'$

**and** *bisims-calls-Some-calls1*:

$\llbracket \text{bisims } Vs \ es \ es' \ xs; \text{calls } es = \lfloor aMvs \rfloor; \text{length } Vs + \text{max-varss } es' \leq \text{length } xs \rrbracket$   
 $\implies \exists es'' \ xs'. \tau\text{reds1}'r \ P \ t \ h \ (es', \ xs) \ (es'', \ xs') \wedge \text{calls1 } es'' = \lfloor aMvs \rfloor \wedge$   
 $\text{bisims } Vs \ es \ es'' \ xs' \wedge \text{take } (\text{length } Vs) \ xs = \text{take } (\text{length } Vs) \ xs'$

**proof**(*induct rule: bisim-bisims.inducts*)

**case** *bisimCallParams* **thus** ?case

**by**(*fastforce simp add: is-vals-conv split: if-split-asm*)

**next**

**case** *bisimBlockNone* **thus** ?case **by**(*fastforce intro: take-eq-take-le-eq*)

**next**

**case** (*bisimBlockSome* *Vs V e e' xs v T*)

**from**  $\langle \text{length } Vs + \text{max-vars } \{ \text{length } Vs: T = \lfloor v \rfloor; e' \} \leq \text{length } xs \rangle$

**have**  $\tau\text{red1}'r \ P \ t \ h \ (\{ \text{length } Vs: T = \lfloor v \rfloor; e' \}, \ xs) \ (\{ \text{length } Vs: T = \text{None}; e' \}, \ xs[\text{length } Vs := v])$

**by**(*auto intro!:  $\tau\text{red1r-1step Block1Some}$* )

**also from** *bisimBlockSome* **obtain**  $e'' \ xs'$

**where**  $\tau\text{red1}'r \ P \ t \ h \ (e', \ xs[\text{length } Vs := v]) \ (e'', \ xs')$

**and**  $\text{call1 } e'' = \lfloor aMvs \rfloor \text{ bisim } (Vs @ [V]) \ e \ e'' \ xs'$

**and**  $\text{take } (\text{length } (Vs @ [V])) \ (xs[\text{length } Vs := v]) = \text{take } (\text{length } (Vs @ [V])) \ xs' \text{ by auto}$

**hence**  $\tau\text{red1}'r \ P \ t \ h \ (\{ \text{length } Vs: T = \text{None}; e' \}, \ xs[\text{length } Vs := v]) \ (\{ \text{length } Vs: T = \text{None}; e'' \}, \ xs')$

**by** *auto*

**also from**  $\langle \text{call1 } e'' = \lfloor aMvs \rfloor \rangle$  **have**  $\text{call1 } \{ \text{length } Vs: T = \text{None}; e'' \} = \lfloor aMvs \rfloor$  **by** *simp*

**moreover from**  $\langle \text{take } (\text{length } (Vs @ [V])) \ (xs[\text{length } Vs := v]) = \text{take } (\text{length } (Vs @ [V])) \ xs' \rangle$

**have**  $\text{take } (\text{length } Vs) \ xs = \text{take } (\text{length } Vs) \ xs'$

**by**(*auto dest: take-eq-take-le-eq[where m=length Vs] simp add: take-update-cancel*)

**moreover** {

**have**  $xs' ! \text{length } Vs = \text{take } (\text{length } (Vs @ [V])) \ xs' ! \text{length } Vs$  **by** *simp*

**also note**  $\langle \text{take } (\text{length } (Vs @ [V])) \ (xs[\text{length } Vs := v]) = \text{take } (\text{length } (Vs @ [V])) \ xs' \rangle$  *[symmetric]*

**also have**  $\text{take } (\text{length } (Vs @ [V])) \ (xs[\text{length } Vs := v]) ! \text{length } Vs = v$

**using**  $\langle \text{length } Vs + \text{max-vars } \{ \text{length } Vs: T = \lfloor v \rfloor; e' \} \leq \text{length } xs \rangle$  **by** *simp*

**finally have**  $\text{bisim } Vs \ \{ V: T = \lfloor v \rfloor; e \} \ \{ \text{length } Vs: T = \text{None}; e'' \} \ xs'$

**using**  $\langle \text{bisim } (Vs @ [V]) \ e \ e'' \ xs' \rangle$  **by** *auto* }

**ultimately show** ?case **by** *blast*

**next**

**case** (*bisimBlockSomeNone* *Vs V e e' xs v T*)

**then obtain**  $e'' \ xs'$  **where**  $\tau\text{red1}'r \ P \ t \ h \ (e', \ xs) \ (e'', \ xs') \ \text{call1 } e'' = \lfloor aMvs \rfloor \text{ bisim } (Vs @ [V]) \ e \ e''$   
 $xs'$

**and**  $\text{take } (\text{length } (Vs @ [V])) \ xs = \text{take } (\text{length } (Vs @ [V])) \ xs' \text{ by auto}$

**hence**  $\tau\text{red1}'r \ P \ t \ h \ (\{ \text{length } Vs: T = \text{None}; e' \}, \ xs) \ (\{ \text{length } Vs: T = \text{None}; e'' \}, \ xs') \text{ by auto}$

**moreover from**  $\langle \text{call1 } e'' = \lfloor aMvs \rfloor \rangle$  **have**  $\text{call1 } \{ \text{length } Vs: T = \text{None}; e'' \} = \lfloor aMvs \rfloor$  **by** *simp*

**moreover from**  $\langle \text{take } (\text{length } (Vs @ [V])) \ xs = \text{take } (\text{length } (Vs @ [V])) \ xs' \rangle$

**have**  $\text{take } (\text{length } Vs) \ xs = \text{take } (\text{length } Vs) \ xs' \text{ by (auto intro: take-eq-take-le-eq)}$

**moreover** {

**have**  $xs' ! \text{length } Vs = \text{take } (\text{length } (Vs @ [V])) \ xs' ! \text{length } Vs$  **by** *simp*

**also note**  $\langle \text{take } (\text{length } (Vs @ [V])) \ xs = \text{take } (\text{length } (Vs @ [V])) \ xs' \rangle$  *[symmetric]*

**also have**  $\text{take } (\text{length } (Vs @ [V])) \ xs ! \text{length } Vs = v$  **using**  $\langle xs ! \text{length } Vs = v \rangle$  **by** *simp*

**finally have**  $\text{bisim } Vs \ \{ V: T = \lfloor v \rfloor; e \} \ \{ \text{length } Vs: T = \text{None}; e'' \} \ xs'$

**using**  $\langle \text{bisim } (Vs @ [V]) \ e \ e'' \ xs' \rangle$  **by** *auto* }

**ultimately show** ?case **by** *blast*

**next**

**case** (*bisimInSynchronized*  $Vs\ e\ e'\ xs\ a$ )  
**then obtain**  $e''\ xs'$  **where**  $\tau red1\ 'r\ P\ t\ h\ (e',\ xs)\ (e'',\ xs')\ call1\ e'' = \lfloor aMvs \rfloor\ bisim\ (Vs\ @\ [fresh-var\ Vs])\ e\ e''\ xs'$   
**and**  $take\ (Suc\ (length\ Vs))\ xs = take\ (Suc\ (length\ Vs))\ xs'$  **by** *auto*  
**hence**  $\tau red1\ 'r\ P\ t\ h\ (insync_{length\ Vs}\ (a)\ e',\ xs)\ (insync_{length\ Vs}\ (a)\ e'',\ xs')$  **by** *auto*  
**moreover from**  $\langle call1\ e'' = \lfloor aMvs \rfloor \rangle$  **have**  $call1\ (insync_{length\ Vs}\ (a)\ e'') = \lfloor aMvs \rfloor$  **by** *simp*  
**moreover from**  $\langle take\ (Suc\ (length\ Vs))\ xs = take\ (Suc\ (length\ Vs))\ xs' \rangle$   
**have**  $take\ (length\ Vs)\ xs = take\ (length\ Vs)\ xs'$  **by** (*auto intro: take-eq-take-le-eq*)  
**moreover** {  
**have**  $xs'!\ length\ Vs = take\ (Suc\ (length\ Vs))\ xs'!\ length\ Vs$  **by** *simp*  
**also note**  $\langle take\ (Suc\ (length\ Vs))\ xs = take\ (Suc\ (length\ Vs))\ xs' \rangle [symmetric]$   
**also have**  $take\ (Suc\ (length\ Vs))\ xs!\ length\ Vs = Addr\ a$   
**using**  $\langle xs!\ length\ Vs = Addr\ a \rangle$  **by** *simp*  
**finally have**  $bisim\ Vs\ (insync(a)\ e)\ (insync_{length\ Vs}\ (a)\ e'')\ xs'$   
**using**  $\langle bisim\ (Vs\ @\ [fresh-var\ Vs])\ e\ e''\ xs' \rangle$  **by** *auto* }  
**ultimately show** *?case* **by** *blast*  
**next**  
**case** *bisimsCons1* **thus** *?case* **by** (*fastforce intro!:  $\tau red1r-inj-\tau reds1r$* )  
**next**  
**case** *bisimsCons2* **thus** *?case* **by** (*fastforce intro!:  $\tau reds1r-cons-\tau reds1r$* )  
**qed** *fastforce+*

**lemma** *sim-move01-into-Red1*:

*sim-move01*  $P\ t\ ta\ e\ E'\ h\ xs\ ta'\ e2'\ h'\ xs'$   
 $\implies$  *if*  $\tau Move0\ P\ h\ (e,\ es1)$   
**then**  $\tau Red1\ 't\ P\ t\ h\ ((E',\ xs),\ exs2)\ ((e2',\ xs'),\ exs2) \wedge ta = \varepsilon \wedge h = h'$   
**else**  $\exists ex2'\ exs2'\ ta'. \tau Red1\ 'r\ P\ t\ h\ ((E',\ xs),\ exs2)\ (ex2',\ exs2') \wedge$   
 $(call\ e = None \vee call1\ E' = None \vee ex2' = (E',\ xs) \wedge exs2' = exs2) \wedge$   
 $False, P, t \vdash 1\ \langle ex2'/exs2', h \rangle -ta' \rightarrow \langle (e2',\ xs')/exs2, h' \rangle \wedge$   
 $\neg \tau Move1\ P\ h\ (ex2',\ exs2') \wedge ta-bisim01\ ta\ ta'$

**apply**(*auto simp add: sim-move01-def intro:  $\tau red1t-into-\tau Red1t\ \tau red1r-into-\tau Red1r\ red1Red\ split: if-split-asm$* )  
**apply**(*fastforce intro: red1Red intro!:  $\tau red1r-into-\tau Red1r$* )  
**done**

**lemma** *sim-move01-max-vars-decr*:

*sim-move01*  $P\ t\ ta\ e0\ e\ h\ xs\ ta'\ e'\ h'\ xs' \implies max-vars\ e' \leq max-vars\ e$

**by**(*fastforce simp add: sim-move01-def split: if-split-asm dest:  $\tau red1t-max-vars\ red1-max-vars-decr\ \tau red1r-max-vars$* )

**lemma** *Red1-simulates-red0*:

**assumes** *wf: wf-J-prog P*

**and** *red:  $P, t \vdash 0\ \langle e1/es1, h \rangle -ta \rightarrow \langle e1'/es1', h' \rangle$*

**and** *bisiml: bisim-list1 (e1, es1) (ex2, exs2)*

**shows**  $\exists ex2''\ exs2''. bisim-list1\ (e1',\ es1')\ (ex2'',\ exs2'') \wedge$

(*if*  $\tau Move0\ P\ h\ (e1,\ es1)$

**then**  $\tau Red1\ 't\ (compP1\ P)\ t\ h\ (ex2,\ exs2)\ (ex2'',\ exs2'') \wedge ta = \varepsilon \wedge h = h'$

**else**  $\exists ex2'\ exs2'\ ta'. \tau Red1\ 'r\ (compP1\ P)\ t\ h\ (ex2,\ exs2)\ (ex2',\ exs2') \wedge$

$(call\ e1 = None \vee call1\ (fst\ ex2) = None \vee ex2' = ex2 \wedge exs2' = exs2) \wedge$

$False, compP1\ P, t \vdash 1\ \langle ex2'/exs2', h \rangle -ta' \rightarrow \langle ex2''/exs2'', h' \rangle \wedge$

$\neg \tau Move1\ P\ h\ (ex2',\ exs2') \wedge ta-bisim01\ ta\ ta'$ )

(**is**  $\exists ex2''\ exs2'' . - \wedge ?red\ ex2''\ exs2''$ )

**using** *red*

```

proof(cases)
  case (red0Red XS')
  note [simp] = ⟨es1' = es1⟩
    and red = ⟨extTA2J0 P,P,t ⊢ ⟨e1,(h, Map.empty)⟩ -ta→ ⟨e1',(h', XS')⟩⟩
    and notsynth = ⟨∀ aMvs. call e1 = [aMvs] ⟶ synthesized-call P h aMvs⟩
from bisiml obtain E xs where ex2: ex2 = (E, xs)
  and bisim: bisim [] e1 E xs and fv: fv e1 = {}
  and length: max-vars E ≤ length xs and bsl: bisim-list es1 exs2
  and D: D e1 [{}]  

by(auto elim!: bisim-list1-elim)
from bisim-max-vars[OF bisim] length red1-simulates-red-aux[OF wf red bisim] fv notsynth
obtain ta' e2' xs' where sim: sim-move01 (compP1 P) t ta e1 E h xs ta' e2' h' xs'
  and bisim': bisim [] e1' e2' xs' and XS': XS' ⊆m Map.empty by auto
from sim-move01-into-Red1[OF sim, of es1 exs2]
have ?red (e2', xs') exs2 unfolding ex2 by auto
moreover {
  note bsl bisim' moreover
  from fv red-fv-subset[OF wf-prog-wwf-prog[OF wf] red]
  have fv e1' = {} by simp
  moreover from red D have D e1' [dom XS']
    by(auto dest: red-preserves-defass[OF wf] split: if-split-asm)
  with red-dom-lcl[OF red] ⟨fv e1 = {}⟩ have D e1' [{}]  

    by(auto elim!: D-mono' simp add: hyperset-defs)
  moreover from sim have length xs = length xs' max-vars e2' ≤ max-vars E
    by(auto dest: sim-move01-preserves-len sim-move01-max-vars-decr)
  with length have length': max-vars e2' ≤ length xs' by(auto)
  ultimately have bisim-list1 (e1', es1) ((e2', xs'), exs2) by(rule bisim-list1I) }
  ultimately show ?thesis using ex2 by(simp split del: if-split)(rule exI conjI|assumption)+
next
case (red0Call a M vs U Ts T pns body D)
note [simp] = ⟨ta = ε⟩ ⟨h' = h⟩
  and es1' = ⟨es1' = e1 # es1⟩
  and e1' = ⟨e1' = blocks (this # pns) (Class D # Ts) (Addr a # vs) body⟩
  and call = ⟨call e1 = [(a, M, vs)]⟩
  and ha = ⟨typeof-addr h a = [U]⟩
  and sees = ⟨P ⊢ class-type-of U sees M: Ts→T = [(pns, body)] in D⟩
  and len = ⟨length vs = length pns⟩ ⟨length Ts = length pns⟩
from bisiml obtain E xs where ex2: ex2 = (E, xs)
  and bisim: bisim [] e1 E xs and fv: fv e1 = {}
  and length: max-vars E ≤ length xs and bsl: bisim-list es1 exs2
  and D: D e1 [{}]  

by(auto elim!: bisim-list1-elim)

from bisim-call-Some-call1[OF bisim call, of compP1 P t h] length
obtain e' xs' where red: τred1'r (compP1 P) t h (E, xs) (e', xs')
  and call1 e' = [(a, M, vs)] bisim [] e1 e' xs'
  and take 0 xs = take 0 xs' by auto

let ?e1' = blocks (this # pns) (Class D # Ts) (Addr a # vs) body
let ?e2' = blocks1 0 (Class D # Ts) (compE1 (this # pns) body)
let ?xs2' = Addr a # vs @ replicate (max-vars (compE1 (this # pns) body)) undefined-value
let ?exs2' = (e', xs') # exs2

have τRed1'r (compP1 P) t h ((E, xs), exs2) ((e', xs'), exs2)
  using red by(rule τred1r-into-τRed1r)
moreover {

```

```

note  $\langle \text{call1 } e' = \lfloor (a, M, vs) \rfloor \rangle$ 
moreover note ha
moreover have  $\text{compP1 } P \vdash \text{class-type-of } U \text{ sees } M:Ts \rightarrow T = \text{map-option } (\lambda(pns, \text{body}). \text{compE1 } (this \# pns) \text{ body}) \lfloor (pns, \text{body}) \rfloor \text{ in } D$ 
using sees unfolding compP1-def by (rule sees-method-compP)
hence  $\text{sees}': \text{compP1 } P \vdash \text{class-type-of } U \text{ sees } M:Ts \rightarrow T = \lfloor \text{compE1 } (this \# pns) \text{ body} \rfloor \text{ in } D$  by simp
moreover from len have  $\text{length } vs = \text{length } Ts$  by simp
ultimately have  $\text{False}, \text{compP1 } P, t \vdash 1 \langle (e', xs') / \text{exs2}, h \rangle -\varepsilon \rightarrow \langle (\text{?e2}', \text{?xs2}') / \text{?exs2}', h \rangle$  by (rule red1Call)
moreover have  $\tau \text{Move1 } P \ h \ ((e', xs'), \text{exs2})$  using  $\langle \text{call1 } e' = \lfloor (a, M, vs) \rfloor \rangle$  ha sees
by (auto simp add: synthesized-call-def  $\tau$ external'-def dest: sees-method-fun dest!:  $\tau \text{move1-not-call1}$  [where  $P=P$  and  $h=h$ ])
ultimately have  $\tau \text{Red1}' (\text{compP1 } P) \ t \ h \ ((e', xs'), \text{exs2}) ((\text{?e2}', \text{?xs2}'), \text{?exs2}')$  by auto }
ultimately have  $\tau \text{Red1}' t (\text{compP1 } P) \ t \ h \ ((E, xs), \text{exs2}) ((\text{?e2}', \text{?xs2}'), \text{?exs2}')$  by (rule rtran-clp-into-tranclp1)
moreover
{ from red have  $\text{length } xs' = \text{length } xs$  by (rule  $\tau \text{red1r-preserves-len}$ )
moreover from red have  $\text{max-vars } e' \leq \text{max-vars } E$  by (rule  $\tau \text{red1r-max-vars}$ )
ultimately have  $\text{max-vars } e' \leq \text{length } xs'$  using length by simp }
with bsl  $\langle \text{bisim } [] \ e1 \ e' \ xs' \rangle \text{ fv } D$  have bisim-list ( $e1 \# es1$ )  $\text{?exs2}'$ 
using  $\langle \text{call } e1 = \lfloor (a, M, vs) \rfloor \rangle \langle \text{call1 } e' = \lfloor (a, M, vs) \rfloor \rangle$  by (rule bisim-listCons)
hence bisim-list1 ( $e1', es1'$ ) ( $(\text{?e2}', \text{?xs2}'), \text{?exs2}'$ )
unfolding  $e1' \ es1'$ 
proof (rule bisim-list1I)
from wf-prog-wwf-prog [OF wf] sees
have wf-mdecl wwf-J-mdecl  $P \ D \ (M, Ts, T, \lfloor (pns, \text{body}) \rfloor)$  by (rule sees-wf-mdecl)
hence  $\text{fv}': \text{fv } \text{body} \subseteq \text{set } pns \cup \{this\}$  by (auto simp add: wf-mdecl-def)
moreover from  $\langle P \vdash \text{class-type-of } U \text{ sees } M: Ts \rightarrow T = \lfloor (pns, \text{body}) \rfloor \text{ in } D \rangle$  have  $\neg \text{contains-insync } \text{body}$ 
by (auto dest!: sees-wf-mdecl [OF wf] WT-expr-locks simp add: wf-mdecl-def contains-insync-conv)
ultimately have bisim ( $[this] @ pns$ ) body ( $\text{compE1 } ([this] @ pns) \text{ body}$ )  $\text{?xs2}'$ 
by  $-(\text{rule compE1-bisim}, \text{auto})$ 
with  $\langle \text{length } vs = \text{length } pns \rangle \langle \text{length } Ts = \text{length } pns \rangle$ 
have bisim ( $[] @ [this]$ ) (blocks  $pns \ Ts \ vs \ \text{body}$ ) (blocks1 ( $\text{Suc } 0$ )  $Ts$  ( $\text{compE1 } (this \# pns) \text{ body}$ ))  $\text{?xs2}'$ 
by  $-(\text{drule blocks-bisim}, \text{auto simp add: nth-append})$ 
from bisimBlockSomeNone [OF this, of Addr a Class D]
show bisim  $[] \ \text{?e1}' \ \text{?e2}' \ \text{?xs2}'$  by simp
from  $\text{fv}' \ len$  show  $\text{fv } \text{?e1}' = \{\}$  by auto

from wf sees
have wf-mdecl wwf-J-mdecl  $P \ D \ (M, Ts, T, \lfloor (pns, \text{body}) \rfloor)$  by (rule sees-wf-mdecl)
hence  $\mathcal{D} \ \text{body} \ \lfloor \text{set } pns \cup \{this\} \rfloor$  by (auto simp add: wf-mdecl-def)
with  $\langle \text{length } vs = \text{length } pns \rangle \langle \text{length } Ts = \text{length } pns \rangle$ 
have  $\mathcal{D} \ (\text{blocks } pns \ Ts \ vs \ \text{body}) \ \lfloor \text{dom } [this \mapsto \text{Addr } a] \rfloor$  by (auto)
thus  $\mathcal{D} \ \text{?e1}' \ \lfloor \{\} \rfloor$  by auto

from len show  $\text{max-vars } \text{?e2}' \leq \text{length } \text{?xs2}'$  by (auto simp add: blocks1-max-vars)
qed
moreover have  $\tau \text{Move0 } P \ h \ (e1, es1)$  using call ha sees
by (fastforce simp add: synthesized-call-def  $\tau$ external'-def dest: sees-method-fun  $\tau \text{move0-callD}$  [where  $P=P$  and  $h=h$ ])
ultimately show ?thesis using  $\text{ex2 } \langle \text{call } e1 = \lfloor (a, M, vs) \rfloor \rangle$ 

```

```

  by(simp del:  $\tau\text{Move1.simps}$ )(rule exI conjI|assumption)+
next
case (red0Return e)
note es1 =  $\langle es1 = e \# es1' \rangle$  and e1' =  $\langle e1' = \text{inline-call } e1 \ e \rangle$ 
  and [simp] =  $\langle ta = \varepsilon \rangle \langle h' = h \rangle$ 
  and fin =  $\langle \text{final } e1 \rangle$ 
from bisiml es1 obtain E' xs' E xs exs' aMvs where ex2:  $ex2 = (E', xs')$  and exs2:  $exs2 = (E,$ 
xs) # exs'
  and bisim':  $\text{bisim} \ [] \ e1 \ E' \ xs'$ 
  and bisim:  $\text{bisim} \ [] \ e \ E \ xs$ 
  and fv:  $\text{fv } e = \{\}$ 
  and length:  $\text{max-vars } E \leq \text{length } xs$ 
  and bisiml:  $\text{bisim-list } es1' \ exs'$ 
  and D:  $\mathcal{D} \ e \ [\{\}]$ 
  and call:  $\text{call } e = [aMvs]$ 
  and call1:  $\text{call1 } E = [aMvs]$ 
  by(fastforce elim: bisim-list1-elim)
let ?e2' =  $\text{inline-call } E' \ E$ 

from  $\langle \text{final } e1 \rangle$  bisim' have final E' by(auto)
hence red':  $\text{False.comp } P1 \ P, t \vdash 1 \ \langle ex2/exs2, h \rangle \rightarrow \langle (\text{?e2}', xs)/exs', h \rangle$ 
  unfolding ex2 exs2 by(rule red1Return)
moreover have  $\tau\text{Move0 } P \ h \ (e1, es1) = \tau\text{Move1 } P \ h \ ((E', xs'), exs2)$ 
  using  $\langle \text{final } e1 \rangle \langle \text{final } E' \rangle$  by auto
moreover {
  note bisiml
  moreover
  from bisim' fin bisim
  have bisim [] (inline-call e1 e) (inline-call E' E) xs
    using call by(rule bisim-inline-call)(simp add: fv)
  moreover from fv-inline-call[of e1 e] fv fin
  have fv (inline-call e1 e) = {} by auto
  moreover from fin have D (inline-call e1 e) [\{\}]
    using call D by(rule defass-inline-call)
  moreover have  $\text{max-vars } ?e2' \leq \text{max-vars } E + \text{max-vars } E'$  by(rule inline-call-max-vars1)
  with  $\langle \text{final } E' \rangle$  length have  $\text{max-vars } ?e2' \leq \text{length } xs$  by(auto elim!: final.cases)
  ultimately have bisim-list1 (inline-call e1 e, es1') ((?e2', xs), exs')
    by(rule bisim-list1I) }
ultimately show ?thesis using e1'  $\langle \text{final } e1 \rangle \langle \text{final } E' \rangle$  ex2
  apply(simp del:  $\tau\text{Move0.simps}$   $\tau\text{Move1.simps}$ )
  apply(rule exI conjI impI|assumption)+
  apply(rule tranclp.r-into-trancl, simp)
  apply blast
done
qed

```

lemma sim-move10-into-red0:

```

  assumes wwf: wwf-J-prog P
  and sim: sim-move10 P t ta e2 e2' e h Map.empty ta' e' h' x'
  and fv: fv e = {}
  shows if  $\tau\text{move1 } P \ h \ e2$ 
    then  $(\tau\text{Red0t } P \ t \ h \ (e, es) \ (e', es) \vee \text{countInitBlock } e2' < \text{countInitBlock } e2 \wedge e' = e \wedge x' =$ 
Map.empty)  $\wedge ta = \varepsilon \wedge h = h'$ 
    else  $\exists e'' \ ta'. \tau\text{Red0r } P \ t \ h \ (e, es) \ (e'', es) \wedge$ 

```

$(call1\ e2 = None \vee call\ e = None \vee e'' = e) \wedge$   
 $P, t \vdash 0 \langle e''/es, h \rangle -ta' \rightarrow \langle e'/es, h' \rangle \wedge$   
 $\neg \tau Move0\ P\ h\ (e'', es) \wedge ta\text{-}bisim01\ ta' (extTA2J1\ (compP1\ P)\ ta)$   
**proof**(cases  $\tau move1\ P\ h\ e2$ )  
**case** *True*  
**with** *sim* **have**  $\neg final\ e2$   
**and** *red*:  $\tau red0t\ (extTA2J0\ P)\ P\ t\ h\ (e, Map.empty)\ (e', x') \vee$   
 $countInitBlock\ e2' < countInitBlock\ e2 \wedge e' = e \wedge x' = Map.empty$   
**and** [*simp*]:  $h' = h\ ta = \varepsilon\ ta' = \varepsilon$  **by**(*simp-all add: sim-move10-def*)  
**from** *red* **have**  $\tau Red0t\ P\ t\ h\ (e, es)\ (e', es) \vee$   
 $countInitBlock\ e2' < countInitBlock\ e2 \wedge e' = e \wedge x' = Map.empty$   
**proof**  
**assume** *red*:  $\tau red0t\ (extTA2J0\ P)\ P\ t\ h\ (e, Map.empty)\ (e', x')$   
**from**  $\tau red0t\text{-}fv\text{-}subset[OF\ wwf\ red]\ \tau red0t\text{-}dom\text{-}lcl[OF\ wwf\ red]\ fv$   
**have**  $dom\ x' \subseteq \{\}$  **by**(*auto split: if-split-asm*)  
**hence**  $x' = Map.empty$  **by** *auto*  
**with** *red* **have**  $\tau red0t\ (extTA2J0\ P)\ P\ t\ h\ (e, Map.empty)\ (e', Map.empty)$  **by** *simp*  
**with** *wwf* **have**  $\tau Red0t\ P\ t\ h\ (e, es)\ (e', es)$   
**using** *fv* **by**(*rule \tau red0t-into-\tau Red0t*)  
**thus** *?thesis* ..  
**qed** *simp*  
**with** *True* **show** *?thesis* **by** *simp*  
**next**  
**case** *False*  
**with** *sim* **obtain**  $e''\ xs''$  **where**  $\neg final\ e2$   
**and**  $\tau red$ :  $\tau red0r\ (extTA2J0\ P)\ P\ t\ h\ (e, Map.empty)\ (e'', xs'')$   
**and** *red*:  $extTA2J0\ P, P, t \vdash \langle e'', (h, xs'') \rangle -ta' \rightarrow \langle e', (h', x') \rangle$   
**and** *call*:  $call1\ e2 = None \vee call\ e = None \vee e'' = e$   
**and**  $\neg \tau move0\ P\ h\ e''\ ta\text{-}bisim01\ ta' (extTA2J1\ (compP1\ P)\ ta)\ no\text{-}call\ P\ h\ e''$   
**by**(*auto simp add: sim-move10-def split: if-split-asm*)  
**from**  $\tau red0r\text{-}fv\text{-}subset[OF\ wwf\ \tau red]\ \tau red0r\text{-}dom\text{-}lcl[OF\ wwf\ \tau red]\ fv$   
**have**  $dom\ xs'' \subseteq \{\}$  **by**(*auto*)  
**hence**  $xs'' = Map.empty$  **by**(*auto*)  
**with**  $\tau red$  **have**  $\tau red0r\ (extTA2J0\ P)\ P\ t\ h\ (e, Map.empty)\ (e'', Map.empty)$  **by** *simp*  
**with** *wwf* **have**  $\tau Red0r\ P\ t\ h\ (e, es)\ (e'', es)$   
**using** *fv* **by**(*rule \tau red0r-into-\tau Red0r*)  
**moreover from** *red*  $\langle xs'' = Map.empty \rangle$   
**have**  $extTA2J0\ P, P, t \vdash \langle e'', (h, Map.empty) \rangle -ta' \rightarrow \langle e', (h', x') \rangle$  **by** *simp*  
**from**  $red0Red[OF\ this]\ \langle no\text{-}call\ P\ h\ e'' \rangle$   
**have**  $P, t \vdash 0 \langle e''/es, h \rangle -ta' \rightarrow \langle e'/es, h' \rangle$  **by**(*simp add: no-call-def*)  
**moreover from**  $\langle \neg \tau move0\ P\ h\ e'' \rangle\ red$   
**have**  $\neg \tau Move0\ P\ h\ (e'', es)$  **by** *auto*  
**ultimately show** *?thesis* **using** *False*  $\langle ta\text{-}bisim01\ ta' (extTA2J1\ (compP1\ P)\ ta) \rangle\ call$   
**by**(*simp del: \tau Move0.simps*) *blast*  
**qed**

**lemma** *red0-simulates-Red1*:

**assumes** *wf*: *wf-J-prog* *P*  
**and** *red*: *False, compP1*  $P, t \vdash 1 \langle ex2/exs2, h \rangle -ta \rightarrow \langle ex2'/exs2', h' \rangle$   
**and** *bisiml*: *bisim-list1*  $(e, es)\ (ex2, exs2)$   
**shows**  $\exists e'\ es'.\ bisim\text{-}list1\ (e', es')\ (ex2', exs2') \wedge$   
 $(if\ \tau Move1\ P\ h\ (ex2, exs2)$   
 $then\ (\tau Red0t\ P\ t\ h\ (e, es)\ (e', es') \vee countInitBlock\ (fst\ ex2') < countInitBlock\ (fst$   
 $ex2) \wedge exs2' = exs2 \wedge e' = e \wedge es' = es) \wedge$



```

    ta = ε ∧ h = h'
  else ∃ e'' es'' ta'. τRed0r P t h (e, es) (e'', es'') ∧
    (call1 (fst ex2) = None ∨ call e = None ∨ e'' = e ∧ es'' = es) ∧
    P, t ⊢ 0 ⟨e''/es'', h⟩ -ta'→ ⟨e'/es', h'⟩ ∧
    ¬ τMove0 P h (e'', es'') ∧ ta-bisim01 ta' ta)

  (is ∃ e' es' . - ∧ ?red e' es')
using red
proof(cases)
  case (red1Red E xs TA E' xs')
  note red = ⟨False, compP1 P, t ⊢ 1 ⟨E, (h, xs)⟩ -TA→ ⟨E', (h', xs')⟩⟩
  and ex2 = ⟨ex2 = (E, xs)⟩
  and ex2' = ⟨ex2' = (E', xs')⟩
  and [simp] = ⟨ta = extTA2J1 (compP1 P) TA⟩ ⟨exs2' = exs2⟩
  from bisiml ex2 have bisim: bisim [] e E xs and fv: fv e = {}
  and length: max-vars E ≤ length xs and bsl: bisim-list es exs2
  and D: D e [{}]] by(auto elim: bisim-list1-elim)
  from red-simulates-red1-aux[OF wf red, simplified, OF bisim, of Map.empty] fv length D
  obtain TA' e2' x' where red': sim-move10 P t TA E E' e h Map.empty TA' e2' h' x'
  and bisim'': bisim [] e2' E' xs' and lcl': x' ⊆m Map.empty by auto
  from red have ¬ final E by auto
  with sim-move10-into-red0[OF wf-prog-wwf-prog[OF wf] red', of es] fv ex2 ex2'
  have red'': ?red e2' es by fastforce
  moreover {
    note bsl bisim''
    moreover from red' fv have fv e2' = {}
    by(fastforce simp add: sim-move10-def split: if-split-asm dest: τred0r-fv-subset[OF wf-prog-wwf-prog[OF wf]]
    τred0t-fv-subset[OF wf-prog-wwf-prog[OF wf]] red-fv-subset[OF wf-prog-wwf-prog[OF wf]])
    moreover from red' have dom x' ⊆ dom (Map.empty) ∪ fv e
    unfolding sim-move10-def
    apply(auto split: if-split-asm del: subsetI dest: τred0r-dom-lcl[OF wf-prog-wwf-prog[OF wf]]
    τred0t-dom-lcl[OF wf-prog-wwf-prog[OF wf]])
    apply(frul-tac [1-2] τred0r-fv-subset[OF wf-prog-wwf-prog[OF wf]])
    apply(auto dest!: τred0r-dom-lcl[OF wf-prog-wwf-prog[OF wf]] red-dom-lcl del: subsetI, blast+)
    done
    with fv have dom x' ⊆ {} by(auto)
    hence x' = Map.empty by(auto)
    with D red' have D e2' [{}]]
    by(auto dest!: sim-move10-preserves-defass[OF wf] split: if-split-asm)
    moreover from red have length xs' = length xs by(auto dest: red1-preserves-len)
    with red1-max-vars[OF red] length
    have max-vars E' ≤ length xs' by simp
    ultimately have bisim-list1 (e2', es) ((E', xs'), exs2)
    by(rule bisim-list1I) }
  ultimately show ?thesis using ex2'
  by(clarsimp split: if-split-asm)(rule exI conjI|assumption|simp)+
next
  case (red1Call E a M vs U Ts T body D xs)
  note [simp] = ⟨ex2 = (E, xs)⟩ ⟨h' = h⟩ ⟨ta = ε⟩
  and ex2' = ⟨ex2' = (blocks1 0 (Class D#Ts) body, Addr a # vs @ replicate (max-vars body)
  undefined-value)⟩
  and exs' = ⟨exs2' = (E, xs) # exs2⟩
  and call = ⟨call1 E = [(a, M, vs)]⟩ and ha = ⟨typeof-addr h a = [U]⟩
  and sees = ⟨compP1 P ⊢ class-type-of U sees M: Ts→T = [body] in D⟩
  and len = ⟨length vs = length Ts⟩

```

```

let ?e2' = blocks1 0 (Class D # Ts) body
let ?xs2' = Addr a # vs @ replicate (max-vars body) undefined-value
from bisim1 have bisim: bisim [] e E xs and fv: fv e = {}
  and length: max-vars E ≤ length xs and bsl: bisim-list es xs2
  and D: D e [] by(auto elim: bisim-list1-elim)

from bisim <call1 E = [(a, M, vs)]>
have call e = [(a, M, vs)] by(rule bisim-call1-Some-call)
moreover note ha
moreover from <compP1 P ⊢ class-type-of U sees M: Ts → T = [body] in D>
obtain pns Jbody where sees': P ⊢ class-type-of U sees M: Ts → T = [(pns, Jbody)] in D
  and body: body = compE1 (this # pns) Jbody
  by(auto dest: sees-method-compPD)
let ?e' = blocks (this # pns) (Class D # Ts) (Addr a # vs) Jbody
note sees' moreover from wf sees' have length Ts = length pns
  by(auto dest!: sees-wf-mdecl simp add: wf-mdecl-def)
with len have length vs = length pns length Ts = length pns by simp-all
ultimately have red': P, t ⊢ 0 <e/es, h> -ε → <?e'/e#es, h> by(rule red0Call)
moreover from <call e = [(a, M, vs)]> ha sees'
have τMove0 P h (e, es)
  by(fastforce simp add: synthesized-call-def dest: τmove0-callD[where P=P and h=h] sees-method-fun)
ultimately have τRed0t P t h (e, es) (?e', e#es) by auto
moreover
from bsl bisim fv D length <call e = [(a, M, vs)]> <call1 E = [(a, M, vs)]>
have bisim-list (e # es) ((E, xs) # xs2) by(rule bisim-list.intros)
hence bisim-list1 (?e', e # es) (xs2', (E, xs) # xs2) unfolding xs2'
proof(rule bisim-list1I)
  from wf-prog-wwf-prog[OF wf] sees'
  have wf-mdecl wwf-J-mdecl P D (M, Ts, T, [(pns, Jbody)]) by(rule sees-wf-mdecl)
  hence fv Jbody ⊆ set pns ∪ {this} by(auto simp add: wf-mdecl-def)
  moreover from sees' have ¬ contains-insync Jbody
    by(auto dest!: sees-wf-mdecl[OF wf] WT-expr-locks simp add: wf-mdecl-def contains-insync-conv)
  ultimately have bisim ([] @ this # pns) Jbody (compE1 ([] @ this # pns) Jbody) ?xs2'
    by -(rule compE1-bisim, auto)
  with <length vs = length Ts> <length Ts = length pns> body
  have bisim [] ?e' (blocks1 (length ([] :: vname list)) (Class D # Ts) body) ?xs2'
    by -(rule blocks-bisim, auto simp add: nth-append nth-Cons')
  thus bisim [] ?e' ?e2' ?xs2' by simp
  from <length vs = length Ts> <length Ts = length pns> <fv Jbody ⊆ set pns ∪ {this}>
  show fv ?e' = {} by auto
  from wf sees'
  have wf-mdecl wf-J-mdecl P D (M, Ts, T, [(pns, Jbody)]) by(rule sees-wf-mdecl)
  hence D Jbody [set pns ∪ {this}] by(auto simp add: wf-mdecl-def)
  with <length vs = length Ts> <length Ts = length pns>
  have D (blocks pns Ts vs Jbody) [dom [this ↦ Addr a]] by(auto)
  thus D ?e' [] by simp
  from len show max-vars ?e2' ≤ length ?xs2' by(simp add: blocks1-max-vars)
qed
moreover have τMove1 P h (xs2, xs2) using ha <call1 E = [(a, M, vs)]> sees'
  by(auto simp add: synthesized-call-def τexternal'-def dest!: τmove1-not-call1[where P=P and
h=h] dest: sees-method-fun)
ultimately show ?thesis using xs'
  by(simp del: τMove1.simps τMove0.simps)(rule exI conjI rtranclp.rtrancl-refl|assumption)+
next

```

```

case (red1Return  $E' x' E x$ )
note [simp] =  $\langle h' = h \rangle \langle ta = \varepsilon \rangle$ 
  and  $ex2 = \langle ex2 = (E', x') \rangle$  and  $exs2 = \langle exs2 = (E, x) \# exs2' \rangle$ 
  and  $ex2' = \langle ex2' = (\text{inline-call } E' E, x) \rangle$ 
  and  $fin = \langle \text{final } E' \rangle$ 
from bisiml  $ex2 exs2$  obtain  $e' es' aMvs$  where  $es: es = e' \# es'$ 
  and bsl: bisim-list  $es' exs2'$ 
  and bisim: bisim  $\square e E' x'$ 
  and bisim': bisim  $\square e' E x$ 
  and fv: fv  $e' = \{\}$ 
  and length: max-vars  $E \leq \text{length } x$ 
  and D:  $\mathcal{D} e' [\{\}]$ 
  and call: call  $e' = \lfloor aMvs \rfloor$ 
  and call1: call1  $E = \lfloor aMvs \rfloor$ 
  by(fastforce elim!: bisim-list1-elim)

from  $\langle \text{final } E' \rangle$  bisim have fin': final  $e$  by(auto)
hence  $P, t \vdash 0 \langle e/e' \# es', h \rangle \rightarrow \langle \text{inline-call } e e'/es', h \rangle$  by(rule red0Return)
moreover from bisim fin' bisim' call
have bisim  $\square (\text{inline-call } e e') (\text{inline-call } E' E) x$ 
  by(rule bisim-inline-call)(simp add: fv)
with bsl have bisim-list1  $(\text{inline-call } e e', es') (ex2', exs2')$  unfolding  $ex2'$ 
proof(rule bisim-list1I)
  from fin' fv-inline-call[of  $e e'$ ] fv show fv  $(\text{inline-call } e e') = \{\}$  by auto
  from fin' show  $\mathcal{D} (\text{inline-call } e e') [\{\}]$  using call D by(rule defass-inline-call)

  from call1-imp-call[OF call1]
  have max-vars  $(\text{inline-call } E' E) \leq \text{max-vars } E + \text{max-vars } E'$ 
    by(rule inline-call-max-vars)
  with fin length show max-vars  $(\text{inline-call } E' E) \leq \text{length } x$  by(auto elim!: final.cases)
qed
moreover have  $\tau\text{Move1 } P h (ex2, exs2) = \tau\text{Move0 } P h (e, es)$  using  $ex2 call1 call fin fin'$  by(auto)
ultimately show ?thesis using es
  by(simp del:  $\tau\text{Move1.simps } \tau\text{Move0.simps}$ ) blast
qed

end

sublocale J0-J1-heap-base < red0-Red1': FWdelay-bisimulation-base
  final-expr0
  mred0 P
  final-expr1
  mred1' (compP1 P)
  convert-RA
   $\lambda t. bisim-red0-Red1$ 
  bisim-wait01
   $\tau\text{MOVE0 } P$ 
   $\tau\text{MOVE1 } (compP1 P)$ 
  for  $P$ 
.

context J0-J1-heap-base begin

lemma delay-bisimulation-red0-Red1:

```

**assumes** *wf*: *wf-J-prog P*  
**shows** *delay-bisimulation-measure* (*mred0 P t*) (*mred1' (compP1 P) t*) *bisim-red0-Red1* (*ta-bisim*  
(*λt. bisim-red0-Red1*)) (*τMOVE0 P*) (*τMOVE1 (compP1 P)*) (*λes es'. False*) (*λ(((e', xs'), exs'), h')*  
(*((e, xs), exs), h). countInitBlock e' < countInitBlock e*)  
(*is delay-bisimulation-measure - - - - ?μ1 ?μ2*)  
**proof**(*unfold-locales*)  
**fix** *s1 s2 s1'*  
**assume** *bisim-red0-Red1 s1 s2 red0-mthr.silent-move P t s1 s1'*  
**moreover obtain** *ex1 exs1 h1* **where** *s1: s1 = ((ex1, exs1), h1)* **by**(*cases s1, auto*)  
**moreover obtain** *ex1' exs1' h1'* **where** *s1': s1' = ((ex1', exs1'), h1')* **by**(*cases s1', auto*)  
**moreover obtain** *ex2 exs2 h2* **where** *s2: s2 = ((ex2, exs2), h2)* **by**(*cases s2, auto*)  
**ultimately have** *bisim: bisim-list1 (ex1, exs1) (ex2, exs2)*  
**and** *heap: h1 = h2*  
**and** *red: P, t ⊢ 0 <ex1/exs1, h1> -ε→ <ex1'/exs1', h1'>*  
**and** *τ: τMove0 P h1 (ex1, exs1)*  
**by**(*auto simp add: bisim-red0-Red1-def red0-mthr.silent-move-iff*)  
**from** *Red1-simulates-red0[OF wf red bisim] τ*  
**obtain** *ex2'' exs2''* **where** *bisim': bisim-list1 (ex1', exs1') (ex2'', exs2'')*  
**and** *red': τRed1't (compP1 P) t h1 (ex2, exs2) (ex2'', exs2'')*  
**and** [*simp*]: *h1' = h1* **by** *auto*  
**from** *τRed1't-into-Red1'-τmthr-silent-movet[OF red'] bisim' heap*  
**have** *∃ s2'. Red1-mthr.silent-movet False (compP1 P) t s2 s2' ∧ bisim-red0-Red1 s1' s2'*  
**unfolding** *s2 s1' by(auto simp add: bisim-red0-Red1-def)*  
**thus** *bisim-red0-Red1 s1' s2 ∧ ?μ1^++ s1' s1 ∨ (∃ s2'. Red1-mthr.silent-movet False (compP1 P)*  
*t s2 s2' ∧ bisim-red0-Red1 s1' s2') ..*  
**next**  
**fix** *s1 s2 s2'*  
**assume** *bisim-red0-Red1 s1 s2 Red1-mthr.silent-move False (compP1 P) t s2 s2'*  
**moreover obtain** *ex1 exs1 h1* **where** *s1: s1 = ((ex1, exs1), h1)* **by**(*cases s1, auto*)  
**moreover obtain** *ex2 exs2 h2* **where** *s2: s2 = ((ex2, exs2), h2)* **by**(*cases s2, auto*)  
**moreover obtain** *ex2' exs2' h2'* **where** *s2': s2' = ((ex2', exs2'), h2')* **by**(*cases s2', auto*)  
**ultimately have** *bisim: bisim-list1 (ex1, exs1) (ex2, exs2)*  
**and** *heap: h1 = h2*  
**and** *red: False, compP1 P, t ⊢ 1 <ex2/exs2, h2> -ε→ <ex2'/exs2', h2'>*  
**and** *τ: τMove1 P h2 (ex2, exs2)*  
**by**(*fastforce simp add: bisim-red0-Red1-def Red1-mthr.silent-move-iff*)+  
**from** *red0-simulates-Red1[OF wf red bisim] τ*  
**obtain** *e' es'* **where** *bisim': bisim-list1 (e', es') (ex2', exs2')*  
**and** *red': τRed0t P t h2 (ex1, exs1) (e', es') ∨*  
*countInitBlock (fst ex2') < countInitBlock (fst ex2) ∧ exs2' = exs2 ∧ e' = ex1 ∧ es' =*  
*exs1*  
**and** [*simp*]: *h2' = h2* **by** *auto*  
**from** *red'*  
**show** *bisim-red0-Red1 s1 s2' ∧ ?μ2^++ s2' s2 ∨ (∃ s1'. red0-mthr.silent-movet P t s1 s1' ∧*  
*bisim-red0-Red1 s1' s2')*  
(*is ?refl ∨ ?step*)  
**proof**  
**assume** *τRed0t P t h2 (ex1, exs1) (e', es')*  
**from** *τRed0t-into-red0-τmthr-silent-movet[OF this] bisim' heap*  
**have** *?step unfolding s1 s2' by(auto simp add: bisim-red0-Red1-def)*  
**thus** *?thesis ..*  
**next**  
**assume** *countInitBlock (fst ex2') < countInitBlock (fst ex2) ∧ exs2' = exs2 ∧ e' = ex1 ∧ es' =*  
*exs1*

hence *?refl* using *bisim' heap unfolding* *s1 s2' s2*  
 by (*auto simp add: bisim-red0-Red1-def split-beta*)  
 thus *?thesis ..*  
 qed  
 next  
 fix *s1 s2 ta1 s1'*  
 assume *bisim-red0-Red1 s1 s2* and *mred0 P t s1 ta1 s1'* and  $\tau: \neg \tau \text{MOVE0 } P \ s1 \ ta1 \ s1'$   
 moreover obtain *ex1 exs1 h1* where *s1: s1 = ((ex1, exs1), h1)* by(*cases s1, auto*)  
 moreover obtain *ex1' exs1' h1'* where *s1': s1' = ((ex1', exs1'), h1')* by(*cases s1', auto*)  
 moreover obtain *ex2 exs2 h2* where *s2: s2 = ((ex2, exs2), h2)* by(*cases s2, auto*)  
 ultimately have *heap: h2 = h1*  
 and *bisim: bisim-list1 (ex1, exs1) (ex2, exs2)*  
 and *red: P, t  $\vdash 0 \langle ex1/exs1, h1 \rangle -ta1 \rightarrow \langle ex1'/exs1', h1' \rangle$*   
 by(*auto simp add: bisim-red0-Red1-def*)  
 from  $\tau$  have  $\neg \tau \text{Move0 } P \ h1 \ (ex1, exs1)$  unfolding *s1*  
 using *red* by(*auto elim!: red0.cases dest: red- $\tau$ -taD*[where *extTA=extTA2J0 P, OF extTA2J0- $\varepsilon$* ])  
 with *Red1-simulates-red0*[*OF wf red bisim*]  
 obtain *ex2'' exs2'' ex2' exs2' ta'*  
 where *bisim': bisim-list1 (ex1', exs1') (ex2'', exs2'')*  
 and *red':  $\tau \text{Red1}'r \ (compP1 \ P) \ t \ h1 \ (ex2, exs2) \ (ex2', exs2')$*   
 and *red'':  $False, compP1 \ P, t \vdash 1 \langle ex2'/exs2', h1 \rangle -ta' \rightarrow \langle ex2''/exs2'', h1' \rangle$*   
 and  $\tau': \neg \tau \text{Move1 } P \ h1 \ (ex2', exs2')$   
 and *ta: ta-bisim01 ta1 ta' by fastforce*  
 from *red''* have *mred1' (compP1 P) t ((ex2', exs2'), h1) ta' ((ex2'', exs2''), h1')* by *auto*  
 moreover from  $\tau'$  have  $\neg \tau \text{MOVE1 } (compP1 \ P) \ ((ex2', exs2'), h1) \ ta' \ ((ex2'', exs2''), h1')$  by  
*simp*  
 moreover from *red'* have *Red1-mthr.silent-moves False (compP1 P) t s2 ((ex2', exs2'), h1)*  
 unfolding *s2 heap* by(*rule  $\tau \text{Red1}'r$ -into-Red1'- $\tau$ mthr-silent-moves*)  
 moreover from *bisim'* have *bisim-red0-Red1 ((ex1', exs1'), h1') ((ex2'', exs2''), h1')*  
 by(*auto simp add: bisim-red0-Red1-def*)  
 ultimately  
 show  $\exists s2' s2'' ta2. \text{Red1-mthr.silent-moves False } (compP1 \ P) \ t \ s2 \ s2' \wedge \text{mred1}' (compP1 \ P) \ t \ s2' \wedge$   
*ta2 s2''  $\wedge$*   
 $\neg \tau \text{MOVE1 } (compP1 \ P) \ s2' \ ta2 \ s2'' \wedge \text{bisim-red0-Red1 } s1' \ s2'' \wedge \text{ta-bisim01 } ta1 \ ta2$   
 using *ta unfolding s1' by blast*  
 next  
 fix *s1 s2 ta2 s2'*  
 assume *bisim-red0-Red1 s1 s2* and *mred1' (compP1 P) t s2 ta2 s2'* and  $\tau: \neg \tau \text{MOVE1 } (compP1 \ P) \ s2 \ ta2 \ s2'$   
 moreover obtain *ex1 exs1 h1* where *s1: s1 = ((ex1, exs1), h1)* by(*cases s1, auto*)  
 moreover obtain *ex2 exs2 h2* where *s2: s2 = ((ex2, exs2), h2)* by(*cases s2, auto*)  
 moreover obtain *ex2' exs2' h2'* where *s2': s2' = ((ex2', exs2'), h2')* by(*cases s2', auto*)  
 ultimately have *heap: h2 = h1*  
 and *bisim: bisim-list1 (ex1, exs1) (ex2, exs2)*  
 and *red:  $False, compP1 \ P, t \vdash 1 \langle ex2/exs2, h1 \rangle -ta2 \rightarrow \langle ex2'/exs2', h2' \rangle$*   
 by(*auto simp add: bisim-red0-Red1-def*)  
 from  $\tau$  heap have  $\neg \tau \text{Move1 } P \ h2 \ (ex2, exs2)$  unfolding *s2*  
 using *red* by(*fastforce elim!: Red1.cases dest: red1- $\tau$ -taD*)  
 with *red0-simulates-Red1*[*OF wf red bisim*]  
 obtain *e' es' e'' es'' ta'*  
 where *bisim': bisim-list1 (e', es') (ex2', exs2')*  
 and *red':  $\tau \text{Red0}r \ P \ t \ h1 \ (ex1, exs1) \ (e'', es'')$*   
 and *red'':  $P, t \vdash 0 \langle e''/es'', h1 \rangle -ta' \rightarrow \langle e'/es', h2' \rangle$*   
 and  $\tau': \neg \tau \text{Move0 } P \ h1 \ (e'', es'')$

```

    and ta: ta-bisim01 ta' ta2 using heap by fastforce
  from red'' have mred0 P t ((e'', es''), h1) ta' ((e', es'), h2') by auto
  moreover from red' have red0-mthr.silent-moves P t ((ex1, exs1), h1) ((e'', es''), h1)
    by(rule  $\tau$ Red0r-into-red0- $\tau$ mthr-silent-moves)
  moreover from  $\tau'$  have  $\neg \tau$ MOVE0 P ((e'', es''), h1) ta' ((e', es'), h2') by simp
  moreover from bisim' have bisim-red0-Red1 ((e', es'), h2') ((ex2', exs2'), h2')
    by(auto simp add: bisim-red0-Red1-def)
  ultimately
  show  $\exists s1' s1'' ta1. \text{red0-mthr.silent-moves } P t s1 s1' \wedge$ 
     $mred0 P t s1' ta1 s1'' \wedge \neg \tau$ MOVE0 P s1' ta1 s1''  $\wedge$ 
     $\text{bisim-red0-Red1 } s1'' s2' \wedge \text{ta-bisim01 } ta1 ta2$ 
    using ta unfolding s1 s2' by blast
next
  show wfP ? $\mu$ 1 by auto
next
  have wf (measure countInitBlock) ..
  hence wf (inv-image (measure countInitBlock) ( $\lambda((e', xs'), exs', h'). e')$ ) ..
  also have inv-image (measure countInitBlock) ( $\lambda((e', xs'), exs', h'). e') = \{(s2', s2). ?\mu2 s2' s2\}$ 
    by(simp add: inv-image-def split-beta)
  finally show wfP ? $\mu$ 2 by(simp add: wfP-def)
qed

lemma delay-bisimulation-diverge-red0-Red1:
  assumes wf-J-prog P
  shows delay-bisimulation-diverge (mred0 P t) (mred1' (compP1 P) t) bisim-red0-Red1 (ta-bisim ( $\lambda t. \text{bisim-red0-Red1}$ ) ( $\tau$ MOVE0 P) ( $\tau$ MOVE1 (compP1 P)))
proof -
  interpret delay-bisimulation-measure
    mred0 P t mred1' (compP1 P) t bisim-red0-Red1 ta-bisim ( $\lambda t. \text{bisim-red0-Red1}$ )  $\tau$ MOVE0 P  $\tau$ MOVE1 (compP1 P)
     $\lambda es es'. \text{False } \lambda((e', xs'), exs', h') (((e, xs), exs), h). \text{countInitBlock } e' < \text{countInitBlock } e$ 
    using assms by(rule delay-bisimulation-red0-Red1)
  show ?thesis by unfold-locales
qed

lemma red0-Red1'-FWweak-bisim:
  assumes wf: wf-J-prog P
  shows FWdelay-bisimulation-diverge final-expr0 (mred0 P) final-expr1 (mred1' (compP1 P))
    ( $\lambda t. \text{bisim-red0-Red1}$ ) bisim-wait01 ( $\tau$ MOVE0 P) ( $\tau$ MOVE1 (compP1 P))
proof -
  interpret delay-bisimulation-diverge
    mred0 P t
    mred1' (compP1 P) t
    bisim-red0-Red1
    ta-bisim ( $\lambda t. \text{bisim-red0-Red1}$ )  $\tau$ MOVE0 P  $\tau$ MOVE1 (compP1 P)
  for t
  using wf by(rule delay-bisimulation-diverge-red0-Red1)
  show ?thesis
proof
  fix t and s1 and s2 :: (('addr expr1  $\times$  'addr locals1)  $\times$  ('addr expr1  $\times$  'addr locals1) list)  $\times$  'heap
  assume bisim-red0-Red1 s1 s2 ( $\lambda(x1, m). \text{final-expr0 } x1$ ) s1
  moreover hence ( $\lambda(x2, m). \text{final-expr1 } x2$ ) s2
    by(cases s1)(cases s2, auto simp add: bisim-red0-Red1-def final-iff elim!: bisim-list1-elim elim: bisim-list.cases)

```

ultimately show  $\exists s2'. \text{Red1-mthr.silent-moves False (compP1 P) } t \text{ } s2 \text{ } s2' \wedge \text{bisim-red0-Red1 } s1 \text{ } s2' \wedge (\lambda(x2, m). \text{final-expr1 } x2) \text{ } s2'$

by blast

next

fix  $t \text{ } s1$  and  $s2 :: ('addr \text{expr1} \times 'addr \text{locals1}) \times ('addr \text{expr1} \times 'addr \text{locals1}) \text{ list} \times 'heap$

assume  $\text{bisim-red0-Red1 } s1 \text{ } s2 (\lambda(x2, m). \text{final-expr1 } x2) \text{ } s2$

moreover hence  $(\lambda(x1, m). \text{final-expr0 } x1) \text{ } s1$

by(cases  $s1$ )(cases  $s2$ , auto simp add:  $\text{bisim-red0-Red1-def final-iff elim!}:: \text{bisim-list1-elim elim}:: \text{bisim-list.cases}$ )

ultimately show  $\exists s1'. \text{red0-mthr.silent-moves } P \text{ } t \text{ } s1 \text{ } s1' \wedge \text{bisim-red0-Red1 } s1' \text{ } s2 \wedge (\lambda(x1, m). \text{final-expr0 } x1) \text{ } s1'$

by blast

next

fix  $t' \text{ } x \text{ } m1 \text{ } x' \text{ } m2 \text{ } t \text{ } x1 \text{ } x2 \text{ } x1' \text{ } ta1 \text{ } x1'' \text{ } m1' \text{ } x2' \text{ } ta2 \text{ } x2'' \text{ } m2'$

assume  $b: \text{bisim-red0-Red1 } (x, m1) (x', m2)$

and  $bo: \text{bisim-red0-Red1 } (x1, m1) (x2, m2)$

and  $\tau\text{red1}: \text{red0-mthr.silent-moves } P \text{ } t (x1, m1) (x1', m1)$

and  $\text{red1}: \text{mred0 } P \text{ } t (x1', m1) \text{ } ta1 \text{ } (x1'', m1')$

and  $\tau1: \neg \tau\text{MOVE0 } P (x1', m1) \text{ } ta1 \text{ } (x1'', m1')$

and  $\tau\text{red2}: \text{Red1-mthr.silent-moves False (compP1 P) } t (x2, m2) (x2', m2)$

and  $\text{red2}: \text{mred1}' (\text{compP1 } P) \text{ } t (x2', m2) \text{ } ta2 \text{ } (x2'', m2')$

and  $bo': \text{bisim-red0-Red1 } (x1'', m1') (x2'', m2')$

and  $tb: \text{ta-bisim } (\lambda t. \text{bisim-red0-Red1}) \text{ } ta1 \text{ } ta2$

from  $b$  have  $m1 = m2$  by(auto simp add:  $\text{bisim-red0-Red1-def split-beta}$ )

moreover from  $bo'$  have  $m1' = m2'$  by(auto simp add:  $\text{bisim-red0-Red1-def split-beta}$ )

ultimately show  $\text{bisim-red0-Red1 } (x, m1') (x', m2')$  using  $b$

by(auto simp add:  $\text{bisim-red0-Red1-def split-beta}$ )

next

fix  $t \text{ } x1 \text{ } m1 \text{ } x2 \text{ } m2 \text{ } x1' \text{ } ta1 \text{ } x1'' \text{ } m1' \text{ } x2' \text{ } ta2 \text{ } x2'' \text{ } m2' \text{ } w$

assume  $\text{bisim-red0-Red1 } (x1, m1) (x2, m2)$

and  $\text{red0-mthr.silent-moves } P \text{ } t (x1, m1) (x1', m1)$

and  $\text{red0}: \text{mred0 } P \text{ } t (x1', m1) \text{ } ta1 \text{ } (x1'', m1')$

and  $\neg \tau\text{MOVE0 } P (x1', m1) \text{ } ta1 \text{ } (x1'', m1')$

and  $\text{Red1-mthr.silent-moves False (compP1 P) } t (x2, m2) (x2', m2)$

and  $\text{red1}: \text{mred1}' (\text{compP1 } P) \text{ } t (x2', m2) \text{ } ta2 \text{ } (x2'', m2')$

and  $\neg \tau\text{MOVE1 } (\text{compP1 } P) (x2', m2) \text{ } ta2 \text{ } (x2'', m2')$

and  $\text{bisim-red0-Red1 } (x1'', m1') (x2'', m2')$

and  $\text{ta-bisim01 } ta1 \text{ } ta2$

and  $\text{Suspend}: \text{Suspend } w \in \text{set } \{\{ta1\}\}_w \text{ } \text{Suspend } w \in \text{set } \{\{ta2\}\}_w$

from  $\text{red0 red1 Suspend}$  show  $\text{bisim-wait01 } x1'' \text{ } x2''$

by(cases  $x2'$ )(cases  $x2''$ , auto dest:  $\text{Red-Suspend-is-call Red1-Suspend-is-call simp add: split-beta bisim-wait01-def is-call-def}$ )

next

fix  $t \text{ } x1 \text{ } m1 \text{ } x2 \text{ } m2 \text{ } ta1 \text{ } x1' \text{ } m1'$

assume  $\text{bisim-red0-Red1 } (x1, m1) (x2, m2)$

and  $\text{bisim-wait01 } x1 \text{ } x2$

and  $\text{mred0 } P \text{ } t (x1, m1) \text{ } ta1 \text{ } (x1', m1')$

and  $\text{wakeup}: \text{Notified} \in \text{set } \{\{ta1\}\}_w \vee \text{WokenUp} \in \text{set } \{\{ta1\}\}_w$

moreover obtain  $e0 \text{ } es0$  where  $[\text{simp}]: x1 = (e0, es0)$  by(cases  $x1$ )

moreover obtain  $e0' \text{ } es0'$  where  $[\text{simp}]: x1' = (e0', es0')$  by(cases  $x1'$ )

moreover obtain  $e1 \text{ } xs1 \text{ } exs1$  where  $[\text{simp}]: x2 = ((e1, xs1), exs1)$  by(cases  $x2$ ) auto

ultimately have  $\text{bisim}: \text{bisim-list1 } (e0, es0) ((e1, xs1), exs1)$

and  $m1: m2 = m1$

and  $\text{call}: \text{call } e0 \neq \text{None call1 } e1 \neq \text{None}$

```

    and red:  $P, t \vdash 0 \langle e0/es0, m1 \rangle -ta1 \rightarrow \langle e0'/es0', m1' \rangle$ 
    by(auto simp add: bisim-wait01-def bisim-red0-Red1-def)
  from red wakeup have  $\neg \tau \text{Move0 } P \ m1 \ (e0, es0)$ 
    by(auto elim!: red0.cases dest: red- $\tau$ -taD[where extTA=extTA2J0 P, simplified])
  with Red1-simulates-red0[OF wf red bisim] call m1
  show  $\exists ta2 \ x2' \ m2'. \ mred1' \ (compP1 \ P) \ t \ (x2, m2) \ ta2 \ (x2', m2') \wedge \text{bisim-red0-Red1} \ (x1', m1') \ (x2', m2') \wedge \text{ta-bisim01} \ ta1 \ ta2$ 
    by(auto simp add: bisim-red0-Red1-def)
  next
  fix t x1 m1 x2 m2 ta2 x2' m2'
  assume bisim-red0-Red1 (x1, m1) (x2, m2)
    and bisim-wait01 x1 x2
    and mred1' (compP1 P) t (x2, m2) ta2 (x2', m2')
    and wakeup:  $\text{Notified} \in \text{set } \{ta2\}_w \vee \text{WokenUp} \in \text{set } \{ta2\}_w$ 
  moreover obtain e0 es0 where [simp]:  $x1 = (e0, es0)$  by(cases x1)
  moreover obtain e1 xs1 exs1 where [simp]:  $x2 = ((e1, xs1), exs1)$  by(cases x2) auto
  moreover obtain e1' xs1' exs1' where [simp]:  $x2' = ((e1', xs1'), exs1')$  by(cases x2') auto
  ultimately have bisim: bisim-list1 (e0, es0) ((e1, xs1), exs1)
    and m1:  $m2 = m1$ 
    and call:  $\text{call } e0 \neq \text{None} \ \text{call1 } e1 \neq \text{None}$ 
    and red:  $\text{False}, compP1 \ P, t \vdash 1 \langle (e1, xs1)/exs1, m1 \rangle -ta2 \rightarrow \langle (e1', xs1')/exs1', m2' \rangle$ 
    by(auto simp add: bisim-wait01-def bisim-red0-Red1-def)
  from red wakeup have  $\neg \tau \text{Move1 } P \ m1 \ ((e1, xs1), exs1)$ 
    by(auto elim!: Red1.cases dest: red1- $\tau$ -taD)
  with red0-simulates-Red1[OF wf red bisim] m1 call
  show  $\exists ta1 \ x1' \ m1'. \ mred0 \ P \ t \ (x1, m1) \ ta1 \ (x1', m1') \wedge \text{bisim-red0-Red1} \ (x1', m1') \ (x2', m2') \wedge \text{ta-bisim01} \ ta1 \ ta2$ 
    by(auto simp add: bisim-red0-Red1-def)
  next
  show  $(\exists x. \text{final-expr0 } x) \longleftrightarrow (\exists x. \text{final-expr1 } x)$ 
    by(auto simp add: split-paired-Ex final-iff)
qed
qed

```

lemma bisim-J0-J1-start:

```

  assumes wf: wf-J-prog P
  and start: wf-start-state P C M vs
  shows red0-Red1'.mbisim (J0-start-state P C M vs) (J1-start-state (compP1 P) C M vs)

```

proof –

```

  from start obtain Ts T pns body D
    where sees:  $P \vdash C \text{ sees } M:Ts \rightarrow T = \lfloor (pns, body) \rfloor$  in D
    and conf:  $P, \text{start-heap} \vdash vs \lfloor \cdot \rfloor Ts$ 
    by cases auto
  from conf have vs:  $\text{length } vs = \text{length } Ts$  by(rule list-all2-lengthD)
  from sees-wf-mdecl[OF wf-prog-wwf-prog[OF wf] sees]
  have fvbod:  $\text{fv } body \subseteq \{this\} \cup \text{set } pns$  and len:  $\text{length } pns = \text{length } Ts$ 
    by(auto simp add: wf-mdecl-def)
  with vs have fv:  $\text{fv } (blocks \ pns \ Ts \ vs \ body) \subseteq \{this\}$  by auto
  have wfCM: wf-J-mdecl P D (M, Ts, T, pns, body)
    using sees-wf-mdecl[OF wf sees] by(auto simp add: wf-mdecl-def)
  then obtain T' where wtbody:  $P, [this \# pns \vdash] \text{Class } D \# Ts \vdash body :: T'$  by auto
  hence elbody:  $\text{expr-locks } body = (\lambda l. 0)$  by(rule WT-expr-locks)
  hence cisbody:  $\neg \text{contains-insync } body$  by(auto simp add: contains-insync-conv)
  from wfCM len vs have dabody:  $D \ (blocks \ pns \ Ts \ vs \ body) \lfloor \{this\} \rfloor$  by auto

```



```

from sees have sees1: compP1 P ⊢ C sees M:Ts→T = [compE1 (this # pns) body] in D
  by(auto dest: sees-method-compP[where f=(λC M Ts T (pns, body). compE1 (this # pns) body)])

let ?e = blocks1 0 (Class C # Ts) (compE1 (this # pns) body)
let ?xs = Null # vs @ replicate (max-vars body) undefined-value
from fubody cisbody len vs
have bisim [] (blocks (this # pns) (Class D # Ts) (Null # vs) body) (blocks1 (length ( [] :: vname
list)) (Class D # Ts) (compE1 (this # pns) body)) ?xs
  by-(rule blocks-bisim,(fastforce simp add: nth-Cons' nth-append)+)
with fv dabody len vs elbody sees sees1
show ?thesis unfolding start-state-def
  by(auto intro!: red0-Red1'.mbisimI split: if-split-asm)(auto simp add: bisim-red0-Red1-def blocks1-max-vars
intro!: bisim-list.intros bisim-listII wset-thread-okI)
qed

end

end

```

## 7.25 Preservation of well-formedness from source code to intermediate language

```

theory JJ1WellForm imports
  ../J/JWellForm
  J1WellForm
  Compiler1
begin

```

The compiler preserves well-formedness. Is less trivial than it may appear. We start with two simple properties: preservation of well-typedness

```

lemma assumes wf: wf-prog wfmd P
shows compE1-pres-wt: [ P, [Vs[↦] Ts] ⊢ e :: U; size Ts = size Vs ] ⇒ compP f P, Ts ⊢1 compE1
Vs e :: U
and compEs1-pres-wt: [ P, [Vs[↦] Ts] ⊢ es [::] Us; size Ts = size Vs ] ⇒ compP f P, Ts ⊢1 compEs1
Vs es [::] Us
proof(induct Vs e and Vs es arbitrary: Ts U and Ts Us rule: compE1-compEs1-induct)
  case Var thus ?case by(fastforce simp:map-upds-apply-eq-Some split:if-split-asm)
next
  case LAss thus ?case by(fastforce simp:map-upds-apply-eq-Some split:if-split-asm)
next
  case Call thus ?case
    by(fastforce dest: sees-method-compP[where f = f])
next
  case Block thus ?case by(fastforce simp:nth-append)
next
  case (Synchronized Vs V exp1 exp2 Ts U)
    note IH1 = ⟨∧ Ts U. [P, [Vs[↦] Ts] ⊢ exp1 :: U;
      length Ts = length Vs] ⇒ compP f P, Ts ⊢1 compE1 Vs exp1 :: U⟩
    note IH2 = ⟨∧ Ts U. [P, [(Vs @ [fresh-var Vs]) [↦] Ts] ⊢ exp2 :: U; length Ts = length (Vs @
[fresh-var Vs])]
      ⇒ compP f P, Ts ⊢1 compE1 (Vs @ [fresh-var Vs]) exp2 :: U⟩
    note length = ⟨length Ts = length Vs⟩
    from ⟨P, [Vs[↦] Ts] ⊢ syncV (exp1) exp2 :: U⟩

```

```

obtain  $U1$  where  $wt1: P, [Vs \mapsto Ts] \vdash exp1 :: U1$ 
  and  $wt2: P, [Vs \mapsto Ts] \vdash exp2 :: U$ 
  and  $U1: is-refT\ U1\ U1 \neq NT$ 
  by(auto)
from  $IH1[of\ Ts\ U1]\ wt1\ length$ 
have  $wt1': compP\ f\ P, Ts \vdash 1\ compE1\ Vs\ exp1 :: U1$  by simp
from  $length\ fresh-var-fresh[of\ Vs]$  have  $[Vs \mapsto Ts] \subseteq_m [Vs @ [fresh-var\ Vs] \mapsto Ts @ [Class\ Object]]$ 
  by(auto simp add: map-le-def fun-upd-def)
with  $wt2$  have  $P, [Vs @ [fresh-var\ Vs] \mapsto Ts @ [Class\ Object]] \vdash exp2 :: U$ 
  by(rule wt-env-mono)
with  $length\ IH2[of\ Ts @ [Class\ Object]\ U]$ 
have  $compP\ f\ P, Ts @ [Class\ Object] \vdash 1\ compE1\ (Vs @ [fresh-var\ Vs])\ exp2 :: U$  by simp
with  $wt1'\ U1$  show  $?case$  by(auto)
next
  case (TryCatch  $Vs\ exp1\ C\ V\ exp2$ )
    note  $IH1 = \langle \bigwedge Ts\ U. \llbracket P, [Vs \mapsto Ts] \vdash exp1 :: U; length\ Ts = length\ Vs \rrbracket \implies compP\ f\ P, Ts \vdash 1\ compE1\ Vs\ exp1 :: U \rangle$ 
    note  $IH2 = \langle \bigwedge Ts\ U. \llbracket P, [(Vs @ [V]) \mapsto Ts] \vdash exp2 :: U; length\ Ts = length\ (Vs @ [V]) \rrbracket \implies compP\ f\ P, Ts \vdash 1\ compE1\ (Vs @ [V])\ exp2 :: U \rangle$ 
    note  $length = \langle length\ Ts = length\ Vs \rangle$ 
    with  $\langle P, [Vs \mapsto Ts] \vdash try\ exp1\ catch(C\ V)\ exp2 :: U \rangle$ 
    have  $wt1: P, [Vs \mapsto Ts] \vdash exp1 :: U$  and  $wt2: P, [(Vs @ [V]) \mapsto (Ts @ [Class\ C])] \vdash exp2 :: U$ 
      and  $C: P \vdash C \preceq^* Throwable$  by(auto simp add: nth-append)
    from  $wf\ C$  have  $is-class\ P\ C$  by(rule is-class-sub-Throwable)
    with  $IH1[OF\ wt1\ length]\ IH2[OF\ wt2]\ length$  show  $?case$  by(auto)
qed(fastforce)+

```

and the correct block numbering:

The main complication is preservation of definite assignment  $\mathcal{D}$ .

```

lemma fixes  $e :: 'addr\ expr$  and  $es :: 'addr\ expr\ list$ 
  shows  $A-compE1-None[simp]: \mathcal{A}\ e = None \implies \mathcal{A}\ (compE1\ Vs\ e) = None$ 
  and  $As-compEs1-None: \mathcal{A}s\ es = None \implies \mathcal{A}s\ (compEs1\ Vs\ es) = None$ 
apply(induct Vs e and Vs es rule: compE1-compEs1-induct)
apply(auto simp:hyperset-defs)
done

```

```

lemma fixes  $e :: 'addr\ expr$  and  $es :: 'addr\ expr\ list$ 
  shows  $A-compE1: \llbracket \mathcal{A}\ e = \lfloor A \rfloor; fv\ e \subseteq set\ Vs \rrbracket \implies \mathcal{A}\ (compE1\ Vs\ e) = \lfloor index\ Vs\ 'A \rfloor$ 
  and  $As-compEs1: \llbracket \mathcal{A}s\ es = \lfloor A \rfloor; fvs\ es \subseteq set\ Vs \rrbracket \implies \mathcal{A}s\ (compEs1\ Vs\ es) = \lfloor index\ Vs\ 'A \rfloor$ 
proof(induct Vs e and Vs es arbitrary: A and A rule: compE1-compEs1-induct)
  case (Block  $Vs\ V'\ T\ vo\ e$ )
    hence  $fv\ e \subseteq set\ (Vs @ [V'])$  by fastforce
    moreover obtain  $B$  where  $\mathcal{A}\ e = \lfloor B \rfloor$ 
      using  $Block.premis$  by(simp add: hyperset-defs)
    moreover from calculation have  $B \subseteq set\ (Vs @ [V'])$  by(auto dest!:A-fv)
    ultimately show  $?case$  using Block
      by(auto simp add: hyperset-defs image-index)
next
  case (Synchronized  $Vs\ V\ exp1\ exp2\ A$ )
    have  $IH1: \bigwedge A. \llbracket \mathcal{A}\ exp1 = \lfloor A \rfloor; fv\ exp1 \subseteq set\ Vs \rrbracket \implies \mathcal{A}\ (compE1\ Vs\ exp1) = \lfloor index\ Vs\ 'A \rfloor$  by fact
    have  $IH2: \bigwedge A. \llbracket \mathcal{A}\ exp2 = \lfloor A \rfloor; fv\ exp2 \subseteq set\ (Vs @ [fresh-var\ Vs]) \rrbracket \implies \mathcal{A}\ (compE1\ (Vs @ [fresh-var\ Vs])\ exp2) = \lfloor index\ (Vs @ [fresh-var\ Vs])\ 'A \rfloor$  by fact
    from  $\langle fv\ (sync_V\ (exp1)\ exp2) \subseteq set\ Vs \rangle$ 

```

```

have fv1: fv exp1  $\subseteq$  set Vs
  and fv2: fv exp2  $\subseteq$  set Vs by auto
from  $\langle \mathcal{A} \text{ (sync}_V \text{ (exp1) exp2) } = \lfloor A \rfloor \rangle$  have A:  $\mathcal{A} \text{ exp1} \sqcup \mathcal{A} \text{ exp2} = \lfloor A \rfloor$  by (simp)
then obtain A1 A2 where A1:  $\mathcal{A} \text{ exp1} = \lfloor A1 \rfloor$  and A2:  $\mathcal{A} \text{ exp2} = \lfloor A2 \rfloor$  by (auto simp add:
hyperset-defs)
  from A2 fv2 have A2  $\subseteq$  set Vs by (auto dest: A-fv del: subsetI)
  with fresh-var-fresh[of Vs] have (fresh-var Vs)  $\notin$  A2 by (auto)
  from fv2 have fv exp2  $\subseteq$  set (Vs @ [fresh-var Vs]) by auto
  with IH2[OF A2] have  $\mathcal{A} \text{ (compE1 (Vs @ [fresh-var Vs]) exp2) } = \lfloor \text{index (Vs @ [fresh-var Vs]) } \text{ `}$ 
A2] by (auto)
  with IH1[OF A1 fv1] A[symmetric]  $\langle A2 \subseteq \text{set Vs} \rangle \langle \text{(fresh-var Vs)} \notin A2 \rangle$  A1 A2 show ?case
    by (auto simp add: image-index)
next
  case (InSynchronized Vs V a exp A)
  have IH:  $\bigwedge A. [\mathcal{A} \text{ exp} = \lfloor A \rfloor; \text{fv exp} \subseteq \text{set (Vs @ [fresh-var Vs])}] \implies \mathcal{A} \text{ (compE1 (Vs @ [fresh-var}$ 
Vs]) exp) = \lfloor \text{index (Vs @ [fresh-var Vs]) } \text{ ` A} \rfloor by fact
  from  $\langle \mathcal{A} \text{ (insync}_V \text{ (a) exp) } = \lfloor A \rfloor \rangle$  have A:  $\mathcal{A} \text{ exp} = \lfloor A \rfloor$  by simp
  from  $\langle \text{fv (insync}_V \text{ (a) exp) } \subseteq \text{set Vs} \rangle$  have fv:  $\text{fv exp} \subseteq \text{set Vs}$  by simp
  from A fv have A  $\subseteq$  set Vs by (auto dest: A-fv del: subsetI)
  with fresh-var-fresh[of Vs] have (fresh-var Vs)  $\notin$  A by (auto)
  from fv IH[OF A] have  $\mathcal{A} \text{ (compE1 (Vs @ [fresh-var Vs]) exp) } = \lfloor \text{index (Vs @ [fresh-var Vs]) } \text{ `}$ 
A] by simp
  with  $\langle A \subseteq \text{set Vs} \rangle \langle \text{(fresh-var Vs)} \notin A \rangle$  show ?case by (simp add: image-index)
next
  case (TryCatch Vs e1 C V' e2)
  hence fve2:  $\text{fv e2} \subseteq \text{set (Vs @ [V'])}$  by auto
  show ?case
  proof (cases  $\mathcal{A} \text{ e1}$ )
    assume A1:  $\mathcal{A} \text{ e1} = \text{None}$ 
    then obtain A2 where A2:  $\mathcal{A} \text{ e2} = \lfloor A2 \rfloor$  using TryCatch
      by (simp add: hyperset-defs)
    hence A2  $\subseteq \text{set (Vs @ [V'])}$  using TryCatch.prem A-fv[OF A2] by simp blast
    thus ?thesis using TryCatch fve2 A1 A2
      by (auto simp add: hyperset-defs image-index)
  next
    fix A1 assume A1:  $\mathcal{A} \text{ e1} = \lfloor A1 \rfloor$ 
    show ?thesis
    proof (cases  $\mathcal{A} \text{ e2}$ )
      assume A2:  $\mathcal{A} \text{ e2} = \text{None}$ 
      then show ?case using TryCatch A1 by (simp add: hyperset-defs)
    next
      fix A2 assume A2:  $\mathcal{A} \text{ e2} = \lfloor A2 \rfloor$ 
      have A1  $\subseteq \text{set Vs}$  using TryCatch.prem A-fv[OF A1] by simp blast
      moreover
      have A2  $\subseteq \text{set (Vs @ [V'])}$  using TryCatch.prem A-fv[OF A2] by simp blast
      ultimately show ?thesis using TryCatch A1 A2
        by (fastforce simp add: hyperset-defs image-index
          Diff-subset-conv inj-on-image-Int[OF inj-on-index])
    qed
  qed
next
  case (Cond Vs e e1 e2)
  { assume  $\mathcal{A} \text{ e} = \text{None} \vee \mathcal{A} \text{ e1} = \text{None} \vee \mathcal{A} \text{ e2} = \text{None}$ 
    hence ?case using Cond by (auto simp add: hyperset-defs image-Un)
  }

```

```

}
moreover
{ fix  $A$   $A1$   $A2$ 
  assume  $\mathcal{A} \ e = \lfloor A \rfloor$  and  $A1: \mathcal{A} \ e1 = \lfloor A1 \rfloor$  and  $A2: \mathcal{A} \ e2 = \lfloor A2 \rfloor$ 
  moreover
  have  $A1 \subseteq \text{set } Vs$  using  $\text{Cond.prem}s \ A\text{-fv}[OF \ A1]$  by  $\text{simp blast}$ 
  moreover
  have  $A2 \subseteq \text{set } Vs$  using  $\text{Cond.prem}s \ A\text{-fv}[OF \ A2]$  by  $\text{simp blast}$ 
  ultimately have  $?case$  using  $\text{Cond}$ 
    by( $\text{auto simp add: hyperset-defs image-Un}$ 
       $\text{inj-on-image-Int}[OF \ \text{inj-on-index}]$ )
  }
ultimately show  $?case$  by  $\text{fastforce}$ 
qed ( $\text{auto simp add: hyperset-defs}$ )

lemma fixes  $e :: ('a, 'b, 'addr) \text{exp}$  and  $es :: ('a, 'b, 'addr) \text{exp list}$ 
shows  $D\text{-None} \ [iff]: \mathcal{D} \ e \ \text{None}$ 
and  $Ds\text{-None} \ [iff]: \mathcal{D}s \ es \ \text{None}$ 
by( $\text{induct } e$  and  $es$   $\text{rule: } \mathcal{D}.\text{induct } \mathcal{D}s.\text{induct}$ )( $\text{simp-all}$ )

declare  $\text{Un-ac} \ [simp]$ 

lemma fixes  $e :: 'addr \text{expr}$  and  $es :: 'addr \text{expr list}$ 
shows  $D\text{-index-compE1}: \llbracket A \subseteq \text{set } Vs; \text{fv } e \subseteq \text{set } Vs \rrbracket \implies \mathcal{D} \ e \ \lfloor A \rfloor \implies \mathcal{D} \ (\text{compE1 } Vs \ e) \ \lfloor \text{index } Vs \ 'A \rfloor$ 
and  $Ds\text{-index-compEs1}: \llbracket A \subseteq \text{set } Vs; \text{fvs } es \subseteq \text{set } Vs \rrbracket \implies \mathcal{D}s \ es \ \lfloor A \rfloor \implies \mathcal{D}s \ (\text{compEs1 } Vs \ es) \ \lfloor \text{index } Vs \ 'A \rfloor$ 
proof( $\text{induct } e$  and  $es$   $\text{arbitrary: } A \ Vs$  and  $A \ Vs$   $\text{rule: } \mathcal{D}.\text{induct } \mathcal{D}s.\text{induct}$ )
  case ( $\text{BinOp } e1 \ \text{bop } e2$ )
  hence  $IH1: \mathcal{D} \ (\text{compE1 } Vs \ e1) \ \lfloor \text{index } Vs \ 'A \rfloor$  by  $\text{simp}$ 
  show  $?case$ 
  proof ( $\text{cases } \mathcal{A} \ e1$ )
    case  $\text{None}$  thus  $?thesis$  using  $\text{BinOp}$  by  $\text{simp}$ 
  next
    case ( $\text{Some } A1$ )
    have  $\text{indexA1}: \mathcal{A} \ (\text{compE1 } Vs \ e1) = \lfloor \text{index } Vs \ 'A1 \rfloor$ 
      using  $A\text{-compE1}[OF \ \text{Some}] \ \text{BinOp.prem}s$  by  $\text{auto}$ 
    have  $A \cup A1 \subseteq \text{set } Vs$  using  $\text{BinOp.prem}s \ A\text{-fv}[OF \ \text{Some}]$  by  $\text{auto}$ 
    hence  $\mathcal{D} \ (\text{compE1 } Vs \ e2) \ \lfloor \text{index } Vs \ '(A1 \cup A) \rfloor$  using  $\text{BinOp } \text{Some}$  by( $\text{auto}$ )
    hence  $\mathcal{D} \ (\text{compE1 } Vs \ e2) \ \lfloor \text{index } Vs \ 'A1 \cup \text{index } Vs \ 'A \rfloor$ 
      by( $\text{simp add: image-Un}$ )
    thus  $?thesis$  using  $IH1 \ \text{indexA1}$  by  $\text{auto}$ 
  qed
next
  case ( $\text{AAcc } a \ i \ A \ Vs$ )
  have  $IH1: \bigwedge A \ Vs. \llbracket A \subseteq \text{set } Vs; \text{fv } a \subseteq \text{set } Vs; \mathcal{D} \ a \ \lfloor A \rfloor \rrbracket \implies \mathcal{D} \ (\text{compE1 } Vs \ a) \ \lfloor \text{index } Vs \ 'A \rfloor$  by  $\text{fact}$ 
  have  $IH2: \bigwedge A \ Vs. \llbracket A \subseteq \text{set } Vs; \text{fv } i \subseteq \text{set } Vs; \mathcal{D} \ i \ \lfloor A \rfloor \rrbracket \implies \mathcal{D} \ (\text{compE1 } Vs \ i) \ \lfloor \text{index } Vs \ 'A \rfloor$  by  $\text{fact}$ 
  from  $\langle \mathcal{D} \ (a[i]) \ \lfloor A \rfloor \rangle$  have  $D1: \mathcal{D} \ a \ \lfloor A \rfloor$  and  $D2: \mathcal{D} \ i \ (\lfloor A \rfloor \sqcup \mathcal{A} \ a)$  by  $\text{auto}$ 
  from  $\langle \text{fv } (a[i]) \subseteq \text{set } Vs \rangle$  have  $\text{fv1}: \text{fv } a \subseteq \text{set } Vs$  and  $\text{fv2}: \text{fv } i \subseteq \text{set } Vs$  by  $\text{auto}$ 
  show  $?case$ 
  proof( $\text{cases } \mathcal{A} \ a$ )
    case  $\text{None}$  thus  $?thesis$  using  $\text{AAcc}$  by  $\text{simp}$ 

```

```

next
  case (Some A1)
  with fv1 have A (compE1 Vs a) = [index Vs ' A1] by-(rule A-compE1)
  moreover from A-fv[OF Some] fv1 ⟨A ⊆ set Vs⟩ have A1 ∪ A ⊆ set Vs by auto
  from IH2[OF this fv2] Some D2 have D (compE1 Vs i) [index Vs ' A1 ∪ index Vs ' A]
    by(simp add: image-Un)
  ultimately show ?thesis using IH1[OF ⟨A ⊆ set Vs⟩ fv1 D1] by(simp)
qed
next
  case (AAss a i e A Vs)
  have IH1: ∧A Vs. [A ⊆ set Vs; fv a ⊆ set Vs; D a [A]] ⇒ D (compE1 Vs a) [index Vs ' A] by
fact
  have IH2: ∧A Vs. [A ⊆ set Vs; fv i ⊆ set Vs; D i [A]] ⇒ D (compE1 Vs i) [index Vs ' A] by
fact
  have IH3: ∧A Vs. [A ⊆ set Vs; fv e ⊆ set Vs; D e [A]] ⇒ D (compE1 Vs e) [index Vs ' A] by
fact
  from ⟨D (a[i] := e) [A]⟩ have D1: D a [A] and D2: D i ([A] ⊔ A a)
    and D3: D e ([A] ⊔ A a ⊔ A i) by auto
  from ⟨fv (a[i] := e) ⊆ set Vs⟩
  have fv1: fv a ⊆ set Vs and fv2: fv i ⊆ set Vs and fv3: fv e ⊆ set Vs by auto
  show ?case
  proof(cases A a)
    case None thus ?thesis using AAss by simp
  next
    case (Some A1)
    with fv1 have A1: A (compE1 Vs a) = [index Vs ' A1] by-(rule A-compE1)
    from A-fv[OF Some] fv1 ⟨A ⊆ set Vs⟩ have A1 ∪ A ⊆ set Vs by auto
    from IH2[OF this fv2] Some D2 have D2': D (compE1 Vs i) [index Vs ' A1 ∪ index Vs ' A]
      by(simp add: image-Un)
    show ?thesis
    proof(cases A i)
      case None thus ?thesis using AAss D2' Some A1 by simp
    next
      case (Some A2)
      with fv2 have A2: A (compE1 Vs i) = [index Vs ' A2] by-(rule A-compE1)
      moreover from A-fv[OF Some] fv2 ⟨A1 ∪ A ⊆ set Vs⟩ have A1 ∪ A ∪ A2 ⊆ set Vs by auto
      from IH3[OF this fv3] Some ⟨A a = [A1]⟩ D3
      have D (compE1 Vs e) [index Vs ' A1 ∪ index Vs ' A ∪ index Vs ' A2]
        by(simp add: image-Un Un-commute Un-assoc)
      ultimately show ?thesis using IH1[OF ⟨A ⊆ set Vs⟩ fv1 D1] D2' A1 A2 by(simp)
    qed
  qed
next
  case (FAss e1 F D e2)
  hence IH1: D (compE1 Vs e1) [index Vs ' A] by simp
  show ?case
  proof(cases A e1)
    case None thus ?thesis using FAss by simp
  next
    case (Some A1)
    have indexA1: A (compE1 Vs e1) = [index Vs ' A1]
      using A-compE1[OF Some] FAss.prem by auto
    have A ∪ A1 ⊆ set Vs using FAss.prem A-fv[OF Some] by auto
    hence D (compE1 Vs e2) [index Vs ' (A ∪ A1)] using FAss Some by auto

```

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    hence  $\mathcal{D} \text{ (compE1 Vs e2) } \lfloor \text{index Vs ' A} \cup \text{index Vs ' A1} \rfloor$ 
    by(simp add: image-Un)
    thus ?thesis using IH1 indexA1 by auto
  qed
next
  case (CompareAndSwap e1 D F e2 e3 A Vs)
  have IH1:  $\bigwedge A \text{ Vs. } \llbracket A \subseteq \text{set Vs; fv e1} \subseteq \text{set Vs; } \mathcal{D} \text{ e1 } \lfloor A \rfloor \rrbracket \implies \mathcal{D} \text{ (compE1 Vs e1) } \lfloor \text{index Vs ' A} \rfloor$ 
  by fact
  have IH2:  $\bigwedge A \text{ Vs. } \llbracket A \subseteq \text{set Vs; fv e2} \subseteq \text{set Vs; } \mathcal{D} \text{ e2 } \lfloor A \rfloor \rrbracket \implies \mathcal{D} \text{ (compE1 Vs e2) } \lfloor \text{index Vs ' A} \rfloor$ 
  by fact
  have IH3:  $\bigwedge A \text{ Vs. } \llbracket A \subseteq \text{set Vs; fv e3} \subseteq \text{set Vs; } \mathcal{D} \text{ e3 } \lfloor A \rfloor \rrbracket \implies \mathcal{D} \text{ (compE1 Vs e3) } \lfloor \text{index Vs ' A} \rfloor$ 
  by fact
  from  $\langle \mathcal{D} \text{ (e1.compareAndSwap(D.F, e2, e3)) } \lfloor A \rfloor \rangle$  have D1:  $\mathcal{D} \text{ e1 } \lfloor A \rfloor$  and D2:  $\mathcal{D} \text{ e2 } (\lfloor A \rfloor \sqcup \mathcal{A} \text{ e1})$ 
  and D3:  $\mathcal{D} \text{ e3 } (\lfloor A \rfloor \sqcup \mathcal{A} \text{ e1} \sqcup \mathcal{A} \text{ e2})$  by auto
  from  $\langle \text{fv (e1.compareAndSwap(D.F, e2, e3))} \subseteq \text{set Vs} \rangle$ 
  have fv1:  $\text{fv e1} \subseteq \text{set Vs}$  and fv2:  $\text{fv e2} \subseteq \text{set Vs}$  and fv3:  $\text{fv e3} \subseteq \text{set Vs}$  by auto
  show ?case
  proof(cases  $\mathcal{A} \text{ e1}$ )
    case None thus ?thesis using CompareAndSwap by simp
  next
    case (Some A1)
    with fv1 have A1:  $\mathcal{A} \text{ (compE1 Vs e1)} = \lfloor \text{index Vs ' A1} \rfloor$  by-(rule A-compE1)
    from A-fv[OF Some] fv1  $\langle A \subseteq \text{set Vs} \rangle$  have  $A1 \cup A \subseteq \text{set Vs}$  by auto
    from IH2[OF this fv2] Some D2 have D2':  $\mathcal{D} \text{ (compE1 Vs e2) } \lfloor \text{index Vs ' A1} \cup \text{index Vs ' A} \rfloor$ 
    by(simp add: image-Un)
    show ?thesis
  proof(cases  $\mathcal{A} \text{ e2}$ )
    case None thus ?thesis using CompareAndSwap D2' Some A1 by simp
  next
    case (Some A2)
    with fv2 have A2:  $\mathcal{A} \text{ (compE1 Vs e2)} = \lfloor \text{index Vs ' A2} \rfloor$  by-(rule A-compE1)
    moreover from A-fv[OF Some] fv2  $\langle A1 \cup A \subseteq \text{set Vs} \rangle$  have  $A1 \cup A \cup A2 \subseteq \text{set Vs}$  by auto
    from IH3[OF this fv3] Some  $\langle \mathcal{A} \text{ e1} = \lfloor A1 \rfloor \rangle$  D3
    have  $\mathcal{D} \text{ (compE1 Vs e3) } \lfloor \text{index Vs ' A1} \cup \text{index Vs ' A} \cup \text{index Vs ' A2} \rfloor$ 
    by(simp add: image-Un Un-commute Un-assoc)
    ultimately show ?thesis using IH1[OF  $\langle A \subseteq \text{set Vs} \rangle$  fv1 D1] D2' A1 A2 by(simp)
  qed
  qed
next
  case (Call e1 M es)
  hence IH1:  $\mathcal{D} \text{ (compE1 Vs e1) } \lfloor \text{index Vs ' A} \rfloor$  by simp
  show ?case
  proof(cases  $\mathcal{A} \text{ e1}$ )
    case None thus ?thesis using Call by simp
  next
    case (Some A1)
    have indexA1:  $\mathcal{A} \text{ (compE1 Vs e1)} = \lfloor \text{index Vs ' A1} \rfloor$ 
    using A-compE1[OF Some] Call.prem by auto
    have  $A \cup A1 \subseteq \text{set Vs}$  using Call.prem A-fv[OF Some] by auto
    hence  $\mathcal{D} \text{ s (compEs1 Vs es) } \lfloor \text{index Vs ' (A} \cup \text{A1)} \rfloor$  using Call Some by auto
    hence  $\mathcal{D} \text{ s (compEs1 Vs es) } \lfloor \text{index Vs ' A} \cup \text{index Vs ' A1} \rfloor$ 
    by(simp add: image-Un)
    thus ?thesis using IH1 indexA1 by auto
  
```

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qed
next
  case (Synchronized V exp1 exp2 A Vs)
  have IH1:  $\bigwedge A \text{ Vs}. \llbracket A \subseteq \text{set } Vs; \text{fv } \text{exp1} \subseteq \text{set } Vs; \mathcal{D} \text{ exp1 } \llbracket A \rrbracket \implies \mathcal{D} (\text{compE1 } Vs \text{ exp1}) \llbracket \text{index } Vs \text{ ' } A \rrbracket$  by fact
  have IH2:  $\bigwedge A \text{ Vs}. \llbracket A \subseteq \text{set } Vs; \text{fv } \text{exp2} \subseteq \text{set } Vs; \mathcal{D} \text{ exp2 } \llbracket A \rrbracket \implies \mathcal{D} (\text{compE1 } Vs \text{ exp2}) \llbracket \text{index } Vs \text{ ' } A \rrbracket$  by fact
  from  $\langle \mathcal{D} (\text{sync}_V (\text{exp1}) \text{ exp2}) \llbracket A \rrbracket \rangle$  have D1:  $\mathcal{D} \text{ exp1 } \llbracket A \rrbracket$  and D2:  $\mathcal{D} \text{ exp2 } (\llbracket A \rrbracket \sqcup \mathcal{A} \text{ exp1})$  by auto
  from  $\langle \text{fv } (\text{sync}_V (\text{exp1}) \text{ exp2}) \subseteq \text{set } Vs \rangle$  have fv1:  $\text{fv } \text{exp1} \subseteq \text{set } Vs$  and fv2:  $\text{fv } \text{exp2} \subseteq \text{set } Vs$  by auto
  show ?case
  proof(cases  $\mathcal{A} \text{ exp1}$ )
    case None thus ?thesis using Synchronized by(simp)
  next
    case (Some A1)
    with fv1 have A1:  $\mathcal{A} (\text{compE1 } Vs \text{ exp1}) = \llbracket \text{index } Vs \text{ ' } A1 \rrbracket$  by-(rule A-compE1)
    from A-fv[OF Some] fv1  $\langle A \subseteq \text{set } Vs \rangle$  have  $A \cup A1 \subseteq \text{set } Vs$  by auto
    hence  $A \cup A1 \subseteq \text{set } (Vs @ \llbracket \text{fresh-var } Vs \rrbracket)$  by simp
    from IH2[OF this] fv2 Some D2
    have D2':  $\mathcal{D} (\text{compE1 } (Vs @ \llbracket \text{fresh-var } Vs \rrbracket) \text{ exp2}) \llbracket \text{index } (Vs @ \llbracket \text{fresh-var } Vs \rrbracket) \text{ ' } (A \cup A1) \rrbracket$ 
      by(simp)
    moreover from fresh-var-fresh[of Vs]  $\langle A \cup A1 \subseteq \text{set } Vs \rangle$ 
    have  $(\text{fresh-var } Vs) \notin A \cup A1$  by auto
    with  $\langle A \cup A1 \subseteq \text{set } Vs \rangle$ 
    have  $\text{index } (Vs @ \llbracket \text{fresh-var } Vs \rrbracket) \text{ ' } (A \cup A1) = \text{index } Vs \text{ ' } A \cup \text{index } Vs \text{ ' } A1$ 
      by(simp add: image-index image-Un)
    ultimately show ?thesis using IH1[OF  $\langle A \subseteq \text{set } Vs \rangle$  fv1 D1] D2' A1 by(simp)
  qed
next
  case (InSynchronized V a e A Vs)
  have IH:  $\bigwedge A \text{ Vs}. \llbracket A \subseteq \text{set } Vs; \text{fv } e \subseteq \text{set } Vs; \mathcal{D} e \llbracket A \rrbracket \implies \mathcal{D} (\text{compE1 } Vs e) \llbracket \text{index } Vs \text{ ' } A \rrbracket$  by fact
  from IH[of A Vs @  $\llbracket \text{fresh-var } Vs \rrbracket$ ]  $\langle A \subseteq \text{set } Vs \rangle$   $\langle \text{fv } (\text{insync}_V (a) e) \subseteq \text{set } Vs \rangle$   $\langle \mathcal{D} (\text{insync}_V (a) e) \llbracket A \rrbracket \rangle$ 
  have  $\mathcal{D} (\text{compE1 } (Vs @ \llbracket \text{fresh-var } Vs \rrbracket) e) \llbracket \text{index } (Vs @ \llbracket \text{fresh-var } Vs \rrbracket) \text{ ' } A \rrbracket$  by(auto)
  moreover from fresh-var-fresh[of Vs]  $\langle A \subseteq \text{set } Vs \rangle$  have  $(\text{fresh-var } Vs) \notin A$  by auto
  with  $\langle A \subseteq \text{set } Vs \rangle$  have  $\text{index } (Vs @ \llbracket \text{fresh-var } Vs \rrbracket) \text{ ' } A = \text{index } Vs \text{ ' } A$  by(simp add: image-index)
  ultimately show ?case by(simp)
next
  case (TryCatch e1 C V e2)
  have  $\llbracket A \cup \{V\} \subseteq \text{set } (Vs @ \llbracket V \rrbracket); \text{fv } e2 \subseteq \text{set } (Vs @ \llbracket V \rrbracket); \mathcal{D} e2 \llbracket A \cup \{V\} \rrbracket \implies$ 
     $\mathcal{D} (\text{compE1 } (Vs @ \llbracket V \rrbracket) e2) \llbracket \text{index } (Vs @ \llbracket V \rrbracket) \text{ ' } (A \cup \{V\}) \rrbracket$  by fact
  hence  $\mathcal{D} (\text{compE1 } (Vs @ \llbracket V \rrbracket) e2) \llbracket \text{index } (Vs @ \llbracket V \rrbracket) \text{ ' } (A \cup \{V\}) \rrbracket$ 
    using TryCatch.premis by(simp add: Diff-subset-conv)
  moreover have  $\text{index } (Vs @ \llbracket V \rrbracket) \text{ ' } A \subseteq \text{index } Vs \text{ ' } A \cup \{\text{size } Vs\}$ 
    using TryCatch.premis by(auto simp add: image-index split:if-split-asm)
  ultimately show ?case using TryCatch by(auto simp: hyperset-defs elim!: D-mono')
next
  case (Seq e1 e2)
  hence IH1:  $\mathcal{D} (\text{compE1 } Vs e1) \llbracket \text{index } Vs \text{ ' } A \rrbracket$  by simp
  show ?case
  proof(cases  $\mathcal{A} e1$ )
    case None thus ?thesis using Seq by simp

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next
  case (Some A1)
  have indexA1:  $\mathcal{A} \text{ (compE1 Vs e1) } = \lfloor \text{index Vs ' A1} \rfloor$ 
    using A-compE1[OF Some] Seq.premis by auto
  have  $A \cup A1 \subseteq \text{set Vs}$  using Seq.premis A-fv[OF Some] by auto
  hence  $\mathcal{D} \text{ (compE1 Vs e2) } \lfloor \text{index Vs ' (A} \cup A1) \rfloor$  using Seq Some by auto
  hence  $\mathcal{D} \text{ (compE1 Vs e2) } \lfloor \text{index Vs ' A} \cup \text{index Vs ' A1} \rfloor$ 
    by(simp add: image-Un)
  thus ?thesis using IH1 indexA1 by auto
qed
next
case (Cond e e1 e2)
hence IH1:  $\mathcal{D} \text{ (compE1 Vs e) } \lfloor \text{index Vs ' A} \rfloor$  by simp
show ?case
proof (cases  $\mathcal{A} \text{ e}$ )
  case None thus ?thesis using Cond by simp
next
  case (Some B)
  have indexB:  $\mathcal{A} \text{ (compE1 Vs e) } = \lfloor \text{index Vs ' B} \rfloor$ 
    using A-compE1[OF Some] Cond.premis by auto
  have  $A \cup B \subseteq \text{set Vs}$  using Cond.premis A-fv[OF Some] by auto
  hence  $\mathcal{D} \text{ (compE1 Vs e1) } \lfloor \text{index Vs ' (A} \cup B) \rfloor$ 
    and  $\mathcal{D} \text{ (compE1 Vs e2) } \lfloor \text{index Vs ' (A} \cup B) \rfloor$ 
    using Cond Some by auto
  hence  $\mathcal{D} \text{ (compE1 Vs e1) } \lfloor \text{index Vs ' A} \cup \text{index Vs ' B} \rfloor$ 
    and  $\mathcal{D} \text{ (compE1 Vs e2) } \lfloor \text{index Vs ' A} \cup \text{index Vs ' B} \rfloor$ 
    by(simp add: image-Un)+
  thus ?thesis using IH1 indexB by auto
qed
next
case (While e1 e2)
hence IH1:  $\mathcal{D} \text{ (compE1 Vs e1) } \lfloor \text{index Vs ' A} \rfloor$  by simp
show ?case
proof (cases  $\mathcal{A} \text{ e1}$ )
  case None thus ?thesis using While by simp
next
  case (Some A1)
  have indexA1:  $\mathcal{A} \text{ (compE1 Vs e1) } = \lfloor \text{index Vs ' A1} \rfloor$ 
    using A-compE1[OF Some] While.premis by auto
  have  $A \cup A1 \subseteq \text{set Vs}$  using While.premis A-fv[OF Some] by auto
  hence  $\mathcal{D} \text{ (compE1 Vs e2) } \lfloor \text{index Vs ' (A} \cup A1) \rfloor$  using While Some by auto
  hence  $\mathcal{D} \text{ (compE1 Vs e2) } \lfloor \text{index Vs ' A} \cup \text{index Vs ' A1} \rfloor$ 
    by(simp add: image-Un)
  thus ?thesis using IH1 indexA1 by auto
qed
next
case (Block V T vo e A Vs)
have IH:  $\bigwedge A \text{ Vs. } \llbracket A \subseteq \text{set Vs}; \text{fv } e \subseteq \text{set Vs}; \mathcal{D} \text{ e } \lfloor A \rrbracket \implies \mathcal{D} \text{ (compE1 Vs e) } \lfloor \text{index Vs ' A} \rfloor$  by
fact
from  $\langle \text{fv } \{V:T=vo; e\} \subseteq \text{set Vs} \rangle$  have fv:  $\text{fv } e \subseteq \text{set (Vs @ [V])}$  by auto
show ?case
proof (cases vo)
  case None
  with  $\langle \mathcal{D} \{V:T=vo; e\} \lfloor A \rrbracket \rangle$  have D:  $\mathcal{D} \text{ e } \lfloor A - \{V\} \rfloor$  by(auto)

```



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from ⟨A ⊆ set Vs⟩ have A - {V} ⊆ set (Vs @ [V]) by auto
from IH[OF this fv D] have D (compE1 (Vs @ [V]) e) [index (Vs @ [V]) ‘ (A - {V})] .
moreover from ⟨A ⊆ set Vs⟩ have size: size Vs ∉ index Vs ‘ A by (auto simp add: image-def)
hence [index Vs ‘ (A - {V})] ⊆ [index Vs ‘ A] by (auto simp add: hyperset-defs)
ultimately have D (compE1 (Vs @ [V]) e) [index Vs ‘ A] using ⟨A - {V} ⊆ set (Vs @ [V])⟩
  by (simp add: image-index) (erule D-mono', auto)
thus ?thesis using None size by (simp)
next
case (Some v)
with ⟨D {V:T=vo; e} [A]⟩ have D: D e [insert V A] by (auto)
from ⟨A ⊆ set Vs⟩ have insert V A ⊆ set (Vs @ [V]) by auto
from IH[OF this fv D] have D (compE1 (Vs @ [V]) e) [index (Vs @ [V]) ‘ insert V A] by simp
moreover from ⟨A ⊆ set Vs⟩ have index (Vs @ [V]) ‘ insert V A ⊆ insert (length Vs) (index Vs
‘ A)
  by (auto simp add: image-index)
ultimately show ?thesis using Some by (auto elim!: D-mono' simp add: hyperset-defs)
qed
next
case (Cons-exp e1 es)
hence IH1: D (compE1 Vs e1) [index Vs ‘ A] by simp
show ?case
proof (cases A e1)
  case None thus ?thesis using Cons-exp by simp
next
  case (Some A1)
  have indexA1: A (compE1 Vs e1) = [index Vs ‘ A1]
    using A-compE1[OF Some] Cons-exp.prem by auto
  have A ∪ A1 ⊆ set Vs using Cons-exp.prem A-fv[OF Some] by auto
  hence Ds (compEs1 Vs es) [index Vs ‘ (A ∪ A1)] using Cons-exp Some by auto
  hence Ds (compEs1 Vs es) [index Vs ‘ A ∪ index Vs ‘ A1]
    by (simp add: image-Un)
  thus ?thesis using IH1 indexA1 by auto
qed
qed (simp-all add: hyperset-defs)

declare Un-ac [simp del]

lemma index-image-set: distinct xs ⟹ index xs ‘ set xs = {..

```

```

apply(rule wf-prog-compPI)
  prefer 2 apply assumption
apply(case-tac m)
apply(simp add:wf-mdecl-def)
apply(clarify)
apply(frule WT-fv)
apply(fastforce intro!: compE1-pres-wt D-compE1' B syncvars-compE1)
done

end
theory Compiler imports Compiler1 Compiler2 begin

definition J2JVM :: 'addr J-prog  $\Rightarrow$  'addr jvm-prog
where [code del]: J2JVM  $\equiv$  compP2  $\circ$  compP1

lemma J2JVM-code [code]:
  J2JVM = compP ( $\lambda C M Ts T (pns, body). \text{compMb2 } (\text{compE1 } (this\#pns) \text{ body})$ )
by(simp add: J2JVM-def compP2-def o-def compP-compP split-def)

end

```

## 7.26 Correctness of both stages

```

theory Correctness
imports
  J0Bisim
  J1Deadlock
  ../Framework/FWBisimDeadlock
  Correctness2
  Correctness1Threaded
  Correctness1
  JJ1WellForm
  Compiler
begin

locale J-JVM-heap-conf-base =
  J0-J1-heap-base
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  +
  J1-JVM-heap-conf-base
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  hconf compP1 P
for addr2thread-id :: ('addr  $\Rightarrow$  'thread-id)
and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype
and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool

```

```

and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
and hconf :: 'heap  $\Rightarrow$  bool
and P :: 'addr J-prog
begin

```

**definition** *bisimJ2JVM* ::

```

((('addr,'thread-id,'addr expr  $\times$  'addr locals,'heap,'addr) state,
  ('addr,'thread-id,'addr option  $\times$  'addr frame list,'heap,'addr) state) bisim
where bisimJ2JVM = red-red0.mbisim  $\circ_B$  red0-Red1'.mbisim  $\circ_B$  mbisim-Red1'-Red1  $\circ_B$  Red1-execd.mbisim

```

**definition** *tlsimJ2JVM* ::

```

('thread-id  $\times$  ('addr, 'thread-id, 'heap) J-thread-action,
  'thread-id  $\times$  ('addr, 'thread-id, 'heap) jvm-thread-action) bisim
where tlsimJ2JVM = red-red0.mta-bisim  $\circ_B$  red0-Red1'.mta-bisim  $\circ_B$  (=)  $\circ_B$  Red1-execd.mta-bisim

```

**end**

**lemma** *compP2-has-method [simp]: compP2 P  $\vdash$  C has M  $\longleftrightarrow$  P  $\vdash$  C has M*  
**by**(auto simp add: compP2-def compP-has-method)

**locale** *J-JVM-conf-read* =

```

J1-JVM-conf-read
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  hconf compP1 P
for addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype
and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
and hconf :: 'heap  $\Rightarrow$  bool
and P :: 'addr J-prog
begin

```

**sublocale** *J-JVM-heap-conf-base* **by**(unfold-locales)

**theorem** *bisimJ2JVM-weak-bisim:*

```

  assumes wf: wf-J-prog P
  shows delay-bisimulation-diverge-final (mredT P) (execd-mthr.redT (J2JVM P)) bisimJ2JVM tl-
simJ2JVM
  (red-mthr.m $\tau$ move P) (execd-mthr.m $\tau$ move (J2JVM P)) red-mthr.mfinal exec-mthr.mfinal

```

**unfolding** *bisimJ2JVM-def tlsimJ2JVM-def J2JVM-def o-apply*

```

apply(rule delay-bisimulation-diverge-final-compose)
apply(rule FWdelay-bisimulation-diverge.mthr-delay-bisimulation-diverge-final)
apply(rule red-red0-FWbisim[OF wf-prog-wwf-prog[OF wf]])
apply(rule delay-bisimulation-diverge-final-compose)
apply(rule FWdelay-bisimulation-diverge.mthr-delay-bisimulation-diverge-final)
apply(rule red0-Red1'-FWweak-bisim[OF wf])
apply(rule delay-bisimulation-diverge-final-compose)
apply(rule delay-bisimulation-diverge-final.intro)

```

```

apply(rule bisimulation-into-delay.delay-bisimulation)
apply(rule Red1'-Red1-bisim-into-weak[OF compP1-pres-wf[OF wf]])
apply(rule bisimulation-final.delay-bisimulation-final-base)
apply(rule Red1'-Red1-bisimulation-final[OF compP1-pres-wf[OF wf]])
apply(rule FWdelay-bisimulation-diverge.mthr-delay-bisimulation-diverge-final)
apply(rule Red1-exec1-FWwbisim[OF compP1-pres-wf[OF wf]])
done

```

```

lemma bisimJ2JVM-start:
  assumes wf: wf-J-prog P
  and start: wf-start-state P C M vs
  shows bisimJ2JVM (J-start-state P C M vs) (JVM-start-state (J2JVM P) C M vs)
using assms
unfolding bisimJ2JVM-def J2JVM-def o-def
apply(intro bisim-composeI)
  apply(erule (1) bisim-J-J0-start[OF wf-prog-wwf-prog])
  apply(erule (1) bisim-J0-J1-start)
  apply(erule bisim-J1-J1-start[OF compP1-pres-wf])
  apply simp
apply(erule bisim-J1-JVM-start[OF compP1-pres-wf])
apply simp
done

```

**end**

```

fun exception :: 'addr expr  $\times$  'addr locals  $\Rightarrow$  'addr option  $\times$  'addr frame list
where exception (Throw a, xs) = ([a], [])
| exception - = (None, [])

```

```

definition mexception ::
  ('addr, 'thread-id, 'addr expr  $\times$  'addr locals, 'heap, 'addr) state  $\Rightarrow$ 
  ('addr, 'thread-id, 'addr option  $\times$  'addr frame list, 'heap, 'addr) state
where
   $\bigwedge$  ln. mexception s  $\equiv$ 
  (locks s, ( $\lambda t. \text{case } \text{thr } s \text{ } t \text{ of } [(e, \text{ln})] \Rightarrow [(exception \text{ } e, \text{ln})] \mid \text{None} \Rightarrow \text{None}, \text{shr } s), \text{wset } s, \text{interrupts } s)$ 

```

```

declare compP1-def [simp del]

```

```

context J-JVM-heap-conf-base begin

```

```

lemma bisimJ2JVM-mfinal-mexception:
  assumes bisim: bisimJ2JVM s s'
  and fin: exec-mthr.mfinal s'
  and fin': red-mthr.mfinal s
  and tsNotEmpty: thr s t  $\neq$  None
  shows s' = mexception s
proof –
  obtain ls ts m ws is where s: s = (ls, (ts, m), ws, is) by(cases s) fastforce
  from bisim obtain s0 s1 where bisimJ0: red-red0.mbisim s s0
  and bisim01: red0-Red1'.mbisim s0 s1
  and bisim1JVM: Red1-execd.mbisim s1 s'
  unfolding bisimJ2JVM-def by(fastforce simp add: mbisim-Red1'-Red1-def)

```

```

from bisimJ0 s have [simp]: locks s0 = ls wset s0 = ws interrupts s0 = is
  and tbisimJ0:  $\bigwedge t. \text{red-red0.tbisim } (ws\ t = \text{None})\ t\ (ts\ t)\ m\ (thr\ s0\ t)\ (shr\ s0)$ 
  by(auto simp add: red-red0.mbisim-def)
from bisim01 have [simp]: locks s1 = ls wset s1 = ws interrupts s1 = is
  and tbisim01:  $\bigwedge t. \text{red0-Red1'.tbisim } (ws\ t = \text{None})\ t\ (thr\ s0\ t)\ (shr\ s0)\ (thr\ s1\ t)\ (shr\ s1)$ 
  by(auto simp add: red0-Red1'.mbisim-def)
from bisim1JVM have locks s' = ls wset s' = ws interrupts s' = is
  and tbisim1JVM:  $\bigwedge t. \text{Red1-execd.tbisim } (ws\ t = \text{None})\ t\ (thr\ s1\ t)\ (shr\ s1)\ (thr\ s'\ t)\ (shr\ s')$ 
  by(auto simp add: Red1-execd.mbisim-def)
then obtain ts' m' where s': s' = (ls, (ts', m'), ws, is) by(cases s') fastforce
{ fix t e x ln
  assume tst: ts t = [(e, x), ln]
  from tbisimJ0[of t] tst obtain e' exs' where ts0t: thr s0 t = [(e', exs'), ln]
  and bisimtJ0: bisim-red-red0 ((e, x), m) ((e', exs'), shr s0)
  by(auto simp add: red-red0.tbisim-def)
  from tbisim01[of t] ts0t obtain e'' xs'' exs''
  where ts1t: thr s1 t = [((e'', xs''), exs''), ln]
  and bisimt01: bisim-red0-Red1 ((e', exs'), shr s0) (((e'', xs''), exs''), shr s1)
  by(auto simp add: red0-Red1'.tbisim-def)
  from tbisim1JVM[of t] ts1t s' obtain xcp frs
  where ts't: ts' t = [(xcp, frs), ln] and [simp]: m' = shr s1
  and bisimt1JVM: bisim1-list1 t m' (e'', xs'') exs'' xcp frs
  by(fastforce simp add: Red1-execd.tbisim-def)

  from fin ts't s s' have [simp]: frs = [] by(auto dest: exec-mthr.mfinalD)
  from bisimt1JVM have [simp]: exs'' = [] by(auto elim: bisim1-list1.cases)
  from bisimt01 have [simp]: exs' = []
  by(auto simp add: bisim-red0-Red1-def elim!: bisim-list1E elim: bisim-list.cases)
  from tst fin' s have fine: final e by(auto dest: red-mthr.mfinalD)
  hence exception (e, x) = (xcp, frs)
  proof(cases)
    fix v
    assume [simp]: e = Val v
    from bisimtJ0 have e' = Val v by(auto elim!: bisim-red-red0.cases)
    with bisimt01 have e'' = Val v by(auto simp add: bisim-red0-Red1-def elim: bisim-list1E)
    with bisimt1JVM have xcp = None by(auto elim: bisim1-list1.cases)
    thus ?thesis by simp
  next
    fix a
    assume [simp]: e = Throw a
    from bisimtJ0 have e' = Throw a by(auto elim!: bisim-red-red0.cases)
    with bisimt01 have e'' = Throw a by(auto simp add: bisim-red0-Red1-def elim: bisim-list1E)
    with bisimt1JVM have xcp = [a] by(auto elim: bisim1-list1.cases)
    thus ?thesis by simp
  qed
  moreover from bisimtJ0 have shr s0 = m by(auto elim: bisim-red-red0.cases)
  moreover from bisimt01 have shr s1 = shr s0 by(auto simp add: bisim-red0-Red1-def)
  ultimately have ts' t = [exception (e, x), ln] m' = m using ts't by simp-all }
moreover {
  fix t
  assume ts t = None
  with red-red0.mbisim-thrNone-eq[OF bisimJ0, of t] s have thr s0 t = None by simp
  with bisim01 have thr s1 t = None by(auto simp add: red0-Red1'.mbisim-thrNone-eq)
  with bisim1JVM s' have ts' t = None by(simp add: Red1-execd.mbisim-thrNone-eq) }

```

ultimately show *?thesis* using  $s \ s' \ tsNotEmpty$  by (auto simp add: mexception-def fun-eq-iff)  
qed

end

context *J-JVM-conf-read* begin

**theorem** *J2JVM-correct1*:

fixes  $C \ M \ vs$

defines  $s: s \equiv J\text{-start-state } P \ C \ M \ vs$

and comps:  $cs \equiv JVM\text{-start-state } (J2JVM \ P) \ C \ M \ vs$

assumes  $wf: wf\text{-J-prog } P$

and  $wf\text{-start}: wf\text{-start-state } P \ C \ M \ vs$

and  $red: red\text{-mthr.mthr.}\tau\text{Runs } P \ s \ \xi$

obtains  $\xi'$

where  $execd\text{-mthr.mthr.}\tau\text{Runs } (J2JVM \ P) \ cs \ \xi' \ tlist\text{-all2 } tlsimJ2JVM \ (rel\text{-option } bisimJ2JVM) \ \xi$   
 $\xi'$

and  $\bigwedge s'. \llbracket tfinite \ \xi; \text{terminal } \xi = \lfloor s' \rfloor; red\text{-mthr.mfinal } s' \rrbracket$

$\implies tfinite \ \xi' \wedge \text{terminal } \xi' = \lfloor mexception \ s' \rfloor$

and  $\bigwedge s'. \llbracket tfinite \ \xi; \text{terminal } \xi = \lfloor s' \rfloor; red\text{-mthr.deadlock } P \ s' \rrbracket$

$\implies \exists cs'. tfinite \ \xi' \wedge \text{terminal } \xi' = \lfloor cs' \rfloor \wedge execd\text{-mthr.deadlock } (J2JVM \ P) \ cs' \wedge bisimJ2JVM$

$s' \ cs'$

and  $\llbracket tfinite \ \xi; \text{terminal } \xi = None \rrbracket \implies tfinite \ \xi' \wedge \text{terminal } \xi' = None$

and  $\neg tfinite \ \xi \implies \neg tfinite \ \xi'$

**proof** –

from  $wf \ wf\text{-start}$  have  $bisim: bisimJ2JVM \ s \ cs$  **unfolding**  $s$  comps by (rule  $bisimJ2JVM\text{-start}$ )

**note**  $divfin = delay\text{-bisimulation-diverge-final.delay-bisimulation-diverge}[OF \ bisimJ2JVM\text{-weak-bisim}[OF \ wf]]$

**note**  $divfin2 = delay\text{-bisimulation-diverge-final.delay-bisimulation-final-base}[OF \ bisimJ2JVM\text{-weak-bisim}[OF \ wf]]$

from  $delay\text{-bisimulation-diverge.simulation-}\tau\text{Runs1}[OF \ divfin, \ OF \ bisim \ red]$  **obtain**  $\xi'$

where  $exec: execd\text{-mthr.mthr.}\tau\text{Runs } (J2JVM \ P) \ cs \ \xi'$

and  $tlsim: tlist\text{-all2 } tlsimJ2JVM \ (rel\text{-option } bisimJ2JVM) \ \xi \ \xi'$  by blast

**moreover** {

fix  $s'$

assume  $fin: tfinite \ \xi$  and  $s': \text{terminal } \xi = \lfloor s' \rfloor$  and  $final: red\text{-mthr.mfinal } s'$

from  $delay\text{-bisimulation-final-base.}\tau\text{Runs-terminate-final1}[OF \ divfin2, \ OF \ red \ exec \ tlsim \ fin \ s' \ final]$

**obtain**  $cs'$  where  $fin': tfinite \ \xi'$  and  $cs': \text{terminal } \xi' = \lfloor cs' \rfloor$

and  $final': exec\text{-mthr.mfinal } cs'$  by blast

from  $tlsim \ fin \ s' \ cs'$  have  $bisim': bisimJ2JVM \ s' \ cs'$  by (auto dest:  $tlist\text{-all2-tfinite1-terminalD}$ )

from  $red\text{-mthr.mthr.}\tau\text{Runs-into-}\tau\text{rtranc13p}[OF \ red \ fin \ s']$

have  $thr \ s' \ start\text{-tid} \neq None$  **unfolding**  $s$

by (rule  $red\text{-mthr.}\tau\text{rtranc13p-redT-thread-not-disappear}$ ) (simp add:  $start\text{-state-def}$ )

with  $bisim' \ final \ final'$  have  $[simp]: cs' = mexception \ s'$

by (intro  $bisimJ2JVM\text{-mfinal-mexception disjI1}$ )

with  $fin' \ cs'$  have  $tfinite \ \xi' \wedge \text{terminal } \xi' = \lfloor mexception \ s' \rfloor$  by simp }

**moreover** {

fix  $s'$

assume  $fin: tfinite \ \xi$  and  $s': \text{terminal } \xi = \lfloor s' \rfloor$  and  $dead: red\text{-mthr.deadlock } P \ s'$

from  $tlsim \ fin \ s'$

**obtain**  $cs'$  where  $tfinite \ \xi'$  and  $cs': \text{terminal } \xi' = \lfloor cs' \rfloor$

and  $bisim': bisimJ2JVM \ s' \ cs'$

by(cases terminal  $\xi'$ )(fastforce dest: tllist-all2-tfinite1-terminalD tllist-all2-tfiniteD)+  
 from bisim' obtain  $s0' s1' S1'$  where bisim0: red-red0.mbisim  $s' s0'$   
 and bisim01: red0-Red1'.mbisim  $s0' s1'$   
 and bisim11: mbisim-Red1'-Red1  $s1' S1'$   
 and bisim12: Red1-execd.mbisim  $S1' cs'$   
 unfolding bisimJ2JVM-def by auto

note  $b0 = \text{red-red0-FWbisim}[OF \text{ wf-prog-wwf-prog}[OF \text{ wf}]]$   
 note  $b01 = \text{red0-Red1'-FWweak-bisim}[OF \text{ wf}]$   
 note  $b01mthr = \text{FWdelay-bisimulation-diverge.mbisim-delay-bisimulation}[OF \text{ b01}]$   
 note  $b11 = \text{Red1'-Red1-bisim-into-weak}[OF \text{ compP1-pres-wf}[OF \text{ wf}]]$   
 note  $b11delay = \text{bisimulation-into-delay.delay-bisimulation}[OF \text{ b11}]$   
 note  $b12 = \text{Red1-exec1-FWwbisim}[OF \text{ compP1-pres-wf}[OF \text{ wf}]]$   
 note  $b12mthr = \text{FWdelay-bisimulation-diverge.mbisim-delay-bisimulation}[OF \text{ b12}]$

from FWdelay-bisimulation-diverge.deadlock1-imp- $\tau$ s-deadlock2[OF  $b0$ , OF bisim0 dead, of convert-RA]  
 obtain  $s0''$  where red0-mthr.mthr.silent-moves  $P s0' s0''$   
 and bisim0': red-red0.mbisim  $s' s0''$   
 and dead0: red0-mthr.deadlock  $P s0''$  by auto

from delay-bisimulation-diverge.simulation-silents1[OF  $b01mthr$ , OF bisim01  $\langle \text{red0-mthr.mthr.silent-moves } P s0' s0'' \rangle$ ]  
 obtain  $s1''$  where Red1-mthr.mthr.silent-moves False (compP1  $P$ )  $s1' s1''$   
 and red0-Red1'.mbisim  $s0'' s1''$  by auto  
 from FWdelay-bisimulation-diverge.deadlock1-imp- $\tau$ s-deadlock2[OF  $b01$ , OF  $\langle \text{red0-Red1'.mbisim } s0'' s1'' \rangle$  dead0, of convert-RA]  
 obtain  $s1'''$  where Red1-mthr.mthr.silent-moves False (compP1  $P$ )  $s1'' s1'''$   
 and dead1: Red1-mthr.deadlock False (compP1  $P$ )  $s1'''$   
 and bisim01': red0-Red1'.mbisim  $s0'' s1'''$  by auto  
 from  $\langle \text{Red1-mthr.mthr.silent-moves False (compP1 } P) s1' s1'' \rangle \langle \text{Red1-mthr.mthr.silent-moves False (compP1 } P) s1'' s1''' \rangle$   
 have Red1-mthr.mthr.silent-moves False (compP1  $P$ )  $s1' s1'''$  by(rule rtranclp-trans)

from delay-bisimulation-diverge.simulation-silents1[OF  $b11delay$ , OF bisim11 this]  
 obtain  $S1''$  where Red1-mthr.mthr.silent-moves True (compP1  $P$ )  $S1' S1''$   
 and bisim11': mbisim-Red1'-Red1  $s1''' S1''$  by auto  
 from bisim11' have  $s1''' = S1''$  by(simp add: mbisim-Red1'-Red1-def)  
 with dead1 have dead1': Red1-mthr.deadlock True (compP1  $P$ )  $S1''$   
 by(simp add: Red1-Red1'-deadlock-inv)

from delay-bisimulation-diverge.simulation-silents1[OF  $b12mthr$ , OF bisim12  $\langle \text{Red1-mthr.mthr.silent-moves True (compP1 } P) S1' S1'' \rangle$ ]  
 obtain  $cs''$  where execd-mthr.mthr.silent-moves (compP2 (compP1  $P$ ))  $cs' cs''$   
 and Red1-execd.mbisim  $S1'' cs''$  by auto  
 from FWdelay-bisimulation-diverge.deadlock1-imp- $\tau$ s-deadlock2[OF  $b12$   $\langle \text{Red1-execd.mbisim } S1'' cs'' \rangle$  dead1', of convert-RA]  
 obtain  $cs'''$  where execd-mthr.mthr.silent-moves (compP2 (compP1  $P$ ))  $cs'' cs'''$   
 and bisim12': Red1-execd.mbisim  $S1'' cs'''$   
 and dead': execd-mthr.deadlock (compP2 (compP1  $P$ ))  $cs'''$  by auto  
 from  $\langle \text{execd-mthr.mthr.silent-moves (compP2 (compP1 } P) cs' cs'') \rangle \langle \text{execd-mthr.mthr.silent-moves (compP2 (compP1 } P) cs'' cs''' \rangle$   
 have execd-mthr.mthr.silent-moves (compP2 (compP1  $P$ ))  $cs' cs'''$  by(rule rtranclp-trans)  
 hence  $cs''' = cs'$  using execd-mthr.mthr. $\tau$ Runs-terminal-stuck[OF exec  $\langle \text{tfinite } \xi' \rangle \langle \text{terminal } \xi' =$

$\lfloor cs' \rfloor \rangle$   
**by**(cases rule: converse-rtrancpE)(fastforce simp add: J2JVM-def)+  
**with** dead' **have** execd-mthr.deadlock (J2JVM P) cs' **by**(simp add: J2JVM-def)  
**hence**  $\exists cs'. \text{tfinite } \xi' \wedge \text{terminal } \xi' = \lfloor cs' \rfloor \wedge \text{execd-mthr.deadlock (J2JVM P) } cs' \wedge \text{bisimJ2JVM}$   
 $s' cs'$   
**using**  $\langle \text{tfinite } \xi' \rangle \langle \text{terminal } \xi' = \lfloor cs' \rfloor \rangle \text{bisim' by blast}$  }  
**moreover** {  
**assume** tfinite  $\xi$  **and** terminal  $\xi = \text{None}$   
**hence** tfinite  $\xi' \wedge \text{terminal } \xi' = \text{None}$  **using** tlim tlist-all2-tfiniteD[OF tlim]  
**by**(cases terminal  $\xi'$ )(auto dest: tlist-all2-tfinite1-terminalD) }  
**moreover** {  
**assume**  $\neg \text{tfinite } \xi$   
**hence**  $\neg \text{tfinite } \xi'$  **using** tlim **by**(blast dest: tlist-all2-tfiniteD) }  
**ultimately show thesis by**(rule that)  
**qed**

**theorem** J2JVM-correct2:

**fixes** C M vs  
**defines** s:  $s \equiv J\text{-start-state } P \ C \ M \ \text{vs}$   
**and** comps:  $cs \equiv JVM\text{-start-state (J2JVM P) } C \ M \ \text{vs}$   
**assumes** wf: wf-J-prog P  
**and** wf-start: wf-start-state P C M vs  
**and** exec: execd-mthr.mthr. $\tau$ Runs (J2JVM P) cs  $\xi'$   
**obtains**  $\xi$   
**where** red-mthr.mthr. $\tau$ Runs P s  $\xi$  tlist-all2 tlimJ2JVM (rel-option bisimJ2JVM)  $\xi \ \xi'$   
**and**  $\bigwedge cs'. \llbracket \text{tfinite } \xi'; \text{terminal } \xi' = \lfloor cs' \rfloor; \text{exec-mthr.mfinal } cs' \rrbracket$   
 $\implies \exists s'. \text{tfinite } \xi \wedge \text{terminal } \xi = \lfloor s' \rfloor \wedge cs' = \text{mexception } s' \wedge \text{bisimJ2JVM } s' \ cs'$   
**and**  $\bigwedge cs'. \llbracket \text{tfinite } \xi'; \text{terminal } \xi' = \lfloor cs' \rfloor; \text{execd-mthr.deadlock (J2JVM P) } cs' \rrbracket$   
 $\implies \exists s'. \text{tfinite } \xi \wedge \text{terminal } \xi = \lfloor s' \rfloor \wedge \text{red-mthr.deadlock } P \ s' \wedge \text{bisimJ2JVM } s' \ cs'$   
**and**  $\llbracket \text{tfinite } \xi'; \text{terminal } \xi' = \text{None} \rrbracket \implies \text{tfinite } \xi \wedge \text{terminal } \xi = \text{None}$   
**and**  $\neg \text{tfinite } \xi' \implies \neg \text{tfinite } \xi$

**proof** –

**from** wf wf-start **have** bisim: bisimJ2JVM s cs **unfolding** s comps **by**(rule bisimJ2JVM-start)

**note** divfin = delay-bisimulation-diverge-final.delay-bisimulation-diverge[OF bisimJ2JVM-weak-bisim[OF wf]]

**note** divfin2 = delay-bisimulation-diverge-final.delay-bisimulation-final-base[OF bisimJ2JVM-weak-bisim[OF wf]]

**from** delay-bisimulation-diverge.simulation- $\tau$ Runs2[OF divfin, OF bisim exec] **obtain**  $\xi$   
**where** red: red-mthr.mthr. $\tau$ Runs P s  $\xi$   
**and** tlim: tlist-all2 tlimJ2JVM (rel-option bisimJ2JVM)  $\xi \ \xi'$  **by** blast  
**moreover** {  
**fix** cs'  
**assume** fin: tfinite  $\xi'$  **and** cs': terminal  $\xi' = \lfloor cs' \rfloor$  **and** final: exec-mthr.mfinal cs'  
**from** delay-bisimulation-final-base. $\tau$ Runs-terminate-final2[OF divfin2, OF red exec tlim fin cs'  
final]  
**obtain** s' **where** fin': tfinite  $\xi$  **and** s': terminal  $\xi = \lfloor s' \rfloor$   
**and** final': red-mthr.mfinal s' **by** blast  
**from** tlim fin s' cs' **have** bisim': bisimJ2JVM s' cs' **by**(auto dest: tlist-all2-tfinite2-terminalD)  
**from** red-mthr.mthr. $\tau$ Runs-into- $\tau$ rtranc3p[OF red fin' s']  
**have** thr s' start-tid  $\neq \text{None}$  **unfolding** s  
**by**(rule red-mthr. $\tau$ rtranc3p-redT-thread-not-disappear)(simp add: start-state-def)  
**with** bisim' final final' **have** [simp]: cs' = mexception s'



```

    by(intro bisimJ2JVM-mfinal-mexception)
  with fin' s' bisim' have  $\exists s'. \text{tfinite } \xi \wedge \text{terminal } \xi = \lfloor s' \rfloor \wedge cs' = \text{mexception } s' \wedge \text{bisimJ2JVM}$ 
    s' cs' by simp }
  moreover {
    fix cs'
    assume fin: tfinite  $\xi'$  and cs': terminal  $\xi' = \lfloor cs' \rfloor$  and dead': execd-mthr.deadlock (J2JVM P) cs'
    from tlsim fin cs'
    obtain s' where tfinite  $\xi$  and s': terminal  $\xi = \lfloor s' \rfloor$ 
      and bisim': bisimJ2JVM s' cs'
    by(cases terminal  $\xi$ )(fastforce dest: tllist-all2-tfinite2-terminalD tllist-all2-tfiniteD)+
    from bisim' obtain s0' s1' S1' where bisim0: red-red0.mbisim s' s0'
      and bisim01: red0-Red1'.mbisim s0' s1'
      and bisim11: mbisim-Red1'-Red1 s1' S1'
      and bisim12: Red1-execd.mbisim S1' cs'
    unfolding bisimJ2JVM-def by auto

    note b0 = red-red0-FWbisim[OF wf-prog-wwf-prog[OF wf]]
    note b0mthr = FWdelay-bisimulation-diverge.mbisim-delay-bisimulation[OF b0]
    note b01 = red0-Red1'-FWweak-bisim[OF wf]
    note b01mthr = FWdelay-bisimulation-diverge.mbisim-delay-bisimulation[OF b01]
    note b11 = Red1'-Red1-bisim-into-weak[OF compP1-pres-wf[OF wf]]
    note b11delay = bisimulation-into-delay.delay-bisimulation[OF b11]
    note b12 = Red1-exec1-FWwbisim[OF compP1-pres-wf[OF wf]]

    from FWdelay-bisimulation-diverge.deadlock2-imp- $\tau$ s-deadlock1[OF b12 bisim12, of convert-RA]
  dead'
    obtain S1'' where Red1-mthr.mthr.silent-moves True (compP1 P) S1' S1''
      and bisim12': Red1-execd.mbisim S1'' cs'
      and dead': Red1-mthr.deadlock True (compP1 P) S1'' by(auto simp add: J2JVM-def)
    from delay-bisimulation-diverge.simulation-silents2[OF b11delay, OF bisim11  $\langle$ Red1-mthr.mthr.silent-moves
      True (compP1 P) S1' S1'' $\rangle$ ]
    obtain s1'' where Red1-mthr.mthr.silent-moves False (compP1 P) s1' s1''
      and bisim11': mbisim-Red1'-Red1 s1'' S1'' by blast
    from bisim11' have s1'' = S1'' by(simp add: mbisim-Red1'-Red1-def)
    with dead' have dead1: Red1-mthr.deadlock False (compP1 P) s1''
      by(simp add: Red1-Red1'-deadlock-inv)
    from delay-bisimulation-diverge.simulation-silents2[OF b01mthr, OF bisim01  $\langle$ Red1-mthr.mthr.silent-moves
      False (compP1 P) s1' s1'' $\rangle$ ]
    obtain s0'' where red0-mthr.mthr.silent-moves P s0' s0''
      and bisim01': red0-Red1'.mbisim s0'' s1'' by auto
    from FWdelay-bisimulation-diverge.deadlock2-imp- $\tau$ s-deadlock1[OF b01 bisim01' dead1, of con-
      vert-RA]
    obtain s0''' where red0-mthr.mthr.silent-moves P s0'' s0'''
      and bisim01'': red0-Red1'.mbisim s0''' s1''
      and dead0: red0-mthr.deadlock P s0''' by auto
    from  $\langle$ red0-mthr.mthr.silent-moves P s0' s0'' $\rangle$   $\langle$ red0-mthr.mthr.silent-moves P s0'' s0''' $\rangle$ 
    have red0-mthr.mthr.silent-moves P s0' s0''' by(rule rtranclp-trans)
    from delay-bisimulation-diverge.simulation-silents2[OF b0mthr, OF bisim0 this]
    obtain s'' where red-mthr.mthr.silent-moves P s' s''
      and red-red0.mbisim s'' s0''' by blast
    from FWdelay-bisimulation-diverge.deadlock2-imp- $\tau$ s-deadlock1[OF b0  $\langle$ red-red0.mbisim s'' s0''' $\rangle$ 
      dead0, of convert-RA]
    obtain s''' where red-mthr.mthr.silent-moves P s'' s'''
      and red-red0.mbisim s''' s0'''

```

```

    and dead: red-mthr.deadlock P s''' by blast
  from ⟨red-mthr.mthr.silent-moves P s' s''⟩ ⟨red-mthr.mthr.silent-moves P s'' s'''⟩
  have red-mthr.mthr.silent-moves P s' s''' by(rule rtrancpl-trans)
  hence s''' = s' using red-mthr.mthr.τRuns-terminal-stuck[OF red ⟨tfinite ξ⟩ ⟨terminal ξ = [s']⟩]
    by(cases rule: converse-rtrancplE) fastforce+
  with dead have red-mthr.deadlock P s' by(simp)
  hence ∃ s'. tfinite ξ ∧ terminal ξ = [s'] ∧ red-mthr.deadlock P s' ∧ bisim.J2JVM s' cs'
    using ⟨tfinite ξ⟩ ⟨terminal ξ = [s']⟩ bisim' by blast }
  moreover {
    assume tfinite ξ' and terminal ξ' = None
    hence tfinite ξ ∧ terminal ξ = None using tlsim tllist-all2-tfiniteD[OF tlsim]
      by(cases terminal ξ)(auto dest: tllist-all2-tfinite2-terminalD) }
  moreover {
    assume ¬ tfinite ξ'
    hence ¬ tfinite ξ using tlsim by(blast dest: tllist-all2-tfiniteD) }
  ultimately show thesis by(rule that)
qed

end

declare compP1-def [simp]

theorem wt-J2JVM: wf-J-prog P ⇒ wf-jvm-prog (J2JVM P)
unfolding J2JVM-def o-def
by(rule wt-compP2)(rule compP1-pres-wf)

end
theory Preprocessor
imports
  PCompiler
  ../J/Annotate
  ../J/JWellForm
begin

primrec annotate-Mb ::
  'addr J-prog ⇒ cname ⇒ mname ⇒ ty list ⇒ ty ⇒ (vname list × 'addr expr) ⇒ (vname list ×
'addr expr)
where annotate-Mb P C M Ts T (pns, e) = (pns, annotate P [this # pns [↦] Class C # Ts] e)
declare annotate-Mb.simps [simp del]

primrec annotate-Mb-code ::
  'addr J-prog ⇒ cname ⇒ mname ⇒ ty list ⇒ ty ⇒ (vname list × 'addr expr) ⇒ (vname list ×
'addr expr)
where annotate-Mb-code P C M Ts T (pns, e) = (pns, annotate-code P [this # pns [↦] Class C #
Ts] e)
declare annotate-Mb-code.simps [simp del]

definition annotate-prog :: 'addr J-prog ⇒ 'addr J-prog
where annotate-prog P = compP (annotate-Mb P) P

definition annotate-prog-code :: 'addr J-prog ⇒ 'addr J-prog
where annotate-prog-code P = compP (annotate-Mb-code P) P

lemma fixes is-lub

```

```

shows WT-compP: is-lub, P, E ⊢ e :: T ⇒ is-lub, compP f P, E ⊢ e :: T
and WTs-compP: is-lub, P, E ⊢ es [::] Ts ⇒ is-lub, compP f P, E ⊢ es [::] Ts
proof(induct rule: WT-WTs.inducts)
  case (WTCall E e U C M Ts T meth D es Ts')
  from ⟨P ⊢ C sees M: Ts→T = meth in D⟩
  have compP f P ⊢ C sees M: Ts→T = map-option (f D M Ts T) meth in D
    by(auto dest: sees-method-compP[where f=f])
  with WTCall show ?case by(auto)
qed(auto simp del: fun-upd-apply)

lemma fixes is-lub
  shows Anno-compP: is-lub, P, E ⊢ e ∼ e' ⇒ is-lub, compP f P, E ⊢ e ∼ e'
  and Annos-compP: is-lub, P, E ⊢ es [∼] es' ⇒ is-lub, compP f P, E ⊢ es [∼] es'
apply(induct rule: Anno-Annos.inducts)
apply(auto intro: Anno-Annos.intros simp del: fun-upd-apply dest: WT-compP simp add: compC-def)
done

lemma annotate-prog-code-eq-annotate-prog:
  assumes wf: wf-J-prog (annotate-prog-code P)
  shows annotate-prog-code P = annotate-prog P
proof –
  let ?wf-md = λ-. (-, -, -, body). set (block-types body) ⊆ types P
  from wf have wf-prog ?wf-md (annotate-prog-code P)
    unfolding annotate-prog-code-def
    by(rule wf-prog-lift)(auto dest!: WT-block-types-is-type[OF wf[unfolded annotate-prog-code-def]])
  simp add: wf-J-mdecl-def)
  hence wf': wf-prog ?wf-md P
    unfolding annotate-prog-code-def [abs-def]
  proof(rule wf-prog-compPD)
    fix C M Ts T m
    assume compP (annotate-Mb-code P) P ⊢ C sees M: Ts→T = [ annotate-Mb-code P C M Ts T
m] in C
    and wf-mdecl ?wf-md (compP (annotate-Mb-code P) P) C (M, Ts, T, [ annotate-Mb-code P C
M Ts T m])
    moreover obtain pns body where m = (pns, body) by(cases m)
    ultimately show wf-mdecl ?wf-md P C (M, Ts, T, [ m])
      by(fastforce simp add: annotate-Mb-code-def annotate-code-def wf-mdecl-def THE-default-def
the-equality Anno-code-def split: if-split-asm dest: Anno-block-types)
  qed

{ fix C D fs ms M Ts T pns body
  assume (C, D, fs, ms) ∈ set (classes P)
  and (M, Ts, T, [(pns, body)]) ∈ set ms
  from ⟨(C, D, fs, ms) ∈ set (classes P)⟩ have class P C = [(D, fs, ms)] using wf'
    by(cases P)(auto simp add: wf-prog-def dest: map-of-SomeI)
  with wf' have sees: P ⊢ C sees M: Ts→T = [(pns, body)] in C
    using ⟨(M, Ts, T, [(pns, body)]) ∈ set ms⟩ by(rule mdecl-visible)

  from sees-method-compP[OF this, where f=annotate-Mb-code P]
  have sees': annotate-prog-code P ⊢ C sees M: Ts→T = [(pns, annotate-code P [this ↦ Class C,
pns [↦] Ts] body)] in C
    unfolding annotate-prog-code-def annotate-Mb-code-def by(auto)
  with wf
  have wf-mdecl wf-J-mdecl (annotate-prog-code P) C (M, Ts, T, [(pns, annotate-code P [this ↦
```

```

Class C, pns [↦] Ts] body))
  by(rule sees-wf-mdecl)
  hence set Ts ⊆ types P by(auto simp add: wf-mdecl-def annotate-prog-code-def)
  moreover from sees have is-class P C by(rule sees-method-is-class)
  moreover from wf' sees have wf-mdecl ?wf-md P C (M, Ts, T, [(pns, body)]) by(rule sees-wf-mdecl)
  hence set (block-types body) ⊆ types P by(simp add: wf-mdecl-def)
  ultimately have ran [this ↦ Class C, pns [↦] Ts] ∪ set (block-types body) ⊆ types P
    by(auto simp add: ran-def wf-mdecl-def map-upds-def split: if-split-asm dest!: map-of-SomeD
set-zip-rightD)
  hence annotate-code P [this ↦ Class C, pns [↦] Ts] body = annotate P [this ↦ Class C, pns [↦]
Ts] body
    unfolding annotate-code-def annotate-def
    by -(rule arg-cong[where f=THE-default body], auto intro!: ext intro: Anno-code-into-Anno[OF
wf'] Anno-into-Anno-code[OF wf']) }
  thus ?thesis unfolding annotate-prog-code-def annotate-prog-def
    by(cases P)(auto simp add: compC-def compM-def annotate-Mb-def annotate-Mb-code-def map-option-case)
qed

end
theory Compiler-Main
imports
  J0
  Correctness
  Preprocessor
begin

end

```

## Chapter 8

# Memory Models

```
theory MM
imports
  ../Common/Heap
begin

type-synonym addr = nat
type-synonym thread-id = addr

abbreviation (input)
  addr2thread-id :: addr  $\Rightarrow$  thread-id
where addr2thread-id  $\equiv \lambda x. x$ 

abbreviation (input)
  thread-id2addr :: thread-id  $\Rightarrow$  addr
where thread-id2addr  $\equiv \lambda x. x$ 

instantiation nat :: addr begin
definition hash-addr  $\equiv$  int
definition monitor-funfun-to-list  $\equiv$  (funfun-to-list :: nat  $\Rightarrow$  nat  $\Rightarrow$  nat list)
instance
by(intro-classes)(simp add: monitor-funfun-to-list-nat-def)
end

definition new-Addr :: (addr  $\rightarrow$  'b)  $\Rightarrow$  addr option
where new-Addr h  $\equiv$  if  $\exists a. h\ a = \text{None}$  then Some(LEAST a. h a = None) else None

lemma new-Addr-SomeD:
  new-Addr h = Some a  $\implies h\ a = \text{None}$ 
by(auto simp add: new-Addr-def split:if-splits intro: LeastI)

lemma new-Addr-SomeI:
  finite (dom h)  $\implies \exists a. \text{new-Addr}\ h = \text{Some}\ a$ 
by(simp add: new-Addr-def) (metis finite-map-freshness infinite-UNIV-nat)

8.0.1 Code generation

definition gen-new-Addr :: (addr  $\rightarrow$  'b)  $\Rightarrow$  addr  $\Rightarrow$  addr option
where gen-new-Addr h n  $\equiv$  if  $\exists a. a \geq n \wedge h\ a = \text{None}$  then Some(LEAST a. a  $\geq n \wedge h\ a = \text{None}$ )
else None
```

**lemma** *new-Addr-code-code* [code]:  
   *new-Addr h = gen-new-Addr h 0*  
**by**(*simp add: new-Addr-def gen-new-Addr-def*)

**lemma** *gen-new-Addr-code* [code]:  
   *gen-new-Addr h n = (if h n = None then Some n else gen-new-Addr h (Suc n))*  
**apply**(*simp add: gen-new-Addr-def*)  
**apply**(*rule impI*)  
**apply**(*rule conjI*)  
**apply** *safe*[1]  
   **apply**(*auto intro: Least-equality*)[2]  
**apply**(*rule arg-cong*[**where** *f=Least*])  
**apply**(*rule ext*)  
**apply** *auto*[1]  
**apply**(*case-tac n = ab*)  
**apply** *simp*  
**apply** *simp*  
**apply** *clarify*  
**apply**(*subgoal-tac a = n*)  
**apply** *simp*  
**apply**(*rule Least-equality*)  
**apply** *auto*[2]  
**apply**(*rule ccontr*)  
**apply**(*erule-tac x=a in allE*)  
**apply** *simp*  
**done**  
**end**

## 8.1 Sequential consistency

**theory** *SC*  
**imports**  
   *../Common/Conform*  
   *MM*  
**begin**

### 8.1.1 Objects and Arrays

**type-synonym**  
   *fields = vname × cname → addr val*      — field name, defining class, value

**type-synonym**  
   *cells = addr val list*

**datatype** *heapobj*  
   = *Obj cname fields*  
   — class instance with class name and fields

| *Arr ty fields cells*  
   — element type, fields (from object), and list of each cell's content

**lemma** *rec-heapobj* [*simp*]: *rec-heapobj = case-heapobj*

**by**(*auto intro!*: *ext split*: *heapobj.split*)

**primrec** *obj-ty* :: *heapobj*  $\Rightarrow$  *htype*

**where**

*obj-ty* (*Obj C f*) = *Class-type C*  
 | *obj-ty* (*Arr T fs cs*) = *Array-type T (length cs)*

**fun** *is-Arr* :: *heapobj*  $\Rightarrow$  *bool* **where**

*is-Arr* (*Obj C fs*) = *False*  
 | *is-Arr* (*Arr T f el*) = *True*

**lemma** *is-Arr-conv*:

*is-Arr arrobj* = ( $\exists T f el. arrobj = Arr T f el$ )  
**by**(*cases arrobj, auto*)

**lemma** *is-ArrE*:

$\llbracket is-Arr arrobj; \bigwedge T f el. arrobj = Arr T f el \implies thesis \rrbracket \implies thesis$   
 $\llbracket \neg is-Arr arrobj; \bigwedge C fs. arrobj = Obj C fs \implies thesis \rrbracket \implies thesis$   
**by**(*cases arrobj, auto*)**+**

**definition** *init-fields* :: (*'field-name*  $\times$  (*ty*  $\times$  *fmod*)) *list*  $\Rightarrow$  *'field-name*  $\rightarrow$  *addr val*  
**where** *init-fields*  $\equiv$  *map-of*  $\circ$  *map* ( $\lambda(FD, (T, fm)). (FD, default-val T)$ )

**primrec**

— a new, blank object with default values in all fields:

*blank* :: *'m prog*  $\Rightarrow$  *htype*  $\Rightarrow$  *heapobj*

**where**

*blank P (Class-type C)* = *Obj C (init-fields (fields P C))*  
 | *blank P (Array-type T n)* = *Arr T (init-fields (fields P Object)) (replicate n (default-val T))*

**lemma** *obj-ty-blank* [*iff*]:

*obj-ty (blank P hT)* = *hT*  
**by**(*cases hT*)(*simp-all*)

### 8.1.2 Heap

**type-synonym** *heap* = *addr*  $\rightarrow$  *heapobj*

**translations**

(*type*) *heap*  $\leq$  (*type*) *nat*  $\Rightarrow$  *heapobj option*

**abbreviation** *sc-empty* :: *heap*

**where** *sc-empty*  $\equiv$  *Map.empty*

**fun** *the-obj* :: *heapobj*  $\Rightarrow$  *cname*  $\times$  *fields* **where**

*the-obj* (*Obj C fs*) = (*C, fs*)

**fun** *the-arr* :: *heapobj*  $\Rightarrow$  *ty*  $\times$  *fields*  $\times$  *cells* **where**

*the-arr* (*Arr T f el*) = (*T, f, el*)

**abbreviation**

*cname-of* :: *heap*  $\Rightarrow$  *addr*  $\Rightarrow$  *cname* **where**  
*cname-of hp a* == *fst (the-obj (the (hp a)))*

**definition**  $sc\text{-}allocate :: 'm \text{ prog} \Rightarrow heap \Rightarrow htype \Rightarrow (heap \times addr) \text{ set}$

**where**

$$sc\text{-}allocate \ P \ h \ hT = \\ (case \ new\text{-}Addr \ h \ of \ None \Rightarrow \{\} \\ | \ Some \ a \Rightarrow \{(h(a \mapsto blank \ P \ hT), \ a)\})$$

**definition**  $sc\text{-}typeof\text{-}addr :: heap \Rightarrow addr \Rightarrow htype \text{ option}$

**where**  $sc\text{-}typeof\text{-}addr \ h \ a = map\text{-}option \ obj\text{-}ty \ (h \ a)$

**inductive**  $sc\text{-}heap\text{-}read :: heap \Rightarrow addr \Rightarrow addr\text{-}loc \Rightarrow addr \text{ val} \Rightarrow bool$

**for**  $h :: heap$  **and**  $a :: addr$

**where**

$$\begin{aligned} &Obj: \llbracket h \ a = \lfloor Obj \ C \ fs \rfloor; fs \ (F, \ D) = \lfloor v \rfloor \rrbracket \Longrightarrow sc\text{-}heap\text{-}read \ h \ a \ (CField \ D \ F) \ v \\ &| \ Arr: \llbracket h \ a = \lfloor Arr \ T \ f \ el \rfloor; n < length \ el \rrbracket \Longrightarrow sc\text{-}heap\text{-}read \ h \ a \ (ACell \ n) \ (el \ ! \ n) \\ &| \ ArrObj: \llbracket h \ a = \lfloor Arr \ T \ f \ el \rfloor; f \ (F, \ Object) = \lfloor v \rfloor \rrbracket \Longrightarrow sc\text{-}heap\text{-}read \ h \ a \ (CField \ Object \ F) \ v \end{aligned}$$

**hide-fact** (**open**)  $Obj \ Arr \ ArrObj$

**inductive-cases**  $sc\text{-}heap\text{-}read\text{-}cases \ [elim!]:$

$$sc\text{-}heap\text{-}read \ h \ a \ (CField \ C \ F) \ v \\ sc\text{-}heap\text{-}read \ h \ a \ (ACell \ n) \ v$$

**inductive**  $sc\text{-}heap\text{-}write :: heap \Rightarrow addr \Rightarrow addr\text{-}loc \Rightarrow addr \text{ val} \Rightarrow heap \Rightarrow bool$

**for**  $h :: heap$  **and**  $a :: addr$

**where**

$$\begin{aligned} &Obj: \llbracket h \ a = \lfloor Obj \ C \ fs \rfloor; h' = h(a \mapsto Obj \ C \ (fs((F, \ D) \mapsto v))) \rrbracket \Longrightarrow sc\text{-}heap\text{-}write \ h \ a \ (CField \ D \\ &F) \ v \ h' \\ &| \ Arr: \llbracket h \ a = \lfloor Arr \ T \ f \ el \rfloor; h' = h(a \mapsto Arr \ T \ f \ (el[n := v])) \rrbracket \Longrightarrow sc\text{-}heap\text{-}write \ h \ a \ (ACell \ n) \ v \ h' \\ &| \ ArrObj: \llbracket h \ a = \lfloor Arr \ T \ f \ el \rfloor; h' = h(a \mapsto Arr \ T \ (f((F, \ Object) \mapsto v)) \ el) \rrbracket \Longrightarrow sc\text{-}heap\text{-}write \ h \ a \\ &(CField \ Object \ F) \ v \ h' \end{aligned}$$

**hide-fact** (**open**)  $Obj \ Arr \ ArrObj$

**inductive-cases**  $sc\text{-}heap\text{-}write\text{-}cases \ [elim!]:$

$$sc\text{-}heap\text{-}write \ h \ a \ (CField \ C \ F) \ v \ h' \\ sc\text{-}heap\text{-}write \ h \ a \ (ACell \ n) \ v \ h'$$

**consts**  $sc\text{-}spurious\text{-}wakeups :: bool$

**interpretation**  $sc$ :

$$\begin{aligned} &heap\text{-}base \\ &addr2thread\text{-}id \\ &thread\text{-}id2addr \\ &sc\text{-}spurious\text{-}wakeups \\ &sc\text{-}empty \\ &sc\text{-}allocate \ P \\ &sc\text{-}typeof\text{-}addr \\ &sc\text{-}heap\text{-}read \\ &sc\text{-}heap\text{-}write \end{aligned}$$

**for**  $P$  .

Translate notation from  $heap\text{-}base$

**abbreviation**  $sc\text{-}preallocated :: 'm \text{ prog} \Rightarrow heap \Rightarrow bool$

**where**  $sc\text{-}preallocated == sc.preallocated \ TYPE('m)$



**abbreviation**  $sc\text{-}start\text{-}tid :: 'md \text{ prog} \Rightarrow thread\text{-}id$   
**where**  $sc\text{-}start\text{-}tid \equiv sc.start\text{-}tid \text{ TYPE}('md)$

**abbreviation**  $sc\text{-}start\text{-}heap\text{-}ok :: 'm \text{ prog} \Rightarrow bool$   
**where**  $sc\text{-}start\text{-}heap\text{-}ok \equiv sc.start\text{-}heap\text{-}ok \text{ TYPE}('m)$

**abbreviation**  $sc\text{-}start\text{-}heap :: 'm \text{ prog} \Rightarrow heap$   
**where**  $sc\text{-}start\text{-}heap \equiv sc.start\text{-}heap \text{ TYPE}('m)$

**abbreviation**  $sc\text{-}start\text{-}state ::$   
 $(cname \Rightarrow mname \Rightarrow ty \text{ list} \Rightarrow ty \Rightarrow 'm \Rightarrow addr \text{ val list} \Rightarrow 'x)$   
 $\Rightarrow 'm \text{ prog} \Rightarrow cname \Rightarrow mname \Rightarrow addr \text{ val list} \Rightarrow (addr, thread\text{-}id, 'x, heap, addr) \text{ state}$   
**where**  
 $sc\text{-}start\text{-}state \text{ f } P \equiv sc.start\text{-}state \text{ TYPE}('m) \text{ P f } P$

**abbreviation**  $sc\text{-}wf\text{-}start\text{-}state :: 'm \text{ prog} \Rightarrow cname \Rightarrow mname \Rightarrow addr \text{ val list} \Rightarrow bool$   
**where**  $sc\text{-}wf\text{-}start\text{-}state \text{ P} \equiv sc.wf\text{-}start\text{-}state \text{ TYPE}('m) \text{ P P}$

**notation**  $sc.conf \text{ } (-, - \vdash sc - : \leq - \text{ } [51, 51, 51, 51] \text{ } 50)$   
**notation**  $sc.conf s \text{ } (-, - \vdash sc - [:\leq] - \text{ } [51, 51, 51, 51] \text{ } 50)$   
**notation**  $sc.hext \text{ } (- \trianglelefteq_{sc} - \text{ } [51, 51] \text{ } 50)$

**lemma**  $sc\text{-}start\text{-}heap\text{-}ok$ :  $sc\text{-}start\text{-}heap\text{-}ok \text{ P}$   
**apply**( $simp \text{ add: } sc.start\text{-}heap\text{-}ok\text{-}def \text{ } sc.start\text{-}heap\text{-}data\text{-}def \text{ } initialization\text{-}list\text{-}def \text{ } sc.create\text{-}initial\text{-}object\text{-}simps$   
 $sc\text{-}allocate\text{-}def \text{ } sys\text{-}xcpts\text{-}list\text{-}def \text{ } case\text{-}option\text{-}conv\text{-}if \text{ } new\text{-}Addr\text{-}SomeI \text{ } del: blank.simps \text{ } split \text{ } del: option.split$   
 $if\text{-}split$ )  
**done**

**lemma**  $sc\text{-}wf\text{-}start\text{-}state\text{-}iff$ :  
 $sc\text{-}wf\text{-}start\text{-}state \text{ P } C \text{ M } vs \longleftrightarrow (\exists \text{ Ts } T \text{ meth } D. \text{ P } \vdash C \text{ sees } M: Ts \rightarrow T = \lfloor meth \rfloor \text{ in } D \wedge \text{ P, } sc\text{-}start\text{-}heap$   
 $\text{ P } \vdash sc \text{ vs } [:\leq] \text{ Ts})$   
**by**( $simp \text{ add: } sc.wf\text{-}start\text{-}state.simps \text{ } sc\text{-}start\text{-}heap\text{-}ok$ )

**lemma**  $sc\text{-}heap$ :  
 $heap \text{ addr2thread}\text{-}id \text{ thread}\text{-}id2addr \text{ } (sc\text{-}allocate \text{ P}) \text{ } sc\text{-}typeof\text{-}addr \text{ } sc\text{-}heap\text{-}write \text{ P}$

**proof**  
**fix**  $h' \text{ a } h \text{ hT}$   
**assume**  $(h', a) \in sc\text{-}allocate \text{ P } h \text{ hT}$   
**thus**  $sc\text{-}typeof\text{-}addr \text{ h' a} = \lfloor hT \rfloor$   
**by**( $auto \text{ simp add: } sc\text{-}allocate\text{-}def \text{ } sc\text{-}typeof\text{-}addr\text{-}def \text{ } dest: new\text{-}Addr\text{-}SomeD \text{ } split: if\text{-}split\text{-}asm$ )  
**next**  
**fix**  $h' \text{ h } hT \text{ a}$   
**assume**  $(h', a) \in sc\text{-}allocate \text{ P } h \text{ hT}$   
**from**  $this[symmetric]$  **show**  $h \trianglelefteq_{sc} h'$   
**by**( $fastforce \text{ simp add: } sc\text{-}allocate\text{-}def \text{ } sc\text{-}typeof\text{-}addr\text{-}def \text{ } sc.hext\text{-}def \text{ } dest: new\text{-}Addr\text{-}SomeD \text{ } intro!:$   
 $map\text{-}leI$ )  
**next**  
**fix**  $h \text{ a } al \text{ v } h'$   
**assume**  $sc\text{-}heap\text{-}write \text{ h a } al \text{ v } h'$   
**thus**  $h \trianglelefteq_{sc} h'$   
**by**( $cases \text{ al})(auto \text{ intro!: } sc.hextI \text{ simp add: } sc\text{-}typeof\text{-}addr\text{-}def$ )  
**qed**  $simp$

**interpretation** *sc*:

*heap*  
*addr2thread-id*  
*thread-id2addr*  
*sc-spurious-wakeups*  
*sc-empty*  
*sc-allocate P*  
*sc-typeof-addr*  
*sc-heap-read*  
*sc-heap-write*  
**for** *P* **by**(*rule sc-heap*)

**lemma** *sc-hext-new*:

$h \ a = \text{None} \implies h \sqsubseteq_{sc} h(a \mapsto \text{arobj})$

**by**(*rule sc.hextI*)(*auto simp add: sc-typeof-addr-def dest!: new-Addr-SomeD*)

**lemma** *sc-hext-upd-obj*:  $h \ a = \text{Some } (\text{Obj } C \ fs) \implies h \sqsubseteq_{sc} h(a \mapsto (\text{Obj } C \ fs'))$

**by**(*rule sc.hextI*)(*auto simp: fun-upd-apply sc-typeof-addr-def*)

**lemma** *sc-hext-upd-arr*:  $\llbracket h \ a = \text{Some } (\text{Arr } T \ f \ e); \text{length } e = \text{length } e' \rrbracket \implies h \sqsubseteq_{sc} h(a \mapsto (\text{Arr } T \ f' \ e'))$

**by**(*rule sc.hextI*)(*auto simp: fun-upd-apply sc-typeof-addr-def*)

### 8.1.3 Conformance

**definition** *sc-fconf* ::  $'m \text{ prog} \Rightarrow \text{cname} \Rightarrow \text{heap} \Rightarrow \text{fields} \Rightarrow \text{bool}$   $(-, -, - \vdash_{sc} - \sqrt{\ [51, 51, 51, 51] \ 50})$

**where**  $P, C, h \vdash_{sc} fs \sqrt{\ } = (\forall F \ D \ T \ fm. P \vdash C \text{ has } F:T \ (fm) \text{ in } D \longrightarrow (\exists v. fs(F, D) = \text{Some } v \wedge P, h \vdash_{sc} v : \leq T))$

**primrec** *sc-oconf* ::  $'m \text{ prog} \Rightarrow \text{heap} \Rightarrow \text{heapobj} \Rightarrow \text{bool}$   $(-, - \vdash_{sc} - \sqrt{\ [51, 51, 51] \ 50})$

**where**

$P, h \vdash_{sc} \text{Obj } C \ fs \sqrt{\ } \longleftrightarrow \text{is-class } P \ C \wedge P, C, h \vdash_{sc} fs \sqrt{\ }$

$| P, h \vdash_{sc} \text{Arr } T \ fs \ el \sqrt{\ } \longleftrightarrow \text{is-type } P \ (T[]) \wedge P, \text{Object}, h \vdash_{sc} fs \sqrt{\ } \wedge (\forall v \in \text{set } el. P, h \vdash_{sc} v : \leq T)$

**definition** *sc-hconf* ::  $'m \text{ prog} \Rightarrow \text{heap} \Rightarrow \text{bool}$   $(- \vdash_{sc} - \sqrt{\ [51, 51] \ 50})$

**where**  $P \vdash_{sc} h \sqrt{\ } \longleftrightarrow (\forall a \ \text{obj}. h \ a = \text{Some } \text{obj} \longrightarrow P, h \vdash_{sc} \text{obj} \sqrt{\ })$

**interpretation** *sc*: *heap-conf-base*

*addr2thread-id*  
*thread-id2addr*  
*sc-spurious-wakeups*  
*sc-empty*  
*sc-allocate P*  
*sc-typeof-addr*  
*sc-heap-read*  
*sc-heap-write*  
*sc-hconf P*  
*P*

**for** *P* .

**declare** *sc.typeof-addr-thread-id2-addr-addr2thread-id* [*simp del*]

**lemma** *sc-conf-upd-obj*:  $h \ a = \text{Some } (\text{Obj } C \ fs) \implies (P, h(a \mapsto (\text{Obj } C \ fs'))) \vdash_{sc} x : \leq T = (P, h \vdash_{sc} x : \leq T)$

```

apply (unfold sc.conf-def)
apply (rule val.induct)
apply (auto simp:fun-upd-apply)
apply (auto simp add: sc-typeof-addr-def split: if-split-asm)
done

```

**lemma** *sc-conf-upd-arr*:  $h\ a = \text{Some}(\text{Arr } T\ f\ el) \implies (P, h(a \mapsto (\text{Arr } T\ f'\ el'))) \vdash_{sc} x : \leq T' = (P, h \vdash_{sc} x : \leq T')$

```

apply(unfold sc.conf-def)
apply (rule val.induct)
apply (auto simp:fun-upd-apply)
apply(auto simp add: sc-typeof-addr-def split: if-split-asm)
done

```

**lemma** *sc-oconf-hext*:  $P, h \vdash_{sc} \text{obj } \checkmark \implies h \trianglelefteq_{sc} h' \implies P, h' \vdash_{sc} \text{obj } \checkmark$   
**by**(cases obj)(fastforce elim: sc.conf-hext simp add: sc-fconf-def)+

**lemma** *sc-oconf-init-fields*:

```

  assumes  $P \vdash C \text{ has-fields FDTs}$ 
  shows  $P, h \vdash_{sc} (\text{Obj } C\ (\text{init-fields FDTs})) \checkmark$ 
using assms has-fields-is-class[OF assms]
by(auto simp add: has-field-def init-fields-def sc-fconf-def split-def o-def map-of-map[simplified split-def,
where  $f = \lambda p. \text{default-val } (fst\ p)$ ] dest: has-fields-fun)

```

**lemma** *sc-oconf-init*:

```

  is-htype  $P\ hT \implies P, h \vdash_{sc} \text{blank } P\ hT \checkmark$ 
by(cases hT)(auto simp add: sc-fconf-def has-field-def init-fields-def split-def o-def map-of-map[simplified
split-def, where  $f = \lambda p. \text{default-val } (fst\ p)$ ] dest: has-fields-fun)

```

**lemma** *sc-oconf-fupd* [intro?]:

```

   $\llbracket P \vdash C \text{ has } F:T\ (fm) \text{ in } D; P, h \vdash_{sc} v : \leq T; P, h \vdash_{sc} (\text{Obj } C\ fs) \checkmark \rrbracket$ 
   $\implies P, h \vdash_{sc} (\text{Obj } C\ (fs((F,D) \mapsto v))) \checkmark$ 
unfolding has-field-def
by(auto simp add: sc-fconf-def has-field-def dest: has-fields-fun)

```

**lemma** *sc-oconf-fupd-arr* [intro?]:

```

   $\llbracket P, h \vdash_{sc} v : \leq T; P, h \vdash_{sc} (\text{Arr } T\ f\ el) \checkmark \rrbracket$ 
   $\implies P, h \vdash_{sc} (\text{Arr } T\ f\ (el[i := v])) \checkmark$ 
by(auto dest: subsetD[OF set-update-subset-insert])

```

**lemma** *sc-oconf-fupd-arr-fields*:

```

   $\llbracket P \vdash \text{Object has } F:T\ (fm) \text{ in Object}; P, h \vdash_{sc} v : \leq T; P, h \vdash_{sc} (\text{Arr } T'\ f\ el) \checkmark \rrbracket$ 
   $\implies P, h \vdash_{sc} (\text{Arr } T'\ (f((F, \text{Object}) \mapsto v))\ el) \checkmark$ 
by(auto dest: has-fields-fun simp add: sc-fconf-def has-field-def)

```

**lemma** *sc-oconf-new*:  $\llbracket P, h \vdash_{sc} \text{obj } \checkmark; h\ a = \text{None} \rrbracket \implies P, h(a \mapsto \text{arrobj}) \vdash_{sc} \text{obj } \checkmark$   
**by**(erule sc-oconf-hext)(rule sc-hext-new)

**lemmas** *sc-oconf-upd-obj* = *sc-oconf-hext* [OF - *sc-hext-upd-obj*]

**lemma** *sc-oconf-upd-arr*:

```

  assumes  $P, h \vdash_{sc} \text{obj } \checkmark$ 
  and ha:  $h\ a = \lfloor \text{Arr } T\ f\ el \rfloor$ 
  shows  $P, h(a \mapsto \text{Arr } T\ f'\ el') \vdash_{sc} \text{obj } \checkmark$ 

```

**using** *assms*

**by**(*cases obj*)(*auto simp add: sc-conf-upd-arr*[**where**  $h=h$ ,  $OF\ ha$ ] *sc-fconf-def*)

**lemma** *sc-hconfD*:  $\llbracket P \vdash_{sc} h \checkmark; h\ a = \text{Some}\ obj \rrbracket \implies P, h \vdash_{sc} obj \checkmark$

**unfolding** *sc-hconf-def* **by** *blast*

**lemmas** *sc-preallocated-new* = *sc.preallocated-hext*[ $OF - sc-hext-new$ ]

**lemmas** *sc-preallocated-upd-obj* = *sc.preallocated-hext* [ $OF - sc-hext-upd-obj$ ]

**lemmas** *sc-preallocated-upd-arr* = *sc.preallocated-hext* [ $OF - sc-hext-upd-arr$ ]

**lemma** *sc-hconf-new*:  $\llbracket P \vdash_{sc} h \checkmark; h\ a = \text{None}; P, h \vdash_{sc} obj \checkmark \rrbracket \implies P \vdash_{sc} h(a \mapsto obj) \checkmark$

**unfolding** *sc-hconf-def*

**by**(*auto intro: sc-oconf-new*)

**lemma** *sc-hconf-upd-obj*:  $\llbracket P \vdash_{sc} h \checkmark; h\ a = \text{Some}\ (Obj\ C\ fs); P, h \vdash_{sc} (Obj\ C\ fs') \checkmark \rrbracket \implies P \vdash_{sc} h(a \mapsto (Obj\ C\ fs')) \checkmark$

**unfolding** *sc-hconf-def*

**by**(*auto intro: sc-oconf-upd-obj simp del: sc-oconf.simps*)

**lemma** *sc-hconf-upd-arr*:  $\llbracket P \vdash_{sc} h \checkmark; h\ a = \text{Some}\ (Arr\ T\ f\ el); P, h \vdash_{sc} (Arr\ T\ f'\ el') \checkmark \rrbracket \implies P \vdash_{sc} h(a \mapsto (Arr\ T\ f'\ el')) \checkmark$

**unfolding** *sc-hconf-def*

**by**(*auto intro: sc-oconf-upd-arr simp del: sc-oconf.simps*)

**lemma** *sc-heap-conf*:

*heap-conf addr2thread-id thread-id2addr sc-empty (sc-allocate P) sc-typeof-addr sc-heap-write (sc-hconf P) P*

**proof**

**show**  $P \vdash_{sc} sc\text{-empty} \checkmark$  **by**(*simp add: sc-hconf-def*)

**next**

**fix**  $h\ a\ hT$

**assume** *sc-typeof-addr*  $h\ a = \lfloor hT \rfloor\ P \vdash_{sc} h \checkmark$

**thus** *is-htype*  $P\ hT$

**by**(*auto simp add: sc-typeof-addr-def sc-oconf-def dest!: sc-hconfD split: heapobj.split-asm*)

**next**

**fix**  $h\ h'\ hT\ a$

**assume**  $P \vdash_{sc} h \checkmark\ (h', a) \in sc\text{-allocate}\ P\ h\ hT\ is\text{-htype}\ P\ hT$

**thus**  $P \vdash_{sc} h' \checkmark$

**by**(*auto simp add: sc-allocate-def dest!: new-Addr-SomeD intro: sc-hconf-new sc-oconf-init split: if-split-asm*)

**next**

**fix**  $h\ a\ al\ T\ v\ h'$

**assume**  $P \vdash_{sc} h \checkmark$

**and** *sc.addr-loc-type*  $P\ h\ a\ al\ T$

**and**  $P, h \vdash_{sc} v \leq T$

**and** *sc-heap-write*  $h\ a\ al\ v\ h'$

**thus**  $P \vdash_{sc} h' \checkmark$

**by**(*cases al*)(*fastforce elim!: sc.addr-loc-type.cases simp add: sc-typeof-addr-def intro: sc-hconf-upd-obj sc-oconf-fupd sc-hconfD sc-hconf-upd-arr sc-oconf-fupd-arr sc-oconf-fupd-arr-fields*)+

**qed**

**interpretation** *sc*: *heap-conf*

*addr2thread-id*

*thread-id2addr*

*sc-spurious-wakeups*  
*sc-empty*  
*sc-allocate P*  
*sc-typeof-addr*  
*sc-heap-read*  
*sc-heap-write*  
*sc-hconf P*  
*P*

**for** *P*

**by**(*rule sc-heap-conf*)

**lemma** *sc-heap-progress*:

*heap-progress addr2thread-id thread-id2addr sc-empty (sc-allocate P) sc-typeof-addr sc-heap-read*  
*sc-heap-write (sc-hconf P) P*

**proof**

**fix** *h a al T*

**assume** *hconf*:  $P \vdash_{sc} h \checkmark$

**and** *alt*: *sc.addr-loc-type P h a al T*

**from** *alt* **obtain** *arobj* **where** *arobj*:  $h a = \lfloor arobj \rfloor$

**by**(*auto elim!*: *sc.addr-loc-type.cases simp add: sc-typeof-addr-def*)

**from** *alt* **show**  $\exists v. sc\text{-heap-read } h a al v \wedge P, h \vdash_{sc} v : \leq T$

**proof**(*cases*)

**case** (*addr-loc-type-field U F fm D*)

**note** [*simp*] =  $\langle al = CField D F \rangle$

**show** *?thesis*

**proof**(*cases arobj*)

**case** (*Obj C' fs*)

**with**  $\langle sc\text{-typeof-addr } h a = \lfloor U \rfloor \rangle arobj$

**have** [*simp*]:  $C' = class\text{-type-of } U$  **by**(*auto simp add: sc-typeof-addr-def*)

**from** *hconf arobj Obj* **have**  $P, h \vdash_{sc} Obj (class\text{-type-of } U) fs \checkmark$  **by**(*auto dest: sc-hconfD*)

**with**  $\langle P \vdash class\text{-type-of } U \text{ has } F:T (fm) \text{ in } D \rangle$  **obtain** *v*

**where** *fs* (*F*, *D*) =  $\lfloor v \rfloor$   $P, h \vdash_{sc} v : \leq T$  **by**(*fastforce simp add: sc-fconf-def*)

**thus** *?thesis* **using** *Obj arobj* **by**(*auto intro: sc-heap-read.intros*)

**next**

**case** (*Arr T' f el*)

**with**  $\langle sc\text{-typeof-addr } h a = \lfloor U \rfloor \rangle arobj$

**have** [*simp*]:  $U = Array\text{-type } T' (length\ el)$  **by**(*auto simp add: sc-typeof-addr-def*)

**from** *hconf arobj Arr* **have**  $P, h \vdash_{sc} Arr T' f el \checkmark$  **by**(*auto dest: sc-hconfD*)

**from**  $\langle P \vdash class\text{-type-of } U \text{ has } F:T (fm) \text{ in } D \rangle$  **have** [*simp*]:  $D = Object$

**by**(*auto dest: has-field-decl-above*)

**with**  $\langle P, h \vdash_{sc} Arr T' f el \checkmark \rangle \langle P \vdash class\text{-type-of } U \text{ has } F:T (fm) \text{ in } D \rangle$

**obtain** *v* **where** *f* (*F*, *Object*) =  $\lfloor v \rfloor$   $P, h \vdash_{sc} v : \leq T$

**by**(*fastforce simp add: sc-fconf-def*)

**thus** *?thesis* **using** *Arr arobj* **by**(*auto intro: sc-heap-read.intros*)

**qed**

**next**

**case** (*addr-loc-type-cell n' n*)

**with** *arobj* **obtain** *f el*

**where** [*simp*]: *arobj* = *Arr T f el*

**by**(*cases arobj*)(*auto simp add: sc-typeof-addr-def*)

**from** *addr-loc-type-cell arobj*

**have** [*simp*]:  $al = ACell\ n\ n < length\ el$  **by**(*auto simp add: sc-typeof-addr-def*)

**from** *hconf arobj* **have**  $P, h \vdash_{sc} Arr T f el \checkmark$  **by**(*auto dest: sc-hconfD*)

**hence**  $P, h \vdash_{sc} el ! n : \leq T$  **by**(*fastforce*)

```

    thus ?thesis using arobj by(fastforce intro: sc-heap-read.intros)
  qed
next
  fix h a al T v
  assume alt: sc.addr-loc-type P h a al T
  from alt obtain arobj where arobj: h a = [arobj]
  by(auto elim!: sc.addr-loc-type.cases simp add: sc.typeof-addr-def)
  thus  $\exists h'. \text{sc-heap-write } h \ a \ al \ v \ h'$  using alt
  by(cases arobj)(fastforce intro: sc-heap-write.intros elim!: sc.addr-loc-type.cases simp add: sc.typeof-addr-def
dest: has-field-decl-above)+
  qed

```

**interpretation** *sc: heap-progress*

*addr2thread-id*  
*thread-id2addr*  
*sc-spurious-wakeups*  
*sc-empty*  
*sc-allocate P*  
*sc.typeof-addr*  
*sc-heap-read*  
*sc-heap-write*  
*sc-hconf P*  
*P*

**for** *P*

**by**(rule *sc-heap-progress*)

**lemma** *sc-heap-conf-read*:

*heap-conf-read addr2thread-id thread-id2addr sc-empty (sc-allocate P) sc.typeof-addr sc-heap-read*  
*sc-heap-write (sc-hconf P) P*

**proof**

fix h a al v T  
 assume read: *sc-heap-read* h a al v  
 and alt: *sc.addr-loc-type* P h a al T  
 and hconf:  $P \vdash_{sc} h \ \checkmark$   
 thus  $P, h \vdash_{sc} v : \leq T$

by(auto elim!: *sc-heap-read.cases sc.addr-loc-type.cases simp add: sc.typeof-addr-def*)(fastforce dest!:  
*sc-hconfD simp add: sc.fconf-def*)+

**qed**

**interpretation** *sc: heap-conf-read*

*addr2thread-id*  
*thread-id2addr*  
*sc-spurious-wakeups*  
*sc-empty*  
*sc-allocate P*  
*sc.typeof-addr*  
*sc-heap-read*  
*sc-heap-write*  
*sc-hconf P*  
*P*

**for** *P*

**by**(rule *sc-heap-conf-read*)

**abbreviation** *sc-deterministic-heap-ops* :: 'm prog  $\Rightarrow$  bool

**where** *sc-deterministic-heap-ops*  $\equiv$  *sc.deterministic-heap-ops* *TYPE*('m)

**lemma** *sc-deterministic-heap-ops*:  $\neg$  *sc-spurious-wakeups*  $\implies$  *sc-deterministic-heap-ops* *P*  
**by**(*rule* *sc.deterministic-heap-opsI*)(*auto elim*: *sc-heap-read.cases* *sc-heap-write.cases* *simp* *add*: *sc-allocate-def*)

#### 8.1.4 Code generation

**code-pred**

(*modes*:  $i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow \text{bool}$ ,  $i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}$ )  
*sc-heap-read* .

**code-pred**

(*modes*:  $i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow \text{bool}$ ,  $i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}$ )  
*sc-heap-write* .

**lemma** *eval-sc-heap-read-i-i-i-o*:

*Predicate.eval* (*sc-heap-read-i-i-i-o* *h ad al*) = *sc-heap-read* *h ad al*

**by**(*auto elim*: *sc-heap-read-i-i-i-oE* *intro*: *sc-heap-read-i-i-i-oI* *intro!*: *ext*)

**lemma** *eval-sc-heap-write-i-i-i-i-o*:

*Predicate.eval* (*sc-heap-write-i-i-i-i-o* *h ad al v*) = *sc-heap-write* *h ad al v*

**by**(*auto elim*: *sc-heap-write-i-i-i-i-oE* *intro*: *sc-heap-write-i-i-i-i-oI* *intro!*: *ext*)

**end**

**theory** *SC-Interp*

**imports**

*SC*  
*../Compiler/Correctness*  
*../J/Deadlocked*  
*../BV/JVMDeadlocked*

**begin**

Do not interpret these locales, it just takes too long to generate all definitions and theorems.

**lemma** *sc-J-typesafe*:

*J-typesafe* *addr2thread-id* *thread-id2addr* *sc-empty* (*sc-allocate* *P*) *sc-typeof-addr* *sc-heap-read* *sc-heap-write*  
(*sc-hconf* *P*) *P*

**by** *unfold-locales*

**lemma** *sc-JVM-typesafe*:

*JVM-typesafe* *addr2thread-id* *thread-id2addr* *sc-empty* (*sc-allocate* *P*) *sc-typeof-addr* *sc-heap-read*  
*sc-heap-write* (*sc-hconf* *P*) *P*

**by** *unfold-locales*

**lemma** *compP2-compP1-cons*:

*is-type* (*compP2* (*compP1* *P*)) = *is-type* *P*  
*is-class* (*compP2* (*compP1* *P*)) = *is-class* *P*  
*sc.addr-loc-type* (*compP2* (*compP1* *P*)) = *sc.addr-loc-type* *P*  
*sc.conf* (*compP2* (*compP1* *P*)) = *sc.conf* *P*

**by**(*simp-all* *add*: *compP2-def* *heap-base.compP-conf* *heap-base.compP-addr-loc-type* *fun-eq-iff* *split*: *addr-loc.splits*)

**lemma** *sc-J-JVM-conf-read*:

*J-JVM-conf-read* *addr2thread-id* *thread-id2addr* *sc-empty* (*sc-allocate* *P*) *sc-typeof-addr* *sc-heap-read*

```

sc-heap-write (sc-hconf P) P
apply(rule J-JVM-conf-read.intro)
apply(rule J1-JVM-conf-read.intro)
apply(rule JVM-conf-read.intro)
prefer 2
apply(rule JVM-heap-conf.intro)
apply(rule JVM-heap-conf-base'.intro)
apply(unfold compP2-def compP1-def compP-heap compP-heap-conf compP-heap-conf-read)
apply unfold-locales
done

end

```

## 8.2 Sequential consistency with efficient data structures

```

theory SC-Collections
imports
  ../Common/Conform

  ../Basic/JT-ICF
  MM
begin

```

```

hide-const (open) new-Addr
hide-fact (open) new-Addr-SomeD new-Addr-SomeI

```

### 8.2.1 Objects and Arrays

```

type-synonym fields = (char, (cname, addr val) lm) tm
type-synonym array-cells = (nat, addr val) rbt
type-synonym array-fields = (vname, addr val) lm

```

```

datatype heapobj
  = Obj cname fields — class instance with class name and fields
  | Arr ty nat array-fields array-cells — element type, size, fields and cell contents

```

```

lemma rec-heapobj [simp]: rec-heapobj = case-heapobj
by(auto intro!: ext split: heapobj.split)

```

```

primrec obj-ty :: heapobj  $\Rightarrow$  htype
where
  obj-ty (Obj c f) = Class-type c
  | obj-ty (Arr t si f e) = Array-type t si

```

```

fun is-Arr :: heapobj  $\Rightarrow$  bool where
  is-Arr (Obj C fs) = False
  | is-Arr (Arr T si f el) = True

```

```

lemma is-Arr-conv:
  is-Arr arrobj = ( $\exists T si f el. arrobj = Arr T si f el$ )
by(cases arrobj, auto)

```

```

lemma is-ArrE:
   $\llbracket is-Arr arrobj; \bigwedge T si f el. arrobj = Arr T si f el \implies thesis \rrbracket \implies thesis$ 

```



$\llbracket \neg \text{is-Arr } \text{arobj}; \bigwedge C \text{ fs. } \text{arobj} = \text{Obj } C \text{ fs} \implies \text{thesis} \rrbracket \implies \text{thesis}$   
**by**(cases arobj, auto)+

**definition** *init-fields* :: ((vname × cname) × ty) list ⇒ fields

**where**

*init-fields FDTs* ≡  
*foldr* (λ((F, D), T) fields.  
     let F' = String.explode F  
     in *tm-update* F' (*lm-update* D (default-val T)  
         (case *tm-lookup* F' fields of None ⇒ *lm-empty* () | Some *lm* ⇒ *lm*))  
*fields*)  
*FDTs* (*tm-empty* ())

**definition** *init-fields-array* :: (vname × ty) list ⇒ array-fields

**where**

*init-fields-array* ≡ *lm.to-map* ∘ *map* (λ(F, T). (F, default-val T))

**definition** *init-cells* :: ty ⇒ nat ⇒ array-cells

**where** *init-cells* T n = *foldl* (λcells i. *rm-update* i (default-val T) cells) (*rm-empty* ()) [0..*n*]

**primrec** — a new, blank object with default values in all fields:

*blank* :: 'm prog ⇒ htype ⇒ heapobj

**where**

*blank* P (Class-type C) = *Obj* C (*init-fields* (*map* (λ(FD, (T, fm)). (FD, T)) (*TypeRel.fields* P C)))  
| *blank* P (Array-type T n) =  
*Arr* T n (*init-fields-array* (*map* (λ((F, D), (T, fm)). (F, T)) (*TypeRel.fields* P Object))) (*init-cells*  
T n)

**lemma** *obj-ty-blank* [iff]: *obj-ty* (*blank* P hT) = hT

**by**(cases hT) *simp-all*

## 8.2.2 Heap

**type-synonym** *heap* = (*addr*, *heapobj*) *rbt*

**translations**

(*type*) *heap* <= (*type*) (*nat*, *heapobj*) *rbt*

**abbreviation** *sc-empty* :: *heap*

**where** *sc-empty* ≡ *rm-empty* ()

**fun** *the-obj* :: *heapobj* ⇒ cname × fields **where**

*the-obj* (*Obj* C fs) = (C, fs)

**fun** *the-arr* :: *heapobj* ⇒ ty × nat × array-fields × array-cells **where**

*the-arr* (*Arr* T si f el) = (T, si, f, el)

**abbreviation**

*cname-of* :: *heap* ⇒ *addr* ⇒ cname **where**

*cname-of* hp a == *fst* (*the-obj* (*the* (*rm-lookup* a hp)))

**definition** *new-Addr* :: *heap* ⇒ *addr* option

**where** *new-Addr* h = *Some* (case *rm-max* h (λ-. *True*) of None ⇒ 0 | *Some* (a, -) ⇒ a + 1)

**definition**  $sc\text{-}allocate :: 'm\ prog \Rightarrow heap \Rightarrow htype \Rightarrow (heap \times addr)\ set$   
**where**

$sc\text{-}allocate\ P\ h\ hT =$   
 $(case\ new\text{-}Addr\ h\ of\ None \Rightarrow \{\}\$   
 $\quad | Some\ a \Rightarrow \{(rm\text{-}update\ a\ (blank\ P\ hT)\ h,\ a)\})$

**definition**  $sc\text{-}typeof\text{-}addr :: heap \Rightarrow addr \Rightarrow htype\ option$   
**where**  $sc\text{-}typeof\text{-}addr\ h\ a = map\text{-}option\ obj\text{-}ty\ (rm\text{-}lookup\ a\ h)$

**inductive**  $sc\text{-}heap\text{-}read :: heap \Rightarrow addr \Rightarrow addr\text{-}loc \Rightarrow addr\ val \Rightarrow bool$   
**for**  $h :: heap$  **and**  $a :: addr$

**where**

$Obj: \llbracket rm\text{-}lookup\ a\ h = \lfloor Obj\ C\ fs \rfloor; tm\text{-}lookup\ (String.\text{explode}\ F)\ fs = \lfloor fs' \rfloor; lm\text{-}lookup\ D\ fs' = \lfloor v \rfloor \rrbracket$   
 $\implies sc\text{-}heap\text{-}read\ h\ a\ (CField\ D\ F)\ v$   
 $| Arr: \llbracket rm\text{-}lookup\ a\ h = \lfloor Arr\ T\ si\ f\ el \rfloor; n < si \rrbracket \implies sc\text{-}heap\text{-}read\ h\ a\ (ACell\ n)\ (the\ (rm\text{-}lookup\ n\ el))$   
 $| ArrObj: \llbracket rm\text{-}lookup\ a\ h = \lfloor Arr\ T\ si\ f\ el \rfloor; lm\text{-}lookup\ F\ f = \lfloor v \rfloor \rrbracket \implies sc\text{-}heap\text{-}read\ h\ a\ (CField\ Object\ F)\ v$

**hide-fact** (open)  $Obj\ Arr\ ArrObj$

**inductive-cases**  $sc\text{-}heap\text{-}read\text{-}cases\ [elim!]:$

$sc\text{-}heap\text{-}read\ h\ a\ (CField\ C\ F)\ v$   
 $sc\text{-}heap\text{-}read\ h\ a\ (ACell\ n)\ v$

**inductive**  $sc\text{-}heap\text{-}write :: heap \Rightarrow addr \Rightarrow addr\text{-}loc \Rightarrow addr\ val \Rightarrow heap \Rightarrow bool$

**for**  $h :: heap$  **and**  $a :: addr$

**where**

$Obj:$   
 $\llbracket rm\text{-}lookup\ a\ h = \lfloor Obj\ C\ fs \rfloor; F' = String.\text{explode}\ F;$   
 $h' = rm\text{-}update\ a\ (Obj\ C\ (tm\text{-}update\ F'\ (lm\text{-}update\ D\ v\ (case\ tm\text{-}lookup\ (String.\text{explode}\ F)\ fs\ of\ None \Rightarrow lm\text{-}empty\ ()\ | Some\ fs' \Rightarrow fs'))\ fs))\ h \rrbracket$   
 $\implies sc\text{-}heap\text{-}write\ h\ a\ (CField\ D\ F)\ v\ h'$   
 $| Arr:$   
 $\llbracket rm\text{-}lookup\ a\ h = \lfloor Arr\ T\ si\ f\ el \rfloor; h' = rm\text{-}update\ a\ (Arr\ T\ si\ f\ (rm\text{-}update\ n\ v\ el))\ h \rrbracket$   
 $\implies sc\text{-}heap\text{-}write\ h\ a\ (ACell\ n)\ v\ h'$   
 $| ArrObj:$   
 $\llbracket rm\text{-}lookup\ a\ h = \lfloor Arr\ T\ si\ f\ el \rfloor; h' = rm\text{-}update\ a\ (Arr\ T\ si\ (lm\text{-}update\ F\ v\ f)\ el)\ h \rrbracket$   
 $\implies sc\text{-}heap\text{-}write\ h\ a\ (CField\ Object\ F)\ v\ h'$

**hide-fact** (open)  $Obj\ Arr\ ArrObj$

**inductive-cases**  $sc\text{-}heap\text{-}write\text{-}cases\ [elim!]:$

$sc\text{-}heap\text{-}write\ h\ a\ (CField\ C\ F)\ v\ h'$   
 $sc\text{-}heap\text{-}write\ h\ a\ (ACell\ n)\ v\ h'$

**consts**  $sc\text{-}spurious\text{-}wakeups :: bool$

**lemma**  $new\text{-}Addr\ SomeD: new\text{-}Addr\ h = \lfloor a \rfloor \implies rm\text{-}lookup\ a\ h = None$

**apply**(simp add: new-Addr-def)

**apply**(drule rm.max-None[OF rm.invar])

**apply**(simp add: rm.lookup-correct rel-of-def)

```

apply(clarsimp simp add: rm.lookup-correct)
apply(frule rm.max-Some[OF rm.invar])
apply(clarsimp simp add: rel-of-def)
apply(hypsubst-thin)
apply(rule ccontr)
apply(clarsimp)
apply(drule-tac k'=Suc a in rm.max-Some(2)[OF rm.invar])
apply(auto simp add: rel-of-def)
done

```

**interpretation** *sc*:

```

  heap-base
  addr2thread-id
  thread-id2addr
  sc-spurious-wakeups
  sc-empty
  sc-allocate P
  sc-typeof-addr
  sc-heap-read
  sc-heap-write
for P .

```

Translate notation from *heap-base*

```

abbreviation sc-preallocated :: 'm prog  $\Rightarrow$  heap  $\Rightarrow$  bool
where sc-preallocated == sc.preallocated TYPE('m)

```

```

abbreviation sc-start-tid :: 'md prog  $\Rightarrow$  thread-id
where sc-start-tid  $\equiv$  sc.start-tid TYPE('md)

```

```

abbreviation sc-start-heap-ok :: 'm prog  $\Rightarrow$  bool
where sc-start-heap-ok  $\equiv$  sc.start-heap-ok TYPE('m)

```

```

abbreviation sc-start-heap :: 'm prog  $\Rightarrow$  heap
where sc-start-heap  $\equiv$  sc.start-heap TYPE('m)

```

```

abbreviation sc-start-state ::
  (cname  $\Rightarrow$  mname  $\Rightarrow$  ty list  $\Rightarrow$  ty  $\Rightarrow$  'm  $\Rightarrow$  addr val list  $\Rightarrow$  'x)
   $\Rightarrow$  'm prog  $\Rightarrow$  cname  $\Rightarrow$  mname  $\Rightarrow$  addr val list  $\Rightarrow$  (addr, thread-id, 'x, heap, addr) state
where
  sc-start-state f P  $\equiv$  sc.start-state TYPE('m) P f P

```

```

abbreviation sc-wf-start-state :: 'm prog  $\Rightarrow$  cname  $\Rightarrow$  mname  $\Rightarrow$  addr val list  $\Rightarrow$  bool
where sc-wf-start-state P  $\equiv$  sc.wf-start-state TYPE('m) P P

```

```

notation sc.conf ( $\_, - \vdash_{sc} - : \leq -$  [51,51,51,51] 50)
notation sc.confs ( $\_, - \vdash_{sc} - [:\leq] -$  [51,51,51,51] 50)
notation sc.hext ( $- \trianglelefteq_{sc} -$  [51,51] 50)

```

```

lemma new-Addr-SomeI:  $\exists a. \text{new-Addr } h = \text{Some } a$ 
by(simp add: new-Addr-def)

```

```

lemma sc-start-heap-ok: sc.start-heap-ok P
by(simp add: sc.start-heap-ok-def sc.start-heap-data-def initialization-list-def sc.create-initial-object-simps
sc-allocate-def case-option-conv-if new-Addr-SomeI sys-xcpts-list-def del: blank.simps split del: option.split)

```

*if-split*)

**lemma** *sc-wf-start-state-iff*:

*sc-wf-start-state*  $P \ C \ M \ vs \longleftrightarrow (\exists \ Ts \ T \ meth \ D. P \vdash C \ sees \ M: Ts \rightarrow T = \lfloor meth \rfloor \text{ in } D \wedge P, sc\text{-start-heap}$

$P \vdash_{sc} vs \lfloor \leq \rfloor Ts)$

**by**(*simp add: sc-wf-start-state.simps sc-start-heap-ok*)

**lemma** *sc-heap*:

*heap addr2thread-id thread-id2addr (sc-allocate P) sc-typeof-addr sc-heap-write P*

**proof**

**fix**  $h' \ a \ h \ hT$

**assume**  $(h', a) \in sc\text{-allocate } P \ h \ hT$

**thus** *sc-typeof-addr*  $h' \ a = \lfloor hT \rfloor$

**by**(*auto simp add: sc-allocate-def sc-typeof-addr-def rm.lookup-correct rm.update-correct dest: new-Addr-SomeD split: if-split-asm*)

**next**

**fix**  $h \ h' \ hT \ a$

**assume**  $(h', a) \in sc\text{-allocate } P \ h \ hT$

**from** *this[symmetric]* **show**  $h \trianglelefteq_{sc} h'$

**by**(*fastforce simp add: sc-allocate-def sc-typeof-addr-def sc.hext-def rm.lookup-correct rm.update-correct intro!: map-leI dest: new-Addr-SomeD*)

**next**

**fix**  $h \ a \ al \ v \ h'$

**assume** *sc-heap-write*  $h \ a \ al \ v \ h'$

**thus**  $h \trianglelefteq_{sc} h'$

**by**(*cases al*)(*auto intro!: sc.hextI simp add: sc-typeof-addr-def rm.lookup-correct rm.update-correct*)

**qed** *simp*

**interpretation** *sc*:

*heap*

*addr2thread-id*

*thread-id2addr*

*sc-spurious-wakeups*

*sc-empty*

*sc-allocate P*

*sc-typeof-addr*

*sc-heap-read*

*sc-heap-write*

*P*

**for** *P* **by**(*rule sc-heap*)

**declare** *sc.typeof-addr-thread-id2-addr-addr2thread-id* [*simp del*]

**lemma** *sc-hext-new*:

*rm-lookup a h = None*  $\implies h \trianglelefteq_{sc} rm\text{-update } a \ \text{arobj } h$

**by**(*rule sc.hextI*)(*auto simp add: sc-typeof-addr-def rm.lookup-correct rm.update-correct dest!: new-Addr-SomeD*)

**lemma** *sc-hext-upd-obj*: *rm-lookup a h = Some (Obj C fs)*  $\implies h \trianglelefteq_{sc} rm\text{-update } a \ (Obj \ C \ fs') \ h$

**by**(*rule sc.hextI*)(*auto simp: fun-upd-apply sc-typeof-addr-def rm.lookup-correct rm.update-correct*)

**lemma** *sc-hext-upd-arr*:  $\llbracket rm\text{-lookup } a \ h = \text{Some } (Arr \ T \ si \ f \ e) \rrbracket \implies h \trianglelefteq_{sc} rm\text{-update } a \ (Arr \ T \ si \ f' \ e') \ h$

**by**(*rule sc.hextI*)(*auto simp: fun-upd-apply sc-typeof-addr-def rm.lookup-correct rm.update-correct*)

### 8.2.3 Conformance

**definition**  $sc\text{-}oconf :: 'm\ prog \Rightarrow heap \Rightarrow heapobj \Rightarrow bool \quad (-, - \vdash_{sc} - \sqrt{[51, 51, 51] \ 50})$

**where**

$P, h \vdash_{sc} obj \sqrt{} \equiv$   
 (case obj of  
   Obj C fs  $\Rightarrow$   
     is-class P C  $\wedge$   
      $(\forall F D T fm. P \vdash C \text{ has } F:T (fm) \text{ in } D \longrightarrow$   
        $(\exists fs' v. tm\text{-}\alpha fs (String.explode F) = Some fs' \wedge lm\text{-}\alpha fs' D = Some v \wedge P, h \vdash_{sc} v : \leq T))$   
   | Arr T si f el  $\Rightarrow$   
     is-type P (T[])  $\wedge (\forall n. n < si \longrightarrow (\exists v. rm\text{-}\alpha el n = Some v \wedge P, h \vdash_{sc} v : \leq T)) \wedge$   
      $(\forall F T fm. P \vdash Object \text{ has } F:T (fm) \text{ in } Object \longrightarrow (\exists v. lm\text{-}lookup F f = Some v \wedge P, h \vdash_{sc} v : \leq T)))$

**definition**  $sc\text{-}hconf :: 'm\ prog \Rightarrow heap \Rightarrow bool \quad (- \vdash_{sc} - \sqrt{[51, 51] \ 50})$

**where**  $P \vdash_{sc} h \sqrt{} \longleftrightarrow (\forall a obj. rm\text{-}\alpha h a = Some obj \longrightarrow P, h \vdash_{sc} obj \sqrt{})$

**interpretation**  $sc$ :

heap-conf-base  
 addr2thread-id  
 thread-id2addr  
 sc-spurious-wakeups  
 sc-empty  
 sc-allocate P  
 sc-typeof-addr  
 sc-heap-read  
 sc-heap-write  
 sc-hconf P  
 P

**for** P

.

**lemma**  $sc\text{-}conf\text{-}upd\text{-}obj$ :  $rm\text{-}lookup a h = Some (Obj C fs) \implies (P, rm\text{-}update a (Obj C fs') h \vdash_{sc} x : \leq T) = (P, h \vdash_{sc} x : \leq T)$

**apply** (unfold sc.conf-def)

**apply** (rule val.induct)

**apply** (auto simp: fun-upd-apply)

**apply** (auto simp add: sc-typeof-addr-def rm.lookup-correct rm.update-correct split: if-split-asm)

**done**

**lemma**  $sc\text{-}conf\text{-}upd\text{-}arr$ :

$rm\text{-}lookup a h = Some (Arr T si f el) \implies (P, rm\text{-}update a (Arr T si f' el') h \vdash_{sc} x : \leq T') = (P, h \vdash_{sc} x : \leq T')$

**apply** (unfold sc.conf-def)

**apply** (rule val.induct)

**apply** (auto simp: fun-upd-apply)

**apply** (auto simp add: sc-typeof-addr-def rm.lookup-correct rm.update-correct split: if-split-asm)

**done**

**lemma**  $sc\text{-}oconf\text{-}hext$ :  $P, h \vdash_{sc} obj \sqrt{} \implies h \sqsubseteq_{sc} h' \implies P, h' \vdash_{sc} obj \sqrt{} \quad$

**unfolding**  $sc\text{-}oconf\text{-}def$

**by** (fastforce split: heapobj.split elim: sc.conf-hext)

**lemma** *map-of-fields-init-fields*:

**assumes** *map-of FDTs*  $(F, D) = \lfloor (T, fm) \rfloor$   
**shows**  $\exists fs' v. tm-\alpha (init-fields (map (\lambda(FD, (T, fm)). (FD, T)) FDTs)) (String.explode F) = \lfloor fs' \rfloor$   
 $\wedge lm-\alpha fs' D = \lfloor v \rfloor \wedge sc.conf P h v T$   
**using** *assms*  
**by**(*induct FDTs*)(*auto simp add: tm.lookup-correct tm.update-correct lm.update-correct init-fields-def String.explode-inject*)

**lemma** *sc-oconf-init-fields*:

**assumes**  $P \vdash C \text{ has-fields FDTs}$   
**shows**  $P, h \vdash_{sc} (Obj C (init-fields (map (\lambda(FD, (T, fm)). (FD, T)) FDTs))) \checkmark$   
**using** *assms has-fields-is-class[OF assms] map-of-fields-init-fields[of FDTs]*  
**by**(*fastforce simp add: has-field-def sc-oconf-def dest: has-fields-fun*)

**lemma** *sc-oconf-init-arr*:

**assumes** *type: is-type*  $P (T \lfloor \rfloor)$   
**shows**  $P, h \vdash_{sc} Arr T n (init-fields-array (map (\lambda((F, D), (T, fm)). (F, T)) (TypeRel.fields P Object))) (init-cells T n) \checkmark$   
**proof** –  
 { **fix**  $n'$   
**assume**  $n' < n$   
 { **fix** *rm* **and**  $k :: nat$   
**assume**  $\forall i < k. \exists v. rm-\alpha rm i = \lfloor v \rfloor \wedge sc.conf P h v T$   
**with**  $\langle n' < n \rangle$  **have**  $\exists v. rm-\alpha (foldl (\lambda cells i. rm-update i (default-val T) cells) rm [k..<n]) n' = \lfloor v \rfloor \wedge sc.conf P h v T$   
**by**(*induct m≡n−k arbitrary: n k rm*)(*auto simp add: rm.update-correct upt-conv-Cons type*)  
 }  
**from** *this*[*of 0 rm-empty ()*]  
**have**  $\exists v. rm-\alpha (foldl (\lambda cells i. rm-update i (default-val T) cells) (rm-empty ()) [0..<n]) n' = \lfloor v \rfloor$   
 $\wedge sc.conf P h v T$  **by** *simp*  
 }  
**moreover**  
 { **fix**  $F T fm$   
**assume**  $P \vdash Object \text{ has } F:T (fm) \text{ in } Object$   
**then obtain** *FDTs* **where** *has*:  $P \vdash Object \text{ has-fields FDTs}$   
**and** *FDTs*: *map-of FDTs*  $(F, Object) = \lfloor (T, fm) \rfloor$   
**by**(*auto simp add: has-field-def*)  
**from** *has* **have** *snd* ‘*fst* ‘ *set FDTs*  $\subseteq \{Object\}$  **by**(*rule Object-has-fields-Object*)  
**with** *FDTs* **have** *map-of*  $(map ((\lambda(F, T). (F, default-val T)) \circ (\lambda((F, D), T, fm). (F, T))) FDTs)$   
 $F = \lfloor default-val T \rfloor$   
**by**(*induct FDTs*) *auto*  
**with** *has FDTs*  
**have**  $\exists v. lm-lookup F (init-fields-array (map (\lambda((F, D), T, fm). (F, T)) (TypeRel.fields P Object)))$   
 $= \lfloor v \rfloor \wedge$   
 $sc.conf P h v T$   
**by**(*auto simp add: init-fields-array-def lm-correct has-field-def*)  
 }  
**ultimately show** *?thesis* **using** *type* **by**(*auto simp add: sc-oconf-def init-cells-def*)  
**qed**

**lemma** *sc-oconf-fupd* [*intro?*]:

$\llbracket P \vdash C \text{ has } F:T (fm) \text{ in } D; P, h \vdash_{sc} v : \leq T; P, h \vdash_{sc} (Obj C fs) \checkmark;$   
 $fs' = (case tm-lookup (String.explode F) fs of None \Rightarrow lm-empty () \mid Some fs' \Rightarrow fs') \rrbracket$   
 $\implies P, h \vdash_{sc} (Obj C (tm-update (String.explode F) (lm-update D v fs') fs)) \checkmark$

**unfolding** *sc-oconf-def has-field-def*  
**apply**(*auto dest: has-fields-fun simp add: lm.update-correct tm.update-correct tm.lookup-correct String.explode-inject*)  
**apply**(*drule (1) has-fields-fun, fastforce*)  
**apply**(*drule (1) has-fields-fun, fastforce*)  
**done**

**lemma** *sc-oconf-fupd-arr* [intro?]:  
 $\llbracket P, h \vdash_{sc} v : \leq T; P, h \vdash_{sc} (Arr\ T\ si\ f\ el) \checkmark \rrbracket$   
 $\implies P, h \vdash_{sc} (Arr\ T\ si\ f\ (rm\ update\ i\ v\ el)) \checkmark$

**unfolding** *sc-oconf-def*  
**by**(*auto simp add: rm.update-correct*)

**lemma** *sc-oconf-fupd-arr-fields*:  
 $\llbracket P \vdash Object\ has\ F:T\ (fm)\ in\ Object; P, h \vdash_{sc} v : \leq T; P, h \vdash_{sc} (Arr\ T'\ si\ f\ el) \checkmark \rrbracket$   
 $\implies P, h \vdash_{sc} (Arr\ T'\ si\ (lm\ update\ F\ v\ f)\ el) \checkmark$

**unfolding** *sc-oconf-def* **by**(*auto dest: has-field-fun simp add: lm-correct*)

**lemma** *sc-oconf-new*:  $\llbracket P, h \vdash_{sc} obj \checkmark; rm\ lookup\ a\ h = None \rrbracket \implies P, rm\ update\ a\ arrobj\ h \vdash_{sc} obj \checkmark$   
**by**(*erule sc-oconf-hext*)(*rule sc-hext-new*)

**lemmas** *sc-oconf-upd-obj = sc-oconf-hext* [*OF - sc-hext-upd-obj*]

**lemma** *sc-oconf-upd-arr*:  
**assumes**  $P, h \vdash_{sc} obj \checkmark$   
**and** *ha*:  $rm\ lookup\ a\ h = \lfloor Arr\ T\ si\ f\ el \rfloor$   
**shows**  $P, rm\ update\ a\ (Arr\ T\ si\ f'\ el')\ h \vdash_{sc} obj \checkmark$   
**using** *assms*  
**by**(*fastforce simp add: sc-oconf-def sc-conf-upd-arr*[*OF ha*] *split: heapobj.split*)

**lemma** *sc-oconf-blank*:  $is\ htype\ P\ hT \implies P, h \vdash_{sc} blank\ P\ hT \checkmark$   
**apply**(*cases hT*)  
**apply**(*fastforce dest: map-of-fields-init-fields simp add: has-field-def sc-oconf-def*)  
**by**(*auto intro: sc-oconf-init-arr*)

**lemma** *sc-hconfD*:  $\llbracket P \vdash_{sc} h \checkmark; rm\ lookup\ a\ h = Some\ obj \rrbracket \implies P, h \vdash_{sc} obj \checkmark$   
**unfolding** *sc-hconf-def* **by**(*auto simp add: rm.lookup-correct*)

**lemmas** *sc-preallocated-new = sc.preallocated-hext*[*OF - sc-hext-new*]  
**lemmas** *sc-preallocated-upd-obj = sc.preallocated-hext* [*OF - sc-hext-upd-obj*]  
**lemmas** *sc-preallocated-upd-arr = sc.preallocated-hext* [*OF - sc-hext-upd-arr*]

**lemma** *sc-hconf-new*:  $\llbracket P \vdash_{sc} h \checkmark; rm\ lookup\ a\ h = None; P, h \vdash_{sc} obj \checkmark \rrbracket \implies P \vdash_{sc} rm\ update\ a\ obj\ h \checkmark$   
**unfolding** *sc-hconf-def*  
**by**(*auto intro: sc-oconf-new simp add: rm.lookup-correct rm.update-correct*)

**lemma** *sc-hconf-upd-obj*:  $\llbracket P \vdash_{sc} h \checkmark; rm\ lookup\ a\ h = Some\ (Obj\ C\ fs); P, h \vdash_{sc} (Obj\ C\ fs') \checkmark \rrbracket$   
 $\implies P \vdash_{sc} rm\ update\ a\ (Obj\ C\ fs')\ h \checkmark$   
**unfolding** *sc-hconf-def*  
**by**(*auto intro: sc-oconf-upd-obj simp add: rm.lookup-correct rm.update-correct*)

**lemma** *sc-hconf-upd-arr*:  $\llbracket P \vdash_{sc} h \checkmark; rm\ lookup\ a\ h = Some(Arr\ T\ si\ f\ el); P, h \vdash_{sc} (Arr\ T\ si\ f'\ el') \checkmark \rrbracket \implies P \vdash_{sc} rm\ update\ a\ (Arr\ T\ si\ f'\ el')\ h \checkmark$

**unfolding** *sc-hconf-def*

**by**(*auto intro: sc-oconf-upd-arr simp add: rm.lookup-correct rm.update-correct*)

**lemma** *sc-heap-conf*:

*heap-conf addr2thread-id thread-id2addr sc-empty (sc-allocate P) sc-typeof-addr sc-heap-write (sc-hconf P) P*

**proof**

**show**  $P \vdash_{sc} sc\text{-empty} \checkmark$  **by**(*simp add: sc-hconf-def rm.empty-correct*)

**next**

**fix**  $h\ a\ hT$

**assume** *sc-typeof-addr*  $h\ a = \lfloor hT \rfloor\ P \vdash_{sc} h \checkmark$

**thus** *is-htype*  $P\ hT$

**by**(*auto simp add: sc-typeof-addr-def sc-oconf-def dest!: sc-hconfD split: heapobj.split-asm*)

**next**

**fix**  $h'\ hT\ h\ a$

**assume**  $P \vdash_{sc} h \checkmark\ (h', a) \in sc\text{-allocate}\ P\ h\ hT\ is\text{-htype}\ P\ hT$

**thus**  $P \vdash_{sc} h' \checkmark$

**by**(*auto simp add: sc-allocate-def dest!: new-Addr-SomeD intro: sc-hconf-new sc-oconf-blank split: if-split-asm*)

**next**

**fix**  $h\ a\ al\ T\ v\ h'$

**assume**  $P \vdash_{sc} h \checkmark$

**and** *sc.addr-loc-type*  $P\ h\ a\ al\ T$

**and**  $P, h \vdash_{sc} v \leq T$

**and** *sc-heap-write*  $h\ a\ al\ v\ h'$

**thus**  $P \vdash_{sc} h' \checkmark$

**by**(*cases al*)(*fastforce elim!: sc.addr-loc-type.cases simp add: sc-typeof-addr-def intro: sc-hconf-upd-obj sc-oconf-fupd sc-hconfD sc-hconf-upd-arr sc-oconf-fupd-arr sc-oconf-fupd-arr-fields*)+  
**qed**

**interpretation** *sc*:

*heap-conf*

*addr2thread-id*

*thread-id2addr*

*sc-spurious-wakeups*

*sc-empty*

*sc-allocate P*

*sc-typeof-addr*

*sc-heap-read*

*sc-heap-write*

*sc-hconf P*

*P*

**for** *P*

**by**(*rule sc-heap-conf*)

**lemma** *sc-heap-progress*:

*heap-progress addr2thread-id thread-id2addr sc-empty (sc-allocate P) sc-typeof-addr sc-heap-read sc-heap-write (sc-hconf P) P*

**proof**

**fix**  $h\ a\ al\ T$

**assume** *hconf*:  $P \vdash_{sc} h \checkmark$

**and** *alt*: *sc.addr-loc-type*  $P\ h\ a\ al\ T$

**from** *alt* **obtain** *arobj* **where** *arobj*: *rm.lookup*  $a\ h = \lfloor arobj \rfloor$

**by**(*auto elim!: sc.addr-loc-type.cases simp add: sc-typeof-addr-def*)



```

from alt show  $\exists v. \text{sc-heap-read } h \ a \ al \ v \wedge P, h \vdash_{sc} v : \leq T$ 
proof(cases)
  case (addr-loc-type-field  $U \ F \ fm \ D$ )
    note [simp] =  $\langle al = CField \ D \ F \rangle$ 
    show ?thesis
    proof(cases arrobj)
      case (Obj  $C' \ fs$ )
        with  $\langle \text{sc-typeof-addr } h \ a = \lfloor U \rfloor \rangle$  arrobj
        have [simp]:  $C' = \text{class-type-of } U$  by(auto simp add: sc-typeof-addr-def)
        from hconf arrobj Obj have  $P, h \vdash_{sc} \text{Obj} \ (\text{class-type-of } U) \ fs \ \checkmark$  by(auto dest: sc-hconfD)
        with  $\langle P \vdash \text{class-type-of } U \text{ has } F:T \ (fm) \text{ in } D \rangle$  obtain  $fs' \ v$ 
        where  $tm\text{-lookup} \ (String.explode \ F) \ fs = \lfloor fs' \rfloor \ lm\text{-lookup} \ D \ fs' = \lfloor v \rfloor \ P, h \vdash_{sc} v : \leq T$ 
        by(fastforce simp add: sc-oconf-def  $tm\text{-lookup-correct} \ lm\text{-lookup-correct}$ )
        thus ?thesis using Obj arrobj by(auto intro: sc-heap-read.intros)
      next
        case (Arr  $T' \ si \ f \ el$ )
          with  $\langle \text{sc-typeof-addr } h \ a = \lfloor U \rfloor \rangle$  arrobj
          have [simp]:  $U = \text{Array-type } T' \ si$  by(auto simp add: sc-typeof-addr-def)
          from hconf arrobj Arr have  $P, h \vdash_{sc} \text{Arr } T' \ si \ f \ el \ \checkmark$  by(auto dest: sc-hconfD)
          from  $\langle P \vdash \text{class-type-of } U \text{ has } F:T \ (fm) \text{ in } D \rangle$  have [simp]:  $D = \text{Object}$ 
            by(auto dest: has-field-decl-above)
          with  $\langle P, h \vdash_{sc} \text{Arr } T' \ si \ f \ el \ \checkmark \rangle \langle P \vdash \text{class-type-of } U \text{ has } F:T \ (fm) \text{ in } D \rangle$ 
          obtain  $v$  where  $lm\text{-lookup} \ F \ f = \lfloor v \rfloor \ P, h \vdash_{sc} v : \leq T$ 
            by(fastforce simp add: sc-oconf-def)
          thus ?thesis using Arr arrobj by(auto intro: sc-heap-read.intros)
        qed
      next
        case (addr-loc-type-cell  $n' \ n$ )
          with arrobj obtain  $si \ f \ el$ 
            where [simp]:  $arrobj = \text{Arr } T \ si \ f \ el$ 
            by(cases arrobj)(auto simp add: sc-typeof-addr-def)
          from addr-loc-type-cell arrobj
          have [simp]:  $al = \text{ACell } n$  and  $n: n < si$  by(auto simp add: sc-typeof-addr-def)
          from hconf arrobj have  $P, h \vdash_{sc} \text{Arr } T \ si \ f \ el \ \checkmark$  by(auto dest: sc-hconfD)
          with  $n$  obtain  $v$  where  $rm\text{-lookup} \ n \ el = \lfloor v \rfloor \ P, h \vdash_{sc} v : \leq T$ 
            by(fastforce simp add: sc-oconf-def  $rm\text{-lookup-correct}$ )
          thus ?thesis using arrobj  $n$  by(fastforce intro: sc-heap-read.intros)
        qed
      next
        fix  $h \ a \ al \ T \ v$ 
        assume alt:  $\text{sc.addr-loc-type } P \ h \ a \ al \ T$ 
        from alt obtain arrobj where  $arrobj: rm\text{-lookup} \ a \ h = \lfloor arrobj \rfloor$ 
          by(auto elim!: sc.addr-loc-type.cases simp add: sc-typeof-addr-def)
        thus  $\exists h'. \text{sc-heap-write } h \ a \ al \ v \ h'$  using alt
          by(cases arrobj)(fastforce intro: sc-heap-write.intros elim!: sc.addr-loc-type.cases simp add: sc-typeof-addr-def)
        dest: has-field-decl-above)+
      qed
    interpretation sc:
      heap-progress
      addr2thread-id
      thread-id2addr
      sc-spurious-wakeups
      sc-empty

```

```

    sc-allocate P
    sc-typeof-addr
    sc-heap-read
    sc-heap-write
    sc-hconf P
    P
  for P
  by(rule sc-heap-progress)

```

**lemma** *sc-heap-conf-read*:

*heap-conf-read addr2thread-id thread-id2addr sc-empty (sc-allocate P) sc-typeof-addr sc-heap-read sc-heap-write (sc-hconf P) P*

**proof**

```

  fix h a al v T
  assume read: sc-heap-read h a al v
  and alt: sc.addr-loc-type P h a al T
  and hconf: P ⊢sc h √
  thus P, h ⊢sc v :≤ T
  apply(auto elim!: sc-heap-read.cases sc.addr-loc-type.cases simp add: sc-typeof-addr-def)
  apply(fastforce dest!: sc-hconfD simp add: sc-oconf-def tm.lookup-correct lm.lookup-correct rm.lookup-correct)+
  done

```

**qed**

**interpretation** *sc*:

```

  heap-conf-read
  addr2thread-id
  thread-id2addr
  sc-spurious-wakeups
  sc-empty
  sc-allocate P
  sc-typeof-addr
  sc-heap-read
  sc-heap-write
  sc-hconf P
  P
  for P

```

**by**(rule *sc-heap-conf-read*)

**abbreviation** *sc-deterministic-heap-ops* :: 'm prog ⇒ bool

**where** *sc-deterministic-heap-ops* ≡ *sc.deterministic-heap-ops* TYPE('m)

**lemma** *sc-deterministic-heap-ops*: ¬ *sc-spurious-wakeups* ⇒ *sc-deterministic-heap-ops* P

**by**(rule *sc.deterministic-heap-opsI*)(auto elim: *sc-heap-read.cases sc-heap-write.cases simp add: sc-allocate-def*)

## 8.2.4 Code generation

**code-pred**

```

(modes: i ⇒ i ⇒ i ⇒ i ⇒ bool, i ⇒ i ⇒ i ⇒ o ⇒ bool)
sc-heap-read .

```

**code-pred**

```

(modes: i ⇒ i ⇒ i ⇒ i ⇒ bool, i ⇒ i ⇒ i ⇒ i ⇒ o ⇒ bool)
sc-heap-write .

```

**lemma** *eval-sc-heap-read-i-i-i-o*:

*Predicate.eval* (*sc-heap-read-i-i-i-o* *h ad al*) = *sc-heap-read* *h ad al*  
**by**(*auto elim*: *sc-heap-read-i-i-i-oE* *intro*: *sc-heap-read-i-i-i-oI* *intro!*: *ext*)

**lemma** *eval-sc-heap-write-i-i-i-i-o*:

*Predicate.eval* (*sc-heap-write-i-i-i-i-o* *h ad al v*) = *sc-heap-write* *h ad al v*  
**by**(*auto elim*: *sc-heap-write-i-i-i-i-oE* *intro*: *sc-heap-write-i-i-i-i-oI* *intro!*: *ext*)

**end**

## 8.3 Orders as predicates

**theory** *Orders*

**imports**

*Main*

**begin**

### 8.3.1 Preliminaries

transfer *refl-on* et al. from *HOL.Relation* to predicates

**abbreviation** *refl-onP* :: *'a set*  $\Rightarrow$  (*'a*  $\Rightarrow$  *'a*  $\Rightarrow$  *bool*)  $\Rightarrow$  *bool*  
**where** *refl-onP* *A r*  $\equiv$  *refl-on* *A* {(*x*, *y*). *r x y*}

**abbreviation** *reflP* :: (*'a*  $\Rightarrow$  *'a*  $\Rightarrow$  *bool*)  $\Rightarrow$  *bool*  
**where** *reflP* == *refl-onP UNIV*

**abbreviation** *symP* :: (*'a*  $\Rightarrow$  *'a*  $\Rightarrow$  *bool*)  $\Rightarrow$  *bool*  
**where** *symP* *r*  $\equiv$  *sym* {(*x*, *y*). *r x y*}

**abbreviation** *total-onP* :: *'a set*  $\Rightarrow$  (*'a*  $\Rightarrow$  *'a*  $\Rightarrow$  *bool*)  $\Rightarrow$  *bool*  
**where** *total-onP* *A r*  $\equiv$  *total-on* *A* {(*x*, *y*). *r x y*}

**abbreviation** *irreflP* :: (*'a*  $\Rightarrow$  *'a*  $\Rightarrow$  *bool*)  $\Rightarrow$  *bool*  
**where** *irreflP* *r* == *irrefl* {(*x*, *y*). *r x y*}

**definition** *irreflclp* :: (*'a*  $\Rightarrow$  *'a*  $\Rightarrow$  *bool*)  $\Rightarrow$  *'a*  $\Rightarrow$  *'a*  $\Rightarrow$  *bool* (*- $\neq$*  [1000] 1000)  
**where** *r $\neq$*  *a b* = (*r a b*  $\wedge$  *a*  $\neq$  *b*)

**definition** *porder-on* :: *'a set*  $\Rightarrow$  (*'a*  $\Rightarrow$  *'a*  $\Rightarrow$  *bool*)  $\Rightarrow$  *bool*  
**where** *porder-on* *A r*  $\longleftrightarrow$  *refl-onP* *A r*  $\wedge$  *transp* *r*  $\wedge$  *antisymp* *r*

**definition** *torder-on* :: *'a set*  $\Rightarrow$  (*'a*  $\Rightarrow$  *'a*  $\Rightarrow$  *bool*)  $\Rightarrow$  *bool*  
**where** *torder-on* *A r*  $\longleftrightarrow$  *porder-on* *A r*  $\wedge$  *total-onP* *A r*

**definition** *order-consistent* :: (*'a*  $\Rightarrow$  *'a*  $\Rightarrow$  *bool*)  $\Rightarrow$  (*'a*  $\Rightarrow$  *'a*  $\Rightarrow$  *bool*)  $\Rightarrow$  *bool*  
**where** *order-consistent* *r s*  $\longleftrightarrow$  ( $\forall$  *a a'*. *r a a'*  $\wedge$  *s a' a*  $\longrightarrow$  *a* = *a'*)

**definition** *restrictP* :: (*'a*  $\Rightarrow$  *'a*  $\Rightarrow$  *bool*)  $\Rightarrow$  *'a set*  $\Rightarrow$  *'a*  $\Rightarrow$  *'a*  $\Rightarrow$  *bool* (**infixl** | ' 110)  
**where** (*r* | *A*) *a b*  $\longleftrightarrow$  *r a b*  $\wedge$  *a*  $\in$  *A*  $\wedge$  *b*  $\in$  *A*

**definition** *inv-imageP* :: (*'a*  $\Rightarrow$  *'a*  $\Rightarrow$  *bool*)  $\Rightarrow$  (*'b*  $\Rightarrow$  *'a*)  $\Rightarrow$  *'b*  $\Rightarrow$  *'b*  $\Rightarrow$  *bool*  
**where** [*iff*]: *inv-imageP* *r f a b*  $\longleftrightarrow$  *r* (*f a*) (*f b*)

**lemma** *refl-onPI*:  $(\bigwedge a\ a'.\ r\ a\ a' \implies a \in A \wedge a' \in A) \implies (\bigwedge a.\ a : A \implies r\ a\ a) \implies \text{refl-onP}\ A\ r$   
**by**(*rule* *refl-onI*)(*auto*)

**lemma** *refl-onPD*:  $\text{refl-onP}\ A\ r \implies a : A \implies r\ a\ a$   
**by**(*drule* (1) *refl-onD*)(*simp*)

**lemma** *refl-onPD1*:  $\text{refl-onP}\ A\ r \implies r\ a\ b \implies a : A$   
**by**(*erule* *refl-onD1*)(*simp*)

**lemma** *refl-onPD2*:  $\text{refl-onP}\ A\ r \implies r\ a\ b \implies b : A$   
**by**(*erule* *refl-onD2*)(*simp*)

**lemma** *refl-onP-Int*:  $\text{refl-onP}\ A\ r \implies \text{refl-onP}\ B\ s \implies \text{refl-onP}\ (A \cap B)\ (\lambda a\ a'.\ r\ a\ a' \wedge s\ a\ a')$   
**by**(*drule* (1) *refl-on-Int*)(*simp* *add*: *split-def* *inf-fun-def* *inf-set-def*)

**lemma** *refl-onP-Un*:  $\text{refl-onP}\ A\ r \implies \text{refl-onP}\ B\ s \implies \text{refl-onP}\ (A \cup B)\ (\lambda a\ a'.\ r\ a\ a' \vee s\ a\ a')$   
**by**(*drule* (1) *refl-on-Un*)(*simp* *add*: *split-def* *sup-fun-def* *sup-set-def*)

**lemma** *refl-onP-empty*[*simp*]:  $\text{refl-onP}\ \{\}\ (\lambda a\ a'.\ \text{False})$   
**unfolding** *split-def* **by** *simp*

**lemma** *refl-onP-tranclp*:  
**assumes** *refl-onP* *A* *r*  
**shows**  $\text{refl-onP}\ A\ r^{\wedge++}$   
**proof**(*rule* *refl-onPI*)  
**fix** *a* *a'*  
**assume**  $r^{\wedge++}\ a\ a'$   
**thus**  $a \in A \wedge a' \in A$   
**by**(*induct*)(*blast* *intro*: *refl-onPD1*[*OF* *assms*] *refl-onPD2*[*OF* *assms*])+  
**next**  
**fix** *a*  
**assume**  $a \in A$   
**from** *refl-onPD*[*OF* *assms* *this*] **show**  $r^{\wedge++}\ a\ a\ ..$   
**qed**

**lemma** *irreflPI*:  $(\bigwedge a.\ \neg r\ a\ a) \implies \text{irreflP}\ r$   
**unfolding** *irrefl-def* **by** *blast*

**lemma** *irreflPE*:  
**assumes** *irreflP* *r*  
**obtains**  $\forall a.\ \neg r\ a\ a$   
**using** *assms* **unfolding** *irrefl-def* **by** *blast*

**lemma** *irreflPD*:  $\llbracket \text{irreflP}\ r; r\ a\ a \rrbracket \implies \text{False}$   
**unfolding** *irrefl-def* **by** *blast*

**lemma** *irreflclpD*:  
 $r^{\neq}\ a\ b \implies r\ a\ b \wedge a \neq b$   
**by**(*simp* *add*: *irreflclp-def*)

**lemma** *irreflclpI* [*intro!*]:  
 $\llbracket r\ a\ b; a \neq b \rrbracket \implies r^{\neq}\ a\ b$   
**by**(*simp* *add*: *irreflclp-def*)

**lemma** *irreflclpE* [*elim!*]:  
**assumes**  $r \neq a \ b$   
**obtains**  $r \ a \ b \ a \neq b$   
**using** *assms* **by**(*simp add: irreflclp-def*)

**lemma** *transPI*:  $(\bigwedge x \ y \ z. \llbracket r \ x \ y; r \ y \ z \rrbracket \implies r \ x \ z) \implies \text{transp } r$   
**by** (*fact transpI*)

**lemma** *transPD*:  $\llbracket \text{transp } r; r \ x \ y; r \ y \ z \rrbracket \implies r \ x \ z$   
**by** (*fact transpD*)

**lemma** *transP-tranclp*:  $\text{transp } r^{\hat{+}+}$   
**by** (*fact trans-trancl [to-pred]*)

**lemma** *antisymPI*:  $(\bigwedge x \ y. \llbracket r \ x \ y; r \ y \ x \rrbracket \implies x = y) \implies \text{antisymp } r$   
**by** (*fact antisymPI*)

**lemma** *antisymPD*:  $\llbracket \text{antisymp } r; r \ a \ b; r \ b \ a \rrbracket \implies a = b$   
**by** (*fact antisymPD*)

**lemma** *antisym-subset*:  
 $\llbracket \text{antisymp } r; \bigwedge a \ a'. s \ a \ a' \implies r \ a \ a' \rrbracket \implies \text{antisymp } s$   
**by** (*blast intro: antisym-less-eq [of s r]*)

**lemma** *symPI*:  $(\bigwedge x \ y. r \ x \ y \implies r \ y \ x) \implies \text{symP } r$   
**by**(*rule symI*)(*blast*)

**lemma** *symPD*:  $\llbracket \text{symP } r; r \ x \ y \rrbracket \implies r \ y \ x$   
**by**(*blast dest: symD*)

### 8.3.2 Easy properties

**lemma** *porder-onI*:  
 $\llbracket \text{refl-onP } A \ r; \text{antisymp } r; \text{transp } r \rrbracket \implies \text{porder-on } A \ r$   
**unfolding** *porder-on-def* **by** *blast*

**lemma** *porder-onE*:  
**assumes** *porder-on*  $A \ r$   
**obtains** *refl-onP*  $A \ r$  *antisym*  $r$  *transp*  $r$   
**using** *assms* **unfolding** *porder-on-def* **by** *blast*

**lemma** *torder-onI*:  
 $\llbracket \text{porder-on } A \ r; \text{total-onP } A \ r \rrbracket \implies \text{torder-on } A \ r$   
**unfolding** *torder-on-def* **by** *blast*

**lemma** *torder-onE*:  
**assumes** *torder-on*  $A \ r$   
**obtains** *porder-on*  $A \ r$  *total-onP*  $A \ r$   
**using** *assms* **unfolding** *torder-on-def* **by** *blast*

**lemma** *total-onI*:  
 $(\bigwedge x \ y. \llbracket x \in A; y \in A \rrbracket \implies (x, y) \in r \vee x = y \vee (y, x) \in r) \implies \text{total-on } A \ r$   
**unfolding** *total-on-def* **by** *blast*

**lemma** *total-onPI*:

$(\bigwedge x y. \llbracket x \in A; y \in A \rrbracket \implies r x y \vee x = y \vee r y x) \implies \text{total-onP } A r$   
**by**(*rule total-onI*) *simp*

**lemma** *total-onD*:

$\llbracket \text{total-on } A r; x \in A; y \in A \rrbracket \implies (x, y) \in r \vee x = y \vee (y, x) \in r$   
**unfolding** *total-on-def* **by** *blast*

**lemma** *total-onPD*:

$\llbracket \text{total-onP } A r; x \in A; y \in A \rrbracket \implies r x y \vee x = y \vee r y x$   
**by**(*drule (2) total-onD*) *blast*

### 8.3.3 Order consistency

**lemma** *order-consistentI*:

$(\bigwedge a a'. \llbracket r a a'; s a' a \rrbracket \implies a = a') \implies \text{order-consistent } r s$   
**unfolding** *order-consistent-def* **by** *blast*

**lemma** *order-consistentD*:

$\llbracket \text{order-consistent } r s; r a a'; s a' a \rrbracket \implies a = a'$   
**unfolding** *order-consistent-def* **by** *blast*

**lemma** *order-consistent-subset*:

$\llbracket \text{order-consistent } r s; \bigwedge a a'. r' a a' \implies r a a'; \bigwedge a a'. s' a a' \implies s a a' \rrbracket \implies \text{order-consistent } r' s'$   
**by**(*blast intro: order-consistentI order-consistentD*)

**lemma** *order-consistent-sym*:

$\text{order-consistent } r s \implies \text{order-consistent } s r$   
**by**(*blast intro: order-consistentI dest: order-consistentD*)

**lemma** *antisym-order-consistent-self*:

$\text{antisym } r \implies \text{order-consistent } r r$   
**by**(*rule order-consistentI*)(*erule antisymD, simp-all*)

**lemma** *total-on-refl-on-consistent-into*:

**assumes** *r: total-onP A r refl-onP A r*  
**and** *consist: order-consistent r s*  
**and** *x: x ∈ A and y: y ∈ A and s: s x y*  
**shows** *r x y*  
**proof**(*cases x = y*)  
  **case** *True*  
    **with** *r x y* **show** *?thesis* **by**(*blast intro: refl-onPD*)  
**next**  
  **case** *False*  
    **with** *r x y* **have** *r x y ∨ r y x* **by**(*blast dest: total-onD*)  
    **thus** *?thesis*  
  **proof**  
    **assume** *r y x*  
    **with** *s consist* **have** *x = y* **by**(*blast dest: order-consistentD*)  
    **with** *False* **show** *?thesis* **by**(*contradiction*)  
  **qed**  
**qed**

**lemma** *porder-torder-tranclpE* [*consumes 5, case-names base step*]:

```

assumes  $r$ : porder-on  $A$   $r$ 
and  $s$ : torder-on  $B$   $s$ 
and consist: order-consistent  $r$   $s$ 
and B-subset-A:  $B \subseteq A$ 
and transl:  $(\lambda a b. r a b \vee s a b)^{++} x y$ 
obtains  $r x y$ 
  |  $u v$  where  $r x u s u v r v y$ 
proof(atomize-elim)
from  $r$  have refl-onP  $A$   $r$  transp  $r$  by(blast elim: porder-onE)+
from  $s$  have refl-onP  $B$   $s$  total-onP  $B$   $s$  transp  $s$ 
  by(blast elim: torder-onE porder-onE)+

from transl show  $r x y \vee (\exists u v. r x u \wedge s u v \wedge r v y)$ 
proof(induct)
  case (base  $y$ )
  thus ?case
  proof
    assume  $s x y$ 
    with  $s$  have  $x \in B$   $y \in B$ 
      by(blast elim: torder-onE porder-onE dest: refl-onPD1 refl-onPD2)+
    with B-subset-A have  $x \in A$   $y \in A$  by blast+
    with refl-onPD[OF  $\langle \text{refl-onP } A \ r \rangle$ , of  $x$ ] refl-onPD[OF  $\langle \text{refl-onP } A \ r \rangle$ , of  $y$ ]  $\langle s x y \rangle$ 
    show ?thesis by(iprover)
  next
    assume  $r x y$ 
    thus ?thesis ..
  qed
next
  case (step  $y z$ )
  note  $IH = \langle r x y \vee (\exists u v. r x u \wedge s u v \wedge r v y) \rangle$ 
  from  $\langle r y z \vee s y z \rangle$  show ?case
  proof
    assume  $s y z$ 
    with  $\langle \text{refl-onP } B \ s \rangle$  have  $y \in B$   $z \in B$ 
      by(blast dest: refl-onPD2 refl-onPD1)+
    from  $IH$  show ?thesis
    proof
      assume  $r x y$ 
      moreover from  $\langle z \in B \rangle$  B-subset-A  $r$  have  $r z z$ 
        by(blast elim: porder-onE dest: refl-onPD)
      ultimately show ?thesis using  $\langle s y z \rangle$  by blast
    next
      assume  $\exists u v. r x u \wedge s u v \wedge r v y$ 
      then obtain  $u v$  where  $r x u s u v r v y$  by blast
      from  $\langle \text{refl-onP } B \ s \rangle$   $\langle s u v \rangle$  have  $v \in B$  by(rule refl-onPD2)
      with  $\langle \text{total-onP } B \ s \rangle$   $\langle \text{refl-onP } B \ s \rangle$  order-consistent-sym[OF consist]
      have  $s v y$  using  $\langle y \in B \rangle$   $\langle r v y \rangle$ 
        by(rule total-on-refl-on-consistent-into)
      with  $\langle \text{transp } s \rangle$  have  $s v z$  using  $\langle s y z \rangle$  by(rule transPD)
      with  $\langle \text{transp } s \rangle$   $\langle s u v \rangle$  have  $s u z$  by(rule transPD)
      moreover from  $\langle z \in B \rangle$  B-subset-A have  $z \in A$  ..
      with  $\langle \text{refl-onP } A \ r \rangle$  have  $r z z$  by(rule refl-onPD)
      ultimately show ?thesis using  $\langle r x u \rangle$  by blast
    qed

```

```

next
  assume  $r\ y\ z$ 
  with  $IH$  show  $?thesis$ 
    by(blast intro: transPD[OF  $\langle transp\ r \rangle$ ])
qed
qed
qed

lemma torder-on-porder-on-consistent-tranclp-antisym:
  assumes  $r$ : porder-on  $A\ r$ 
  and  $s$ : torder-on  $B\ s$ 
  and consist: order-consistent  $r\ s$ 
  and B-subset-A:  $B \subseteq A$ 
  shows antisymp  $(\lambda x\ y. r\ x\ y \vee s\ x\ y)^{++}$ 
proof(rule antisymPI)
  fix  $x\ y$ 
  let  $?rs = \lambda x\ y. r\ x\ y \vee s\ x\ y$ 
  assume  $?rs^{++}\ x\ y\ ?rs^{++}\ y\ x$ 
  from  $r$  have antisymp  $r\ transp\ r$  by(blast elim: porder-onE)+
  from  $s$  have total-onP  $B\ s$  refl-onP  $B\ s\ transp\ s$  antisymp  $s$ 
    by(blast elim: torder-onE porder-onE)+

  from  $r\ s$  consist B-subset-A  $\langle ?rs^{++}\ x\ y \rangle$ 
  show  $x = y$ 
proof(cases rule: porder-torder-tranclpE)
  case base
  from  $r\ s$  consist B-subset-A  $\langle ?rs^{++}\ y\ x \rangle$ 
  show  $?thesis$ 
proof(cases rule: porder-torder-tranclpE)
  case base
  with  $\langle antisymp\ r \rangle\ \langle r\ x\ y \rangle$  show  $?thesis$  by(rule antisymPD)
next
  case (step  $u\ v$ )
  from  $\langle r\ v\ x \rangle\ \langle r\ x\ y \rangle\ \langle r\ y\ u \rangle$  have  $r\ v\ u$  by(blast intro: transPD[OF  $\langle transp\ r \rangle$ ])
  with consist have  $v = u$  using  $\langle s\ u\ v \rangle$  by(rule order-consistentD)
  with  $\langle r\ y\ u \rangle\ \langle r\ v\ x \rangle$  have  $r\ y\ x$  by(blast intro: transPD[OF  $\langle transp\ r \rangle$ ])
  with  $\langle r\ x\ y \rangle$  show  $?thesis$  by(rule antisymPD[OF  $\langle antisymp\ r \rangle$ ])
qed
next
  case (step  $u\ v$ )
  from  $r\ s$  consist B-subset-A  $\langle ?rs^{++}\ y\ x \rangle$ 
  show  $?thesis$ 
proof(cases rule: porder-torder-tranclpE)
  case base
  from  $\langle r\ v\ y \rangle\ \langle r\ y\ x \rangle\ \langle r\ x\ u \rangle$  have  $r\ v\ u$  by(blast intro: transPD[OF  $\langle transp\ r \rangle$ ])
  with order-consistent-sym[OF consist]  $\langle s\ u\ v \rangle$ 
  have  $u = v$  by(rule order-consistentD)
  with  $\langle r\ v\ y \rangle\ \langle r\ x\ u \rangle$  have  $r\ x\ y$  by(blast intro: transPD[OF  $\langle transp\ r \rangle$ ])
  thus  $?thesis$  using  $\langle r\ y\ x \rangle$  by(rule antisymPD[OF  $\langle antisymp\ r \rangle$ ])
next
  case (step  $u'\ v'$ )
  note  $r$ -into- $s = total-on-refl-on-consistent-into[OF\ \langle total-onP\ B\ s \rangle\ \langle refl-onP\ B\ s \rangle\ order-consistent-sym[OF\ consist]]$ 
  from  $\langle refl-onP\ B\ s \rangle\ \langle s\ u\ v \rangle\ \langle s\ u'\ v' \rangle$ 

```



**have**  $u \in B \ v \in B \ u' \in B \ v' \in B$  **by**(blast dest: refl-onPD1 refl-onPD2)+  
**from**  $\langle r \ v' \ x \rangle \langle r \ x \ u \rangle$  **have**  $r \ v' \ u$  **by**(rule transPD[OF  $\langle \text{transp } r \rangle$ ])  
**with**  $\langle v' \in B \rangle \langle u \in B \rangle$  **have**  $s \ v' \ u$  **by**(rule r-into-s)  
**also note**  $\langle s \ u \ v \rangle$   
**also** (transPD[OF  $\langle \text{transp } s \rangle$ ])  
**from**  $\langle r \ v \ y \rangle \langle r \ y \ u' \rangle$  **have**  $r \ v \ u'$  **by**(rule transPD[OF  $\langle \text{transp } r \rangle$ ])  
**with**  $\langle v \in B \rangle \langle u' \in B \rangle$  **have**  $s \ v \ u'$  **by**(rule r-into-s)  
**finally** (transPD[OF  $\langle \text{transp } s \rangle$ ])  
**have**  $v' = u'$  **using**  $\langle s \ u' \ v' \rangle$  **by**(rule antisymPD[OF  $\langle \text{antisym } s \rangle$ ])  
**moreover with**  $\langle s \ v \ u' \rangle \langle s \ v' \ u \rangle$  **have**  $s \ v \ u$  **by**(blast intro: transPD[OF  $\langle \text{transp } s \rangle$ ])  
**with**  $\langle s \ u \ v \rangle$  **have**  $u = v$  **by**(rule antisymPD[OF  $\langle \text{antisym } s \rangle$ ])  
**ultimately have**  $r \ x \ y \ r \ y \ x$  **using**  $\langle r \ x \ u \rangle \langle r \ v \ y \rangle \langle r \ y \ u' \rangle \langle r \ v' \ x \rangle$   
**by**(blast intro: transPD[OF  $\langle \text{transp } r \rangle$ ])+  
**thus** ?thesis **by**(rule antisymPD[OF  $\langle \text{antisym } r \rangle$ ])  
**qed**  
**qed**  
**qed**

**lemma** porder-on-torder-on-tranclp-porder-onI:

**assumes**  $r$ : porder-on  $A \ r$   
**and**  $s$ : torder-on  $B \ s$   
**and** consist: order-consistent  $r \ s$   
**and** subset:  $B \subseteq A$   
**shows** porder-on  $A \ (\lambda a \ b. \ r \ a \ b \ \vee \ s \ a \ b)^{++}$   
**proof**(rule porder-onI)  
**let** ?rs =  $\lambda a \ b. \ r \ a \ b \ \vee \ s \ a \ b$   
**from**  $r$  **have** refl-onP  $A \ r$  **by**(rule porder-onE)  
**moreover from**  $s$  **have** refl-onP  $B \ s$  **by**(blast elim: torder-onE porder-onE)  
**ultimately have** refl-onP  $(A \cup B) \ ?rs$  **by**(rule refl-onP-Un)  
**also from** subset **have**  $A \cup B = A$  **by** blast  
**finally show** refl-onP  $A \ ?rs^{++}$  **by**(rule refl-onP-tranclp)  
  
**show** transp ?rs<sup>++</sup> **by**(rule transP-tranclp)  
  
**from**  $r \ s$  consist subset **show** antisym ?rs<sup>++</sup>  
**by** (rule torder-on-porder-on-consistent-tranclp-antisym)  
**qed**

**lemma** porder-on-sub-torder-on-tranclp-porder-onI:

**assumes**  $r$ : porder-on  $A \ r$   
**and**  $s$ : torder-on  $B \ s$   
**and** consist: order-consistent  $r \ s$   
**and**  $t$ :  $\bigwedge x \ y. \ t \ x \ y \implies s \ x \ y$   
**and** subset:  $B \subseteq A$   
**shows** porder-on  $A \ (\lambda x \ y. \ r \ x \ y \ \vee \ t \ x \ y)^{++}$   
**proof**(rule porder-onI)  
**let** ?rt =  $\lambda x \ y. \ r \ x \ y \ \vee \ t \ x \ y$   
**let** ?rs =  $\lambda x \ y. \ r \ x \ y \ \vee \ s \ x \ y$   
**from**  $r \ s$  consist subset **have** antisym ?rs<sup>++</sup>  
**by**(rule torder-on-porder-on-consistent-tranclp-antisym)  
**thus** antisym ?rt<sup>++</sup>  
**proof**(rule antisym-subset)  
**fix**  $x \ y$   
**assume** ?rt<sup>++</sup>  $x \ y$  **thus** ?rs<sup>++</sup>  $x \ y$

```

    by(induct)(blast intro: tranclp.r-into-trancl t tranclp.trancl-into-trancl t)+
  qed

from s have refl-onP B s by(blast elim: porder-onE torder-onE)
{ fix x y
  assume t x y
  hence s x y by(rule t)
  hence x ∈ B y ∈ B
  by(blast dest: refl-onPD1[OF ⟨refl-onP B s⟩] refl-onPD2[OF ⟨refl-onP B s⟩])
  with subset have x ∈ A y ∈ A by blast+ }
note t-reflD = this

from r have refl-onP A r by(rule porder-onE)
show refl-onP A ?rt++
proof(rule refl-onPI)
  fix a a'
  assume ?rt++ a a'
  thus a ∈ A ∧ a' ∈ A
  by(induct)(auto dest: refl-onPD1[OF ⟨refl-onP A r⟩] refl-onPD2[OF ⟨refl-onP A r⟩] t-reflD)
next
  fix a
  assume a ∈ A
  with ⟨refl-onP A r⟩ have r a a by(rule refl-onPD)
  thus ?rt++ a a by(blast intro: tranclp.r-into-trancl)
qed

show transp ?rt++ by(rule transP-tranclp)
qed

```

### 8.3.4 Order restrictions

**lemma** *restrictPI* [*intro!*, *simp*]:  
 $\llbracket r \ a \ b; a \in A; b \in A \rrbracket \implies (r \mid^{\cdot} A) \ a \ b$   
**unfolding** *restrictP-def* **by** *simp*

**lemma** *restrictPE* [*elim!*]:  
**assumes**  $(r \mid^{\cdot} A) \ a \ b$   
**obtains**  $r \ a \ b \ a \in A \ b \in A$   
**using** *assms* **unfolding** *restrictP-def* **by** *simp*

**lemma** *restrictP-empty* [*simp*]:  $R \mid^{\cdot} \{\} = (\lambda - . \text{False})$   
**by**(*simp* add: *restrictP-def*[*abs-def*])

**lemma** *refl-on-restrictPI*:  
 $\text{refl-onP } A \ r \implies \text{refl-onP } (A \cap B) \ (r \mid^{\cdot} B)$   
**by**(rule *refl-onPI*)(blast dest: *refl-onPD1* *refl-onPD2* *refl-onPD*)

**lemma** *refl-on-restrictPI'*:  
 $\llbracket \text{refl-onP } A \ r; B = A \cap C \rrbracket \implies \text{refl-onP } B \ (r \mid^{\cdot} C)$   
**by**(*simp* add: *refl-on-restrictPI*)

**lemma** *antisym-restrictPI*:  
 $\text{antisymP } r \implies \text{antisymP } (r \mid^{\cdot} A)$   
**by**(rule *antisymPI*)(blast dest: *antisymPD*)

**lemma** *trans-restrictPI*:

$\text{transp } r \implies \text{transp } (r \mid ' A)$

**by**(*rule transPI*)(*blast dest: transPD*)

**lemma** *porder-on-restrictPI*:

$\text{porder-on } A \ r \implies \text{porder-on } (A \cap B) \ (r \mid ' B)$

**by**(*blast elim: porder-onE intro: refl-on-restrictPI antisym-restrictPI trans-restrictPI porder-onI*)

**lemma** *porder-on-restrictPI'*:

$\llbracket \text{porder-on } A \ r; B = A \cap C \rrbracket \implies \text{porder-on } B \ (r \mid ' C)$

**by**(*simp add: porder-on-restrictPI*)

**lemma** *total-on-restrictPI*:

$\text{total-onP } A \ r \implies \text{total-onP } (A \cap B) \ (r \mid ' B)$

**by**(*blast intro: total-onPI dest: total-onPD*)

**lemma** *total-on-restrictPI'*:

$\llbracket \text{total-onP } A \ r; B = A \cap C \rrbracket \implies \text{total-onP } B \ (r \mid ' C)$

**by**(*simp add: total-on-restrictPI*)

**lemma** *torder-on-restrictPI*:

$\text{torder-on } A \ r \implies \text{torder-on } (A \cap B) \ (r \mid ' B)$

**by**(*blast elim: torder-onE intro: torder-onI porder-on-restrictPI total-on-restrictPI*)

**lemma** *torder-on-restrictPI'*:

$\llbracket \text{torder-on } A \ r; B = A \cap C \rrbracket \implies \text{torder-on } B \ (r \mid ' C)$

**by**(*simp add: torder-on-restrictPI*)

**lemma** *restrictP-commute*:

**fixes**  $r :: 'a \Rightarrow 'a \Rightarrow \text{bool}$

**shows**  $r \mid ' A \mid ' B = r \mid ' B \mid ' A$

**by**(*blast intro!: ext*)

**lemma** *restrictP-subsume1*:

**fixes**  $r :: 'a \Rightarrow 'a \Rightarrow \text{bool}$

**assumes**  $A \subseteq B$

**shows**  $r \mid ' A \mid ' B = r \mid ' A$

**using** *assms* **by**(*blast intro!: ext*)

**lemma** *restrictP-subsume2*:

**fixes**  $r :: 'a \Rightarrow 'a \Rightarrow \text{bool}$

**assumes**  $B \subseteq A$

**shows**  $r \mid ' A \mid ' B = r \mid ' B$

**using** *assms* **by**(*blast intro!: ext*)

**lemma** *restrictP-idem* [*simp*]:

**fixes**  $r :: 'a \Rightarrow 'a \Rightarrow \text{bool}$

**shows**  $r \mid ' A \mid ' A = r \mid ' A$

**by**(*simp add: restrictP-subsume1*)

### 8.3.5 Maximal elements w.r.t. a total order

**definition** *max-torder* ::  $('a \Rightarrow 'a \Rightarrow \text{bool}) \Rightarrow 'a \Rightarrow 'a \Rightarrow 'a$

**where**  $\text{max-torder } r \ a \ b = (\text{if } \text{Domainp } r \ a \wedge \text{Domainp } r \ b \text{ then if } r \ a \ b \text{ then } b \text{ else } a$   
 $\text{else if } a = b \text{ then } a \text{ else } \text{SOME } a. \neg \text{Domainp } r \ a)$

**lemma** *refl-on-DomainD*:  $\text{refl-on } A \ r \implies A = \text{Domain } r$   
**by**(*auto simp add: Domain-unfold dest: refl-onD refl-onD1*)

**lemma** *refl-onP-DomainPD*:  $\text{refl-onP } A \ r \implies A = \{a. \text{Domainp } r \ a\}$   
**by**(*drule refl-on-DomainD*) *auto*

**lemma** *semilattice-max-torder*:  
**assumes** *tot*: *torder-on*  $A \ r$   
**shows** *semilattice* (*max-torder*  $r$ )

**proof** –

**from** *tot* **have** *as*: *antisym*  $r$

**and** *to*: *total-onP*  $A \ r$

**and** *trans*: *transp*  $r$

**and** *refl*: *refl-onP*  $A \ r$

**by**(*auto elim: torder-onE porder-onE*)

**from** *refl* **have**  $\{a. \text{Domainp } r \ a\} = A$  **by** (*rule refl-onP-DomainPD[symmetric]*)

**from** *this* [*symmetric*] **have** *domain*:  $\bigwedge a. \text{Domainp } r \ a \longleftrightarrow a \in A$  **by** *simp*

**show** *?thesis*

**proof**

**fix**  $x \ y \ z$

**show**  $\text{max-torder } r \ (\text{max-torder } r \ x \ y) \ z = \text{max-torder } r \ x \ (\text{max-torder } r \ y \ z)$

**proof** (*cases*  $x \neq y \wedge x \neq z \wedge y \neq z$ )

**case** *True*

**have**  $*$ :  $\bigwedge a \ b. a \neq b \implies \text{max-torder } r \ a \ b = (\text{if } \text{Domainp } r \ a \wedge \text{Domainp } r \ b \text{ then if } r \ a \ b \text{ then } b \text{ else } a \text{ else } \text{SOME } a. \neg \text{Domainp } r \ a)$

**by** (*auto simp add: max-torder-def*)

**with** *True* **show** *?thesis*

**apply** (*simp only: max-torder-def domain*)

**apply** (*auto split!: if-splits*)

**apply** (*blast dest: total-onPD [OF to] transPD [OF trans] antisymPD [OF as] refl-onPD1 [OF refl] refl-onPD2 [OF refl] someI [where P= $\lambda a. a \notin A$ ])+*

**done**

**next**

**have** *max-torder-idem*:  $\bigwedge a. \text{max-torder } r \ a \ a = a$  **by** (*simp add: max-torder-def*)

**case** *False* **then show** *?thesis*

**apply** (*auto simp add: max-torder-idem*)

**apply** (*auto simp add: max-torder-def domain dest: someI [where P= $\lambda a. a \notin A$ ])*

**done**

**qed**

**next**

**fix**  $x \ y$

**show**  $\text{max-torder } r \ x \ y = \text{max-torder } r \ y \ x$

**by** (*auto simp add: max-torder-def domain dest: total-onPD [OF to] antisymPD [OF as]*)

**next**

**fix**  $x$

**show**  $\text{max-torder } r \ x \ x = x$

**by** (*simp add: max-torder-def*)

**qed**

**qed**

**lemma** *max-torder-ge-conv-disj*:

**assumes** *tot*: *torder-on* *A* *r* **and** *x*:  $x \in A$  **and** *y*:  $y \in A$   
**shows**  $r\ z\ (\max\text{-}torder\ r\ x\ y) \longleftrightarrow r\ z\ x \vee r\ z\ y$   
**proof** –  
**from** *tot* **have** *as*: *antisymp* *r*  
**and** *to*: *total-onP* *A* *r*  
**and** *trans*: *transp* *r*  
**and** *reft*: *reft-onP* *A* *r*  
**by**(*auto elim*: *torder-onE* *porder-onE*)  
**from** *reft* **have**  $\{a. Domainp\ r\ a\} = A$  **by** (*rule reft-onP-DomainPD*[*symmetric*])  
**from** *this* [*symmetric*] **have** *domain*:  $\bigwedge a. Domainp\ r\ a \longleftrightarrow a \in A$  **by** *simp*  
**show** ?*thesis* **using** *x y*  
**by**(*simp add*: *max-torder-def domain*)(*blast dest*: *total-onPD*[*OF to*] *transPD*[*OF trans*])  
**qed**

**definition** *Max-torder* ::  $('a \Rightarrow 'a \Rightarrow bool) \Rightarrow 'a\ set \Rightarrow 'a$   
**where**  
*Max-torder* *r* = *semilattice-set.F* (*max-torder* *r*)

**context**  
**fixes** *A* *r*  
**assumes** *tot*: *torder-on* *A* *r*  
**begin**

**lemma** *semilattice-set*:  
*semilattice-set* (*max-torder* *r*)  
**by** (*rule semilattice-set.intro*, *rule semilattice-max-torder*) (*fact tot*)

**lemma** *domain*:  
 $Domainp\ r\ a \longleftrightarrow a \in A$

**proof** –  
**from** *tot* **have**  $\{a. Domainp\ r\ a\} = A$   
**by** (*auto elim*: *torder-onE* *porder-onE* *dest*: *reft-onP-DomainPD* [*symmetric*])  
**from** *this* [*symmetric*] **show** ?*thesis* **by** *simp*  
**qed**

**lemma** *Max-torder-in-Domain*:  
**assumes** *B*: *finite* *B*  $B \neq \{\}$   $B \subseteq A$   
**shows** *Max-torder* *r*  $B \in A$   
**proof** –  
**interpret** *Max-torder*: *semilattice-set max-torder* *r*  
**rewrites**  
*semilattice-set.F* (*max-torder* *r*) = *Max-torder* *r*  
**by** (*fact semilattice-set Max-torder-def* [*symmetric*])+  
**show** ?*thesis* **using** *B*  
**by** (*induct rule*: *finite-ne-induct*) (*simp-all add*: *max-torder-def domain*)  
**qed**

**lemma** *Max-torder-in-set*:  
**assumes** *B*: *finite* *B*  $B \neq \{\}$   $B \subseteq A$   
**shows** *Max-torder* *r*  $B \in B$   
**proof** –  
**interpret** *Max-torder*: *semilattice-set max-torder* *r*  
**rewrites**  
*semilattice-set.F* (*max-torder* *r*) = *Max-torder* *r*

```

    by (fact semilattice-set Max-torder-def [symmetric])+
  show ?thesis using B
    by (induct rule: finite-ne-induct) (auto simp add: max-torder-def domain)
qed

```

**lemma** *Max-torder-above-iff*:

```

  assumes B: finite B B ≠ {} B ⊆ A
  shows r x (Max-torder r B) ⟷ (∃ a ∈ B. r x a)

```

**proof** –

```

  interpret Max-torder: semilattice-set max-torder r
  rewrites

```

```

    semilattice-set.F (max-torder r) = Max-torder r

```

```

  by (fact semilattice-set Max-torder-def [symmetric])+
  from B show ?thesis

```

```

    by (induct rule: finite-ne-induct) (simp-all add: max-torder-ge-conv-disj [OF tot] Max-torder-in-Domain)

```

qed

end

**lemma** *Max-torder-above*:

```

  assumes tot: torder-on A r
  and finite B a ∈ B B ⊆ A
  shows r a (Max-torder r B)

```

**proof** –

```

  from tot have refl: refl-onP A r by(auto elim: torder-onE porder-onE)

```

```

  with ⟨a ∈ B⟩ ⟨B ⊆ A⟩ have r a a by(blast dest: refl-onPD[OF refl])

```

```

  from ⟨a ∈ B⟩ have B ≠ {} by auto

```

```

  from Max-torder-above-iff [OF tot ⟨finite B⟩ this ⟨B ⊆ A⟩, of a] ⟨r a a⟩ ⟨a ∈ B⟩

```

```

  show ?thesis by blast

```

qed

**lemma** *inv-imageP-id [simp]*:  $\text{inv-imageP } R \text{ id} = R$

by(*simp add: fun-eq-iff*)

**lemma** *inv-into-id [simp]*:  $a \in A \implies \text{inv-into } A \text{ id } a = a$

by (*metis f-inv-into-f id-apply image-id*)

end

## 8.4 Axiomatic specification of the JMM

**theory** *JMM-Spec*

**imports**

*Orders*

*../Common/Observable-Events*

*Coinductive.Coinductive-List*

**begin**

### 8.4.1 Definitions

**type-synonym** *JMM-action* = *nat*

**type-synonym** (*'addr, 'thread-id*) *execution* = (*'thread-id* × (*'addr, 'thread-id*) *obs-event action*) *llist*

**definition** *actions* :: (*'addr, 'thread-id*) *execution*  $\Rightarrow$  *JMM-action set*

**where**  $actions\ E = \{n. enat\ n < llength\ E\}$

**definition**  $action-tid :: ('addr, 'thread-id)\ execution \Rightarrow JMM-action \Rightarrow 'thread-id$

**where**  $action-tid\ E\ a = fst\ (lnth\ E\ a)$

**definition**  $action-obs :: ('addr, 'thread-id)\ execution \Rightarrow JMM-action \Rightarrow ('addr, 'thread-id)\ obs-event\ action$

**where**  $action-obs\ E\ a = snd\ (lnth\ E\ a)$

**definition**  $tactions :: ('addr, 'thread-id)\ execution \Rightarrow 'thread-id \Rightarrow JMM-action\ set$

**where**  $tactions\ E\ t = \{a. a \in actions\ E \wedge action-tid\ E\ a = t\}$

**inductive**  $is-new-action :: ('addr, 'thread-id)\ obs-event\ action \Rightarrow bool$

**where**

$NewHeapElem: is-new-action\ (NormalAction\ (NewHeapElem\ a\ hT))$

**inductive**  $is-write-action :: ('addr, 'thread-id)\ obs-event\ action \Rightarrow bool$

**where**

$NewHeapElem: is-write-action\ (NormalAction\ (NewHeapElem\ ad\ hT))$

$| WriteMem: is-write-action\ (NormalAction\ (WriteMem\ ad\ al\ v))$

Initialisation actions are synchronisation actions iff they initialize volatile fields – cf. JMM mailing list, message no. 62 (5 Nov. 2006). However, intuitively correct programs might not be correctly synchronized:

```

x = 0
-----
r1 = x | r2 = x

```

Here, if  $x$  is not volatile, the initial write can belong to at most one thread. Hence, it is happens-before to either  $r1 = x$  or  $r2 = x$ , but not both. In any sequentially consistent execution, both reads must read from the initialisation action  $x = 0$ , but it is not happens-before ordered to one of them.

Moreover, if only volatile initialisations synchronize-with all thread-start actions, this breaks the proof of the DRF guarantee since it assumes that the happens-before relation  $\leq_{hb} a$  for an action  $a$  in a topologically sorted action sequence depends only on the actions before it. Counter example: ( $y$  is volatile)

$[(t1, start), (t1, init\ x), (t2, start), (t1, init\ y), \dots]$

Here,  $(t1, init\ x) \leq_{hb} (t2, start)$  via:  $(t1, init\ x) \leq_{po} (t1, init\ y) \leq_{sw} (t2, start)$ , but in  $[(t1, start), (t1, init\ x), (t2, start)]$ , not  $(t1, init\ x) \leq_{hb} (t2, start)$ .

Sevcik speculated that one might add an initialisation thread which performs all initialisation actions. All normal threads' start action would then synchronize on the final action of the initialisation thread. However, this contradicts the memory chain condition in the final field extension to the JMM (threads must read addresses of objects that they have not created themselves before they can access the fields of the object at that address) – not modelled here.

Instead, we leave every initialisation action in the thread it belongs to, but order it explicitly before the thread's start action and add synchronizes-with edges from *all* initialisation actions to *all* thread start actions.

**inductive**  $saction :: 'm\ prog \Rightarrow ('addr, 'thread-id)\ obs-event\ action \Rightarrow bool$

**for**  $P :: 'm\ prog$

**where**

$NewHeapElem: saction\ P\ (NormalAction\ (NewHeapElem\ a\ hT))$

```

| Read: is-volatile P al  $\implies$  saction P (NormalAction (ReadMem a al v))
| Write: is-volatile P al  $\implies$  saction P (NormalAction (WriteMem a al v))
| ThreadStart: saction P (NormalAction (ThreadStart t))
| ThreadJoin: saction P (NormalAction (ThreadJoin t))
| SyncLock: saction P (NormalAction (SyncLock a))
| SyncUnlock: saction P (NormalAction (SyncUnlock a))
| ObsInterrupt: saction P (NormalAction (ObsInterrupt t))
| ObsInterrupted: saction P (NormalAction (ObsInterrupted t))
| InitialThreadAction: saction P InitialThreadAction
| ThreadFinishAction: saction P ThreadFinishAction

```

**definition** *sactions* :: 'm prog  $\Rightarrow$  ('addr, 'thread-id) execution  $\Rightarrow$  JMM-action set  
**where** *sactions* P E = {a. a  $\in$  actions E  $\wedge$  saction P (action-obs E a)}

**inductive-set** *write-actions* :: ('addr, 'thread-id) execution  $\Rightarrow$  JMM-action set  
**for** E :: ('addr, 'thread-id) execution  
**where**

*write-actions*I:  $\llbracket a \in \text{actions } E; \text{is-write-action (action-obs } E \text{ } a) \rrbracket \implies a \in \text{write-actions } E$

*NewObj* and *NewArr* actions only initialize those fields and array cells that are in fact in the object or array. Hence, they are not a write for - reads from addresses for which no object/array is created during the whole execution - reads from fields/cells that are not part of the object/array at the specified address.

**primrec** *addr-locs* :: 'm prog  $\Rightarrow$  htype  $\Rightarrow$  addr-loc set  
**where**

```

addr-locs P (Class-type C) = {CField D F | D F.  $\exists$  fm T. P  $\vdash$  C has F:T (fm) in D}
| addr-locs P (Array-type T n) = ({ACell n' | n'. n' < n}  $\cup$  {CField Object F | F.  $\exists$  fm T. P  $\vdash$  Object has F:T (fm) in Object})

```

*action-loc-aux* would naturally be an inductive set, but *inductive\_set* does not allow to pattern match on parameters. Hence, specify it using function and derive the setup manually.

**fun** *action-loc-aux* :: 'm prog  $\Rightarrow$  ('addr, 'thread-id) obs-event action  $\Rightarrow$  ('addr  $\times$  addr-loc) set  
**where**

```

action-loc-aux P (NormalAction (NewHeapElem ad (Class-type C))) =
  {(ad, CField D F) | D F T fm. P  $\vdash$  C has F:T (fm) in D}
| action-loc-aux P (NormalAction (NewHeapElem ad (Array-type T n'))) =
  {(ad, ACell n) | n. n < n'}  $\cup$  {(ad, CField D F) | D F T fm. P  $\vdash$  Object has F:T (fm) in D}
| action-loc-aux P (NormalAction (WriteMem ad al v)) = {(ad, al)}
| action-loc-aux P (NormalAction (ReadMem ad al v)) = {(ad, al)}
| action-loc-aux - - = {}

```

**lemma** *action-loc-aux-intros* [intro?]:

```

P  $\vdash$  class-type-of hT has F:T (fm) in D  $\implies$  (ad, CField D F)  $\in$  action-loc-aux P (NormalAction (NewHeapElem ad hT))
n < n'  $\implies$  (ad, ACell n)  $\in$  action-loc-aux P (NormalAction (NewHeapElem ad (Array-type T n')))
(ad, al)  $\in$  action-loc-aux P (NormalAction (WriteMem ad al v))
(ad, al)  $\in$  action-loc-aux P (NormalAction (ReadMem ad al v))

```

**by**(cases hT) *auto*

**lemma** *action-loc-aux-cases* [elim?, cases set: *action-loc-aux*]:

**assumes** *adal*  $\in$  *action-loc-aux* P *obs*

**obtains** (NewHeapElem) hT F T fm D *ad* **where** *obs* = NormalAction (NewHeapElem ad hT) *adal*  
 = (ad, CField D F) P  $\vdash$  class-type-of hT has F:T (fm) in D



| (NewArr)  $n\ n'\ ad\ T$  **where**  $obs = NormalAction\ (NewHeapElem\ ad\ (Array-type\ T\ n'))$   $adal = (ad, ACell\ n)\ n < n'$   
 | (WriteMem)  $ad\ al\ v$  **where**  $obs = NormalAction\ (WriteMem\ ad\ al\ v)$   $adal = (ad, al)$   
 | (ReadMem)  $ad\ al\ v$  **where**  $obs = NormalAction\ (ReadMem\ ad\ al\ v)$   $adal = (ad, al)$   
**using**  $assms$  **by**(cases ( $P, obs$ ) rule:  $action-loc-aux.cases$ )  $fastforce+$

**lemma**  $action-loc-aux-simps$  [simp]:

( $ad', al'$ )  $\in action-loc-aux\ P\ (NormalAction\ (NewHeapElem\ ad\ hT)) \longleftrightarrow$   
 ( $\exists D\ F\ T\ fm. ad = ad' \wedge al' = CField\ D\ F \wedge P \vdash class-type-of\ hT\ has\ F:T\ (fm)\ in\ D$ )  $\vee$   
 ( $\exists n\ T\ n'. ad = ad' \wedge al' = ACell\ n \wedge hT = Array-type\ T\ n' \wedge n < n'$ )  
 ( $ad', al'$ )  $\in action-loc-aux\ P\ (NormalAction\ (WriteMem\ ad\ al\ v)) \longleftrightarrow ad = ad' \wedge al = al'$   
 ( $ad', al'$ )  $\in action-loc-aux\ P\ (NormalAction\ (ReadMem\ ad\ al\ v)) \longleftrightarrow ad = ad' \wedge al = al'$   
 ( $ad', al'$ )  $\notin action-loc-aux\ P\ InitialThreadAction$   
 ( $ad', al'$ )  $\notin action-loc-aux\ P\ ThreadFinishAction$   
 ( $ad', al'$ )  $\notin action-loc-aux\ P\ (NormalAction\ (ExternalCall\ a\ m\ vs\ v))$   
 ( $ad', al'$ )  $\notin action-loc-aux\ P\ (NormalAction\ (ThreadStart\ t))$   
 ( $ad', al'$ )  $\notin action-loc-aux\ P\ (NormalAction\ (ThreadJoin\ t))$   
 ( $ad', al'$ )  $\notin action-loc-aux\ P\ (NormalAction\ (SyncLock\ a))$   
 ( $ad', al'$ )  $\notin action-loc-aux\ P\ (NormalAction\ (SyncUnlock\ a))$   
 ( $ad', al'$ )  $\notin action-loc-aux\ P\ (NormalAction\ (ObsInterrupt\ t))$   
 ( $ad', al'$ )  $\notin action-loc-aux\ P\ (NormalAction\ (ObsInterrupted\ t))$   
**by**(cases  $hT$ )  $auto$

**declare**  $action-loc-aux.simps$  [simp del]

**abbreviation**  $action-loc :: 'm\ prog \Rightarrow ('addr, 'thread-id)\ execution \Rightarrow JMM-action \Rightarrow ('addr \times addr-loc)\ set$

**where**  $action-loc\ P\ E\ a \equiv action-loc-aux\ P\ (action-obs\ E\ a)$

**inductive-set**  $read-actions :: ('addr, 'thread-id)\ execution \Rightarrow JMM-action\ set$

**for**  $E :: ('addr, 'thread-id)\ execution$

**where**

$ReadMem: \llbracket a \in actions\ E; action-obs\ E\ a = NormalAction\ (ReadMem\ ad\ al\ v) \rrbracket \Longrightarrow a \in read-actions\ E$

**fun**  $addr-loc-default :: 'm\ prog \Rightarrow htype \Rightarrow addr-loc \Rightarrow 'addr\ val$

**where**

$addr-loc-default\ P\ (Class-type\ C)\ (CField\ D\ F) = default-val\ (fst\ (the\ (map-of\ (fields\ P\ C)\ (F, D))))$   
 |  $addr-loc-default\ P\ (Array-type\ T\ n)\ (ACell\ n') = default-val\ T$   
 |  $addr-loc-default-Array-CField:$   
 $addr-loc-default\ P\ (Array-type\ T\ n)\ (CField\ D\ F) = default-val\ (fst\ (the\ (map-of\ (fields\ P\ Object)\ (F, Object))))$   
 |  $addr-loc-default\ P\ - = undefined$

**definition**  $new-actions-for :: 'm\ prog \Rightarrow ('addr, 'thread-id)\ execution \Rightarrow ('addr \times addr-loc) \Rightarrow JMM-action\ set$

**where**

$new-actions-for\ P\ E\ adal =$   
 $\{a. a \in actions\ E \wedge adal \in action-loc\ P\ E\ a \wedge is-new-action\ (action-obs\ E\ a)\}$

**inductive-set**  $external-actions :: ('addr, 'thread-id)\ execution \Rightarrow JMM-action\ set$

**for**  $E :: ('addr, 'thread-id)\ execution$

**where**

$\llbracket a \in actions\ E; action-obs\ E\ a = NormalAction\ (ExternalCall\ ad\ M\ vs\ v) \rrbracket$

$\Rightarrow a \in \text{external-actions } E$

**fun** *value-written-aux* :: 'm prog  $\Rightarrow$  ('addr, 'thread-id) obs-event action  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  
**where**

*value-written-aux* *P* (*NormalAction* (*NewHeapElem* *ad* 'hT)) *al* = *addr-loc-default* *P* *hT* *al*  
| *value-written-aux-WriteMem*':  
*value-written-aux* *P* (*NormalAction* (*WriteMem* *ad* *al* 'v)) *al* = (if *al* = *al*' then *v* else undefined)  
| *value-written-aux-undefined*:  
*value-written-aux* *P* - *al* = undefined

**primrec** *value-written* :: 'm prog  $\Rightarrow$  ('addr, 'thread-id) execution  $\Rightarrow$  JMM-action  $\Rightarrow$  ('addr  $\times$  addr-loc)  
 $\Rightarrow$  'addr val

**where** *value-written* *P* *E* *a* (*ad*, *al*) = *value-written-aux* *P* (*action-obs* *E* *a*) *al*

**definition** *value-read* :: ('addr, 'thread-id) execution  $\Rightarrow$  JMM-action  $\Rightarrow$  'addr val

**where**

*value-read* *E* *a* =  
(case *action-obs* *E* *a* of  
  *NormalAction* *obs*  $\Rightarrow$   
    (case *obs* of  
      *ReadMem* *ad* *al* *v*  $\Rightarrow$  *v*  
      | -  $\Rightarrow$  undefined)  
  | -  $\Rightarrow$  undefined)

**definition** *action-order* :: ('addr, 'thread-id) execution  $\Rightarrow$  JMM-action  $\Rightarrow$  JMM-action  $\Rightarrow$  bool (-  $\vdash$  -  
 $\leq a$  - [51,0,50] 50)

**where**

*E*  $\vdash a \leq a'$   $\longleftrightarrow$   
*a*  $\in$  actions *E*  $\wedge$  *a'*  $\in$  actions *E*  $\wedge$   
(if *is-new-action* (*action-obs* *E* *a*)  
  then *is-new-action* (*action-obs* *E* *a'*)  $\longrightarrow a \leq a'$   
  else  $\neg$  *is-new-action* (*action-obs* *E* *a'*)  $\wedge a \leq a'$ )

**definition** *program-order* :: ('addr, 'thread-id) execution  $\Rightarrow$  JMM-action  $\Rightarrow$  JMM-action  $\Rightarrow$  bool (-  $\vdash$  -  
 $\leq po$  - [51,0,50] 50)

**where**

*E*  $\vdash a \leq po a'$   $\longleftrightarrow E \vdash a \leq a' \wedge \text{action-tid } E a = \text{action-tid } E a'$

**inductive** *synchronizes-with* ::

'm prog  
 $\Rightarrow$  ('thread-id  $\times$  ('addr, 'thread-id) obs-event action)  $\Rightarrow$  ('thread-id  $\times$  ('addr, 'thread-id) obs-event  
action)  $\Rightarrow$  bool  
(-  $\vdash$  -  $\rightsquigarrow_{sw}$  - [51, 51, 51] 50)  
**for** *P* :: 'm prog

**where**

*ThreadStart*: *P*  $\vdash$  (*t*, *NormalAction* (*ThreadStart* *t'*))  $\rightsquigarrow_{sw}$  (*t'*, *InitialThreadAction*)  
| *ThreadFinish*: *P*  $\vdash$  (*t*, *ThreadFinishAction*)  $\rightsquigarrow_{sw}$  (*t'*, *NormalAction* (*ThreadJoin* *t*))  
| *UnlockLock*: *P*  $\vdash$  (*t*, *NormalAction* (*SyncUnlock* *a*))  $\rightsquigarrow_{sw}$  (*t'*, *NormalAction* (*SyncLock* *a*))  
| — Only volatile writes synchronize with volatile reads. We could check volatility of *al* here, but this  
is checked by *sactions* in *sync-with* anyway.  
*Volatile*: *P*  $\vdash$  (*t*, *NormalAction* (*WriteMem* *a* *al* *v*))  $\rightsquigarrow_{sw}$  (*t'*, *NormalAction* (*ReadMem* *a* *al* *v'*))  
| *VolatileNew*:  
  *al*  $\in$  *addr-locs* *P* *hT*  
 $\Rightarrow P \vdash$  (*t*, *NormalAction* (*NewHeapElem* *a* *hT*))  $\rightsquigarrow_{sw}$  (*t'*, *NormalAction* (*ReadMem* *a* *al* *v*))

| *NewHeapElem*:  $P \vdash (t, \text{NormalAction } (\text{NewHeapElem } a \ hT)) \rightsquigarrow_{sw} (t', \text{InitialThreadAction})$   
 | *Interrupt*:  $P \vdash (t, \text{NormalAction } (\text{ObsInterrupt } t')) \rightsquigarrow_{sw} (t'', \text{NormalAction } (\text{ObsInterrupted } t'))$

**definition** *sync-order* ::

'm prog  $\Rightarrow$  ('addr, 'thread-id) execution  $\Rightarrow$  JMM-action  $\Rightarrow$  JMM-action  $\Rightarrow$  bool  
 ( $\neg, \vdash - \leq_{so} - [51, 0, 0, 50] \ 50$ )

**where**

$P, E \vdash a \leq_{so} a' \iff a \in \text{sactions } P \ E \wedge a' \in \text{sactions } P \ E \wedge E \vdash a \leq a'$

**definition** *sync-with* ::

'm prog  $\Rightarrow$  ('addr, 'thread-id) execution  $\Rightarrow$  JMM-action  $\Rightarrow$  JMM-action  $\Rightarrow$  bool  
 ( $\neg, \vdash - \leq_{sw} - [51, 0, 0, 50] \ 50$ )

**where**

$P, E \vdash a \leq_{sw} a' \iff$

$P, E \vdash a \leq_{so} a' \wedge P \vdash (\text{action-tid } E \ a, \text{action-obs } E \ a) \rightsquigarrow_{sw} (\text{action-tid } E \ a', \text{action-obs } E \ a')$

**definition** *po-sw* :: 'm prog  $\Rightarrow$  ('addr, 'thread-id) execution  $\Rightarrow$  JMM-action  $\Rightarrow$  JMM-action  $\Rightarrow$  bool

**where** *po-sw*  $P \ E \ a \ a' \iff E \vdash a \leq_{po} a' \vee P, E \vdash a \leq_{sw} a'$

**abbreviation** *happens-before* ::

'm prog  $\Rightarrow$  ('addr, 'thread-id) execution  $\Rightarrow$  JMM-action  $\Rightarrow$  JMM-action  $\Rightarrow$  bool  
 ( $\neg, \vdash - \leq_{hb} - [51, 0, 0, 50] \ 50$ )

**where** *happens-before*  $P \ E \equiv (\text{po-sw } P \ E)^{++}$

**type-synonym** *write-seen* = JMM-action  $\Rightarrow$  JMM-action

**definition** *is-write-seen* :: 'm prog  $\Rightarrow$  ('addr, 'thread-id) execution  $\Rightarrow$  write-seen  $\Rightarrow$  bool **where**

*is-write-seen*  $P \ E \ ws \iff$

$(\forall a \in \text{read-actions } E. \forall ad \ al \ v. \text{action-obs } E \ a = \text{NormalAction } (\text{ReadMem } ad \ al \ v) \longrightarrow$

$ws \ a \in \text{write-actions } E \wedge (ad, al) \in \text{action-loc } P \ E \ (ws \ a) \wedge$

$\text{value-written } P \ E \ (ws \ a) \ (ad, al) = v \wedge \neg P, E \vdash a \leq_{hb} ws \ a \wedge$

$(\text{is-volatile } P \ al \longrightarrow \neg P, E \vdash a \leq_{so} ws \ a) \wedge$

$(\forall w' \in \text{write-actions } E. (ad, al) \in \text{action-loc } P \ E \ w' \longrightarrow$

$(P, E \vdash ws \ a \leq_{hb} w' \wedge P, E \vdash w' \leq_{hb} a \vee \text{is-volatile } P \ al \wedge P, E \vdash ws \ a \leq_{so} w' \wedge P, E \vdash w'$

$\leq_{so} a) \longrightarrow$

$w' = ws \ a))$

**definition** *thread-start-actions-ok* :: ('addr, 'thread-id) execution  $\Rightarrow$  bool

**where**

*thread-start-actions-ok*  $E \iff$

$(\forall a \in \text{actions } E. \neg \text{is-new-action } (\text{action-obs } E \ a) \longrightarrow$

$(\exists i. i \leq a \wedge \text{action-obs } E \ i = \text{InitialThreadAction} \wedge \text{action-tid } E \ i = \text{action-tid } E \ a))$

**primrec** *wf-exec* :: 'm prog  $\Rightarrow$  ('addr, 'thread-id) execution  $\times$  write-seen  $\Rightarrow$  bool ( $\vdash - \sqrt{[51, 50]} \ 51$ )

**where**  $P \vdash (E, ws) \sqrt{\iff \text{is-write-seen } P \ E \ ws \wedge \text{thread-start-actions-ok } E}$

**inductive** *most-recent-write-for* :: 'm prog  $\Rightarrow$  ('addr, 'thread-id) execution  $\Rightarrow$  JMM-action  $\Rightarrow$  JMM-action  $\Rightarrow$  bool

( $\neg, \vdash - \rightsquigarrow_{mrw} - [50, 0, 51] \ 51$ )

**for**  $P ::$  'm prog **and**  $E ::$  ('addr, 'thread-id) execution **and**  $ra ::$  JMM-action **and**  $wa ::$  JMM-action

**where**

$\llbracket ra \in \text{read-actions } E; adal \in \text{action-loc } P \ E \ ra; E \vdash wa \leq a \ ra;$

$wa \in \text{write-actions } E; adal \in \text{action-loc } P \ E \ wa;$

$\bigwedge wa'. \llbracket wa' \in \text{write-actions } E; adal \in \text{action-loc } P \ E \ wa' \rrbracket$

$$\begin{aligned} & \Rightarrow E \vdash wa' \leq_a wa \vee E \vdash ra \leq_a wa' \\ & \Rightarrow P, E \vdash ra \leadsto_{mrw} wa \end{aligned}$$

**primrec** *sequentially-consistent* :: 'm prog  $\Rightarrow$  (('addr, 'thread-id) execution  $\times$  write-seen)  $\Rightarrow$  bool  
**where**

$$\text{sequentially-consistent } P \ (E, ws) \longleftrightarrow (\forall r \in \text{read-actions } E. P, E \vdash r \leadsto_{mrw} ws \ r)$$

### 8.4.2 Actions

**inductive-cases** *is-new-action-cases* [elim!]:

*is-new-action* (NormalAction (ExternalCall a M vs v))  
*is-new-action* (NormalAction (ReadMem a al v))  
*is-new-action* (NormalAction (WriteMem a al v))  
*is-new-action* (NormalAction (NewHeapElem a hT))  
*is-new-action* (NormalAction (ThreadStart t))  
*is-new-action* (NormalAction (ThreadJoin t))  
*is-new-action* (NormalAction (SyncLock a))  
*is-new-action* (NormalAction (SyncUnlock a))  
*is-new-action* (NormalAction (ObsInterrupt t))  
*is-new-action* (NormalAction (ObsInterrupted t))  
*is-new-action* InitialThreadAction  
*is-new-action* ThreadFinishAction

**inductive-simps** *is-new-action-simps* [simp]:

*is-new-action* (NormalAction (NewHeapElem a hT))  
*is-new-action* (NormalAction (ExternalCall a M vs v))  
*is-new-action* (NormalAction (ReadMem a al v))  
*is-new-action* (NormalAction (WriteMem a al v))  
*is-new-action* (NormalAction (ThreadStart t))  
*is-new-action* (NormalAction (ThreadJoin t))  
*is-new-action* (NormalAction (SyncLock a))  
*is-new-action* (NormalAction (SyncUnlock a))  
*is-new-action* (NormalAction (ObsInterrupt t))  
*is-new-action* (NormalAction (ObsInterrupted t))  
*is-new-action* InitialThreadAction  
*is-new-action* ThreadFinishAction

**lemmas** *is-new-action-iff* = *is-new-action.simps*

**inductive-simps** *is-write-action-simps* [simp]:

*is-write-action* InitialThreadAction  
*is-write-action* ThreadFinishAction  
*is-write-action* (NormalAction (ExternalCall a m vs v))  
*is-write-action* (NormalAction (ReadMem a al v))  
*is-write-action* (NormalAction (WriteMem a al v))  
*is-write-action* (NormalAction (NewHeapElem a hT))  
*is-write-action* (NormalAction (ThreadStart t))  
*is-write-action* (NormalAction (ThreadJoin t))  
*is-write-action* (NormalAction (SyncLock a))  
*is-write-action* (NormalAction (SyncUnlock a))  
*is-write-action* (NormalAction (ObsInterrupt t))  
*is-write-action* (NormalAction (ObsInterrupted t))

**declare** *saction.intros* [intro!]

**inductive-cases** *saction-cases* [elim!]:

*saction* *P* (*NormalAction* (*ExternalCall* *a M vs v*))  
*saction* *P* (*NormalAction* (*ReadMem* *a al v*))  
*saction* *P* (*NormalAction* (*WriteMem* *a al v*))  
*saction* *P* (*NormalAction* (*NewHeapElem* *a hT*))  
*saction* *P* (*NormalAction* (*ThreadStart* *t*))  
*saction* *P* (*NormalAction* (*ThreadJoin* *t*))  
*saction* *P* (*NormalAction* (*SyncLock* *a*))  
*saction* *P* (*NormalAction* (*SyncUnlock* *a*))  
*saction* *P* (*NormalAction* (*ObsInterrupt* *t*))  
*saction* *P* (*NormalAction* (*ObsInterrupted* *t*))  
*saction* *P* *InitialThreadAction*  
*saction* *P* *ThreadFinishAction*

**inductive-simps** *saction-simps* [simp]:

*saction* *P* (*NormalAction* (*ExternalCall* *a M vs v*))  
*saction* *P* (*NormalAction* (*ReadMem* *a al v*))  
*saction* *P* (*NormalAction* (*WriteMem* *a al v*))  
*saction* *P* (*NormalAction* (*NewHeapElem* *a hT*))  
*saction* *P* (*NormalAction* (*ThreadStart* *t*))  
*saction* *P* (*NormalAction* (*ThreadJoin* *t*))  
*saction* *P* (*NormalAction* (*SyncLock* *a*))  
*saction* *P* (*NormalAction* (*SyncUnlock* *a*))  
*saction* *P* (*NormalAction* (*ObsInterrupt* *t*))  
*saction* *P* (*NormalAction* (*ObsInterrupted* *t*))  
*saction* *P* *InitialThreadAction*  
*saction* *P* *ThreadFinishAction*

**lemma** *new-action-saction* [simp, intro]: *is-new-action a*  $\implies$  *saction P a*  
**by**(blast elim: *is-new-action.cases*)

**lemmas** *saction-iff* = *saction.simps*

**lemma** *actionsD*: *a*  $\in$  *actions E*  $\implies$  *enat a* < *llength E*

**unfolding** *actions-def* **by** *blast*

**lemma** *actionsE*:

**assumes** *a*  $\in$  *actions E*  
**obtains** *enat a* < *llength E*

**using** *assms* **unfolding** *actions-def* **by** *blast*

**lemma** *actions-lappend*:

*llength xs* = *enat n*  $\implies$  *actions (lappend xs ys)* = *actions xs*  $\cup$  ((+) *n*) ‘ *actions ys*

**unfolding** *actions-def*

**apply** *safe*

**apply**(*erule contrapos-np*)  
**apply**(*rule-tac x=x - n in image-eqI*)  
**apply** *simp-all*  
**apply**(*case-tac* [!] *llength ys*)  
**apply** *simp-all*  
**done**

**lemma** *tactionsE*:

**assumes**  $a \in \text{tactions } E \ t$   
**obtains**  $\text{obs}$  **where**  $a \in \text{actions } E \ \text{action-tid } E \ a = t \ \text{action-obs } E \ a = \text{obs}$   
**using**  $\text{assms}$   
**by**( $\text{cases } \text{lnth } E \ a$ )( $\text{auto simp add: tactions-def action-tid-def action-obs-def}$ )

**lemma**  $\text{sactionsI}$ :  
 $\llbracket a \in \text{actions } E; \text{saction } P \ (\text{action-obs } E \ a) \rrbracket \implies a \in \text{sactions } P \ E$   
**unfolding**  $\text{sactions-def}$  **by**  $\text{blast}$

**lemma**  $\text{sactionsE}$ :  
**assumes**  $a \in \text{sactions } P \ E$   
**obtains**  $a \in \text{actions } E \ \text{saction } P \ (\text{action-obs } E \ a)$   
**using**  $\text{assms}$  **unfolding**  $\text{sactions-def}$  **by**  $\text{blast}$

**lemma**  $\text{sactions-actions}$  [ $\text{simp}$ ]:  
 $a \in \text{sactions } P \ E \implies a \in \text{actions } E$   
**by**( $\text{rule sactionsE}$ )

**lemma**  $\text{value-written-aux-WriteMem}$  [ $\text{simp}$ ]:  
 $\text{value-written-aux } P \ (\text{NormalAction } (\text{WriteMem } \text{ad } \text{al } v)) \ \text{al} = v$   
**by**  $\text{simp}$

**declare**  $\text{value-written-aux-undefined}$  [ $\text{simp del}$ ]  
**declare**  $\text{value-written-aux-WriteMem'}$  [ $\text{simp del}$ ]

**inductive-simps**  $\text{is-write-action-iff}$ :  
 $\text{is-write-action } a$

**inductive-simps**  $\text{write-actions-iff}$ :  
 $a \in \text{write-actions } E$

**lemma**  $\text{write-actions-actions}$  [ $\text{simp}$ ]:  
 $a \in \text{write-actions } E \implies a \in \text{actions } E$   
**by**( $\text{rule write-actions.induct}$ )

**inductive-simps**  $\text{read-actions-iff}$ :  
 $a \in \text{read-actions } E$

**lemma**  $\text{read-actions-actions}$  [ $\text{simp}$ ]:  
 $a \in \text{read-actions } E \implies a \in \text{actions } E$   
**by**( $\text{rule read-actions.induct}$ )

**lemma**  $\text{read-action-action-locE}$ :  
**assumes**  $r \in \text{read-actions } E$   
**obtains**  $\text{ad } \text{al}$  **where**  $(\text{ad}, \text{al}) \in \text{action-loc } P \ E \ r$   
**using**  $\text{assms}$  **by**  $\text{cases auto}$

**lemma**  $\text{read-actions-not-write-actions}$ :  
 $\llbracket a \in \text{read-actions } E; a \in \text{write-actions } E \rrbracket \implies \text{False}$   
**by**( $\text{auto elim!: read-actions.cases write-actions.cases}$ )

**lemma**  $\text{read-actions-Int-write-actions}$  [ $\text{simp}$ ]:  
 $\text{read-actions } E \cap \text{write-actions } E = \{\}$   
 $\text{write-actions } E \cap \text{read-actions } E = \{\}$

**by**(*blast dest: read-actions-not-write-actions*)**+**

**lemma** *action-loc-addr-fun*:

$\llbracket (ad, al) \in \text{action-loc } P \ E \ a; (ad', al') \in \text{action-loc } P \ E \ a \rrbracket \implies ad = ad'$   
**by**(*auto elim!: action-loc-aux-cases*)

**lemma** *value-written-cong* [*cong*]:

$\llbracket P = P'; a = a'; \text{action-obs } E \ a' = \text{action-obs } E' \ a' \rrbracket$   
 $\implies \text{value-written } P \ E \ a = \text{value-written } P' \ E' \ a'$   
**by**(*rule ext*)(*auto split: action.splits*)

**declare** *value-written.simps* [*simp del*]

**lemma** *new-actionsI*:

$\llbracket a \in \text{actions } E; adal \in \text{action-loc } P \ E \ a; \text{is-new-action } (\text{action-obs } E \ a) \rrbracket$   
 $\implies a \in \text{new-actions-for } P \ E \ adal$   
**unfolding** *new-actions-for-def* **by** *blast*

**lemma** *new-actionsE*:

**assumes**  $a \in \text{new-actions-for } P \ E \ adal$   
**obtains**  $a \in \text{actions } E \ adal \in \text{action-loc } P \ E \ a \ \text{is-new-action } (\text{action-obs } E \ a)$   
**using** *assms* **unfolding** *new-actions-for-def* **by** *blast*

**lemma** *action-loc-read-action-singleton*:

$\llbracket r \in \text{read-actions } E; adal \in \text{action-loc } P \ E \ r; adal' \in \text{action-loc } P \ E \ r \rrbracket \implies adal = adal'$   
**by**(*cases adal, cases adal'*)(*fastforce elim: read-actions.cases action-loc-aux-cases*)

**lemma** *addr-locsI*:

$P \vdash \text{class-type-of } hT \text{ has } F:T \text{ (fm) in } D \implies CField \ D \ F \in \text{addr-locs } P \ hT$   
 $\llbracket hT = \text{Array-type } T \ n; n' < n \rrbracket \implies ACell \ n' \in \text{addr-locs } P \ hT$   
**by**(*cases hT*)(*auto dest: has-field-decl-above*)

### 8.4.3 Orders

#### 8.4.4 Action order

**lemma** *action-orderI*:

**assumes**  $a \in \text{actions } E \ a' \in \text{actions } E$   
**and**  $\llbracket \text{is-new-action } (\text{action-obs } E \ a); \text{is-new-action } (\text{action-obs } E \ a') \rrbracket \implies a \leq a'$   
**and**  $\neg \text{is-new-action } (\text{action-obs } E \ a) \implies \neg \text{is-new-action } (\text{action-obs } E \ a') \wedge a \leq a'$   
**shows**  $E \vdash a \leq a'$   
**using** *assms* **unfolding** *action-order-def* **by** *simp*

**lemma** *action-orderE*:

**assumes**  $E \vdash a \leq a'$   
**obtains**  $a \in \text{actions } E \ a' \in \text{actions } E$   
 $\text{is-new-action } (\text{action-obs } E \ a) \ \text{is-new-action } (\text{action-obs } E \ a') \longrightarrow a \leq a'$   
 $\mid a \in \text{actions } E \ a' \in \text{actions } E$   
 $\neg \text{is-new-action } (\text{action-obs } E \ a) \ \neg \text{is-new-action } (\text{action-obs } E \ a') \ a \leq a'$   
**using** *assms* **unfolding** *action-order-def* **by**(*simp split: if-split-asm*)

**lemma** *refl-action-order*:

*refl-onP* (*actions E*) (*action-order E*)  
**by**(*rule refl-onPI*)(*auto elim: action-orderE intro: action-orderI*)

**lemma** *antisym-action-order*:

*antisym* (*action-order* *E*)

**by**(*rule antisymPI*)(*auto elim!*: *action-orderE*)

**lemma** *trans-action-order*:

*transp* (*action-order* *E*)

**by**(*rule transpI*)(*auto elim!*: *action-orderE* *intro*: *action-orderI*)

**lemma** *porder-action-order*:

*porder-on* (*actions* *E*) (*action-order* *E*)

**by**(*blast intro*: *porder-onI* *refl-action-order* *antisym-action-order* *trans-action-order*)

**lemma** *total-action-order*:

*total-onP* (*actions* *E*) (*action-order* *E*)

**by**(*rule total-onPI*)(*auto simp add*: *action-order-def*)

**lemma** *torder-action-order*:

*torder-on* (*actions* *E*) (*action-order* *E*)

**by**(*blast intro*: *torder-onI* *total-action-order* *porder-action-order*)

**lemma** *wf-action-order*: *wfP* (*action-order* *E*)<sup>≠≠</sup>

**unfolding** *wfP-eq-minimal*

**proof**(*intro strip*)

**fix** *Q* **and** *x* :: *JMM-action*

**assume** *x* ∈ *Q*

**show** ∃ *z* ∈ *Q*. ∀ *y*. (*action-order* *E*)<sup>≠≠</sup> *y z* ⟶ *y* ∉ *Q*

**proof**(*cases* ∃ *a* ∈ *Q*. *a* ∈ *actions* *E* ∧ *is-new-action* (*action-obs* *E* *a*))

**case** *True*

**then obtain** *a* **where** *a*: *a* ∈ *actions* *E* ∧ *is-new-action* (*action-obs* *E* *a*) ∧ *a* ∈ *Q* **by** *blast*

**define** *a'* **where** *a'* = (*LEAST* *a'*. *a'* ∈ *actions* *E* ∧ *is-new-action* (*action-obs* *E* *a'*) ∧ *a'* ∈ *Q*)

**from** *a* **have** *a'*: *a'* ∈ *actions* *E* ∧ *is-new-action* (*action-obs* *E* *a'*) ∧ *a'* ∈ *Q*

**unfolding** *a'-def* **by**(*rule LeastI*)

{ **fix** *y*

**assume** *y-le-a'*: (*action-order* *E*)<sup>≠≠</sup> *y* *a'*

**have** *y* ∉ *Q*

**proof**

**assume** *y* ∈ *Q*

**with** *y-le-a'* *a'* **have** *y*: *y* ∈ *actions* *E* ∧ *is-new-action* (*action-obs* *E* *y*) ∧ *y* ∈ *Q*

**by**(*auto elim*: *action-orderE*)

**hence** *a'* ≤ *y* **unfolding** *a'-def* **by**(*rule Least-le*)

**with** *y-le-a'* *a'* **show** *False* **by**(*auto elim*: *action-orderE*)

**qed** }

**with** *a'* **show** *?thesis* **by** *blast*

**next**

**case** *False*

**hence** *not-new*: ∧*a*. [ *a* ∈ *Q*; *a* ∈ *actions* *E* ] ⟹ ¬ *is-new-action* (*action-obs* *E* *a*) **by** *blast*

**show** *?thesis*

**proof**(*cases* *Q* ∩ *actions* *E* = { })

**case** *True*

**with** ⟨*x* ∈ *Q*⟩ **show** *?thesis* **by**(*auto elim*: *action-orderE*)

**next**

**case** *False*

**define** *a'* **where** *a'* = (*LEAST* *a'*. *a'* ∈ *Q* ∧ *a'* ∈ *actions* *E* ∧ ¬ *is-new-action* (*action-obs* *E* *a'*))



```

from False obtain a where  $a \in Q \wedge a \in \text{actions } E$  by blast
with not-new[OF this] have  $a \in Q \wedge a \in \text{actions } E \wedge \neg \text{is-new-action } (\text{action-obs } E \ a)$  by blast
hence  $a': a' \in Q \wedge a' \in \text{actions } E \wedge \neg \text{is-new-action } (\text{action-obs } E \ a')$ 
  unfolding a'-def by (rule LeastI)
{ fix y
  assume  $y \leq a': (\text{action-order } E) \neq y \ a'$ 
  hence  $y \in \text{actions } E$  by (auto elim: action-orderE)
  have  $y \notin Q$ 
  proof
    assume  $y \in Q$ 
    hence y-not-new:  $\neg \text{is-new-action } (\text{action-obs } E \ y)$ 
    using  $\langle y \in \text{actions } E \rangle$  by (rule not-new)
    with  $\langle y \in Q \rangle \langle y \in \text{actions } E \rangle$  have  $a' \leq y$ 
    unfolding a'-def by  $\neg(\text{rule Least-le, blast})$ 
    with y-le-a' y-not-new show False by (auto elim: action-orderE)
  qed }
with a' show ?thesis by blast
qed
qed
qed

```

**lemma** *action-order-is-new-actionD*:

```

 $\llbracket E \vdash a \leq a'; \text{is-new-action } (\text{action-obs } E \ a') \rrbracket \implies \text{is-new-action } (\text{action-obs } E \ a)$ 
by (auto elim: action-orderE)

```

#### 8.4.5 Program order

**lemma** *program-orderI*:

```

assumes  $E \vdash a \leq a'$  and  $\text{action-tid } E \ a = \text{action-tid } E \ a'$ 
shows  $E \vdash a \leq_{po} a'$ 
using assms unfolding program-order-def by auto

```

**lemma** *program-orderE*:

```

assumes  $E \vdash a \leq_{po} a'$ 
obtains  $t \text{ obs } \text{obs}'$ 
where  $E \vdash a \leq a'$ 
and  $\text{action-tid } E \ a = t \ \text{action-obs } E \ a = \text{obs}$ 
and  $\text{action-tid } E \ a' = t \ \text{action-obs } E \ a' = \text{obs}'$ 
using assms unfolding program-order-def
by (cases lnth E a) (cases lnth E a', auto simp add: action-obs-def action-tid-def)

```

**lemma** *refl-on-program-order*:

```

 $\text{refl-onP } (\text{actions } E) \ (\text{program-order } E)$ 
by (rule refl-onPI) (auto elim: action-orderE program-orderE intro: program-orderI refl-onPD[OF refl-action-order])

```

**lemma** *antisym-program-order*:

```

 $\text{antisymP } (\text{program-order } E)$ 
using antisymPD[OF antisym-action-order]
by (auto intro: antisymPI elim!: program-orderE)

```

**lemma** *trans-program-order*:

```

 $\text{transP } (\text{program-order } E)$ 
by (rule transPI) (auto elim!: program-orderE intro: program-orderI dest: transPD[OF trans-action-order])

```

**lemma** *porder-program-order*:

*porder-on (actions E) (program-order E)*

**by**(*blast intro: porder-onI refl-on-program-order antisym-program-order trans-program-order*)

**lemma** *total-program-order-on-tactions*:

*total-onP (tactions E t) (program-order E)*

**by**(*rule total-onPI*)(*auto elim: tactionsE simp add: program-order-def dest: total-onD[OF total-action-order]*)

### 8.4.6 Synchronization order

**lemma** *sync-orderI*:

$\llbracket E \vdash a \leq a'; a \in \text{sactions } P E; a' \in \text{sactions } P E \rrbracket \implies P, E \vdash a \leq_{so} a'$

**unfolding** *sync-order-def* **by** *blast*

**lemma** *sync-orderE*:

**assumes**  $P, E \vdash a \leq_{so} a'$

**obtains**  $a \in \text{sactions } P E \ a' \in \text{sactions } P E \ E \vdash a \leq_a a'$

**using** *assms* **unfolding** *sync-order-def* **by** *blast*

**lemma** *refl-on-sync-order*:

*refl-onP (sactions P E) (sync-order P E)*

**by**(*rule refl-onPI*)(*fastforce elim: sync-orderE intro: sync-orderI refl-onPD[OF refl-action-order]*)+

**lemma** *antisym-sync-order*:

*antisymp (sync-order P E)*

**using** *antisympD[OF antisym-action-order]*

**by**(*rule antisympI*)(*auto elim!: sync-orderE*)

**lemma** *trans-sync-order*:

*transp (sync-order P E)*

**by**(*rule transPI*)(*auto elim!: sync-orderE intro: sync-orderI dest: transPD[OF trans-action-order]*)

**lemma** *porder-sync-order*:

*porder-on (sactions P E) (sync-order P E)*

**by**(*blast intro: porder-onI refl-on-sync-order antisym-sync-order trans-sync-order*)

**lemma** *total-sync-order*:

*total-onP (sactions P E) (sync-order P E)*

**apply**(*rule total-onPI*)

**apply**(*simp add: sync-order-def*)

**apply**(*rule total-onPD[OF total-action-order]*)

**apply** *simp-all*

**done**

**lemma** *torder-sync-order*:

*torder-on (sactions P E) (sync-order P E)*

**by**(*blast intro: torder-onI porder-sync-order total-sync-order*)

### 8.4.7 Synchronizes with

**lemma** *sync-withI*:

$\llbracket P, E \vdash a \leq_{so} a'; P \vdash (\text{action-tid } E \ a, \text{action-obs } E \ a) \rightsquigarrow_{sw} (\text{action-tid } E \ a', \text{action-obs } E \ a') \rrbracket$

$\implies P, E \vdash a \leq_{sw} a'$

**unfolding** *sync-with-def* **by** *blast*

**lemma** *sync-withE*:  
 assumes  $P, E \vdash a \leq_{sw} a'$   
 obtains  $P, E \vdash a \leq_{so} a' \vdash (action-tid\ E\ a, action-obs\ E\ a) \rightsquigarrow_{sw} (action-tid\ E\ a', action-obs\ E\ a')$   
 using *assms* **unfolding** *sync-with-def* **by** *blast*

**lemma** *irrefl-synchronizes-with*:  
*irreflP* (*synchronizes-with* *P*)  
**by**(*rule* *irreflPI*)(*auto* *elim*: *synchronizes-with.cases*)

**lemma** *irrefl-sync-with*:  
*irreflP* (*sync-with* *P* *E*)  
**by**(*rule* *irreflPI*)(*auto* *elim*: *sync-withE* *intro*: *irreflPD*[*OF* *irrefl-synchronizes-with*])

**lemma** *antisym-sync-with*:  
*antisymP* (*sync-with* *P* *E*)  
**using** *antisymPD*[*OF* *antisym-sync-order*, *of* *P* *E*]  
**by**  $\neg$ (*rule* *antisymPI*, *auto* *elim*: *sync-withE*)

**lemma** *consistent-program-order-sync-order*:  
*order-consistent* (*program-order* *E*) (*sync-order* *P* *E*)  
**apply**(*rule* *order-consistent-subset*)  
**apply**(*rule* *antisym-order-consistent-self*[*OF* *antisym-action-order*[*of* *E*]])  
**apply**(*blast* *elim*: *program-orderE* *sync-orderE*)  
**done**

**lemma** *consistent-program-order-sync-with*:  
*order-consistent* (*program-order* *E*) (*sync-with* *P* *E*)  
**by**(*rule* *order-consistent-subset*[*OF* *consistent-program-order-sync-order*])(*blast* *elim*: *sync-withE*)  
**auto**

#### 8.4.8 Happens before

**lemma** *porder-happens-before*:  
*porder-on* (*actions* *E*) (*happens-before* *P* *E*)  
**unfolding** *po-sw-def* [*abs-def*]  
**by**(*rule* *porder-on-sub-torder-on-tranclp-porder-onI*[*OF* *porder-program-order* *torder-sync-order* *consistent-program-order-sync-order*])(*auto* *elim*: *sync-withE*)

**lemma** *porder-tranclp-po-so*:  
*porder-on* (*actions* *E*)  $(\lambda a\ a'. program-order\ E\ a\ a' \vee sync-order\ P\ E\ a\ a')^{\wedge++}$   
**by**(*rule* *porder-on-torder-on-tranclp-porder-onI*[*OF* *porder-program-order* *torder-sync-order* *consistent-program-order-sync-order*])  
**auto**

**lemma** *happens-before-refl*:  
 assumes  $a \in actions\ E$   
 shows  $P, E \vdash a \leq_{hb} a$   
**using** *porder-happens-before*[*of* *E* *P*]  
**by**(*rule* *porder-onE*)(*erule* *refl-onPD*[*OF* - *assms*])

**lemma** *happens-before-into-po-so-tranclp*:  
 assumes  $P, E \vdash a \leq_{hb} a'$   
 shows  $(\lambda a\ a'. E \vdash a \leq_{po} a' \vee P, E \vdash a \leq_{so} a')^{\wedge++} a\ a'$   
**using** *assms* **unfolding** *po-sw-def* [*abs-def*]  
**by**(*induct*)(*blast* *elim*: *sync-withE* *intro*: *tranclp.trancl-into-trancl*)  
**+**

**lemma** *po-sw-into-action-order*:

*po-sw*  $P\ E\ a\ a' \implies E \vdash a \leq a'$

**by**(*auto elim: program-orderE sync-withE sync-orderE simp add: po-sw-def*)

**lemma** *happens-before-into-action-order*:

**assumes**  $P, E \vdash a \leq_{hb} a'$

**shows**  $E \vdash a \leq a'$

**using** *assms*

**by** *induct(blast intro: po-sw-into-action-order transPD[OF trans-action-order])*+

**lemma** *action-order-consistent-with-happens-before*:

*order-consistent* (*action-order*  $E$ ) (*happens-before*  $P\ E$ )

**by**(*blast intro: order-consistent-subset antisym-order-consistent-self antisym-action-order happens-before-into-action*)

**lemma** *happens-before-new-actionD*:

**assumes** *hb*:  $P, E \vdash a \leq_{hb} a'$

**and** *new*: *is-new-action* (*action-obs*  $E\ a'$ )

**shows** *is-new-action* (*action-obs*  $E\ a$ ) *action-tid*  $E\ a = \text{action-tid } E\ a'\ a \leq a'$

**using** *hb*

**proof**(*induct rule: converse-tranclp-induct*)

**case** (*base*  $a$ )

**case 1 from** *new base show* ?*case*

**by**(*auto dest: po-sw-into-action-order elim: action-orderE*)

**case 2 from** *new base show* ?*case*

**by**(*auto simp add: po-sw-def elim!: sync-withE elim: program-orderE synchronizes-with.cases*)

**case 3 from** *new base show* ?*case*

**by**(*auto dest: po-sw-into-action-order elim: action-orderE*)

**next**

**case** (*step*  $a\ a''$ )

**note** *po-sw* =  $\langle \text{po-sw } P\ E\ a\ a' \rangle$

**and** *new* =  $\langle \text{is-new-action } (\text{action-obs } E\ a'') \rangle$

**and** *tid* =  $\langle \text{action-tid } E\ a'' = \text{action-tid } E\ a' \rangle$

**case 1 from** *new po-sw show* ?*case*

**by**(*auto dest: po-sw-into-action-order elim: action-orderE*)

**case 2 from** *new po-sw tid show* ?*case*

**by**(*auto simp add: po-sw-def elim!: sync-withE elim: program-orderE synchronizes-with.cases*)

**case 3 from** *new po-sw*  $\langle a'' \leq a' \rangle$  **show** ?*case*

**by**(*auto dest!: po-sw-into-action-order elim!: action-orderE*)

**qed**

**lemma** *external-actions-not-new*:

$\llbracket a \in \text{external-actions } E; \text{is-new-action } (\text{action-obs } E\ a) \rrbracket \implies \text{False}$

**by**(*erule external-actions.cases*)(*simp*)

### 8.4.9 Most recent writes and sequential consistency

**lemma** *most-recent-write-for-fun*:

$\llbracket P, E \vdash ra \leadsto_{mrw} wa; P, E \vdash ra \leadsto_{mrw} wa' \rrbracket \implies wa = wa'$

**apply**(*erule most-recent-write-for.cases*)

**apply** *clarsimp*

```

apply(erule meta-allE)+
apply(erule meta-impE)
  apply(rotate-tac 3)
  apply assumption
apply(erule (1) meta-impE)
apply(erule (1) action-loc-read-action-singleton)
  apply(rotate-tac 1)
  apply assumption
apply(fastforce dest: antisymPD[OF antisym-action-order] elim: write-actions.cases read-actions.cases)
done

```

**lemma** *THE-most-recent-writeI*:  $P, E \vdash r \rightsquigarrow_{mrw} w \implies (THE\ w.\ P, E \vdash r \rightsquigarrow_{mrw} w) = w$   
**by**(blast dest: most-recent-write-for-fun)+

**lemma** *most-recent-write-for-write-actionsD*:  
**assumes**  $P, E \vdash ra \rightsquigarrow_{mrw} wa$   
**shows**  $wa \in \text{write-actions } E$   
**using** *assms* **by** *cases*

**lemma** *most-recent-write-recent*:  
 $\llbracket P, E \vdash r \rightsquigarrow_{mrw} w; adal \in \text{action-loc } P\ E\ r; w' \in \text{write-actions } E; adal \in \text{action-loc } P\ E\ w' \rrbracket$   
 $\implies E \vdash w' \leq_a w \vee E \vdash r \leq_a w'$   
**apply**(erule most-recent-write-for.cases)  
**apply**(erule (1) action-loc-read-action-singleton)  
**apply**(rotate-tac 1)  
**apply** assumption  
**apply** clarsimp  
**done**

**lemma** *is-write-seenI*:  
 $\llbracket \bigwedge a\ ad\ al\ v.\ \llbracket a \in \text{read-actions } E; \text{action-obs } E\ a = \text{NormalAction } (\text{ReadMem } ad\ al\ v) \rrbracket$   
 $\implies ws\ a \in \text{write-actions } E;$   
 $\bigwedge a\ ad\ al\ v.\ \llbracket a \in \text{read-actions } E; \text{action-obs } E\ a = \text{NormalAction } (\text{ReadMem } ad\ al\ v) \rrbracket$   
 $\implies (ad, al) \in \text{action-loc } P\ E\ (ws\ a);$   
 $\bigwedge a\ ad\ al\ v.\ \llbracket a \in \text{read-actions } E; \text{action-obs } E\ a = \text{NormalAction } (\text{ReadMem } ad\ al\ v) \rrbracket$   
 $\implies \text{value-written } P\ E\ (ws\ a)\ (ad, al) = v;$   
 $\bigwedge a\ ad\ al\ v.\ \llbracket a \in \text{read-actions } E; \text{action-obs } E\ a = \text{NormalAction } (\text{ReadMem } ad\ al\ v) \rrbracket$   
 $\implies \neg P, E \vdash a \leq_{hb} ws\ a;$   
 $\bigwedge a\ ad\ al\ v.\ \llbracket a \in \text{read-actions } E; \text{action-obs } E\ a = \text{NormalAction } (\text{ReadMem } ad\ al\ v); \text{is-volatile } P\ al \rrbracket$   
 $\implies \neg P, E \vdash a \leq_{so} ws\ a;$   
 $\bigwedge a\ ad\ al\ v\ a'.\ \llbracket a \in \text{read-actions } E; \text{action-obs } E\ a = \text{NormalAction } (\text{ReadMem } ad\ al\ v);$   
 $a' \in \text{write-actions } E; (ad, al) \in \text{action-loc } P\ E\ a'; P, E \vdash ws\ a \leq_{hb} a';$   
 $P, E \vdash a' \leq_{hb} a \rrbracket \implies a' = ws\ a;$   
 $\bigwedge a\ ad\ al\ v\ a'.\ \llbracket a \in \text{read-actions } E; \text{action-obs } E\ a = \text{NormalAction } (\text{ReadMem } ad\ al\ v);$   
 $a' \in \text{write-actions } E; (ad, al) \in \text{action-loc } P\ E\ a'; \text{is-volatile } P\ al; P, E \vdash ws\ a \leq_{so} a';$   
 $P, E \vdash a' \leq_{so} a \rrbracket \implies a' = ws\ a \rrbracket$   
 $\implies \text{is-write-seen } P\ E\ ws$   
**unfolding** *is-write-seen-def*  
**by**(blast 30)

**lemma** *is-write-seenD*:  
 $\llbracket \text{is-write-seen } P\ E\ ws; a \in \text{read-actions } E; \text{action-obs } E\ a = \text{NormalAction } (\text{ReadMem } ad\ al\ v) \rrbracket$   
 $\implies ws\ a \in \text{write-actions } E \wedge (ad, al) \in \text{action-loc } P\ E\ (ws\ a) \wedge \text{value-written } P\ E\ (ws\ a)\ (ad, al)$

$= v \wedge \neg P, E \vdash a \leq_{hb} ws \ a \wedge (is\text{-}volatile \ P \ al \longrightarrow \neg P, E \vdash a \leq_{so} ws \ a) \wedge$   
 $(\forall a' \in write\text{-}actions \ E. (ad, al) \in action\text{-}loc \ P \ E \ a' \wedge (P, E \vdash ws \ a \leq_{hb} a' \wedge P, E \vdash a' \leq_{hb} a \vee$   
 $is\text{-}volatile \ P \ al \wedge P, E \vdash ws \ a \leq_{so} a' \wedge P, E \vdash a' \leq_{so} a) \longrightarrow a' = ws \ a)$

**unfolding** *is-write-seen-def* **by** *blast*

**lemma** *thread-start-actions-okI*:

$(\bigwedge a. \llbracket a \in actions \ E; \neg is\text{-}new\text{-}action \ (action\text{-}obs \ E \ a) \rrbracket$   
 $\implies \exists i. i \leq a \wedge action\text{-}obs \ E \ i = InitialThreadAction \wedge action\text{-}tid \ E \ i = action\text{-}tid \ E \ a)$   
 $\implies thread\text{-}start\text{-}actions\text{-}ok \ E$

**unfolding** *thread-start-actions-ok-def* **by** *blast*

**lemma** *thread-start-actions-okD*:

$\llbracket thread\text{-}start\text{-}actions\text{-}ok \ E; a \in actions \ E; \neg is\text{-}new\text{-}action \ (action\text{-}obs \ E \ a) \rrbracket$   
 $\implies \exists i. i \leq a \wedge action\text{-}obs \ E \ i = InitialThreadAction \wedge action\text{-}tid \ E \ i = action\text{-}tid \ E \ a$

**unfolding** *thread-start-actions-ok-def* **by** *blast*

**lemma** *thread-start-actions-ok-prefix*:

$\llbracket thread\text{-}start\text{-}actions\text{-}ok \ E'; lprefix \ E \ E' \rrbracket \implies thread\text{-}start\text{-}actions\text{-}ok \ E$

**apply**(*clarsimp simp add: lprefix-conv-lappend*)

**apply**(*rule thread-start-actions-okI*)

**apply**(*drule-tac a=a in thread-start-actions-okD*)

**apply**(*simp add: actions-def*)

**apply**(*auto simp add: action-obs-def lnth-lappend1 actions-def action-tid-def le-less-trans[where*  
*y=enat a for a]*)

**apply** (*metis add.right-neutral add-strict-mono not-gr-zero*)

**done**

**lemma** *wf-execI [intro ?]*:

$\llbracket is\text{-}write\text{-}seen \ P \ E \ ws;$   
 $thread\text{-}start\text{-}actions\text{-}ok \ E \rrbracket$

$\implies P \vdash (E, ws) \checkmark$

**by** *simp*

**lemma** *wf-exec-is-write-seenD*:

$P \vdash (E, ws) \checkmark \implies is\text{-}write\text{-}seen \ P \ E \ ws$

**by** *simp*

**lemma** *wf-exec-thread-start-actions-okD*:

$P \vdash (E, ws) \checkmark \implies thread\text{-}start\text{-}actions\text{-}ok \ E$

**by** *simp*

**lemma** *sequentially-consistentI*:

$(\bigwedge r. r \in read\text{-}actions \ E \implies P, E \vdash r \leadsto_{mrw} ws \ r)$   
 $\implies sequentially\text{-}consistent \ P \ (E, ws)$

**by** *simp*

**lemma** *sequentially-consistentE*:

**assumes** *sequentially-consistent P (E, ws) a ∈ read-actions E*

**obtains**  $P, E \vdash a \leadsto_{mrw} ws \ a$

**using** *assms* **by** *simp*

**declare** *sequentially-consistent.simps* [*simp del*]

### 8.4.10 Similar actions

Similar actions differ only in the values written/read.

**inductive** *sim-action* ::

$(\text{'addr}, \text{'thread-id}) \text{ obs-event action} \Rightarrow (\text{'addr}, \text{'thread-id}) \text{ obs-event action} \Rightarrow \text{bool}$   
 $(- \approx - [50, 50] \ 51)$

**where**

*InitialThreadAction*:  $\text{InitialThreadAction} \approx \text{InitialThreadAction}$   
*ThreadFinishAction*:  $\text{ThreadFinishAction} \approx \text{ThreadFinishAction}$   
*NewHeapElem*:  $\text{NormalAction} (\text{NewHeapElem } a \ hT) \approx \text{NormalAction} (\text{NewHeapElem } a \ hT)$   
*ReadMem*:  $\text{NormalAction} (\text{ReadMem } ad \ al \ v) \approx \text{NormalAction} (\text{ReadMem } ad \ al \ v')$   
*WriteMem*:  $\text{NormalAction} (\text{WriteMem } ad \ al \ v) \approx \text{NormalAction} (\text{WriteMem } ad \ al \ v')$   
*ThreadStart*:  $\text{NormalAction} (\text{ThreadStart } t) \approx \text{NormalAction} (\text{ThreadStart } t)$   
*ThreadJoin*:  $\text{NormalAction} (\text{ThreadJoin } t) \approx \text{NormalAction} (\text{ThreadJoin } t)$   
*SyncLock*:  $\text{NormalAction} (\text{SyncLock } a) \approx \text{NormalAction} (\text{SyncLock } a)$   
*SyncUnlock*:  $\text{NormalAction} (\text{SyncUnlock } a) \approx \text{NormalAction} (\text{SyncUnlock } a)$   
*ExternalCall*:  $\text{NormalAction} (\text{ExternalCall } a \ M \ vs \ v) \approx \text{NormalAction} (\text{ExternalCall } a \ M \ vs \ v)$   
*ObsInterrupt*:  $\text{NormalAction} (\text{ObsInterrupt } t) \approx \text{NormalAction} (\text{ObsInterrupt } t)$   
*ObsInterrupted*:  $\text{NormalAction} (\text{ObsInterrupted } t) \approx \text{NormalAction} (\text{ObsInterrupted } t)$

**definition** *sim-actions* ::  $(\text{'addr}, \text{'thread-id}) \text{ execution} \Rightarrow (\text{'addr}, \text{'thread-id}) \text{ execution} \Rightarrow \text{bool} \ (- [\approx] - [51, 50] \ 51)$

**where** *sim-actions* = *llist-all2*  $(\lambda(t, a) (t', a'). t = t' \wedge a \approx a')$

**lemma** *sim-action-refl* [*intro!*, *simp*]:

$obs \approx obs$

**apply**(*cases obs*)

**apply**(*rename-tac obs'*)

**apply**(*case-tac obs'*)

**apply**(*auto intro: sim-action.intros*)

**done**

**inductive-cases** *sim-action-cases* [*elim!*]:

*InitialThreadAction*  $\approx obs$   
*ThreadFinishAction*  $\approx obs$   
*NormalAction* (*NewHeapElem* *a hT*)  $\approx obs$   
*NormalAction* (*ReadMem* *ad al v*)  $\approx obs$   
*NormalAction* (*WriteMem* *ad al v*)  $\approx obs$   
*NormalAction* (*ThreadStart* *t*)  $\approx obs$   
*NormalAction* (*ThreadJoin* *t*)  $\approx obs$   
*NormalAction* (*SyncLock* *a*)  $\approx obs$   
*NormalAction* (*SyncUnlock* *a*)  $\approx obs$   
*NormalAction* (*ObsInterrupt* *t*)  $\approx obs$   
*NormalAction* (*ObsInterrupted* *t*)  $\approx obs$   
*NormalAction* (*ExternalCall* *a M vs v*)  $\approx obs$

$obs \approx \text{InitialThreadAction}$

$obs \approx \text{ThreadFinishAction}$

$obs \approx \text{NormalAction} (\text{NewHeapElem } a \ hT)$

$obs \approx \text{NormalAction} (\text{ReadMem } ad \ al \ v')$

$obs \approx \text{NormalAction} (\text{WriteMem } ad \ al \ v')$

$obs \approx \text{NormalAction} (\text{ThreadStart } t)$

$obs \approx \text{NormalAction} (\text{ThreadJoin } t)$

$obs \approx \text{NormalAction} (\text{SyncLock } a)$

$obs \approx NormalAction (SyncUnlock a)$   
 $obs \approx NormalAction (ObsInterrupt t)$   
 $obs \approx NormalAction (ObsInterrupted t)$   
 $obs \approx NormalAction (ExternalCall a M vs v)$

**inductive-simps** *sim-action-simps* [simp]:

$InitialThreadAction \approx obs$   
 $ThreadFinishAction \approx obs$   
 $NormalAction (NewHeapElem a hT) \approx obs$   
 $NormalAction (ReadMem ad al v) \approx obs$   
 $NormalAction (WriteMem ad al v) \approx obs$   
 $NormalAction (ThreadStart t) \approx obs$   
 $NormalAction (ThreadJoin t) \approx obs$   
 $NormalAction (SyncLock a) \approx obs$   
 $NormalAction (SyncUnlock a) \approx obs$   
 $NormalAction (ObsInterrupt t) \approx obs$   
 $NormalAction (ObsInterrupted t) \approx obs$   
 $NormalAction (ExternalCall a M vs v) \approx obs$

$obs \approx InitialThreadAction$   
 $obs \approx ThreadFinishAction$   
 $obs \approx NormalAction (NewHeapElem a hT)$   
 $obs \approx NormalAction (ReadMem ad al v')$   
 $obs \approx NormalAction (WriteMem ad al v')$   
 $obs \approx NormalAction (ThreadStart t)$   
 $obs \approx NormalAction (ThreadJoin t)$   
 $obs \approx NormalAction (SyncLock a)$   
 $obs \approx NormalAction (SyncUnlock a)$   
 $obs \approx NormalAction (ObsInterrupt t)$   
 $obs \approx NormalAction (ObsInterrupted t)$   
 $obs \approx NormalAction (ExternalCall a M vs v)$

**lemma** *sim-action-trans* [trans]:

$\llbracket obs \approx obs'; obs' \approx obs'' \rrbracket \implies obs \approx obs''$

**by**(*erule sim-action.cases*) *auto*

**lemma** *sim-action-sym* [sym]:

**assumes**  $obs \approx obs'$

**shows**  $obs' \approx obs$

**using** *assms* **by** *cases simp-all*

**lemma** *sim-actions-sym* [sym]:

$E [\approx] E' \implies E' [\approx] E$

**unfolding** *sim-actions-def*

**by**(*auto simp add: llist-all2-conv-all-lnth split-beta intro: sim-action-sym*)

**lemma** *sim-actions-action-obsD*:

$E [\approx] E' \implies action-obs E a \approx action-obs E' a$

**unfolding** *sim-actions-def action-obs-def*

**by**(*cases enat a < llength E*)(*auto dest: llist-all2-lnthD llist-all2-llengthD simp add: split-beta lnth-beyond split: enat.split*)

**lemma** *sim-actions-action-tidD*:

$E [\approx] E' \implies action-tid E a = action-tid E' a$



**unfolding** *sim-actions-def action-tid-def*  
**by**(cases enat a < llength E)(auto dest: llist-all2-lnthD llist-all2-llengthD simp add: lnth-beyond split: enat.split)

**lemma** *action-loc-aux-sim-action:*  
 $a \approx a' \implies \text{action-loc-aux } P \ a = \text{action-loc-aux } P \ a'$   
**by**(auto elim!: action-loc-aux-cases intro: action-loc-aux-intros)

**lemma** *eq-into-sim-actions:*  
**assumes**  $E = E'$   
**shows**  $E [\approx] E'$   
**unfolding** *sim-actions-def assms*  
**by**(rule llist-all2-reflI)(auto)

#### 8.4.11 Well-formedness conditions for execution sets

**locale** *executions-base* =  
**fixes**  $\mathcal{E} :: ('addr, 'thread-id) \text{ execution set}$   
**and**  $P :: 'm \text{ prog}$   
  
**locale** *drf* =  
*executions-base*  $\mathcal{E} \ P$   
**for**  $\mathcal{E} :: ('addr, 'thread-id) \text{ execution set}$   
**and**  $P :: 'm \text{ prog} +$   
**assumes**  *$\mathcal{E}$ -new-actions-for-fun:*  
 $\llbracket E \in \mathcal{E}; a \in \text{new-actions-for } P \ E \ \text{adal}; a' \in \text{new-actions-for } P \ E \ \text{adal} \rrbracket \implies a = a'$   
**and**  *$\mathcal{E}$ -sequential-completion:*  
 $\llbracket E \in \mathcal{E}; P \vdash (E, ws) \checkmark; \bigwedge a. \llbracket a < r; a \in \text{read-actions } E \rrbracket \implies P, E \vdash a \rightsquigarrow_{mrw} ws \ a \rrbracket$   
 $\implies \exists E' \in \mathcal{E}. \exists ws'. P \vdash (E', ws') \checkmark \wedge \text{ltake } (enat \ r) \ E = \text{ltake } (enat \ r) \ E' \wedge \text{sequentially-consistent}$   
 $P \ (E', ws') \wedge$   
 $\text{action-tid } E \ r = \text{action-tid } E' \ r \wedge \text{action-obs } E \ r \approx \text{action-obs } E' \ r \wedge$   
 $(r \in \text{actions } E \longrightarrow r \in \text{actions } E')$

**locale** *executions-aux* =  
*executions-base*  $\mathcal{E} \ P$   
**for**  $\mathcal{E} :: ('addr, 'thread-id) \text{ execution set}$   
**and**  $P :: 'm \text{ prog} +$   
**assumes** *init-before-read:*  
 $\llbracket E \in \mathcal{E}; P \vdash (E, ws) \checkmark; r \in \text{read-actions } E; \text{adal} \in \text{action-loc } P \ E \ r;$   
 $\bigwedge a. \llbracket a < r; a \in \text{read-actions } E \rrbracket \implies P, E \vdash a \rightsquigarrow_{mrw} ws \ a \rrbracket$   
 $\implies \exists i < r. i \in \text{new-actions-for } P \ E \ \text{adal}$   
**and**  *$\mathcal{E}$ -new-actions-for-fun:*  
 $\llbracket E \in \mathcal{E}; a \in \text{new-actions-for } P \ E \ \text{adal}; a' \in \text{new-actions-for } P \ E \ \text{adal} \rrbracket \implies a = a'$

**locale** *sc-legal* =  
*executions-aux*  $\mathcal{E} \ P$   
**for**  $\mathcal{E} :: ('addr, 'thread-id) \text{ execution set}$   
**and**  $P :: 'm \text{ prog} +$   
**assumes**  *$\mathcal{E}$ -hb-completion:*  
 $\llbracket E \in \mathcal{E}; P \vdash (E, ws) \checkmark; \bigwedge a. \llbracket a < r; a \in \text{read-actions } E \rrbracket \implies P, E \vdash a \rightsquigarrow_{mrw} ws \ a \rrbracket$   
 $\implies \exists E' \in \mathcal{E}. \exists ws'. P \vdash (E', ws') \checkmark \wedge \text{ltake } (enat \ r) \ E = \text{ltake } (enat \ r) \ E' \wedge$   
 $(\forall a \in \text{read-actions } E'. \text{if } a < r \text{ then } ws' \ a = ws \ a \text{ else } P, E' \vdash ws' \ a \leq_{hb} a) \wedge$   
 $\text{action-tid } E' \ r = \text{action-tid } E \ r \wedge$   
 $(\text{if } r \in \text{read-actions } E \text{ then sim-action else } (=)) (\text{action-obs } E' \ r) (\text{action-obs } E \ r) \wedge$

$$(r \in \text{actions } E \longrightarrow r \in \text{actions } E')$$

**locale** *jmm-consistent* =  
 drf?: drf  $\mathcal{E}$   $P$  +  
 sc-legal  $\mathcal{E}$   $P$   
**for**  $\mathcal{E} :: ('addr, 'thread-id)$  *execution set*  
**and**  $P :: 'm$  *prog*

#### 8.4.12 Legal executions

**record**  $('addr, 'thread-id)$  *pre-justifying-execution* =  
 committed :: JMM-action set  
 justifying-exec ::  $('addr, 'thread-id)$  *execution*  
 justifying-ws :: *write-seen*

**record**  $('addr, 'thread-id)$  *justifying-execution* =  
 $('addr, 'thread-id)$  *pre-justifying-execution* +  
 action-translation :: JMM-action  $\Rightarrow$  JMM-action

**type-synonym**  $('addr, 'thread-id)$  *justification* = nat  $\Rightarrow ('addr, 'thread-id)$  *justifying-execution*

**definition** *wf-action-translation-on* ::  
 $('addr, 'thread-id)$  *execution*  $\Rightarrow ('addr, 'thread-id)$  *execution*  $\Rightarrow$  JMM-action set  $\Rightarrow$  (JMM-action  $\Rightarrow$  JMM-action)  $\Rightarrow$  bool

**where**

*wf-action-translation-on*  $E E' A f \longleftrightarrow$   
*inj-on*  $f (\text{actions } E) \wedge$   
 $(\forall a \in A. \text{action-tid } E a = \text{action-tid } E' (f a) \wedge \text{action-obs } E a \approx \text{action-obs } E' (f a))$

**abbreviation** *wf-action-translation* ::  $('addr, 'thread-id)$  *execution*  $\Rightarrow ('addr, 'thread-id)$  *justifying-execution*  $\Rightarrow$  bool

**where**

*wf-action-translation*  $E J \equiv$   
*wf-action-translation-on* (*justifying-exec*  $J$ )  $E$  (*committed*  $J$ ) (*action-translation*  $J$ )

**context**

**fixes**  $P :: 'm$  *prog*  
**and**  $E :: ('addr, 'thread-id)$  *execution*  
**and**  $ws ::$  *write-seen*  
**and**  $J :: ('addr, 'thread-id)$  *justification*

**begin**

This context defines the causality constraints for the JMM. The weak versions are for the fixed JMM as presented by Sevcik and Aspinall at ECOOP 2008.

Committed actions are an ascending chain with all actions of  $E$  as a limit

**definition** *is-commit-sequence* :: bool **where**

*is-commit-sequence*  $\longleftrightarrow$   
*committed*  $(J 0) = \{\}$   $\wedge$   
 $(\forall n. \text{action-translation } (J n) \text{ 'committed } (J n) \subseteq \text{action-translation } (J (\text{Suc } n)) \text{ 'committed } (J (\text{Suc } n))) \wedge$   
 $\text{actions } E = (\bigcup n. \text{action-translation } (J n) \text{ 'committed } (J n))$

**definition** *justification-well-formed* :: bool **where**

*justification-well-formed*  $\longleftrightarrow (\forall n. P \vdash (\text{justifying-exec } (J n), \text{justifying-ws } (J n)) \checkmark)$

**definition** *committed-subset-actions* :: *bool* **where** — JMM constraint 1  

$$\text{committed-subset-actions} \longleftrightarrow (\forall n. \text{committed} (J\ n) \subseteq \text{actions} (\text{justifying-exec} (J\ n)))$$

**definition** *happens-before-committed* :: *bool* **where** — JMM constraint 2  

$$\begin{aligned} \text{happens-before-committed} &\longleftrightarrow \\ (\forall n. \text{happens-before } P (\text{justifying-exec} (J\ n)) \mid \text{' committed} (J\ n) = \\ &\text{inv-image } P (\text{happens-before } P\ E) (\text{action-translation} (J\ n)) \mid \text{' committed} (J\ n)) \end{aligned}$$

**definition** *happens-before-committed-weak* :: *bool* **where** — relaxed JMM constraint  

$$\begin{aligned} \text{happens-before-committed-weak} &\longleftrightarrow \\ (\forall n. \forall r \in \text{read-actions} (\text{justifying-exec} (J\ n)) \cap \text{committed} (J\ n). \\ &\text{let } r' = \text{action-translation} (J\ n)\ r; \\ &\quad w' = \text{ws } r'; \\ &\quad w = \text{inv-into} (\text{actions} (\text{justifying-exec} (J\ n))) (\text{action-translation} (J\ n))\ w' \text{ in} \\ &\quad (P, E \vdash w' \leq_{hb} r' \longleftrightarrow P, \text{justifying-exec} (J\ n) \vdash w \leq_{hb} r) \wedge \\ &\quad \neg P, \text{justifying-exec} (J\ n) \vdash r \leq_{hb} w) \end{aligned}$$

**definition** *sync-order-committed* :: *bool* **where** — JMM constraint 3  

$$\begin{aligned} \text{sync-order-committed} &\longleftrightarrow \\ (\forall n. \text{sync-order } P (\text{justifying-exec} (J\ n)) \mid \text{' committed} (J\ n) = \\ &\text{inv-image } P (\text{sync-order } P\ E) (\text{action-translation} (J\ n)) \mid \text{' committed} (J\ n)) \end{aligned}$$

**definition** *value-written-committed* :: *bool* **where** — JMM constraint 4  

$$\begin{aligned} \text{value-written-committed} &\longleftrightarrow \\ (\forall n. \forall w \in \text{write-actions} (\text{justifying-exec} (J\ n)) \cap \text{committed} (J\ n). \\ &\text{let } w' = \text{action-translation} (J\ n)\ w \\ &\text{in } (\forall \text{adal} \in \text{action-loc } P\ E\ w'. \text{value-written } P (\text{justifying-exec} (J\ n))\ w\ \text{adal} = \text{value-written } P \\ &\quad E\ w'\ \text{adal})) \end{aligned}$$

**definition** *write-seen-committed* :: *bool* **where** — JMM constraint 5  

$$\begin{aligned} \text{write-seen-committed} &\longleftrightarrow \\ (\forall n. \forall r' \in \text{read-actions} (\text{justifying-exec} (J\ n)) \cap \text{committed} (J\ n). \\ &\text{let } r = \text{action-translation} (J\ n)\ r'; \\ &\quad r'' = \text{inv-into} (\text{actions} (\text{justifying-exec} (J\ (\text{Suc } n)))) (\text{action-translation} (J\ (\text{Suc } n)))\ r \\ &\text{in } \text{action-translation} (J\ (\text{Suc } n)) (\text{justifying-ws} (J\ (\text{Suc } n))\ r'') = \text{ws } r) \end{aligned}$$

uncommitted reads see writes that happen before them – JMM constraint 6

**definition** *uncommitted-reads-see-hb* :: *bool* **where**  

$$\begin{aligned} \text{uncommitted-reads-see-hb} &\longleftrightarrow \\ (\forall n. \forall r' \in \text{read-actions} (\text{justifying-exec} (J\ (\text{Suc } n))). \\ &\text{action-translation} (J\ (\text{Suc } n))\ r' \in \text{action-translation} (J\ n)\ \text{' committed} (J\ n) \vee \\ &\quad P, \text{justifying-exec} (J\ (\text{Suc } n)) \vdash \text{justifying-ws} (J\ (\text{Suc } n))\ r' \leq_{hb} r') \end{aligned}$$

newly committed reads see already committed writes and write-seen relationship must not change any more – JMM constraint 7

**definition** *committed-reads-see-committed-writes* :: *bool* **where**  

$$\begin{aligned} \text{committed-reads-see-committed-writes} &\longleftrightarrow \\ (\forall n. \forall r' \in \text{read-actions} (\text{justifying-exec} (J\ (\text{Suc } n))) \cap \text{committed} (J\ (\text{Suc } n)). \\ &\text{let } r = \text{action-translation} (J\ (\text{Suc } n))\ r'; \\ &\quad \text{committed-}n = \text{action-translation} (J\ n)\ \text{' committed} (J\ n) \\ &\text{in } r \in \text{committed-}n \vee \\ &\quad (\text{action-translation} (J\ (\text{Suc } n)) (\text{justifying-ws} (J\ (\text{Suc } n))\ r') \in \text{committed-}n \wedge \text{ws } r \in \\ &\quad \text{committed-}n)) \end{aligned}$$

**definition** *committed-reads-see-committed-writes-weak* :: bool **where**

*committed-reads-see-committed-writes-weak*  $\longleftrightarrow$   
 $(\forall n. \forall r' \in \text{read-actions } (\text{justifying-exec } (J (\text{Suc } n)))) \cap \text{committed } (J (\text{Suc } n)).$   
 let  $r = \text{action-translation } (J (\text{Suc } n)) \ r'$ ;  
 $\text{committed-}n = \text{action-translation } (J \ n) \text{ ' committed } (J \ n)$   
 in  $r \in \text{committed-}n \vee \text{ws } r \in \text{committed-}n$

external actions must be committed as soon as hb-subsequent actions are committed – JMM constraint 9

**definition** *external-actions-committed* :: bool **where**

*external-actions-committed*  $\longleftrightarrow$   
 $(\forall n. \forall a \in \text{external-actions } (\text{justifying-exec } (J \ n)). \forall a' \in \text{committed } (J \ n).$   
 $P.\text{justifying-exec } (J \ n) \vdash a \leq_{\text{hb}} a' \longrightarrow a \in \text{committed } (J \ n))$

well-formedness conditions for action translations

**definition** *wf-action-translations* :: bool **where**

*wf-action-translations*  $\longleftrightarrow$   
 $(\forall n. \text{wf-action-translation-on } (\text{justifying-exec } (J \ n)) \ E \ (\text{committed } (J \ n)) \ (\text{action-translation } (J \ n)))$

**end**

Rule 8 of the justification for the JMM is incorrect because there might be no transitive reduction of the happens-before relation for an infinite execution, if infinitely many initialisation actions have to be ordered before the start action of every thread. Hence, *is-justified-by* omits this constraint.

**primrec** *is-justified-by* ::

$'m \text{ prog} \Rightarrow ('addr, 'thread-id) \text{ execution} \times \text{write-seen} \Rightarrow ('addr, 'thread-id) \text{ justification} \Rightarrow \text{bool}$   
 $(- \vdash - \text{justified'-by } - [51, 50, 50] \ 50)$

**where**

$P \vdash (E, \text{ws}) \text{ justified-by } J \longleftrightarrow$   
 $\text{is-commit-sequence } E \ J \wedge$   
 $\text{justification-well-formed } P \ J \wedge$   
 $\text{committed-subset-actions } J \wedge$   
 $\text{happens-before-committed } P \ E \ J \wedge$   
 $\text{sync-order-committed } P \ E \ J \wedge$   
 $\text{value-written-committed } P \ E \ J \wedge$   
 $\text{write-seen-committed } \text{ws } J \wedge$   
 $\text{uncommitted-reads-see-hb } P \ J \wedge$   
 $\text{committed-reads-see-committed-writes } \text{ws } J \wedge$   
 $\text{external-actions-committed } P \ J \wedge$   
 $\text{wf-action-translations } E \ J$

Sevcik requires in the fixed JMM that external actions may only be committed when everything that happens before has already been committed. On the level of legality, this constraint is vacuous because it is always possible to delay committing external actions, so we omit it here.

**primrec** *is-weakly-justified-by* ::

$'m \text{ prog} \Rightarrow ('addr, 'thread-id) \text{ execution} \times \text{write-seen} \Rightarrow ('addr, 'thread-id) \text{ justification} \Rightarrow \text{bool}$   
 $(- \vdash - \text{weakly'-justified'-by } - [51, 50, 50] \ 50)$

**where**

$P \vdash (E, \text{ws}) \text{ weakly-justified-by } J \longleftrightarrow$   
 $\text{is-commit-sequence } E \ J \wedge$   
 $\text{justification-well-formed } P \ J \wedge$

*committed-subset-actions*  $J \wedge$   
*happens-before-committed-weak*  $P \ E \ ws \ J \wedge$   
 — no *sync-order* constraint  
*value-written-committed*  $P \ E \ J \wedge$   
*write-seen-committed*  $ws \ J \wedge$   
*uncommitted-reads-see-hb*  $P \ J \wedge$   
*committed-reads-see-committed-writes-weak*  $ws \ J \wedge$   
*wf-action-translations*  $E \ J$

Notion of conflict is strengthened to explicitly exclude volatile locations. Otherwise, the following program is not correctly synchronised:

```

volatile x = 0;
-----
r = x; | x = 1;

```

because in the SC execution [Init x 0, (t1, Read x 0), (t2, Write x 1)], the read and write are unrelated in hb, because synchronises-with is asymmetric for volatiles.

The JLS considers conflicting volatiles for data races, but this is only a remark on the DRF guarantee. See JMM mailing list posts #2477 to 2488.

**definition** *non-volatile-conflict* ::

$'m \text{ prog} \Rightarrow ('addr, 'thread-id) \text{ execution} \Rightarrow JMM\text{-action} \Rightarrow JMM\text{-action} \Rightarrow bool$   
 $(-, \vdash / (-) \dagger (-) [51, 50, 50, 50] 51)$

**where**

$P, E \vdash a \dagger a' \longleftrightarrow$   
 $(a \in \text{read-actions } E \wedge a' \in \text{write-actions } E \vee$   
 $a \in \text{write-actions } E \wedge a' \in \text{read-actions } E \vee$   
 $a \in \text{write-actions } E \wedge a' \in \text{write-actions } E) \wedge$   
 $(\exists ad \ al. (ad, al) \in \text{action-loc } P \ E \ a \cap \text{action-loc } P \ E \ a' \wedge \neg \text{is-volatile } P \ al)$

**definition** *correctly-synchronized* ::  $'m \text{ prog} \Rightarrow ('addr, 'thread-id) \text{ execution set} \Rightarrow bool$

**where**

$\text{correctly-synchronized } P \ \mathcal{E} \longleftrightarrow$   
 $(\forall E \in \mathcal{E}. \forall ws. P \vdash (E, ws) \checkmark \longrightarrow \text{sequentially-consistent } P \ (E, ws)$   
 $\longrightarrow (\forall a \in \text{actions } E. \forall a' \in \text{actions } E. P, E \vdash a \dagger a'$   
 $\longrightarrow P, E \vdash a \leq_{hb} a' \vee P, E \vdash a' \leq_{hb} a))$

**primrec** *gen-legal-execution* ::

$'m \text{ prog} \Rightarrow ('addr, 'thread-id) \text{ execution} \times \text{write-seen} \Rightarrow ('addr, 'thread-id) \text{ justification} \Rightarrow bool$   
 $\Rightarrow 'm \text{ prog} \Rightarrow ('addr, 'thread-id) \text{ execution set} \Rightarrow ('addr, 'thread-id) \text{ execution} \times \text{write-seen} \Rightarrow bool$

**where**

$\text{gen-legal-execution is-justification } P \ \mathcal{E} \ (E, ws) \longleftrightarrow$   
 $E \in \mathcal{E} \wedge P \vdash (E, ws) \checkmark \wedge$   
 $(\exists J. \text{is-justification } P \ (E, ws) \ J \wedge \text{range } (justifying\text{-exec} \circ J) \subseteq \mathcal{E})$

**abbreviation** *legal-execution* ::

$'m \text{ prog} \Rightarrow ('addr, 'thread-id) \text{ execution set} \Rightarrow ('addr, 'thread-id) \text{ execution} \times \text{write-seen} \Rightarrow bool$

**where**

$\text{legal-execution} \equiv \text{gen-legal-execution is-justified-by}$

**abbreviation** *weakly-legal-execution* ::

$'m \text{ prog} \Rightarrow ('addr, 'thread-id) \text{ execution set} \Rightarrow ('addr, 'thread-id) \text{ execution} \times \text{write-seen} \Rightarrow bool$

**where**

*weakly-legal-execution*  $\equiv$  *gen-legal-execution is-weakly-justified-by*

**declare** *gen-legal-execution.simps* [simp del]

**lemma** *sym-non-volatile-conflict*:

*symP* (*non-volatile-conflict* *P E*)

**unfolding** *non-volatile-conflict-def*

**by**(*rule symPI*) *blast*

**lemma** *legal-executionI*:

$\llbracket E \in \mathcal{E}; P \vdash (E, ws) \checkmark; \text{is-justification } P (E, ws) J; \text{range } (justifying-exec \circ J) \subseteq \mathcal{E} \rrbracket$

$\implies \text{gen-legal-execution is-justification } P \mathcal{E} (E, ws)$

**unfolding** *gen-legal-execution.simps* **by** *blast*

**lemma** *legal-executionE*:

**assumes** *gen-legal-execution is-justification* *P*  $\mathcal{E}$  (*E*, *ws*)

**obtains** *J* **where** *E*  $\in \mathcal{E}$  *P*  $\vdash (E, ws) \checkmark$  *is-justification* *P* (*E*, *ws*) *J* *range* (*justifying-exec*  $\circ$  *J*)  $\subseteq \mathcal{E}$

**using** *assms* **unfolding** *gen-legal-execution.simps* **by** *blast*

**lemma** *legal-ED*: *gen-legal-execution is-justification* *P*  $\mathcal{E}$  (*E*, *ws*)  $\implies E \in \mathcal{E}$

**by**(*erule legal-executionE*)

**lemma** *legal-wf-execD*:

*gen-legal-execution is-justification* *P*  $\mathcal{E}$  *Ews*  $\implies P \vdash Ews \checkmark$

**by**(*cases Ews*)(*auto elim: legal-executionE*)

**lemma** *correctly-synchronizedD*:

$\llbracket \text{correctly-synchronized } P \mathcal{E}; E \in \mathcal{E}; P \vdash (E, ws) \checkmark; \text{sequentially-consistent } P (E, ws) \rrbracket$

$\implies \forall a a'. a \in \text{actions } E \longrightarrow a' \in \text{actions } E \longrightarrow P, E \vdash a \nmid a' \longrightarrow P, E \vdash a \leq_{hb} a' \vee P, E \vdash a' \leq_{hb} a$

**unfolding** *correctly-synchronized-def* **by** *blast*

**lemma** *wf-action-translation-on-actionD*:

$\llbracket \text{wf-action-translation-on } E E' A f; a \in A \rrbracket$

$\implies \text{action-tid } E a = \text{action-tid } E' (f a) \wedge \text{action-obs } E a \approx \text{action-obs } E' (f a)$

**unfolding** *wf-action-translation-on-def* **by** *blast*

**lemma** *wf-action-translation-on-inj-onD*:

*wf-action-translation-on* *E E' A f*  $\implies \text{inj-on } f (\text{actions } E)$

**unfolding** *wf-action-translation-on-def* **by** *simp*

**lemma** *wf-action-translation-on-action-locD*:

$\llbracket \text{wf-action-translation-on } E E' A f; a \in A \rrbracket$

$\implies \text{action-loc } P E a = \text{action-loc } P E' (f a)$

**apply**(*drule* (1) *wf-action-translation-on-actionD*)

**apply**(*cases* (*P*, *action-obs E a*) *rule: action-loc-aux.cases*)

**apply** *auto*

**done**

**lemma** *weakly-justified-write-seen-hb-read-committed*:

**assumes** *J*: *P*  $\vdash (E, ws)$  *weakly-justified-by J*

**and** *r*: *r*  $\in \text{read-actions } (justifying-exec (J n))$  *r*  $\in \text{committed } (J n)$

**shows** *ws* (*action-translation* (*J n*) *r*)  $\in \text{action-translation } (J n) \text{ ' committed } (J n)$

**using** *r*

```

proof(induct n arbitrary: r)
  case 0
  from J have [simp]: committed (J 0) = {}
    by(simp add: is-commit-sequence-def)
  with 0 show ?case by simp
next
  case (Suc n)
  let ?E =  $\lambda n. \text{justifying-exec } (J \ n)$ 
    and ?ws =  $\lambda n. \text{justifying-ws } (J \ n)$ 
    and ?C =  $\lambda n. \text{committed } (J \ n)$ 
    and ? $\varphi$  =  $\lambda n. \text{action-translation } (J \ n)$ 

  note  $r = \langle r \in \text{read-actions } (?E \ (Suc \ n)) \rangle$ 
  hence  $r \in \text{actions } (?E \ (Suc \ n))$  by simp

  from J have wfan: wf-action-translation-on (?E n) E (?C n) (? $\varphi$  n)
    and wfaSn: wf-action-translation-on (?E (Suc n)) E (?C (Suc n)) (? $\varphi$  (Suc n))
    by(simp-all add: wf-action-translations-def)

  from wfaSn have injSn: inj-on (? $\varphi$  (Suc n)) (actions (?E (Suc n)))
    by(rule wf-action-translation-on-inj-onD)
  from J have C-sub-A: ?C (Suc n)  $\subseteq$  actions (?E (Suc n))
    by(simp add: committed-subset-actions-def)
  from J have CnCSn: ? $\varphi$  n ‘ ?C n  $\subseteq$  ? $\varphi$  (Suc n) ‘ ?C (Suc n)
    by(simp add: is-commit-sequence-def)

  from J have wsSn: is-write-seen P (?E (Suc n)) (?ws (Suc n))
    by(simp add: justification-well-formed-def)
  from r obtain ad al v where action-obs (?E (Suc n)) r = NormalAction (ReadMem ad al v) by
cases
  from is-write-seenD[OF wsSn r this]
  have wsSn: ?ws (Suc n)  $r \in \text{actions } (?E \ (Suc \ n))$  by simp

  show ?case
  proof(cases ? $\varphi$  (Suc n)  $r \in ?\varphi \ n \ ‘ \ ?C \ n$ )
    case True
    then obtain r' where r':  $r' \in ?C \ n$ 
      and r-r': ? $\varphi$  (Suc n)  $r = ?\varphi \ n \ r'$  by(auto)
    from r' wfan have action-tid (?E n)  $r' = \text{action-tid } E \ (? \varphi \ n \ r')$ 
      and action-obs (?E n)  $r' \approx \text{action-obs } E \ (? \varphi \ n \ r')$ 
      by(blast dest: wf-action-translation-on-actionD)+
    moreover from r' CnCSn have ? $\varphi$  (Suc n)  $r \in ?\varphi \ (Suc \ n) \ ‘ \ ?C \ (Suc \ n)$ 
      unfolding r-r' by auto
    hence  $r \in ?C \ (Suc \ n)$ 
      unfolding inj-on-image-mem-iff[OF injSn  $\langle r \in \text{actions } (?E \ (Suc \ n)) \rangle \ C\text{-sub-A}$ ] .
    with wfaSn have action-tid (?E (Suc n))  $r = \text{action-tid } E \ (? \varphi \ (Suc \ n) \ r)$ 
      and action-obs (?E (Suc n))  $r \approx \text{action-obs } E \ (? \varphi \ (Suc \ n) \ r)$ 
      by(blast dest: wf-action-translation-on-actionD)+
    ultimately have tid: action-tid (?E n)  $r' = \text{action-tid } (?E \ (Suc \ n)) \ r$ 
      and obs: action-obs (?E n)  $r' \approx \text{action-obs } (?E \ (Suc \ n)) \ r$ 
      unfolding r-r' by(auto intro: sim-action-trans sim-action-sym)

  from J have ?C n  $\subseteq$  actions (?E n) by(simp add: committed-subset-actions-def)
  with r' have  $r' \in \text{actions } (?E \ n)$  by blast

```

**with**  $r \text{ obs}$  **have**  $r' \in \text{read-actions } (?E \ n)$   
**by**  $\text{cases}(\text{auto intro: read-actions.intros})$   
**hence**  $ws: ws \ (?\varphi \ n \ r') \in ?\varphi \ n \text{ ' } ?C \ n$  **using**  $r'$  **by**  $(\text{rule Suc})$

**have**  $r\text{-conv-inv: } r = \text{inv-into } (\text{actions } (?E \ (\text{Suc } n))) \ (?\varphi \ (\text{Suc } n)) \ (?\varphi \ n \ r')$   
**using**  $\langle r \in \text{actions } (?E \ (\text{Suc } n)) \rangle$  **unfolding**  $r\text{-r'}$   $[\text{symmetric}]$   
**by**  $(\text{simp add: inv-into-f-f} [\text{OF injSn}])$   
**with**  $\langle r' \in ?C \ n \rangle \ r \ J \ \langle r' \in \text{read-actions } (?E \ n) \rangle$   
**have**  $ws\text{-eq: } ?\varphi \ (\text{Suc } n) \ (?\text{ws } (\text{Suc } n) \ r) = ws \ (?\varphi \ n \ r')$   
**by**  $(\text{simp add: Let-def write-seen-committed-def})$   
**with**  $ws \ CnCSn$  **have**  $?\varphi \ (\text{Suc } n) \ (?\text{ws } (\text{Suc } n) \ r) \in ?\varphi \ (\text{Suc } n) \text{ ' } ?C \ (\text{Suc } n)$  **by**  $\text{auto}$   
**hence**  $?\text{ws } (\text{Suc } n) \ r \in ?C \ (\text{Suc } n)$   
**by**  $(\text{subst } (\text{asm}) \text{ inj-on-image-mem-iff} [\text{OF injSn wsSn C-sub-A}])$   
**moreover from**  $ws \ CnCSn$  **have**  $ws \ (?\varphi \ (\text{Suc } n) \ r) \in ?\varphi \ (\text{Suc } n) \text{ ' } ?C \ (\text{Suc } n)$   
**unfolding**  $r\text{-r'}$  **by**  $\text{auto}$   
**ultimately show**  $?thesis$  **by**  $\text{simp}$

**next**  
**case**  $\text{False}$   
**with**  $r \ \langle r \in ?C \ (\text{Suc } n) \rangle \ J$   
**have**  $ws \ (?\varphi \ (\text{Suc } n) \ r) \in ?\varphi \ n \text{ ' } ?C \ n$   
**unfolding**  $\text{is-weakly-justified-by.simps Let-def committed-reads-see-committed-writes-weak-def}$   
**by**  $\text{blast}$   
**hence**  $ws \ (?\varphi \ (\text{Suc } n) \ r) \in ?\varphi \ (\text{Suc } n) \text{ ' } ?C \ (\text{Suc } n)$   
**using**  $CnCSn$  **by**  $\text{blast+}$   
**thus**  $?thesis$  **by**  $(\text{simp add: inj-on-image-mem-iff} [\text{OF injSn wsSn C-sub-A}])$

**qed**  
**qed**

**lemma**  $\text{justified-write-seen-hb-read-committed:}$   
**assumes**  $J: P \vdash (E, \text{ws}) \text{ justified-by } J$   
**and**  $r: r \in \text{read-actions } (\text{justifying-exec } (J \ n)) \ r \in \text{committed } (J \ n)$   
**shows**  $\text{justifying-ws } (J \ n) \ r \in \text{committed } (J \ n)$  **(is**  $?thesis1$ **)**  
**and**  $ws \ (\text{action-translation } (J \ n) \ r) \in \text{action-translation } (J \ n) \text{ ' } \text{committed } (J \ n)$  **(is**  $?thesis2$ **)**

**proof** –  
**have**  $?thesis1 \wedge ?thesis2$  **using**  $r$   
**proof**  $(\text{induct } n \text{ arbitrary: } r)$   
**case**  $0$   
**from**  $J$  **have**  $[\text{simp}]: \text{committed } (J \ 0) = \{\}$   
**by**  $(\text{simp add: is-commit-sequence-def})$   
**with**  $0$  **show**  $?case$  **by**  $\text{simp}$

**next**  
**case**  $(\text{Suc } n)$   
**let**  $?E = \lambda n. \text{justifying-exec } (J \ n)$   
**and**  $?\text{ws} = \lambda n. \text{justifying-ws } (J \ n)$   
**and**  $?C = \lambda n. \text{committed } (J \ n)$   
**and**  $?\varphi = \lambda n. \text{action-translation } (J \ n)$

**note**  $r = \langle r \in \text{read-actions } (?E \ (\text{Suc } n)) \rangle$   
**hence**  $r \in \text{actions } (?E \ (\text{Suc } n))$  **by**  $\text{simp}$

**from**  $J$  **have**  $wfan: \text{wf-action-translation-on } (?E \ n) \ E \ (?C \ n) \ (?\varphi \ n)$   
**and**  $wfaSn: \text{wf-action-translation-on } (?E \ (\text{Suc } n)) \ E \ (?C \ (\text{Suc } n)) \ (?\varphi \ (\text{Suc } n))$   
**by**  $(\text{simp-all add: wf-action-translations-def})$



**from**  $wfaSn$  **have**  $injSn$ :  $inj\text{-}on\ (\varphi\ (Suc\ n))\ (actions\ (\varphi E\ (Suc\ n)))$   
**by**(rule  $wf\text{-}action\text{-}translation\text{-}on\text{-}inj\text{-}onD$ )  
**from**  $J$  **have**  $C\text{-}sub\text{-}A$ :  $\varphi C\ (Suc\ n) \subseteq actions\ (\varphi E\ (Suc\ n))$   
**by**(simp add:  $committed\text{-}subset\text{-}actions\text{-}def$ )  
**from**  $J$  **have**  $CnCSn$ :  $\varphi n \text{ ' } \varphi C\ n \subseteq \varphi\ (Suc\ n) \text{ ' } \varphi C\ (Suc\ n)$   
**by**(simp add:  $is\text{-}commit\text{-}sequence\text{-}def$ )

**from**  $J$  **have**  $wsSn$ :  $is\text{-}write\text{-}seen\ P\ (\varphi E\ (Suc\ n))\ (\varphi ws\ (Suc\ n))$   
**by**(simp add:  $justification\text{-}well\text{-}formed\text{-}def$ )  
**from**  $r$  **obtain**  $ad\ al\ v$  **where**  $action\text{-}obs\ (\varphi E\ (Suc\ n))\ r = NormalAction\ (ReadMem\ ad\ al\ v)$  **by**

*cases*

**from**  $is\text{-}write\text{-}seenD[OF\ wsSn\ r\ this]$   
**have**  $wsSn$ :  $\varphi ws\ (Suc\ n)\ r \in actions\ (\varphi E\ (Suc\ n))$  **by** *simp*

**show**  $\varphi case$

**proof**(cases  $\varphi\ (Suc\ n)\ r \in \varphi n \text{ ' } \varphi C\ n$ )

**case** *True*

**then obtain**  $r'$  **where**  $r'$ :  $r' \in \varphi C\ n$

**and**  $r\text{-}r'$ :  $\varphi\ (Suc\ n)\ r = \varphi n\ r'$  **by**(*auto*)

**from**  $r'$   $wfan$  **have**  $action\text{-}tid\ (\varphi E\ n)\ r' = action\text{-}tid\ E\ (\varphi n\ r')$

**and**  $action\text{-}obs\ (\varphi E\ n)\ r' \approx action\text{-}obs\ E\ (\varphi n\ r')$

**by**(blast dest:  $wf\text{-}action\text{-}translation\text{-}on\text{-}actionD$ )**+**

**moreover from**  $r'$   $CnCSn$  **have**  $\varphi\ (Suc\ n)\ r \in \varphi\ (Suc\ n) \text{ ' } \varphi C\ (Suc\ n)$

**unfolding**  $r\text{-}r'$  **by** *auto*

**hence**  $r \in \varphi C\ (Suc\ n)$

**unfolding**  $inj\text{-}on\text{-}image\text{-}mem\text{-}iff[OF\ injSn\ \langle r \in actions\ (\varphi E\ (Suc\ n)) \rangle\ C\text{-}sub\text{-}A]$  .

**with**  $wfaSn$  **have**  $action\text{-}tid\ (\varphi E\ (Suc\ n))\ r = action\text{-}tid\ E\ (\varphi\ (Suc\ n)\ r)$

**and**  $action\text{-}obs\ (\varphi E\ (Suc\ n))\ r \approx action\text{-}obs\ E\ (\varphi\ (Suc\ n)\ r)$

**by**(blast dest:  $wf\text{-}action\text{-}translation\text{-}on\text{-}actionD$ )**+**

**ultimately have**  $tid$ :  $action\text{-}tid\ (\varphi E\ n)\ r' = action\text{-}tid\ (\varphi E\ (Suc\ n))\ r$

**and**  $obs$ :  $action\text{-}obs\ (\varphi E\ n)\ r' \approx action\text{-}obs\ (\varphi E\ (Suc\ n))\ r$

**unfolding**  $r\text{-}r'$  **by**(*auto intro: sim-action-trans sim-action-sym*)

**from**  $J$  **have**  $\varphi C\ n \subseteq actions\ (\varphi E\ n)$  **by**(simp add:  $committed\text{-}subset\text{-}actions\text{-}def$ )

**with**  $r'$  **have**  $r' \in actions\ (\varphi E\ n)$  **by** *blast*

**with**  $r\ obs$  **have**  $r' \in read\text{-}actions\ (\varphi E\ n)$

**by** *cases(auto intro: read-actions.intros)*

**hence**  $\varphi ws\ n\ r' \in \varphi C\ n \wedge ws\ (\varphi n\ r') \in \varphi n \text{ ' } \varphi C\ n$  **using**  $r'$  **by**(rule *Suc*)

**then obtain**  $ws$ :  $ws\ (\varphi n\ r') \in \varphi n \text{ ' } \varphi C\ n$  ..

**have**  $r\text{-}conv\text{-}inv$ :  $r = inv\text{-}into\ (actions\ (\varphi E\ (Suc\ n)))\ (\varphi\ (Suc\ n))\ (\varphi n\ r')$

**using**  $\langle r \in actions\ (\varphi E\ (Suc\ n)) \rangle$  **unfolding**  $r\text{-}r'$ [*symmetric*]

**by**(simp add:  $inv\text{-}into\text{-}f\text{-}f[OF\ injSn]$ )

**with**  $\langle r' \in \varphi C\ n \rangle\ r\ J\ \langle r' \in read\text{-}actions\ (\varphi E\ n) \rangle$

**have**  $ws\text{-}eq$ :  $\varphi\ (Suc\ n)\ (\varphi ws\ (Suc\ n)\ r) = ws\ (\varphi n\ r')$

**by**(simp add:  $Let\text{-}def\ write\text{-}seen\text{-}committed\text{-}def$ )

**with**  $ws\ CnCSn$  **have**  $\varphi\ (Suc\ n)\ (\varphi ws\ (Suc\ n)\ r) \in \varphi\ (Suc\ n) \text{ ' } \varphi C\ (Suc\ n)$  **by** *auto*

**hence**  $\varphi ws\ (Suc\ n)\ r \in \varphi C\ (Suc\ n)$

**by**(subst (*asm*)  $inj\text{-}on\text{-}image\text{-}mem\text{-}iff[OF\ injSn\ wsSn\ C\text{-}sub\text{-}A]$ )

**moreover from**  $ws\ CnCSn$  **have**  $ws\ (\varphi\ (Suc\ n)\ r) \in \varphi\ (Suc\ n) \text{ ' } \varphi C\ (Suc\ n)$

**unfolding**  $r\text{-}r'$  **by** *auto*

**ultimately show**  $\varphi thesis$  **by** *simp*

**next**

**case** *False*

```

with  $r \langle r \in ?C \text{ (Suc } n) \rangle J$ 
have  $? \varphi \text{ (Suc } n) \text{ (} ?ws \text{ (Suc } n) \text{ } r) \in ? \varphi \text{ } n \text{ ' } ?C \text{ } n$ 
  and  $ws \text{ (} ? \varphi \text{ (Suc } n) \text{ } r) \in ? \varphi \text{ } n \text{ ' } ?C \text{ } n$ 
  unfolding is-justified-by.simps Let-def committed-reads-see-committed-writes-def
  by blast+
hence  $? \varphi \text{ (Suc } n) \text{ (} ?ws \text{ (Suc } n) \text{ } r) \in ? \varphi \text{ (Suc } n) \text{ ' } ?C \text{ (Suc } n)$ 
  and  $ws \text{ (} ? \varphi \text{ (Suc } n) \text{ } r) \in ? \varphi \text{ (Suc } n) \text{ ' } ?C \text{ (Suc } n)$ 
  using CnCnS by blast+
thus ?thesis by(simp add: inj-on-image-mem-iff[OF injSn wsSn C-sub-A])
qed
qed
thus ?thesis1 ?thesis2 by simp-all
qed

lemma is-justified-by-imp-is-weakly-justified-by:
  assumes justified: P  $\vdash (E, ws) \text{ justified-by } J$ 
  and wf: P  $\vdash (E, ws) \checkmark$ 
  shows  $P \vdash (E, ws) \text{ weakly-justified-by } J$ 
  unfolding is-weakly-justified-by.simps
proof(intro conjI)
  let  $?E = \lambda n. \text{justifying-exec } (J \text{ } n)$ 
  and  $?ws = \lambda n. \text{justifying-ws } (J \text{ } n)$ 
  and  $?C = \lambda n. \text{committed } (J \text{ } n)$ 
  and  $? \varphi = \lambda n. \text{action-translation } (J \text{ } n)$ 

  from justified
  show is-commit-sequence E J justification-well-formed P J committed-subset-actions J
    value-written-committed P E J write-seen-committed ws J uncommitted-reads-see-hb P J
    wf-action-translations E J by(simp-all)

  show happens-before-committed-weak P E ws J
    unfolding happens-before-committed-weak-def Let-def
  proof(intro strip conjI)
    fix  $n \text{ } r$ 
    assume  $r \in \text{read-actions } (?E \text{ } n) \cap ?C \text{ } n$ 
    hence read: r  $\in \text{read-actions } (?E \text{ } n)$  and committed: r  $\in ?C \text{ } n$  by simp-all
    with justified have committed-ws: ?ws n r  $\in ?C \text{ } n$ 
      and committed-ws': ws  $(? \varphi \text{ } n \text{ } r) \in ? \varphi \text{ } n \text{ ' } ?C \text{ } n$ 
      by(rule justified-write-seen-hb-read-committed)+
    from committed-ws' obtain  $w$  where  $w: ws \text{ (} ? \varphi \text{ } n \text{ } r) = ? \varphi \text{ } n \text{ } w$ 
      and committed-w: w  $\in ?C \text{ } n$  by blast

    from committed-w justified have  $w \in \text{actions } (?E \text{ } n)$  by(auto simp add: committed-subset-actions-def)
    moreover from justified have inj-on  $(? \varphi \text{ } n) \text{ (actions } (?E \text{ } n))$ 
      by(auto simp add: wf-action-translations-def dest: wf-action-translation-on-inj-onD)
    ultimately have w-def: w  $= \text{inv-into (actions } (?E \text{ } n)) \text{ (} ? \varphi \text{ } n) \text{ (ws (} ? \varphi \text{ } n \text{ } r))$ 
      by(simp-all add: w)

    from committed committed-w
    have  $P, ?E \text{ } n \vdash w \leq_{hb} r \longleftrightarrow (\text{happens-before } P \text{ (} ?E \text{ } n) \mid ?C \text{ } n) \text{ } w \text{ } r$  by auto
    also have  $\dots \longleftrightarrow (\text{inv-imageP (happens-before } P \text{ } E) \text{ (} ? \varphi \text{ } n) \mid ?C \text{ } n) \text{ } w \text{ } r$ 
      using justified by(simp add: happens-before-committed-def)
    also have  $\dots \longleftrightarrow P, E \vdash ? \varphi \text{ } n \text{ } w \leq_{hb} ? \varphi \text{ } n \text{ } r$  using committed committed-w by auto
    finally show  $P, E \vdash ws \text{ (} ? \varphi \text{ } n \text{ } r) \leq_{hb} ? \varphi \text{ } n \text{ } r \longleftrightarrow P, ?E \text{ } n \vdash \text{inv-into (actions } (?E \text{ } n)) \text{ (} ? \varphi \text{ } n) \text{ (ws$ 

```

$(? \varphi \ n \ r)) \leq_{hb} r$   
**unfolding**  $w[symmetric]$  **unfolding**  $w-def \ ..$

**have**  $P, ?E \vdash r \leq_{hb} w \longleftrightarrow (happens-before \ P \ ( ?E \ n) \mid ' \ ?C \ n) \ r \ w$   
**using** *committed-committed-w* **by** *auto*  
**also have**  $\dots \longleftrightarrow (inv-imageP \ (happens-before \ P \ E) \ ( ? \varphi \ n) \mid ' \ ?C \ n) \ r \ w$   
**using** *justified* **by** (*simp add: happens-before-committed-def*)  
**also have**  $\dots \longleftrightarrow P, E \vdash ? \varphi \ n \ r \leq_{hb} ws \ ( ? \varphi \ n \ r)$  **using** *w committed committed-w* **by** *auto*  
**also** {  
**from** *read* **obtain**  $ad \ al \ v$  **where**  $action-obs \ ( ?E \ n) \ r = NormalAction \ (ReadMem \ ad \ al \ v)$  **by**  
*cases auto*  
**with** *justified committed* **obtain**  $v'$  **where**  $obs': action-obs \ E \ ( ? \varphi \ n \ r) = NormalAction \ (ReadMem \ ad \ al \ v')$   
**by** (*fastforce simp add: wf-action-translations-def dest!: wf-action-translation-on-actionD*)  
**moreover from** *committed justified* **have**  $? \varphi \ n \ r \in actions \ E$   
**by** (*auto simp add: is-commit-sequence-def*)  
**ultimately have**  $read': ? \varphi \ n \ r \in read-actions \ E$  **by** (*auto intro: read-actions.intros*)  
**from** *wf* **have** *is-write-seen*  $P \ E \ ws$  **by** (*rule wf-exec-is-write-seenD*)  
**from** *is-write-seenD* [*OF this read' obs'*]  
**have**  $\neg P, E \vdash ? \varphi \ n \ r \leq_{hb} ws \ ( ? \varphi \ n \ r)$  **by** *simp* }  
**ultimately show**  $\neg P, ?E \vdash r \leq_{hb} inv-into \ (actions \ ( ?E \ n)) \ ( ? \varphi \ n) \ (ws \ ( ? \varphi \ n \ r))$   
**unfolding**  $w-def$  **by** *simp*  
**qed**

**from** *justified* **have** *committed-reads-see-committed-writes*  $ws \ J$  **by** *simp*  
**thus** *committed-reads-see-committed-writes-weak*  $ws \ J$   
**by** (*auto simp add: committed-reads-see-committed-writes-def committed-reads-see-committed-writes-weak-def*)  
**qed**

**corollary** *legal-imp-weakly-legal-execution:*  
*legal-execution*  $P \ \mathcal{E} \ Ews \implies weakly-legal-execution \ P \ \mathcal{E} \ Ews$   
**by** (*cases Ews*) (*auto 4 4 simp add: gen-legal-execution.simps simp del: is-justified-by.simps is-weakly-justified-by.simps*  
*intro: is-justified-by-imp-is-weakly-justified-by*)

**lemma** *drop-0th-justifying-exec:*  
**assumes**  $P \vdash (E, ws) \text{ justified-by } J$   
**and**  $wf: P \vdash (E', ws') \checkmark$   
**shows**  $P \vdash (E, ws) \text{ justified-by } (J(0 := \{\}, justifying-exec = E', justifying-ws = ws',$   
*action-translation = id))*  
**(is - - justified-by ?J)**  
**using** *assms*  
**unfolding** *is-justified-by.simps is-commit-sequence-def*  
*justification-well-formed-def committed-subset-actions-def happens-before-committed-def*  
*sync-order-committed-def value-written-committed-def write-seen-committed-def uncommitted-reads-see-hb-def*  
*committed-reads-see-committed-writes-def external-actions-committed-def wf-action-translations-def*  
**proof** (*intro conjI strip*)  
**let**  $?E = \lambda n. justifying-exec \ ( ?J \ n)$   
**and**  $? \varphi = \lambda n. action-translation \ ( ?J \ n)$   
**and**  $?C = \lambda n. committed \ ( ?J \ n)$   
**and**  $?ws = \lambda n. justifying-ws \ ( ?J \ n)$   
  
**show**  $?C \ 0 = \{\}$  **by** *simp*  
  
**from** *assms* **have**  $C-0: committed \ (J \ 0) = \{\}$  **by** (*simp add: is-commit-sequence-def*)

hence  $(\bigcup n. ?\varphi\ n \text{ ' } ?C\ n) = (\bigcup n. \text{action-translation } (J\ n) \text{ ' committed } (J\ n))$   
 by  $-(\text{rule SUP-cong, simp-all})$   
 also have  $\dots = \text{actions } E \text{ using } \text{assms by}(\text{simp add: is-commit-sequence-def})$   
 finally show  $\text{actions } E = (\bigcup n. ?\varphi\ n \text{ ' } ?C\ n) \dots$

```

fix n
{ fix r'
  assume r' ∈ read-actions (?E (Suc n))
  thus ?φ (Suc n) r' ∈ ?φ n ' ?C n ∨ P, ?E (Suc n) ⊢ ?ws (Suc n) r' ≤hb r'
    using assms by(auto dest!: bspec simp add: uncommitted-reads-see-hb-def is-commit-sequence-def)
}
{ fix r'
  assume r': r' ∈ read-actions (?E (Suc n)) ∩ ?C (Suc n)

  have n ≠ 0
  proof
    assume n = 0
    hence r' ∈ read-actions (justifying-exec (J 1)) ∩ committed (J 1) using r' by simp
    hence action-translation (J 1) r' ∈ action-translation (J 0) ' committed (J 0) ∨
      ws (action-translation (J 1) r') ∈ action-translation (J 0) ' committed (J 0) using assms
    unfolding One-nat-def is-justified-by.simps Let-def committed-reads-see-committed-writes-def
    by(metis (lifting))
    thus False unfolding C-0 by simp
  qed
  thus let r = ?φ (Suc n) r'; committed-n = ?φ n ' ?C n
    in r ∈ committed-n ∨
      (?φ (Suc n) (?ws (Suc n) r') ∈ committed-n ∧ ws r ∈ committed-n)
    using assms r' by(simp add: committed-reads-see-committed-writes-def) }
{ fix a'
  assume a ∈ external-actions (?E n)
  and a' ∈ ?C n P, ?E n ⊢ a ≤hb a'
  moreover hence n > 0 by(simp split: if-split-asm)
  ultimately show a ∈ ?C n using assms
    by(simp add: external-actions-committed-def) blast }

from assms have wf-action-translation E (?J 0)
  by(simp add: wf-action-translations-def wf-action-translation-on-def)
thus wf-action-translation E (?J n) using assms by(simp add: wf-action-translations-def)
qed auto

```

**lemma** *drop-0th-weakly-justifying-exec*:  
 assumes  $P \vdash (E, ws) \text{ weakly-justified-by } J$   
 and  $wf: P \vdash (E', ws') \checkmark$   
 shows  $P \vdash (E, ws) \text{ weakly-justified-by } (J(0 := \{\text{committed} = \{\}, \text{justifying-exec} = E', \text{justifying-ws} = ws', \text{action-translation} = \text{id}\}))$   
 (is  $\vdash$  - weakly-justified-by  $?J$ )  
 using assms  
 unfolding is-weakly-justified-by.simps is-commit-sequence-def  
 justification-well-formed-def committed-subset-actions-def happens-before-committed-weak-def  
 value-written-committed-def write-seen-committed-def uncommitted-reads-see-hb-def  
 committed-reads-see-committed-writes-weak-def external-actions-committed-def wf-action-translations-def  
**proof**(intro conjI strip)  
 let  $?E = \lambda n. \text{justifying-exec } (?J\ n)$   
 and  $?φ = \lambda n. \text{action-translation } (?J\ n)$

```

and ?C = λn. committed (?J n)
and ?ws = λn. justifying-ws (?J n)

show ?C 0 = {} by simp

from assms have C-0: committed (J 0) = {} by (simp add: is-commit-sequence-def)
hence (⋃ n. ?φ n ‘ ?C n) = (⋃ n. action-translation (J n) ‘ committed (J n))
  by -(rule SUP-cong, simp-all)
also have ... = actions E using assms by (simp add: is-commit-sequence-def)
finally show actions E = (⋃ n. ?φ n ‘ ?C n) ..

fix n
{ fix r'
  assume r' ∈ read-actions (?E (Suc n))
  thus ?φ (Suc n) r' ∈ ?φ n ‘ ?C n ∨ P, ?E (Suc n) ⊢ ?ws (Suc n) r' ≤hb r'
    using assms by (auto dest!: bspec simp add: uncommitted-reads-see-hb-def is-commit-sequence-def)
}
{ fix r'
  assume r': r' ∈ read-actions (?E (Suc n)) ∩ ?C (Suc n)

  have n ≠ 0
  proof
    assume n = 0
    hence r' ∈ read-actions (justifying-exec (J 1)) ∩ committed (J 1) using r' by simp
    hence action-translation (J 1) r' ∈ action-translation (J 0) ‘ committed (J 0) ∨
      ws (action-translation (J 1) r') ∈ action-translation (J 0) ‘ committed (J 0) using assms
    unfolding One-nat-def is-weakly-justified-by.simps Let-def committed-reads-see-committed-writes-weak-def
      by (metis (lifting))
    thus False unfolding C-0 by simp
  qed
  thus let r = ?φ (Suc n) r'; committed-n = ?φ n ‘ ?C n
    in r ∈ committed-n ∨ ws r ∈ committed-n
    using assms r' by (simp add: committed-reads-see-committed-writes-weak-def) }

from assms have wf-action-translation E (?J 0)
  by (simp add: wf-action-translations-def wf-action-translation-on-def)
thus wf-action-translation E (?J n) using assms by (simp add: wf-action-translations-def)
qed auto

```

### 8.4.13 Executions with common prefix

```

lemma actions-change-prefix:
  assumes read: a ∈ actions E
  and prefix: ltake n E [≈] ltake n E'
  and rn: enat a < n
  shows a ∈ actions E'
using llist-all2-llengthD[OF prefix[unfolded sim-actions-def]] read rn
by (simp add: actions-def min-def split: if-split-asm)

```

```

lemma action-obs-change-prefix:
  assumes prefix: ltake n E [≈] ltake n E'
  and rn: enat a < n
  shows action-obs E a ≈ action-obs E' a
proof -

```

```

from rn have action-obs E a = action-obs (ltake n E) a
  by(simp add: action-obs-def lnth-ltake)
also from prefix have  $\dots \approx \text{action-obs } (ltake\ n\ E')\ a$ 
  by(rule sim-actions-action-obsD)
also have  $\dots = \text{action-obs } E'\ a$  using rn
  by(simp add: action-obs-def lnth-ltake)
finally show ?thesis .
qed

```

```

lemma action-obs-change-prefix-eq:
  assumes prefix: ltake n E = ltake n E'
  and rn: enat a < n
  shows action-obs E a = action-obs E' a

```

**proof** –

```

from rn have action-obs E a = action-obs (ltake n E) a
  by(simp add: action-obs-def lnth-ltake)
also from prefix have  $\dots = \text{action-obs } (ltake\ n\ E')\ a$ 
  by(simp add: action-obs-def)
also have  $\dots = \text{action-obs } E'\ a$  using rn
  by(simp add: action-obs-def lnth-ltake)
finally show ?thesis .

```

**qed**

```

lemma read-actions-change-prefix:
  assumes read: r ∈ read-actions E
  and prefix: ltake n E [≈] ltake n E' enat r < n
  shows r ∈ read-actions E'
using read action-obs-change-prefix[OF prefix] actions-change-prefix[OF - prefix]
by(cases)(auto intro: read-actions.intros)

```

```

lemma sim-action-is-write-action-eq:
  assumes obs ≈ obs'
  shows is-write-action obs ⟷ is-write-action obs'
using assms by cases simp-all

```

```

lemma write-actions-change-prefix:
  assumes write: w ∈ write-actions E
  and prefix: ltake n E [≈] ltake n E' enat w < n
  shows w ∈ write-actions E'
using write action-obs-change-prefix[OF prefix] actions-change-prefix[OF - prefix]
by(cases)(auto intro: write-actions.intros dest: sim-action-is-write-action-eq)

```

```

lemma action-loc-change-prefix:
  assumes ltake n E [≈] ltake n E' enat a < n
  shows action-loc P E a = action-loc P E' a
using action-obs-change-prefix[OF assms]
by(fastforce elim!: action-loc-aux-cases intro: action-loc-aux-intros)

```

```

lemma sim-action-is-new-action-eq:
  assumes obs ≈ obs'
  shows is-new-action obs = is-new-action obs'
using assms by cases auto

```

```

lemma action-order-change-prefix:

```

**assumes**  $ao: E \vdash a \leq_a a'$   
**and**  $prefix: ltake\ n\ E \approx ltake\ n\ E'$   
**and**  $an: enat\ a < n$   
**and**  $a'n: enat\ a' < n$   
**shows**  $E' \vdash a \leq_a a'$   
**using**  $ao\ actions-change-prefix[OF - prefix\ an]\ actions-change-prefix[OF - prefix\ a'n]\ action-obs-change-prefix[OF\ prefix\ an]\ action-obs-change-prefix[OF\ prefix\ a'n]$   
**by**( $auto\ simp\ add: action-order-def\ split: if-split-asm\ dest: sim-action-is-new-action-eq$ )

**lemma** *value-written-change-prefix*:  
**assumes**  $eq: ltake\ n\ E = ltake\ n\ E'$   
**and**  $an: enat\ a < n$   
**shows**  $value-written\ P\ E\ a = value-written\ P\ E'\ a$   
**using**  $action-obs-change-prefix-eq[OF\ eq\ an]$   
**by**( $simp\ add: value-written-def\ fun-eq-iff$ )

**lemma** *action-tid-change-prefix*:  
**assumes**  $prefix: ltake\ n\ E \approx ltake\ n\ E'$   
**and**  $an: enat\ a < n$   
**shows**  $action-tid\ E\ a = action-tid\ E'\ a$   
**proof** –  
**from**  $an$  **have**  $action-tid\ E\ a = action-tid\ (ltake\ n\ E)\ a$   
**by**( $simp\ add: action-tid-def\ lnth-ltake$ )  
**also from**  $prefix$  **have**  $\dots = action-tid\ (ltake\ n\ E')\ a$   
**by**( $rule\ sim-actions-action-tidD$ )  
**also from**  $an$  **have**  $\dots = action-tid\ E'\ a$   
**by**( $simp\ add: action-tid-def\ lnth-ltake$ )  
**finally show**  $?thesis$  .  
**qed**

**lemma** *program-order-change-prefix*:  
**assumes**  $po: E \vdash a \leq_{po} a'$   
**and**  $prefix: ltake\ n\ E \approx ltake\ n\ E'$   
**and**  $an: enat\ a < n$   
**and**  $a'n: enat\ a' < n$   
**shows**  $E' \vdash a \leq_{po} a'$   
**using**  $po\ action-order-change-prefix[OF - prefix\ an\ a'n]\ action-tid-change-prefix[OF\ prefix\ an]\ action-tid-change-prefix[OF\ prefix\ a'n]$   
**by**( $auto\ elim!: program-orderE\ intro: program-orderI$ )

**lemma** *sim-action-sactionD*:  
**assumes**  $obs \approx obs'$   
**shows**  $saction\ P\ obs \longleftrightarrow saction\ P\ obs'$   
**using**  $assms$  **by**  $cases\ simp-all$

**lemma** *sactions-change-prefix*:  
**assumes**  $sync: a \in sactions\ P\ E$   
**and**  $prefix: ltake\ n\ E \approx ltake\ n\ E'$   
**and**  $rn: enat\ a < n$   
**shows**  $a \in sactions\ P\ E'$   
**using**  $sync\ action-obs-change-prefix[OF\ prefix\ rn]\ actions-change-prefix[OF - prefix\ rn]$   
**unfolding**  $sactions-def$  **by**( $simp\ add: sim-action-sactionD$ )

**lemma** *sync-order-change-prefix*:

**assumes** *so*:  $P, E \vdash a \leq_{so} a'$   
**and** *prefix*:  $l\text{take } n \ E \ [\approx] \ l\text{take } n \ E'$   
**and** *an*:  $\text{enat } a < n$   
**and** *a'n*:  $\text{enat } a' < n$   
**shows**  $P, E' \vdash a \leq_{so} a'$

**using** *so* *action-order-change-prefix*[*OF* - *prefix an a'n*] *sactions-change-prefix*[*OF* - *prefix an, of P*]  
*sactions-change-prefix*[*OF* - *prefix a'n, of P*]

**by**(*simp add: sync-order-def*)

**lemma** *sim-action-synchronizes-withD*:

**assumes**  $\text{obs} \approx \text{obs}' \ \text{obs}'' \approx \text{obs}'''$   
**shows**  $P \vdash (t, \text{obs}) \rightsquigarrow_{sw} (t', \text{obs}'') \longleftrightarrow P \vdash (t, \text{obs}') \rightsquigarrow_{sw} (t', \text{obs}''')$

**using** *assms*

**by**(*auto elim!: sim-action.cases synchronizes-with.cases intro: synchronizes-with.intros*)

**lemma** *sync-with-change-prefix*:

**assumes** *sw*:  $P, E \vdash a \leq_{sw} a'$   
**and** *prefix*:  $l\text{take } n \ E \ [\approx] \ l\text{take } n \ E'$   
**and** *an*:  $\text{enat } a < n$   
**and** *a'n*:  $\text{enat } a' < n$   
**shows**  $P, E' \vdash a \leq_{sw} a'$

**using** *sw* *sync-order-change-prefix*[*OF* - *prefix an a'n, of P*]  
*action-tid-change-prefix*[*OF prefix an*] *action-tid-change-prefix*[*OF prefix a'n*]  
*action-obs-change-prefix*[*OF prefix an*] *action-obs-change-prefix*[*OF prefix a'n*]

**by**(*auto simp add: sync-with-def dest: sim-action-synchronizes-withD*)

**lemma** *po-sw-change-prefix*:

**assumes** *posw*:  $\text{po-sw } P \ E \ a \ a'$   
**and** *prefix*:  $l\text{take } n \ E \ [\approx] \ l\text{take } n \ E'$   
**and** *an*:  $\text{enat } a < n$   
**and** *a'n*:  $\text{enat } a' < n$   
**shows**  $\text{po-sw } P \ E' \ a \ a'$

**using** *posw* *sync-with-change-prefix*[*OF* - *prefix an a'n, of P*] *program-order-change-prefix*[*OF* - *prefix an a'n*]

**by**(*auto simp add: po-sw-def*)

**lemma** *happens-before-new-not-new*:

**assumes** *tsa-ok*: *thread-start-actions-ok* *E*  
**and** *a*:  $a \in \text{actions } E$   
**and** *a'*:  $a' \in \text{actions } E$   
**and** *new-a*: *is-new-action* (*action-obs* *E* *a*)  
**and** *new-a'*:  $\neg \text{is-new-action } (\text{action-obs } E \ a')$   
**shows**  $P, E \vdash a \leq_{hb} a'$

**proof** –

**from** *thread-start-actions-okD*[*OF tsa-ok a' new-a*]

**obtain** *i* **where**  $i \leq a'$

**and** *obs-i*: *action-obs* *E* *i* = *InitialThreadAction*

**and** *action-tid* *E* *i* = *action-tid* *E* *a'* **by** *auto*

**from**  $\langle i \leq a' \rangle \ a' \ \text{have } i \in \text{actions } E$

**by**(*auto simp add: actions-def le-less-trans[where y=enat a']*)

**with**  $\langle i \leq a' \rangle \ \text{obs-i } a' \ \text{new-a' have } E \vdash i \leq_a a' \ \text{by} \ (\text{simp add: action-order-def})$



hence  $E \vdash i \leq_{po} a'$  **using**  $\langle \text{action-tid } E \ i = \text{action-tid } E \ a' \rangle$   
**by**(rule *program-orderI*)

**moreover** {  
**from**  $\langle i \in \text{actions } E \rangle \text{ obs-}i$   
**have**  $i \in \text{sactions } P \ E$  **by**(auto intro: *sactionsI*)  
**from**  $a \ \langle i \in \text{actions } E \rangle \text{ new-}a \ \text{obs-}i$  **have**  $E \vdash a \leq a \ i$  **by**(simp add: *action-order-def*)  
**moreover from**  $a \ \text{new-}a$  **have**  $a \in \text{sactions } P \ E$  **by**(auto intro: *sactionsI*)  
**ultimately have**  $P, E \vdash a \leq_{so} i$  **using**  $\langle i \in \text{sactions } P \ E \rangle$  **by**(rule *sync-orderI*)  
**moreover from**  $\text{new-}a \ \text{obs-}i$  **have**  $P \vdash (\text{action-tid } E \ a, \text{action-obs } E \ a) \rightsquigarrow_{sw} (\text{action-tid } E \ i, \text{action-obs } E \ i)$   
**by cases**(auto intro: *synchronizes-with.intros*)  
**ultimately have**  $P, E \vdash a \leq_{sw} i$  **by**(rule *sync-withI*) }  
**ultimately show** *?thesis unfolding po-sw-def [abs-def]* **by**(blast intro: *tranclp.r-into-trancl tranclp-trans*)  
**qed**

**lemma** *happens-before-change-prefix*:

**assumes** *hb*:  $P, E \vdash a \leq_{hb} a'$   
**and** *tso-ok*: *thread-start-actions-ok*  $E'$   
**and** *prefix*:  $\text{ltake } n \ E \ [\approx] \ \text{ltake } n \ E'$   
**and** *an*:  $\text{enat } a < n$   
**and** *a'n*:  $\text{enat } a' < n$   
**shows**  $P, E' \vdash a \leq_{hb} a'$   
**using** *hb an a'n*  
**proof induct**  
**case** (*base*  $a'$ )  
**thus** *?case* **by**(rule *tranclp.r-into-trancl*[**where**  $r = \text{po-sw } P \ E'$ , *OF po-sw-change-prefix*[*OF - prefix*]])  
**next**  
**case** (*step*  $a' \ a''$ )  
**show** *?case*  
**proof**(*cases is-new-action (action-obs E a')  $\wedge$   $\neg$  is-new-action (action-obs E a'')*)  
**case** *False*  
**from**  $\langle \text{po-sw } P \ E \ a' \ a'' \rangle$  **have**  $E \vdash a' \leq a''$  **by**(rule *po-sw-into-action-order*)  
**with**  $\langle \text{enat } a'' < n \rangle$  *False* **have**  $\text{enat } a' < n$   
**by**(safe elim!: *action-orderE*)(metis *Suc-leI Suc-n-not-le-n enat-ord-simps(2) le-trans nat-neq-iff xtrans(10)*)+  
**with**  $\langle \text{enat } a < n \rangle$  **have**  $P, E' \vdash a \leq_{hb} a'$  **by**(rule *step*)  
**moreover from**  $\langle \text{po-sw } P \ E \ a' \ a'' \rangle$  *prefix*  $\langle \text{enat } a' < n \rangle \ \langle \text{enat } a'' < n \rangle$   
**have**  $\text{po-sw } P \ E' \ a' \ a''$  **by**(rule *po-sw-change-prefix*)  
**ultimately show** *?thesis ..*  
**next**  
**case** *True*  
**then obtain** *new-a'*: *is-new-action (action-obs E a')*  
**and**  $\neg \text{is-new-action (action-obs E a'')}$  ..  
**from**  $\langle P, E \vdash a \leq_{hb} a' \rangle$  *new-a'*  
**have** *new-a*: *is-new-action (action-obs E a)*  
**and** *tid*:  $\text{action-tid } E \ a = \text{action-tid } E \ a'$   
**and**  $a \leq a'$  **by**(rule *happens-before-new-actionD*)+  
  
**note** *tso-ok moreover*  
**from** *porder-happens-before*[*of E P*] **have**  $a \in \text{actions } E$   
**by**(rule *porder-onE*)(erule *reft-onPD1*, rule  $\langle P, E \vdash a \leq_{hb} a' \rangle$ )  
**hence**  $a \in \text{actions } E'$  **using** *an* **by**(rule *actions-change-prefix*[*OF - prefix*])

**moreover**  
**from**  $\langle po\text{-}sw\ P\ E\ a'\ a'' \rangle\ refl\text{-}on\text{-}program\text{-}order[of\ E]\ refl\text{-}on\text{-}sync\text{-}order[of\ P\ E]$   
**have**  $a'' \in actions\ E$   
**unfolding**  $po\text{-}sw\text{-}def$  **by**( $auto\ dest: refl\text{-}onPD2\ elim!: sync\text{-}withE$ )  
**hence**  $a'' \in actions\ E'$  **using**  $\langle enat\ a'' < n \rangle$  **by**( $rule\ actions\text{-}change\text{-}prefix[OF\ \text{-}\ prefix]$ )  
**moreover**  
**from**  $new\text{-}a\ action\text{-}obs\text{-}change\text{-}prefix[OF\ prefix\ an]$   
**have**  $is\text{-}new\text{-}action\ (action\text{-}obs\ E'\ a)$  **by**( $cases$ )  $auto$   
**moreover**  
**from**  $\langle \neg is\text{-}new\text{-}action\ (action\text{-}obs\ E\ a'') \rangle\ action\text{-}obs\text{-}change\text{-}prefix[OF\ prefix\ \langle enat\ a'' < n \rangle]$   
**have**  $\neg is\text{-}new\text{-}action\ (action\text{-}obs\ E'\ a'')$  **by**( $auto\ elim: is\text{-}new\text{-}action.cases$ )  
**ultimately show**  $P, E' \vdash a \leq_{hb} a''$  **by**( $rule\ happens\text{-}before\text{-}new\text{-}not\text{-}new$ )  
**qed**  
**qed**

**lemma** *thread-start-actions-ok-change*:  
**assumes**  $tsa: thread\text{-}start\text{-}actions\text{-}ok\ E$   
**and**  $sim: E \approx E'$   
**shows**  $thread\text{-}start\text{-}actions\text{-}ok\ E'$   
**proof**( $rule\ thread\text{-}start\text{-}actions\text{-}okI$ )  
**fix**  $a$   
**assume**  $a \in actions\ E' \neg is\text{-}new\text{-}action\ (action\text{-}obs\ E'\ a)$   
**from**  $sim$  **have**  $len\text{-}eq: llength\ E = llength\ E'$  **by**( $simp\ add: sim\text{-}actions\text{-}def$ )( $rule\ llist\text{-}all2\text{-}llengthD$ )  
**with**  $sim$  **have**  $sim': ltake\ (llength\ E)\ E \approx ltake\ (llength\ E)\ E'$  **by**( $simp\ add: ltake\text{-}all$ )  
  
**from**  $\langle a \in actions\ E' \rangle\ len\text{-}eq$  **have**  $enat\ a < llength\ E$  **by**( $simp\ add: actions\text{-}def$ )  
**with**  $\langle a \in actions\ E' \rangle\ sim'$ [*symmetric*] **have**  $a \in actions\ E$  **by**( $rule\ actions\text{-}change\text{-}prefix$ )  
**moreover have**  $\neg is\text{-}new\text{-}action\ (action\text{-}obs\ E\ a)$   
**using**  $action\text{-}obs\text{-}change\text{-}prefix[OF\ sim'\ \langle enat\ a < llength\ E \rangle]\ \langle \neg is\text{-}new\text{-}action\ (action\text{-}obs\ E'\ a) \rangle$   
**by**( $auto\ elim!: is\text{-}new\text{-}action.cases$ )  
**ultimately obtain**  $i$  **where**  $i \leq a\ action\text{-}obs\ E\ i = InitialThreadAction\ action\text{-}tid\ E\ i = action\text{-}tid\ E\ a$   
**by**( $blast\ dest: thread\text{-}start\text{-}actions\text{-}okD[OF\ tsa]$ )  
**thus**  $\exists i \leq a. action\text{-}obs\ E'\ i = InitialThreadAction \wedge action\text{-}tid\ E'\ i = action\text{-}tid\ E'\ a$   
**using**  $action\text{-}tid\text{-}change\text{-}prefix[OF\ sim',\ of\ i]\ action\text{-}tid\text{-}change\text{-}prefix[OF\ sim',\ of\ a]\ \langle enat\ a < llength\ E \rangle$   
 $action\text{-}obs\text{-}change\text{-}prefix[OF\ sim',\ of\ i]$   
**by**( $cases\ llength\ E$ )( $auto\ intro!: exI[\textbf{where}\ x=i]$ )  
**qed**

**context** *executions-aux* **begin**

**lemma**  $\mathcal{E}\text{-}new\text{-}same\text{-}addr\text{-}singleton$ :  
**assumes**  $E: E \in \mathcal{E}$   
**shows**  $\exists a. new\text{-}actions\text{-}for\ P\ E\ adal \subseteq \{a\}$   
**by**( $blast\ dest: \mathcal{E}\text{-}new\text{-}actions\text{-}for\text{-}fun[OF\ E]$ )

**lemma** *new-action-before-read*:  
**assumes**  $E: E \in \mathcal{E}$   
**and**  $wf: P \vdash (E, ws) \checkmark$   
**and**  $ra: ra \in read\text{-}actions\ E$   
**and**  $adal: adal \in action\text{-}loc\ P\ E\ ra$   
**and**  $new: wa \in new\text{-}actions\text{-}for\ P\ E\ adal$   
**and**  $sc: \bigwedge a. \llbracket a < ra; a \in read\text{-}actions\ E \rrbracket \implies P, E \vdash a \leadsto_{mrw} ws\ a$

**shows**  $wa < ra$   
**using**  $\mathcal{E}$ -new-same-addr-singleton[*OF*  $E$ , *of*  $adal$ ] *init-before-read*[*OF*  $E$   $wf$   $ra$   $adal$   $sc$ ] *new*  
**by** *auto*

**lemma** *mrw-before*:

**assumes**  $E: E \in \mathcal{E}$   
**and**  $wf: P \vdash (E, ws) \checkmark$   
**and**  $mrw: P, E \vdash r \rightsquigarrow_{mrw} w$   
**and**  $sc: \bigwedge a. \llbracket a < r; a \in \text{read-actions } E \rrbracket \implies P, E \vdash a \rightsquigarrow_{mrw} ws \ a$   
**shows**  $w < r$   
**using** *mrw read-actions-not-write-actions*[*of*  $r$   $E$ ]  
**apply** *cases*  
**apply**(*erule action-orderE*)  
**apply**(*erule* (1) *new-action-before-read*[*OF*  $E$   $wf$ ])  
**apply**(*simp add: new-actions-for-def*)  
**apply**(*erule* (1)  $sc$ )  
**apply**(*cases*  $w = r$ )  
**apply** *auto*  
**done**

**lemma** *mrw-change-prefix*:

**assumes**  $E': E' \in \mathcal{E}$   
**and**  $mrw: P, E \vdash r \rightsquigarrow_{mrw} w$   
**and** *tso-ok*: *thread-start-actions-ok*  $E'$   
**and** *prefix*:  $l\text{take } n \ E \ [\approx] \ l\text{take } n \ E'$   
**and**  $an: \text{enat } r < n$   
**and**  $a'n: \text{enat } w < n$   
**shows**  $P, E' \vdash r \rightsquigarrow_{mrw} w$   
**using** *mrw*  
**proof** *cases*  
**fix**  $adal$   
**assume**  $r: r \in \text{read-actions } E$   
**and**  $adal-r: adal \in \text{action-loc } P \ E \ r$   
**and**  $war: E \vdash w \leq_a r$   
**and**  $w: w \in \text{write-actions } E$   
**and**  $adal-w: adal \in \text{action-loc } P \ E \ w$   
**and**  $mrw: \bigwedge wa'. \llbracket wa' \in \text{write-actions } E; adal \in \text{action-loc } P \ E \ wa' \rrbracket$   
 $\implies E \vdash wa' \leq_a w \vee E \vdash r \leq_a wa'$   
**show** *?thesis*  
**proof**(*rule most-recent-write-for.intros*)  
**from**  $r$  *prefix*  $an$  **show**  $r': r \in \text{read-actions } E'$   
**by**(*rule read-actions-change-prefix*)  
**from**  $adal-r$  **show**  $adal \in \text{action-loc } P \ E' \ r$   
**by**(*simp add: action-loc-change-prefix*[*OF* *prefix*[*symmetric*]  $an$ ])  
**from**  $war$  *prefix*  $a'n$   $an$  **show**  $E' \vdash w \leq_a r$  **by**(*rule action-order-change-prefix*)  
**from**  $w$  *prefix*  $a'n$  **show**  $w': w \in \text{write-actions } E'$  **by**(*rule write-actions-change-prefix*)  
**from**  $adal-w$  **show**  $adal-w': adal \in \text{action-loc } P \ E' \ w$  **by**(*simp add: action-loc-change-prefix*[*OF* *prefix*[*symmetric*]  $a'n$ ])  
**fix**  $wa'$   
**assume**  $wa': wa' \in \text{write-actions } E'$   
**and**  $adal-wa': adal \in \text{action-loc } P \ E' \ wa'$   
**show**  $E' \vdash wa' \leq_a w \vee E' \vdash r \leq_a wa'$   
**proof**(*cases*  $\text{enat } wa' < n$ )

```

case True
note  $wa'n = this$ 
with  $wa' \text{ prefix}[symmetric]$  have  $wa' \in \text{write-actions } E$  by(rule write-actions-change-prefix)
moreover from  $adal-wa'$  have  $adal \in \text{action-loc } P \ E \ wa'$ 
  by(simp add: action-loc-change-prefix[OF prefix wa'n])
ultimately have  $E \vdash wa' \leq_a w \vee E \vdash r \leq_a wa'$  by(rule mrw)
thus ?thesis
proof
  assume  $E \vdash wa' \leq_a w$ 
  hence  $E' \vdash wa' \leq_a w$  using prefix wa'n a'n by(rule action-order-change-prefix)
  thus ?thesis ..
next
  assume  $E \vdash r \leq_a wa'$ 
  hence  $E' \vdash r \leq_a wa'$  using prefix an wa'n by(rule action-order-change-prefix)
  thus ?thesis ..
qed
next
case False note  $wa'n = this$ 
show ?thesis
proof(cases is-new-action (action-obs E' wa'))
  case False
  hence  $E' \vdash r \leq_a wa'$  using wa'n r' wa' an
    by(auto intro!: action-orderI (metis enat-ord-code(1) linorder-le-cases order-le-less-trans))
  thus ?thesis ..
next
  case True
with  $wa' \text{ adal-wa'}$  have  $new: wa' \in \text{new-actions-for } P \ E' \ adal$  by(simp add: new-actions-for-def)
show ?thesis
proof(cases is-new-action (action-obs E' w))
  case True
  with  $adal-w' \ a'n \ w'$  have  $w \in \text{new-actions-for } P \ E' \ adal$  by(simp add: new-actions-for-def)
  with  $E' \ new$  have  $wa' = w$  by(rule E-new-actions-for-fun)
  thus ?thesis using  $w'$  by(auto intro: refl-onPD[OF refl-action-order])
next
  case False
  with  $True \ wa' \ w'$  show ?thesis by(auto intro!: action-orderI)
qed
qed
qed
qed
qed

lemma action-order-read-before-write:
  assumes  $E: E \in \mathcal{E} \ P \vdash (E, \ ws) \ \checkmark$ 
  and  $ao: E \vdash w \leq_a r$ 
  and  $r: r \in \text{read-actions } E$ 
  and  $w: w \in \text{write-actions } E$ 
  and  $adal: adal \in \text{action-loc } P \ E \ r \ adal \in \text{action-loc } P \ E \ w$ 
  and  $sc: \bigwedge a. \llbracket a < r; a \in \text{read-actions } E \rrbracket \implies P, E \vdash a \rightsquigarrow_{mrw} ws \ a$ 
  shows  $w < r$ 
using ao
proof(cases rule: action-orderE)
  case 1
  from init-before-read[OF E r adal(1) sc]

```

```

obtain  $i$  where  $i < r$   $i \in \text{new-actions-for } P \ E$  adal by blast
moreover from  $\langle \text{is-new-action } (\text{action-obs } E \ w) \rangle$  adal(2)  $\langle w \in \text{actions } E \rangle$ 
have  $w \in \text{new-actions-for } P \ E$  adal by(simp add: new-actions-for-def)
ultimately show  $w < r$  using  $E$  by(auto dest:  $\mathcal{E}$ -new-actions-for-fun)
next
  case 2
  with  $r \ w$  show ?thesis
    by(cases w = r)(auto dest: read-actions-not-write-actions)
qed

end

end

```

## 8.5 The data race free guarantee of the JMM

```

theory JMM-DRF
imports
  JMM-Spec
begin

context drf begin

lemma drf-lemma:
  assumes  $\text{wf}: P \vdash (E, \text{ws}) \checkmark$ 
  and  $E: E \in \mathcal{E}$ 
  and  $\text{sync}: \text{correctly-synchronized } P \ \mathcal{E}$ 
  and  $\text{read-before}: \bigwedge r. r \in \text{read-actions } E \implies P, E \vdash \text{ws } r \leq_{hb} r$ 
  shows  $\text{sequentially-consistent } P \ (E, \text{ws})$ 
proof(rule ccontr)
  let  $?Q = \{r. r \in \text{read-actions } E \wedge \neg P, E \vdash r \leadsto_{mrw} \text{ws } r\}$ 

  assume  $\neg ?thesis$ 
  then obtain  $r$  where  $r \in \text{read-actions } E \wedge \neg P, E \vdash r \leadsto_{mrw} \text{ws } r$ 
    by(auto simp add: sequentially-consistent-def)
  hence  $r \in ?Q$  by simp
  with  $\text{wf-action-order}[of \ E]$  obtain  $r'$ 
    where  $r' \in ?Q$ 
    and  $(\text{action-order } E) \neq \hat{*} r' \ r$ 
    and  $r'\text{-min}: \bigwedge a. (\text{action-order } E) \neq a \ r' \implies a \notin ?Q$ 
    by(rule wfP-minimalE) blast
  from  $\langle r' \in ?Q \rangle$  have  $r': r' \in \text{read-actions } E$ 
    and  $\text{not-mrw}: \neg P, E \vdash r' \leadsto_{mrw} \text{ws } r'$  by blast+

  from  $r'$  obtain  $ad \ al \ v$  where  $\text{obs-}r': \text{action-obs } E \ r' = \text{NormalAction } (\text{ReadMem } ad \ al \ v)$ 
    by(cases) auto
  from  $\text{wf}$  have  $\text{ws}: \text{is-write-seen } P \ E \ \text{ws}$ 
    and  $\text{tsa-ok}: \text{thread-start-actions-ok } E$ 
    by(rule wf-exec-is-write-seenD wf-exec-thread-start-actions-okD)+
  from  $\text{is-write-seenD}[OF \ \text{ws } r' \ \text{obs-}r']$ 
  have  $\text{ws-}r: \text{ws } r' \in \text{write-actions } E$ 
    and  $\text{adal}: (ad, al) \in \text{action-loc } P \ E \ (\text{ws } r')$ 
    and  $v: v = \text{value-written } P \ E \ (\text{ws } r') \ (ad, al)$ 

```

**and not-hb:**  $\neg P, E \vdash r' \leq_{hb} ws \ r'$  **by** *auto*  
**from**  $r'$  **have**  $P, E \vdash ws \ r' \leq_{hb} r'$  **by** (*rule read-before*)  
**hence**  $E \vdash ws \ r' \leq_a r'$  **by** (*rule happens-before-into-action-order*)  
**from** *not-mrw*  
**have**  $\exists w'. w' \in \text{write-actions } E \wedge (ad, al) \in \text{action-loc } P \ E \ w' \wedge$   
 $\neg P, E \vdash w' \leq_{hb} ws \ r' \wedge \neg P, E \vdash w' \leq_{so} ws \ r' \wedge$   
 $\neg P, E \vdash r' \leq_{hb} w' \wedge \neg P, E \vdash r' \leq_{so} w' \wedge E \vdash w' \leq_a r'$   
**proof** (*rule contrapos-np*)  
**assume** *inbetween:*  $\neg ?thesis$   
**note**  $r'$   
**moreover from**  $obs-r'$  **have**  $(ad, al) \in \text{action-loc } P \ E \ r'$  **by** *simp*  
**moreover note**  $\langle E \vdash ws \ r' \leq_a r' \rangle \ ws-r \ adal$   
**moreover**  
**{ fix**  $w'$   
**assume**  $w' \in \text{write-actions } E \ (ad, al) \in \text{action-loc } P \ E \ w'$   
**with** *inbetween* **have**  $P, E \vdash w' \leq_{hb} ws \ r' \vee P, E \vdash w' \leq_{so} ws \ r' \vee P, E \vdash r' \leq_{hb} w' \vee P, E \vdash r'$   
 $\leq_{so} w' \vee \neg E \vdash w' \leq_a r'$  **by** *simp*  
**moreover from** *total-onPD* [*OF total-action-order, of*  $w' \ E \ r'$ ]  $\langle w' \in \text{write-actions } E \rangle \ r'$   
**have**  $E \vdash w' \leq_a r' \vee E \vdash r' \leq_a w'$  **by** (*auto dest: read-actions-not-write-actions*)  
**ultimately have**  $E \vdash w' \leq_a ws \ r' \vee E \vdash r' \leq_a w'$  **unfolding** *sync-order-def*  
**by** (*blast intro: happens-before-into-action-order*) **}**  
**ultimately show**  $P, E \vdash r' \rightsquigarrow_{mrw} ws \ r'$  **by** (*rule most-recent-write-for.intros*)  
**qed**  
**then obtain**  $w'$  **where**  $w': w' \in \text{write-actions } E$   
**and**  $adal-w'$ :  $(ad, al) \in \text{action-loc } P \ E \ w'$   
**and**  $\neg P, E \vdash w' \leq_{hb} ws \ r' \neg P, E \vdash r' \leq_{hb} w' \ E \vdash w' \leq_a r'$   
**and so:**  $\neg P, E \vdash w' \leq_{so} ws \ r' \neg P, E \vdash r' \leq_{so} w'$  **by** *blast*  
  
**have**  $ws \ r' \neq w'$  **using**  $\langle \neg P, E \vdash w' \leq_{hb} ws \ r' \rangle \ ws-r$   
**by** (*auto intro: happens-before-refl*)  
  
**have** *vol:*  $\neg \text{is-volatile } P \ al$   
**proof**  
**assume** *vol-al:* *is-volatile*  $P \ al$   
**with**  $r' \ obs-r'$  **have**  $r' \in \text{sactions } P \ E$  **by** *cases* (*rule sactionsI, simp-all*)  
**moreover from**  $w' \ vol-al \ adal-w'$  **have**  $w' \in \text{sactions } P \ E$   
**by** (*cases*) (*auto intro: sactionsI elim!: is-write-action.cases*)  
**ultimately have**  $P, E \vdash w' \leq_{so} r' \vee w' = r' \vee P, E \vdash r' \leq_{so} w'$   
**using** *total-sync-order* [*of*  $P \ E$ ] **by** (*blast dest: total-onPD*)  
**moreover have**  $w' \neq r'$  **using**  $w' \ r'$  **by** (*auto dest: read-actions-not-write-actions*)  
**ultimately have**  $P, E \vdash w' \leq_{so} r'$  **using**  $\langle \neg P, E \vdash r' \leq_{so} w' \rangle$  **by** *simp*  
**moreover from**  $ws-r \ vol-al \ adal$  **have**  $ws \ r' \in \text{sactions } P \ E$   
**by** (*cases*) (*auto intro: sactionsI elim!: is-write-action.cases*)  
**with** *total-sync-order* [*of*  $P \ E$ ]  $\langle w' \in \text{sactions } P \ E \rangle \ \langle \neg P, E \vdash w' \leq_{so} ws \ r' \rangle \ \langle ws \ r' \neq w' \rangle$   
**have**  $P, E \vdash ws \ r' \leq_{so} w'$  **by** (*blast dest: total-onPD*)  
**ultimately show** *False*  
**using** *is-write-seenD* [*OF*  $ws \ r' \ obs-r'$ ]  $w' \ adal-w' \ vol-al \ \langle ws \ r' \neq w' \rangle$  **by** *auto*  
**qed**  
  
**{ fix**  $a$   
**assume**  $a < r'$  **and**  $a \in \text{read-actions } E$   
**hence**  $(\text{action-order } E) \neq a \ r'$  **using**  $r' \ obs-r'$  **by** (*auto intro: action-orderI*)  
**from**  $r'-min$  [*OF this*]  $\langle a \in \text{read-actions } E \rangle$   
**have**  $P, E \vdash a \rightsquigarrow_{mrw} ws \ a$  **by** *simp* **}**

**from**  $\mathcal{E}$ -sequential-completion[ $OF\ E\ wf\ this,\ of\ r'$ ]  $r'$   
**obtain**  $E'\ ws'$  **where**  $E' \in \mathcal{E}\ P \vdash (E', ws') \checkmark$   
**and**  $eq$ :  $ltake\ (enat\ r')\ E = ltake\ (enat\ r')\ E'$   
**and**  $sc'$ : sequentially-consistent  $P\ (E', ws')$   
**and**  $r''$ :  $action-tid\ E\ r' = action-tid\ E'\ r'\ action-obs\ E\ r' \approx action-obs\ E'\ r'$   
**and**  $r' \in actions\ E'$   
**by** *auto*

**from**  $\langle P \vdash (E', ws') \checkmark \rangle$  **have**  $t\text{-}sa\text{-}ok'$ : *thread-start-actions-ok*  $E'$   
**by**(*rule wf-exec-thread-start-actions-okD*)

**from**  $\langle r' \in read\text{-}actions\ E \rangle$  **have**  $enat\ r' < llength\ E$  **by**(*auto elim: read-actions.cases actionsE*)  
**moreover from**  $\langle r' \in actions\ E' \rangle$  **have**  $enat\ r' < llength\ E'$  **by**(*auto elim: actionsE*)  
**ultimately have**  $eq'$ :  $ltake\ (enat\ (Suc\ r'))\ E [\approx] ltake\ (enat\ (Suc\ r'))\ E'$   
**using**  $eq[THEN\ eq\text{-}into\text{-}sim\text{-}actions]$   $r''$   
**by**(*auto simp add: ltake-Suc-conv-snoc-lnth sim-actions-def split-beta action-tid-def action-obs-def*  
*intro!: llist-all2-lappendI*)

**from**  $r'$  **have**  $r''$ :  $r' \in read\text{-}actions\ E'$   
**by**(*rule read-actions-change-prefix[OF -eq']*) *simp*  
**from**  $obs\text{-}r'$  **have**  $(ad, al) \in action\text{-}loc\ P\ E\ r'$  **by** *simp*  
**hence**  $adal\text{-}r''$ :  $(ad, al) \in action\text{-}loc\ P\ E'\ r'$   
**by**(*subst (asm) action-loc-change-prefix[OF eq']*) *simp*

**from**  $\langle \neg P, E \vdash w' \leq_{hb} ws\ r' \rangle$   
**have**  $\neg is\text{-}new\text{-}action\ (action\text{-}obs\ E\ w')$   
**proof**(*rule contrapos-nn*)  
**assume**  $new\text{-}w'$ :  $is\text{-}new\text{-}action\ (action\text{-}obs\ E\ w')$   
**show**  $P, E \vdash w' \leq_{hb} ws\ r'$   
**proof**(*cases is-new-action (action-obs E (ws r'))*)  
**case** *True*  
**with**  $adal\ new\text{-}w'\ adal\text{-}w'\ w'\ ws\text{-}r$   
**have**  $ws\ r' \in new\text{-}actions\text{-}for\ P\ E\ (ad, al)\ w' \in new\text{-}actions\text{-}for\ P\ E\ (ad, al)$   
**by**(*auto simp add: new-actions-for-def*)  
**with**  $\langle E \in \mathcal{E} \rangle$  **have**  $ws\ r' = w'$  **by**(*rule  $\mathcal{E}$ -new-actions-for-fun*)  
**thus**  $?thesis$  **using**  $w'$  **by**(*auto intro: happens-before-refl*)  
**next**  
**case** *False*  
**with**  $t\text{-}sa\text{-}ok\ w'\ ws\text{-}r\ new\text{-}w'$   
**show**  $?thesis$  **by**(*auto intro: happens-before-new-not-new*)  
**qed**  
**qed**

**with**  $\langle E \vdash w' \leq_a r' \rangle$  **have**  $w' \leq r'$  **by**(*auto elim!: action-orderE*)  
**moreover from**  $w'\ r'$  **have**  $w' \neq r'$  **by**(*auto intro: read-actions-not-write-actions*)  
**ultimately have**  $w' < r'$  **by** *simp*  
**with**  $w'$  **have**  $w' \in write\text{-}actions\ E'$   
**by**(*auto intro: write-actions-change-prefix[OF -eq']*)  
**hence**  $w' \in actions\ E'$  **by** *simp*

**from**  $adal\text{-}w'\ \langle w' < r' \rangle$   
**have**  $(ad, al) \in action\text{-}loc\ P\ E'\ w'$   
**by**(*subst action-loc-change-prefix[symmetric, OF eq']*) *simp-all*

**from**  $vol\ \langle r' \in read\text{-}actions\ E' \rangle\ \langle w' \in write\text{-}actions\ E' \rangle\ \langle (ad, al) \in action\text{-}loc\ P\ E'\ w' \rangle\ adal\text{-}r''$

**have**  $P, E' \vdash r' \dagger w'$  **unfolding** *non-volatile-conflict-def* **by** *auto*  
**with**  $\text{sync } \langle E' \in \mathcal{E} \rangle \langle P \vdash (E', \text{ws}') \checkmark \rangle \text{sc}' \langle r' \in \text{actions } E' \rangle \langle w' \in \text{actions } E' \rangle$   
**have**  $\text{hb}'\text{-}r'\text{-}w'$ :  $P, E' \vdash r' \leq_{\text{hb}} w' \vee P, E' \vdash w' \leq_{\text{hb}} r'$   
**by**(*rule correctly-synchronizedD*[*rule-format*])  
**hence**  $P, E \vdash r' \leq_{\text{hb}} w' \vee P, E \vdash w' \leq_{\text{hb}} r'$  **using**  $\langle w' < r' \rangle$   
**by**(*auto intro: happens-before-change-prefix*[*OF - tsa-ok eq'*[*symmetric*]])  
**with**  $\langle \neg P, E \vdash r' \leq_{\text{hb}} w' \rangle$  **have**  $P, E \vdash w' \leq_{\text{hb}} r'$  **by** *simp*

**have**  $P, E \vdash \text{ws } r' \leq_{\text{hb}} w'$   
**proof**(*cases is-new-action* (*action-obs*  $E$  ( $\text{ws } r'$ )))  
**case** *False*  
**with**  $\langle E \vdash \text{ws } r' \leq_a r' \rangle$  **have**  $\text{ws } r' \leq r'$  **by**(*auto elim!*: *action-orderE*)  
**moreover from**  $\text{ws-r } r'$  **have**  $\text{ws } r' \neq r'$  **by**(*auto dest: read-actions-not-write-actions*)  
**ultimately have**  $\text{ws } r' < r'$  **by** *simp*  
**with**  $\text{ws-r}$  **have**  $\text{ws } r' \in \text{write-actions } E'$   
**by**(*auto intro: write-actions-change-prefix*[*OF - eq'*])  
**hence**  $\text{ws } r' \in \text{actions } E'$  **by** *simp*

**from** *adal*  $\langle \text{ws } r' < r' \rangle$   
**have**  $(\text{ad}, \text{al}) \in \text{action-loc } P \ E' \ (\text{ws } r')$   
**by**(*subst action-loc-change-prefix*[*symmetric, OF eq'*]) *simp-all*  
**hence**  $P, E' \vdash \text{ws } r' \dagger w'$   
**using**  $\langle \text{ws } r' \in \text{write-actions } E' \rangle \langle w' \in \text{write-actions } E' \rangle \langle (\text{ad}, \text{al}) \in \text{action-loc } P \ E' \ w' \rangle \text{vol}$   
**unfolding** *non-volatile-conflict-def* **by** *auto*  
**with**  $\text{sync } \langle E' \in \mathcal{E} \rangle \langle P \vdash (E', \text{ws}') \checkmark \rangle \text{sc}' \langle \text{ws } r' \in \text{actions } E' \rangle \langle w' \in \text{actions } E' \rangle$   
**have**  $P, E' \vdash \text{ws } r' \leq_{\text{hb}} w' \vee P, E' \vdash w' \leq_{\text{hb}} \text{ws } r'$   
**by**(*rule correctly-synchronizedD*[*rule-format*])  
**thus**  $P, E \vdash \text{ws } r' \leq_{\text{hb}} w'$  **using**  $\langle w' < r' \rangle \langle \text{ws } r' < r' \rangle \langle \neg P, E \vdash w' \leq_{\text{hb}} \text{ws } r' \rangle$   
**by**(*auto dest: happens-before-change-prefix*[*OF - tsa-ok eq'*[*symmetric*]])

**next**  
**case** *True*  
**with**  $\text{tsa-ok } \text{ws-r } w' \langle \neg \text{is-new-action } (\text{action-obs } E \ w') \rangle$   
**show**  $P, E \vdash \text{ws } r' \leq_{\text{hb}} w'$  **by**(*auto intro: happens-before-new-not-new*)

**qed**  
**moreover**  
**from** *wf* **have** *is-write-seen*  $P \ E \ \text{ws}$  **by**(*rule wf-exec-is-write-seenD*)  
**ultimately have**  $w' = \text{ws } r'$   
**using** *is-write-seenD*[*OF*  $\langle \text{is-write-seen } P \ E \ \text{ws} \rangle \langle r' \in \text{read-actions } E \rangle \text{obs-r}'$ ]  
 $\langle w' \in \text{write-actions } E \rangle \langle (\text{ad}, \text{al}) \in \text{action-loc } P \ E \ w' \rangle \langle P, E \vdash w' \leq_{\text{hb}} r' \rangle$   
**by** *auto*  
**with** *porder-happens-before*[*of*  $E \ P$ ]  $\langle \neg P, E \vdash w' \leq_{\text{hb}} \text{ws } r' \rangle \ \text{ws-r}$  **show** *False*  
**by**(*auto dest: refl-onPD*[**where**  $a = \text{ws } r'$ ] *elim!*: *porder-onE*)

**qed**

**lemma** *justified-action-committedD*:  
**assumes** *justified*:  $P \vdash (E, \text{ws})$  *weakly-justified-by*  $J$   
**and**  $a: a \in \text{actions } E$   
**obtains**  $n \ a'$  **where**  $a = \text{action-translation } (J \ n) \ a' \ a' \in \text{committed } (J \ n)$   
**proof**(*atomize-elim*)  
**from** *justified* **have**  $\text{actions } E = (\bigcup n. \text{action-translation } (J \ n) \ \text{'committed } (J \ n))$   
**by**(*simp add: is-commit-sequence-def*)  
**with**  $a$  **show**  $\exists n \ a'. a = \text{action-translation } (J \ n) \ a' \wedge a' \in \text{committed } (J \ n)$  **by** *auto*

**qed**



**theorem** *drf-weak*:

**assumes** *sync*: *correctly-synchronized*  $P \mathcal{E}$   
**and** *legal*: *weakly-legal-execution*  $P \mathcal{E} (E, ws)$   
**shows** *sequentially-consistent*  $P (E, ws)$   
**using** *legal-wf-execD*[*OF legal*] *legal-ED*[*OF legal*] *sync*  
**proof**(*rule drf-lemma*)  
**fix**  $r$   
**assume**  $r \in \text{read-actions } E$   
  
**from** *legal* **obtain**  $J$  **where**  $E: E \in \mathcal{E}$   
**and** *wf-exec*:  $P \vdash (E, ws) \checkmark$   
**and**  $J: P \vdash (E, ws) \text{ weakly-justified-by } J$   
**and** *range-J*:  $\text{range } (\text{justifying-exec} \circ J) \subseteq \mathcal{E}$   
**by**(*rule legal-executionE*)  
  
**let**  $?E = \lambda n. \text{justifying-exec } (J \ n)$   
**and**  $?ws = \lambda n. \text{justifying-ws } (J \ n)$   
**and**  $?C = \lambda n. \text{committed } (J \ n)$   
**and**  $? \varphi = \lambda n. \text{action-translation } (J \ n)$   
  
**from**  $\langle r \in \text{read-actions } E \rangle$  **have**  $r \in \text{actions } E$  **by** *simp*  
**with**  $J$  **obtain**  $n \ r'$  **where**  $r: r = \text{action-translation } (J \ n) \ r'$   
**and**  $r': r' \in ?C \ n$  **by**(*rule justified-action-committedD*)  
  
**note**  $\langle r \in \text{read-actions } E \rangle$   
**moreover from**  $J$  **have** *wfan*: *wf-action-translation-on*  $(?E \ n) \ E \ (?C \ n) \ (? \varphi \ n)$   
**by**(*simp add: wf-action-translations-def*)  
**hence** *action-obs*  $(?E \ n) \ r' \approx \text{action-obs } E \ r$  **using**  $r'$  **unfolding**  $r$   
**by**(*blast dest: wf-action-translation-on-actionD*)  
**moreover from**  $J \ r'$  **have**  $r' \in \text{actions } (?E \ n)$   
**by**(*auto simp add: committed-subset-actions-def*)  
**ultimately have**  $r' \in \text{read-actions } (?E \ n)$  **unfolding**  $r$   
**by** *cases*(*auto intro: read-actions.intros*)  
**hence**  $P, E \vdash ws \ (? \varphi \ n \ r') \leq_{hb} ? \varphi \ n \ r'$  **using**  $\langle r' \in ?C \ n \rangle$   
**proof**(*induct n arbitrary: r'*)  
**case**  $0$   
**from**  $J$  **have**  $?C \ 0 = \{\}$  **by**(*simp add: is-commit-sequence-def*)  
**with**  $0$  **have** *False* **by** *simp*  
**thus**  $?case \ ..$   
**next**  
**case**  $(\text{Suc } n \ r)$   
**note**  $r = \langle r \in \text{read-actions } (?E \ (\text{Suc } n)) \rangle$   
**from**  $J$  **have** *wfan*: *wf-action-translation-on*  $(?E \ n) \ E \ (?C \ n) \ (? \varphi \ n)$   
**and** *wfaSn*: *wf-action-translation-on*  $(?E \ (\text{Suc } n)) \ E \ (?C \ (\text{Suc } n)) \ (? \varphi \ (\text{Suc } n))$   
**by**(*simp-all add: wf-action-translations-def*)  
  
**from** *wfaSn* **have** *injSn*: *inj-on*  $(? \varphi \ (\text{Suc } n)) \ (\text{actions } (?E \ (\text{Suc } n)))$   
**by**(*rule wf-action-translation-on-inj-onD*)  
**from**  $J$  **have**  $C\text{-sub-}A: ?C \ (\text{Suc } n) \subseteq \text{actions } (?E \ (\text{Suc } n))$   
**by**(*simp add: committed-subset-actions-def*)  
  
**from**  $J$  **have** *wf*:  $P \vdash (?E \ (\text{Suc } n), ?ws \ (\text{Suc } n)) \checkmark$  **by**(*simp add: justification-well-formed-def*)  
**moreover from** *range-J* **have**  $?E \ (\text{Suc } n) \in \mathcal{E}$  **by** *auto*  
**ultimately have** *sc*: *sequentially-consistent*  $P \ (?E \ (\text{Suc } n), ?ws \ (\text{Suc } n))$  **using** *sync*

```

proof(rule drf-lemma)
  fix  $r'$ 
  assume  $r': r' \in \text{read-actions } (?E \text{ (Suc } n))$ 
  hence  $r' \in \text{actions } (?E \text{ (Suc } n))$  by simp

  show  $P, ?E \text{ (Suc } n) \vdash ?ws \text{ (Suc } n) \ r' \leq_{hb} r'$ 
  proof(cases  $? \varphi \text{ (Suc } n) \ r' \in ? \varphi \ n \ \text{' } ?C \ n$ )
    case True
      then obtain  $r''$  where  $r'': r'' \in ?C \ n$ 
        and  $r'-r'': ? \varphi \text{ (Suc } n) \ r' = ? \varphi \ n \ r''$  by(auto)
      from  $r''$  wfan have  $\text{action-tid } (?E \ n) \ r'' = \text{action-tid } E \text{ } (? \varphi \ n \ r'')$ 
        and  $\text{action-obs } (?E \ n) \ r'' \approx \text{action-obs } E \text{ } (? \varphi \ n \ r'')$ 
        by(blast dest: wf-action-translation-on-actionD)+
      moreover from  $J$  have  $? \varphi \ n \ \text{' } ?C \ n \subseteq ? \varphi \text{ (Suc } n) \ \text{' } ?C \text{ (Suc } n)$ 
        by(simp add: is-commit-sequence-def)
      with  $r''$  have  $? \varphi \text{ (Suc } n) \ r' \in ? \varphi \text{ (Suc } n) \ \text{' } ?C \text{ (Suc } n)$ 
        unfolding  $r'-r''$  by auto
      hence  $r' \in ?C \text{ (Suc } n)$ 
        unfolding inj-on-image-mem-iff[OF injSn  $\langle r' \in \text{actions } (?E \text{ (Suc } n)) \rangle \ C\text{-sub-A}$ ] .
      with wfaSn have  $\text{action-tid } (?E \text{ (Suc } n)) \ r' = \text{action-tid } E \text{ } (? \varphi \text{ (Suc } n) \ r')$ 
        and  $\text{action-obs } (?E \text{ (Suc } n)) \ r' \approx \text{action-obs } E \text{ } (? \varphi \text{ (Suc } n) \ r')$ 
        by(blast dest: wf-action-translation-on-actionD)+
      ultimately have  $\text{tid: action-tid } (?E \ n) \ r'' = \text{action-tid } (?E \text{ (Suc } n)) \ r'$ 
        and  $\text{obs: action-obs } (?E \ n) \ r'' \approx \text{action-obs } (?E \text{ (Suc } n)) \ r'$ 
        unfolding  $r'-r''$  by(auto intro: sim-action-trans sim-action-sym)

    from  $J$  have  $?C \ n \subseteq \text{actions } (?E \ n)$  by(simp add: committed-subset-actions-def)
    with  $r''$  have  $r'' \in \text{actions } (?E \ n)$  by blast
    with  $r' \text{ obs}$  have  $r'' \in \text{read-actions } (?E \ n)$ 
      by cases(auto intro: read-actions.intros)
    hence  $hb'': P, E \vdash ws \text{ } (? \varphi \ n \ r'') \leq_{hb} ? \varphi \ n \ r''$ 
      using  $\langle r'' \in ?C \ n \rangle$  by(rule Suc)

    have  $r\text{-conv-inv: } r' = \text{inv-into } (\text{actions } (?E \text{ (Suc } n))) \text{ } (? \varphi \text{ (Suc } n)) \text{ } (? \varphi \ n \ r'')$ 
      using  $\langle r' \in \text{actions } (?E \text{ (Suc } n)) \rangle$  unfolding  $r'-r''$ [symmetric]
      by(simp add: inv-into-f-f[OF injSn])
    with  $\langle r'' \in ?C \ n \rangle \ r' \ J \ \langle r'' \in \text{read-actions } (?E \ n) \rangle$ 
    have  $ws\text{-eq[symmetric]: } ? \varphi \text{ (Suc } n) \text{ } (?ws \text{ (Suc } n) \ r') = ws \text{ } (? \varphi \ n \ r'')$ 
      by(simp add: write-seen-committed-def)
    with  $r'-r''$ [symmetric]  $hb''$  have  $P, E \vdash ? \varphi \text{ (Suc } n) \text{ } (?ws \text{ (Suc } n) \ r') \leq_{hb} ? \varphi \text{ (Suc } n) \ r'$  by
simp

  moreover

    from  $J \ r' \ \langle r' \in \text{committed } (J \text{ (Suc } n)) \rangle$ 
    have  $ws \text{ } (? \varphi \text{ (Suc } n) \ r') \in ? \varphi \text{ (Suc } n) \ \text{' } ?C \text{ (Suc } n)$ 
      by(rule weakly-justified-write-seen-hb-read-committed)
    then obtain  $w'$  where  $w': ws \text{ } (? \varphi \text{ (Suc } n) \ r') = ? \varphi \text{ (Suc } n) \ w'$ 
      and  $\text{committed-}w': w' \in ?C \text{ (Suc } n)$  by blast
    with  $C\text{-sub-A}$  have  $w'\text{-action: } w' \in \text{actions } (?E \text{ (Suc } n))$  by auto

    hence  $w'\text{-def: } w' = \text{inv-into } (\text{actions } (?E \text{ (Suc } n))) \text{ } (? \varphi \text{ (Suc } n)) \text{ } (ws \text{ } (? \varphi \text{ (Suc } n) \ r'))$ 
      using injSn unfolding  $w'$  by simp

```

**from**  $J \ r' \ \langle r' \in \text{committed } (J \ (\text{Suc } n)) \rangle$   
**have**  $\text{hb-eq}: P, E \vdash \text{ws } (? \varphi \ (\text{Suc } n) \ r') \leq_{\text{hb}} ? \varphi \ (\text{Suc } n) \ r' \longleftrightarrow P, ?E \ (\text{Suc } n) \vdash w' \leq_{\text{hb}} r'$   
**unfolding**  $w'\text{-def}$  **by** ( $\text{simp add: happens-before-committed-weak-def}$ )

**from**  $r' \text{ obtain } ad \ al \ v \text{ where } \text{action-obs } (?E \ (\text{Suc } n)) \ r' = \text{NormalAction } (\text{ReadMem } ad \ al \ v)$   
**by** ( $\text{cases}$ )

**from**  $\text{is-write-seenD}[OF \ \text{wf-exec-is-write-seenD}[OF \ \text{wf}] \ r' \ \text{this}]$   
**have**  $?ws \ (\text{Suc } n) \ r' \in \text{actions } (?E \ (\text{Suc } n))$  **by** ( $\text{auto}$ )  
**with**  $\text{injSn}$  **have**  $w' = ?ws \ (\text{Suc } n) \ r'$   
**unfolding**  $w'\text{-def}$   $\text{ws-eq}[\text{folded } r'-r'']$  **by** ( $\text{rule inv-into-f-f}$ )  
**thus**  $?thesis$  **using**  $\text{hb'' hb-eq } w'\text{-action } r'-r''[\text{symmetric}] \ w' \ \text{injSn}$  **by**  $\text{simp}$

**next**  
**case**  $\text{False}$   
**with**  $J \ r'$  **show**  $?thesis$  **by** ( $\text{auto simp add: uncommitted-reads-see-hb-def}$ )  
**qed**  
**qed**

**from**  $r$  **have**  $r \in \text{actions } (?E \ (\text{Suc } n))$  **by**  $\text{simp}$   
**let**  $?w = \text{inv-into } (\text{actions } (?E \ (\text{Suc } n))) \ (? \varphi \ (\text{Suc } n)) \ (\text{ws } (? \varphi \ (\text{Suc } n) \ r))$   
**from**  $J \ r \ \langle r \in ?C \ (\text{Suc } n) \rangle$  **have**  $\text{ws-rE-comm}: \text{ws } (? \varphi \ (\text{Suc } n) \ r) \in ? \varphi \ (\text{Suc } n) \ \text{' } ?C \ (\text{Suc } n)$   
**by** ( $\text{rule weakly-justified-write-seen-hb-read-committed}$ )  
**hence**  $?w \in ?C \ (\text{Suc } n)$  **using**  $C\text{-sub-A}$  **by** ( $\text{auto simp add: inv-into-f-f}[OF \ \text{injSn}]$ )  
**with**  $C\text{-sub-A}$  **have**  $w: ?w \in \text{actions } (?E \ (\text{Suc } n))$  **by**  $\text{blast}$

**from**  $\text{ws-rE-comm } C\text{-sub-A}$  **have**  $w\text{-eq}: ? \varphi \ (\text{Suc } n) \ ?w = \text{ws } (? \varphi \ (\text{Suc } n) \ r)$   
**by** ( $\text{auto simp: f-inv-into-f}[\text{where } f = ? \varphi \ (\text{Suc } n)]$ )  
**from**  $r$  **obtain**  $ad \ al \ v$   
**where**  $\text{obsr}: \text{action-obs } (?E \ (\text{Suc } n)) \ r = \text{NormalAction } (\text{ReadMem } ad \ al \ v)$  **by**  $\text{cases}$   
**hence**  $\text{adal-r}: (ad, al) \in \text{action-loc } P \ (?E \ (\text{Suc } n)) \ r$  **by**  $\text{simp}$   
**from**  $J \ \text{wfaSn} \ \langle r \in ?C \ (\text{Suc } n) \rangle$   
**have**  $\text{obs-sim}: \text{action-obs } (?E \ (\text{Suc } n)) \ r \approx \text{action-obs } E \ (? \varphi \ (\text{Suc } n) \ r) \ ? \varphi \ (\text{Suc } n) \ r \in \text{actions } E$   
**by** ( $\text{auto dest: wf-action-translation-on-actionD simp add: committed-subset-actions-def is-commit-sequence-def}$ )  
**with**  $\text{obsr}$  **have**  $rE: ? \varphi \ (\text{Suc } n) \ r \in \text{read-actions } E$  **by** ( $\text{fastforce intro: read-actions.intros}$ )  
**from**  $\text{obs-sim obsr}$  **obtain**  $v'$   
**where**  $\text{obsrE}: \text{action-obs } E \ (? \varphi \ (\text{Suc } n) \ r) = \text{NormalAction } (\text{ReadMem } ad \ al \ v')$  **by**  $\text{auto}$   
**from**  $\text{wf-exec}$  **have**  $\text{is-write-seen } P \ E \ \text{ws}$  **by** ( $\text{rule wf-exec-is-write-seenD}$ )  
**from**  $\text{is-write-seenD}[OF \ \text{this } rE \ \text{obsrE}]$   
**have**  $\text{ws } (? \varphi \ (\text{Suc } n) \ r) \in \text{write-actions } E$   
**and**  $(ad, al) \in \text{action-loc } P \ E \ (\text{ws } (? \varphi \ (\text{Suc } n) \ r))$   
**and**  $\text{nhb}: \neg P, E \vdash ? \varphi \ (\text{Suc } n) \ r \leq_{\text{hb}} \text{ws } (? \varphi \ (\text{Suc } n) \ r)$   
**and**  $\text{vol}: \text{is-volatile } P \ al \implies \neg P, E \vdash ? \varphi \ (\text{Suc } n) \ r \leq_{\text{so}} \text{ws } (? \varphi \ (\text{Suc } n) \ r)$  **by**  $\text{simp-all}$

**show**  $?case$   
**proof** ( $\text{cases is-volatile } P \ al$ )  
**case**  $\text{False}$

**from**  $\text{wf-action-translation-on-actionD}[OF \ \text{wfaSn} \ \langle ?w \in ?C \ (\text{Suc } n) \rangle]$   
**have**  $\text{action-obs } (?E \ (\text{Suc } n)) \ ?w \approx \text{action-obs } E \ (? \varphi \ (\text{Suc } n) \ ?w)$  **by**  $\text{simp}$   
**with**  $w\text{-eq}$  **have**  $\text{obs-sim-w}: \text{action-obs } (?E \ (\text{Suc } n)) \ ?w \approx \text{action-obs } E \ (\text{ws } (? \varphi \ (\text{Suc } n) \ r))$  **by**  $\text{simp}$

**with**  $\langle \text{ws } (? \varphi \ (\text{Suc } n) \ r) \in \text{write-actions } E \rangle \ \langle ?w \in \text{actions } (?E \ (\text{Suc } n)) \rangle$   
**have**  $?w \in \text{write-actions } (?E \ (\text{Suc } n))$   
**by**  $\text{cases}(\text{fastforce intro: write-actions.intros is-write-action.intros elim!: is-write-action.cases})$   
**from**  $\langle (ad, al) \in \text{action-loc } P \ E \ (\text{ws } (? \varphi \ (\text{Suc } n) \ r)) \rangle \ \text{obs-sim-w}$

```

have (ad, al) ∈ action-loc P (?E (Suc n)) ?w by cases(auto intro: action-loc-aux-intros)
with r adal-r ⟨?w ∈ write-actions (?E (Suc n))⟩ False
have P, ?E (Suc n) ⊢ r † ?w by(auto simp add: non-volatile-conflict-def)
with sc ⟨r ∈ actions (?E (Suc n))⟩ w
have P, ?E (Suc n) ⊢ r ≤hb ?w ∨ P, ?E (Suc n) ⊢ ?w ≤hb r
  by(rule correctly-synchronizedD[rule-format, OF sync ⟨?E (Suc n) ∈ E⟩ wf])
moreover from J r ⟨r ∈ ?C (Suc n)⟩
have P, ?E (Suc n) ⊢ ?w ≤hb r ⟷ P, E ⊢ ws (?φ (Suc n) r) ≤hb ?φ (Suc n) r
  and ¬ P, ?E (Suc n) ⊢ r ≤hb ?w
  by(simp-all add: happens-before-committed-weak-def)
ultimately show ?thesis by auto
next
case True
with rE obsrE have ?φ (Suc n) r ∈ sactions P E by cases (auto intro: sactionsI)
moreover from ⟨ws (?φ (Suc n) r) ∈ write-actions E⟩ ⟨(ad, al) ∈ action-loc P E (ws (?φ (Suc
n) r))⟩ True
have ws (?φ (Suc n) r) ∈ sactions P E by cases(auto intro!: sactionsI elim: is-write-action.cases)
moreover have ?φ (Suc n) r ≠ ws (?φ (Suc n) r)
  using ⟨ws (?φ (Suc n) r) ∈ write-actions E⟩ rE by(auto dest: read-actions-not-write-actions)
ultimately have P, E ⊢ ws (?φ (Suc n) r) ≤so ?φ (Suc n) r
  using total-sync-order[of P E] vol[OF True] by(auto dest: total-onPD)
moreover from ⟨ws (?φ (Suc n) r) ∈ write-actions E⟩ ⟨(ad, al) ∈ action-loc P E (ws (?φ (Suc
n) r))⟩ True
have P ⊢ (action-tid E (ws (?φ (Suc n) r)), action-obs E (ws (?φ (Suc n) r))) ∼sw
  (action-tid E (?φ (Suc n) r), action-obs E (?φ (Suc n) r))
  by cases(fastforce elim!: is-write-action.cases intro: synchronizes-with.intros addr-locsI simp
add: obsrE)
ultimately have P, E ⊢ ws (?φ (Suc n) r) ≤sw ?φ (Suc n) r by(rule sync-withI)
thus ?thesis unfolding po-sw-def by blast
qed
qed
thus P, E ⊢ ws r ≤hb r unfolding r .
qed

```

corollary drf:

```

[[ correctly-synchronized P E; legal-execution P E (E, ws) ]]
⇒ sequentially-consistent P (E, ws)
by(erule drf-weak)(rule legal-imp-weakly-legal-execution)

```

end

end

## 8.6 Sequentially consistent executions are legal

theory SC-Legal imports

JMM-Spec

begin

context executions-base begin

primrec commit-for-sc :: 'm prog ⇒ ('addr, 'thread-id) execution × write-seen ⇒ ('addr, 'thread-id) justification

where

$\text{commit-for-sc } P (E, ws) \ n =$   
 (if  $\text{enat } n \leq \text{llength } E$  then  
   let  $(E', ws') = \text{SOME } (E', ws'). E' \in \mathcal{E} \wedge P \vdash (E', ws') \checkmark \wedge \text{enat } n \leq \text{llength } E' \wedge$   
      $\text{ltake } (\text{enat } (n - 1)) E = \text{ltake } (\text{enat } (n - 1)) E' \wedge$   
      $(n > 0 \longrightarrow \text{action-tid } E' (n - 1) = \text{action-tid } E (n - 1) \wedge$   
        $(\text{if } n - 1 \in \text{read-actions } E \text{ then sim-action else } (=))$   
        $(\text{action-obs } E' (n - 1)) (\text{action-obs } E (n - 1)) \wedge$   
        $(\forall i < n - 1. i \in \text{read-actions } E \longrightarrow ws' i = ws i)) \wedge$   
        $(\forall r \in \text{read-actions } E'. n - 1 \leq r \longrightarrow P, E' \vdash ws' r \leq_{hb} r)$   
   in  $\langle \text{committed} = \{..<n\}, \text{justifying-exec} = E', \text{justifying-ws} = ws', \text{action-translation} = id \rangle$   
 else  $\langle \text{committed} = \text{actions } E, \text{justifying-exec} = E, \text{justifying-ws} = ws, \text{action-translation} = id \rangle$ )

end

context *sc-legal* begin

lemma *commit-for-sc-correct*:

assumes  $E: E \in \mathcal{E}$

and  $wf: P \vdash (E, ws) \checkmark$

and  $sc: \text{sequentially-consistent } P (E, ws)$

shows *wf-action-translation-commit-for-sc*:

$\bigwedge n. \text{wf-action-translation } E (\text{commit-for-sc } P (E, ws) n) (\text{is } \bigwedge n. ?thesis1 n)$

and *commit-for-sc-in- $\mathcal{E}$* :

$\bigwedge n. \text{justifying-exec } (\text{commit-for-sc } P (E, ws) n) \in \mathcal{E} (\text{is } \bigwedge n. ?thesis2 n)$

and *commit-for-sc-wf*:

$\bigwedge n. P \vdash (\text{justifying-exec } (\text{commit-for-sc } P (E, ws) n), \text{justifying-ws } (\text{commit-for-sc } P (E, ws) n))$

$\checkmark$

$(\text{is } \bigwedge n. ?thesis3 n)$

and *commit-for-sc-justification*:

$P \vdash (E, ws) \text{justified-by } \text{commit-for-sc } P (E, ws) (\text{is } ?thesis4)$

proof –

let  $? \varphi = \text{commit-for-sc } P (E, ws)$

note  $[simp] = \text{split-beta}$

from  $wf$  have  $tsok: \text{thread-start-actions-ok } E$  by *simp*

let  $?P = \lambda n (E', ws'). E' \in \mathcal{E} \wedge P \vdash (E', ws') \checkmark \wedge (\text{enat } n \leq \text{llength } E \longrightarrow \text{enat } n \leq \text{llength } E') \wedge$   
    $\text{ltake } (\text{enat } (n - 1)) E = \text{ltake } (\text{enat } (n - 1)) E' \wedge$   
    $(n > 0 \longrightarrow \text{action-tid } E' (n - 1) = \text{action-tid } E (n - 1) \wedge$   
      $(\text{if } n - 1 \in \text{read-actions } E \text{ then sim-action else } (=))$   
      $(\text{action-obs } E' (n - 1)) (\text{action-obs } E (n - 1)) \wedge$   
      $(\forall i < n - 1. i \in \text{read-actions } E \longrightarrow ws' i = ws i)) \wedge$   
      $(\forall r \in \text{read-actions } E'. n - 1 \leq r \longrightarrow P, E' \vdash ws' r \leq_{hb} r)$

define  $E' \ ws'$  where  $E' \ n = \text{fst } (Eps \ (?P \ n))$  and  $ws' \ n = \text{snd } (Eps \ (?P \ n))$  for  $n$

hence  $[simp]$ :

$\bigwedge n. \text{commit-for-sc } P (E, ws) \ n =$

(if  $\text{enat } n \leq \text{llength } E$

  then  $\langle \text{committed} = \{..<n\}, \text{justifying-exec} = E' \ n, \text{justifying-ws} = ws' \ n, \text{action-translation} = id \rangle$

  else  $\langle \text{committed} = \text{actions } E, \text{justifying-exec} = E, \text{justifying-ws} = ws, \text{action-translation} = id \rangle$ )

by *simp*

note  $[simp \ del] = \text{commit-for-sc.simps}$

have  $(\forall n. ?thesis1 \ n) \wedge (\forall n. ?thesis2 \ n) \wedge (\forall n. ?thesis3 \ n) \wedge ?thesis4$

**unfolding** *is-justified-by.simps is-commit-sequence-def justification-well-formed-def committed-subset-actions-def happens-before-committed-def sync-order-committed-def value-written-committed-def uncommitted-reads-see-hb-def committed-reads-see-committed-writes-def external-actions-committed-def wf-action-translations-def write-seen-committed-def*  
**proof**(*intro conjI strip LetI*)

**show** *committed* ( $? \varphi \ 0$ ) = {}  
**by**(*auto simp add: actions-def zero-enat-def[symmetric]*)  
**show** *actions-E*: *actions*  $E = (\bigcup n. \text{action-translation } (? \varphi \ n) \text{ ' committed } (? \varphi \ n))$   
**by**(*auto simp add: actions-def less-le-trans[where y=enat n for n] split: if-split-asm*)  
**hence** *committed-subset-E*:  $\bigwedge n. \text{action-translation } (? \varphi \ n) \text{ ' committed } (? \varphi \ n) \subseteq \text{actions } E$  **by** *fastforce*

**{ fix**  $n$   
**have**  $?P \ n \ (Eps \ (?P \ n))$   
**proof**(*cases n*)  
**case**  $0$   
**from**  $\mathcal{E}\text{-hb-completion}[OF \ E \ wf, \ of \ 0]$  **have**  $\exists Ews. ?P \ 0 \ Ews$   
**by**(*fastforce simp add: zero-enat-def[symmetric]*)  
**thus** *?thesis* **unfolding**  $0$  **by**(*rule someI-ex*)  
**next**  
**case** ( $Suc \ n'$ )  
**moreover**  
**from** *sc* **have**  $sc': \bigwedge a. \llbracket a < n'; a \in \text{read-actions } E \rrbracket \implies P, E \vdash a \rightsquigarrow_{mrw} ws \ a$   
**by**(*simp add: sequentially-consistent-def*)  
**from**  $\mathcal{E}\text{-hb-completion}[OF \ E \ wf \ this, \ of \ n']$   
**obtain**  $E' \ ws'$  **where**  $E' \in \mathcal{E}$  **and**  $P \vdash (E', \ ws')$   $\checkmark$   
**and** *eq*:  $ltake \ (enat \ n') \ E = ltake \ (enat \ n') \ E'$   
**and** *hb*:  $\forall a \in \text{read-actions } E'. \text{ if } a < n' \text{ then } ws' \ a = ws \ a \text{ else } P, E' \vdash ws' \ a \leq_{hb} a$   
**and** *n-sim*:  $\text{action-tid } E' \ n' = \text{action-tid } E \ n'$   
*(if*  $n' \in \text{read-actions } E$  *then* *sim-action* *else*  $(=)$  *)*  $(\text{action-obs } E' \ n') \ (\text{action-obs } E \ n')$   
**and**  $n: n' \in \text{actions } E \implies n' \in \text{actions } E'$  **by** *blast*  
**moreover** {  
**assume**  $enat \ n \leq llength \ E$   
**with**  $n \ Suc$  **have**  $enat \ n \leq llength \ E'$   
**by**(*simp add: actions-def Suc-ile-eq*) }  
**moreover** {  
**fix**  $i$   
**assume**  $i \in \text{read-actions } E$   
**moreover from** *eq* **have**  $ltake \ (enat \ n') \ E \ [\approx] \ ltake \ (enat \ n') \ E'$   
**by**(*rule eq-into-sim-actions*)  
**moreover assume**  $i < n'$   
**hence**  $enat \ i < enat \ n'$  **by** *simp*  
**ultimately have**  $i \in \text{read-actions } E'$  **by**(*rule read-actions-change-prefix*)  
**with** *hb*[*rule-format, OF this*]  $\langle i < n' \rangle$   
**have**  $ws' \ i = ws \ i$  **by** *simp* }  
**ultimately have**  $?P \ n \ (E', \ ws')$  **by** *simp*  
**thus** *?thesis* **by**(*rule someI*)  
**qed** }  
**hence**  $P \ [simplified]: \bigwedge n. ?P \ n \ (E' \ n, \ ws' \ n)$  **by**(*simp add: E'-def ws'-def*)  
**{ fix**  $n$   
**assume**  $n\text{-E}: enat \ n \leq llength \ E$   
**have**  $ltake \ (enat \ n) \ (E' \ n) \ [\approx] \ ltake \ (enat \ n) \ E$  **unfolding** *sim-actions-def*

```

proof(rule llist-all2-all-lnthI)
  show llength (ltake (enat n) (E' n)) = llength (ltake (enat n) E)
    using n-E P[of n] by(clarsimp simp add: min-def)
next
  fix n'
  assume n': enat n' < llength (ltake (enat n) (E' n))
  show ( $\lambda(t, a) (t', a'). t = t' \wedge a \approx a'$ ) (lnth (ltake (enat n) (E' n)) n') (lnth (ltake (enat n)
E) n')
  proof(cases n = Suc n')
    case True
    with P[of n] show ?thesis
    by(simp add: action-tid-def action-obs-def lnth-ltake split: if-split-asm)
  next
    case False
    with n' have n' < n - 1 by auto
    moreover from P[of n] have lnth (ltake (enat (n - 1)) (E' n)) n' = lnth (ltake (enat (n -
1)) E) n' by simp
    ultimately show ?thesis by(simp add: lnth-ltake)
  qed
qed }
note sim = this
note len-eq = llist-all2-llengthD[OF this[unfolded sim-actions-def]]

{ fix n
  show wf-action-translation E (? $\varphi$  n)
  proof(cases enat n  $\leq$  llength E)
    case False thus ?thesis by(simp add: wf-action-translation-on-def)
  next
    case True
    hence {.. $n$ }  $\subseteq$  actions E
    by(auto simp add: actions-def min-def less-le-trans[where y=enat n] split: if-split-asm)
    moreover
    from True len-eq[OF True] have {.. $n$ }  $\subseteq$  actions (E' n)
    by(auto simp add: actions-def min-def less-le-trans[where y=enat n] split: if-split-asm)
    moreover {
      fix a assume a < n
      moreover from sim[OF True]
      have action-tid (ltake (enat n) (E' n)) a = action-tid (ltake (enat n) E) a
        action-obs (ltake (enat n) (E' n)) a  $\approx$  action-obs (ltake (enat n) E) a
        by(rule sim-actions-action-tidD sim-actions-action-obsD)+
      ultimately have action-tid (E' n) a = action-tid E a action-obs (E' n) a  $\approx$  action-obs E a
        by(simp-all add: action-tid-def action-obs-def lnth-ltake) }
    ultimately show ?thesis by(auto simp add: wf-action-translation-on-def del: subsetI)
  qed
  thus wf-action-translation E (? $\varphi$  n) . }
note wfa = this

{ fix n from P E show justifying-exec (? $\varphi$  n)  $\in$   $\mathcal{E}$ 
  by(cases enat n  $\leq$  llength E) simp-all }
note En = this

{ fix n from P wf show P  $\vdash$  (justifying-exec (? $\varphi$  n), justifying-ws (? $\varphi$  n))  $\checkmark$ 
  by(cases enat n  $\leq$  llength E) simp-all
  thus P  $\vdash$  (justifying-exec (? $\varphi$  n), justifying-ws (? $\varphi$  n))  $\checkmark$  . }

```

**note** *wfn* = *this*

```
{ fix n show action-translation (?φ n) ‘ committed (?φ n) ⊆ action-translation (?φ (Suc n)) ‘
committed (?φ (Suc n))
  by(auto simp add: actions-def less-le-trans[where y=enat n]) (metis Suc-ile-eq order-less-imp-le)
}
```

**note** *committed-subset* = *this*

```
{ fix n
  from len-eq[of n] have enat n ≤ llength E ⇒ {..n} ⊆ actions (E' n)
  by(auto simp add: E'-def actions-def min-def less-le-trans[where y=enat n] split: if-split-asm)
  thus committed (?φ n) ⊆ actions (justifying-exec (?φ n))
  by(simp add: actions-def E'-def) }
```

**note** *committed-actions* = *this*

**fix** *n*

**show** *happens-before* *P* (*justifying-exec* (?φ *n*)) | ‘ *committed* (?φ *n*) =  
*inv-image**P* (*happens-before* *P* *E*) (*action-translation* (?φ *n*)) | ‘ *committed* (?φ *n*)

**proof**(*cases* *enat n* ≤ *llength E*)

*case False* **thus** ?*thesis* **by** *simp*

**next**

*case True* **thus** ?*thesis*

**proof**(*safe intro!*: *ext*)

**fix** *a b*

**assume** *hb*: *P,justifying-exec* (?φ *n*) ⊢ *a* ≤*hb* *b*

**and** *a*: *a* ∈ *committed* (?φ *n*)

**and** *b*: *b* ∈ *committed* (?φ *n*)

**from** *hb True* **have** *P,E' n* ⊢ *a* ≤*hb* *b* **by**(*simp* *add*: *E'-def*)

**moreover** **note** *tsok sim[OF True]*

**moreover** **from** *a b True*

**have** *enat a* < *enat n* *enat b* < *enat n* **by** *simp-all*

**ultimately** **have** *P,E* ⊢ *a* ≤*hb* *b* **by**(*rule happens-before-change-prefix*)

**thus** *P,E* ⊢ *action-translation* (?φ *n*) *a* ≤*hb* *action-translation* (?φ *n*) *b* **by** *simp*

**next**

**fix** *a b*

**assume** *a*: *a* ∈ *committed* (?φ *n*)

**and** *b*: *b* ∈ *committed* (?φ *n*)

**and** *hb*: *P,E* ⊢ *action-translation* (?φ *n*) *a* ≤*hb* *action-translation* (?φ *n*) *b*

**from** *hb True* **have** *P,E* ⊢ *a* ≤*hb* *b* **by** *simp*

**moreover** **from** *wfn[of n] True* **have** *thread-start-actions-ok* (*E' n*) **by**(*simp*)

**moreover** **from** *sim[OF True]* **have** *ltake* (*enat n*) *E* [≈] *ltake* (*enat n*) (*E' n*)

**by**(*rule sim-actions-sym*)

**moreover** **from** *a b True* **have** *enat a* < *enat n* *enat b* < *enat n* **by** *simp-all*

**ultimately** **have** *P,E' n* ⊢ *a* ≤*hb* *b* **by**(*rule happens-before-change-prefix*)

**thus** *P,justifying-exec* (?φ *n*) ⊢ *a* ≤*hb* *b* **using** *True* **by**(*simp* *add*: *E'-def*)

**qed**

**qed**

**show** *sync-order* *P* (*justifying-exec* (?φ *n*)) | ‘ *committed* (?φ *n*) =

*inv-image**P* (*sync-order* *P* *E*) (*action-translation* (?φ *n*)) | ‘ *committed* (?φ *n*)

**proof**(*cases* *enat n* ≤ *llength E*)

*case False* **thus** ?*thesis* **by** *simp*

**next**

*case True* **thus** ?*thesis*



```

proof(safe intro!: ext)
  fix a b
  assume hb:  $P, \text{justifying-exec } (? \varphi \ n) \vdash a \leq_{so} b$ 
    and a:  $a \in \text{committed } (? \varphi \ n)$ 
    and b:  $b \in \text{committed } (? \varphi \ n)$ 
  from hb True have  $P, E' \ n \vdash a \leq_{so} b$  by(simp add: E'-def)
  moreover note sim[OF True]
  moreover from a b True
  have enat a < enat n enat b < enat n by simp-all
  ultimately have  $P, E \vdash a \leq_{so} b$  by(rule sync-order-change-prefix)
  thus  $P, E \vdash \text{action-translation } (? \varphi \ n) \ a \leq_{so} \text{action-translation } (? \varphi \ n) \ b$  by simp
next
  fix a b
  assume a:  $a \in \text{committed } (? \varphi \ n)$ 
    and b:  $b \in \text{committed } (? \varphi \ n)$ 
    and hb:  $P, E \vdash \text{action-translation } (? \varphi \ n) \ a \leq_{so} \text{action-translation } (? \varphi \ n) \ b$ 
  from hb True have  $P, E \vdash a \leq_{so} b$  by simp
  moreover from sim[OF True] have ltake (enat n) E  $\approx$  ltake (enat n) (E' n)
    by(rule sim-actions-sym)
  moreover from a b True have enat a < enat n enat b < enat n by simp-all
  ultimately have  $P, E' \ n \vdash a \leq_{so} b$  by(rule sync-order-change-prefix)
  thus  $P, \text{justifying-exec } (? \varphi \ n) \vdash a \leq_{so} b$  using True by(simp add: E'-def)
qed
qed

{ fix w w' adal
  assume w:  $w \in \text{write-actions } (\text{justifying-exec } (? \varphi \ n)) \cap \text{committed } (? \varphi \ n)$ 
    and w':  $w' = \text{action-translation } (? \varphi \ n) \ w$ 
    and adal:  $\text{adal} \in \text{action-loc } P \ E \ w'$ 
  show value-written P (justifying-exec (?  $\varphi$  n)) w adal = value-written P E w' adal
  proof(cases enat n  $\leq$  llength E)
    case False thus ?thesis using w' by simp
  next
    case True
    note n-E = this
    have action-obs E w = action-obs (E' n) w
    proof(cases w < n - 1)
      case True
      with P[of n] w' n-E show ?thesis
      by(clarsimp simp add: action-obs-change-prefix-eq)
    next
      case False
      with w True have w = n - 1 n > 0 by auto
      moreover
      with True have w  $\in$  actions E
      by(simp add: actions-def)(metis Suc-ile-eq Suc-pred)
      with True w wf-action-translation-on-actionD[OF wfa, of w n] w'
      have w'  $\in$  write-actions E
      by(auto intro!: write-actions.intros elim!: write-actions.cases is-write-action.cases)
      hence w'  $\notin$  read-actions E by(blast dest: read-actions-not-write-actions)
      ultimately show ?thesis using P[of n] w' True by clarsimp
    qed
    with True w' show ?thesis by(cases adal)(simp add: value-written.simps)
  qed }

```

```

{ fix r' r r''
  assume r': r' ∈ read-actions (justifying-exec (commit-for-sc P (E, ws) n)) ∩ committed (?φ n)
  and r: r = action-translation (?φ n) r'
  and r'': r'' = inv-into (actions (justifying-exec (?φ (Suc n)))) (action-translation (?φ (Suc n)))
r
  from r' r committed-subset[of n] have r ∈ actions E
  by(auto split: if-split-asm elim!: read-actions.cases simp add: actions-def Suc-ile-eq less-trans[where
y=enat n])
  with r' r have r-actions: r ∈ read-actions E
  by(fastforce dest: wf-action-translation-on-actionD[OF wfa] split: if-split-asm elim!: read-actions.cases
intro: read-actions.intros)
  moreover from r' committed-subset[of n] committed-actions[of Suc n]
  have r' ∈ actions (justifying-exec (?φ (Suc n))) by(auto split: if-split-asm elim: read-actions.cases)
  ultimately have r'' = r' using r' r r'' by(cases enat (Suc n) ≤ llength E) simp-all
  moreover from r' have r' < n
  by(simp add: actions-def split: if-split-asm)(metis enat-ord-code(2) linorder-linear order-less-le-trans)
  ultimately show action-translation (?φ (Suc n)) (justifying-ws (?φ (Suc n)) r'') = ws r
  using P[of Suc n] r' r r-actions by(clarsimp split: if-split-asm) }

{ fix r'
  assume r': r' ∈ read-actions (justifying-exec (?φ (Suc n)))
  show action-translation (?φ (Suc n)) r' ∈ action-translation (?φ n) ' committed (?φ n) ∨
    P.justifying-exec (?φ (Suc n)) ⊢ justifying-ws (?φ (Suc n)) r' ≤hb r' (is ?committed ∨ ?hb)
  proof(cases r' < n)
    case True
    hence ?committed using r'
    by(auto elim!: actionsE split: if-split-asm dest!: read-actions-actions)(metis Suc-ile-eq linorder-not-le
not-less-iff-gr-or-eq)
    thus ?thesis ..
  next
    case False
    hence r' ≥ n by simp
    hence enat (Suc n) ≤ llength E using False r'
    by(auto split: if-split-asm dest!: read-actions-actions elim!: actionsE) (metis Suc-ile-eq
enat-ord-code(2) not-le-imp-less order-less-le-trans)
    hence ?hb using P[of Suc n] r' ⟨r' ≥ n⟩ by simp
    thus ?thesis ..
  qed }

{ fix r' r C-n
  assume r': r' ∈ read-actions (justifying-exec (?φ (Suc n))) ∩ committed (?φ (Suc n))
  and r: r = action-translation (?φ (Suc n)) r'
  and C-n: C-n = action-translation (?φ n) ' committed (?φ n)
  show r ∈ C-n ∨ action-translation (?φ (Suc n)) (justifying-ws (?φ (Suc n)) r') ∈ C-n ∧ ws r ∈
C-n
    (is - ∨ (?C-ws-n ∧ ?C-ws))
  proof(cases r ∈ C-n)
    case True thus ?thesis ..
  next
    case False
    with r' r C-n have [simp]: r' = n
    apply(auto split: if-split-asm dest!: read-actions-actions elim!: actionsE)
    apply(metis enat-ord-code(1) less-SucI less-eq-Suc-le not-less-eq-eq order-trans)

```

```

    by (metis Suc-ile-eq enat-ord-code(1) leD leI linorder-cases)
  from  $r'$  have  $\text{len-}E$ :  $\text{enat } (\text{Suc } n) \leq \text{llength } E$ 
    by (clarsimp simp add: actions-def Suc-ile-eq split: if-split-asm)
  with  $r'$   $P[\text{of } \text{Suc } n]$  have  $P, \text{justifying-exec } (? \varphi (\text{Suc } n)) \vdash \text{ws}' (\text{Suc } n) r' \leq_{hb} r'$  by (simp)
  hence  $\text{justifying-exec } (? \varphi (\text{Suc } n)) \vdash \text{ws}' (\text{Suc } n) r' \leq_a r'$  by (rule happens-before-into-action-order)
  moreover from  $r'$  have  $r' \in \text{read-actions } (\text{justifying-exec } (? \varphi (\text{Suc } n)))$  by (simp)
  moreover then obtain  $\text{ad al } v$  where  $\text{action-obs } (\text{justifying-exec } (? \varphi (\text{Suc } n))) r' = \text{NormalAction } (\text{ReadMem ad al } v)$ 
    by cases auto
  with  $\text{wfn}[\text{of } \text{Suc } n] \langle r' \in \text{read-actions} \rightarrow \text{len-}E$  obtain  $\text{adal}$ 
    where  $\text{ws}' (\text{Suc } n) r' \in \text{write-actions } (\text{justifying-exec } (? \varphi (\text{Suc } n)))$ 
    and  $\text{adal} \in \text{action-loc } P (\text{justifying-exec } (? \varphi (\text{Suc } n))) r'$ 
    and  $\text{adal} \in \text{action-loc } P (\text{justifying-exec } (? \varphi (\text{Suc } n))) (\text{ws}' (\text{Suc } n) r')$ 
    by (clarsimp)(auto dest: is-write-seenD)
  moreover {
    from  $\text{En}[\text{of } \text{Suc } n] \text{len-}E$  have  $E' (\text{Suc } n) \in \mathcal{E}$  by (simp)
    moreover
    fix  $a$  assume  $a \in \text{read-actions } (\text{justifying-exec } (? \varphi (\text{Suc } n)))$  and  $a < r'$ 
    hence  $a \in \text{read-actions } (E' (\text{Suc } n)) \text{enat } a < \text{enat } (\text{Suc } n)$  using  $\text{len-}E$  by (simp-all)
    with  $\text{sim}[\text{OF } \text{len-}E]$  have  $a: a \in \text{read-actions } E$  by  $\neg(\text{rule read-actions-change-prefix})$ 
    with  $\langle a < r' \rangle$  have  $\text{mrw}: P, E \vdash a \rightsquigarrow_{\text{mrw}} \text{ws } a$  using  $\text{sc}$  by (simp add: sequentially-consistent-def)
    from  $P[\text{of } \text{Suc } n] \langle a < r' \rangle a \text{len-}E$  have  $\text{ws } a = \text{ws}' (\text{Suc } n) a$  by (simp)
    with  $\text{mrw}$  have  $\text{mrw}': P, E \vdash a \rightsquigarrow_{\text{mrw}} \text{ws}' (\text{Suc } n) a$  by (simp)
    moreover from  $\text{wfn}[\text{of } \text{Suc } n] \text{wf len-}E$  have  $\text{thread-start-actions-ok } (E' (\text{Suc } n))$  by (simp)
    moreover note  $\text{sim}[\text{OF } \text{len-}E, \text{symmetric}]$ 
    moreover from  $E \text{wf mrw}'$  have  $\text{ws}' (\text{Suc } n) a < a$ 
      by (rule mrw-before)(erule sequentially-consistentE[OF sc])
    with  $\langle a < r' \rangle$  have  $\text{ws}' (\text{Suc } n) a < r'$  by (simp)
    ultimately have  $P, E' (\text{Suc } n) \vdash a \rightsquigarrow_{\text{mrw}} \text{ws}' (\text{Suc } n) a$  using  $\langle a < r' \rangle$ 
      by  $\neg(\text{rule mrw-change-prefix, simp+})$ 
    hence  $P, \text{justifying-exec } (? \varphi (\text{Suc } n)) \vdash a \rightsquigarrow_{\text{mrw}} \text{justifying-ws } (? \varphi (\text{Suc } n)) a$  using  $\text{len-}E$  by
    simp
  }
  ultimately have  $\text{ws}' (\text{Suc } n) r' < r'$  by (rule action-order-read-before-write[OF En wfn])
  with  $\text{len-}E \text{C-n}$  have  $?C\text{-ws-n}$  by (clarsimp (metis Suc-ile-eq linorder-le-cases order-less-irrefl
order-trans))
  moreover
  from  $r'$  have  $r' \in \text{committed } (? \varphi (\text{Suc } n))$  by (blast)
  with  $r' r \text{len-}E \text{wf-action-translation-on-actionD}[\text{OF wfa this}] \text{committed-subset-}E[\text{of } \text{Suc } n]$ 
  have  $r \in \text{read-actions } E$  by (fastforce elim!: read-actions.cases intro: read-actions.intros split:
if-split-asm)
  with  $\text{sc}$  obtain  $P, E \vdash r \rightsquigarrow_{\text{mrw}} \text{ws } r$  by (rule sequentially-consistentE)
  with  $E \text{wf}$  have  $\text{ws } r < r$  by (rule mrw-before)(rule sequentially-consistentE[OF sc])
  with  $\text{C-n len-}E r$  have  $?C\text{-ws}$  by (auto simp add: Suc-ile-eq)
  ultimately show  $?thesis$  by (simp)
qed }

{ fix  $a a'$ 
  assume  $a: a \in \text{external-actions } (\text{justifying-exec } (? \varphi n))$ 
    and  $a': a' \in \text{committed } (? \varphi n)$ 
    and  $hb: P, \text{justifying-exec } (? \varphi n) \vdash a \leq_{hb} a'$ 
  from  $hb$  have  $\text{justifying-exec } (? \varphi n) \vdash a \leq_a a'$ 
    by (rule happens-before-into-action-order)
  with  $a$  have  $a \leq a'$  by (auto elim!: action-orderE dest: external-actions-not-new)

```

```

    with a' a show a ∈ committed (?φ n) by(auto elim: external-actions.cases) }
qed
thus  $\bigwedge n. ?thesis1\ n \bigwedge n. ?thesis2\ n \bigwedge n. ?thesis3\ n\ ?thesis4$ 
  by blast+
qed

theorem SC-is-legal:
  assumes E: E ∈  $\mathcal{E}$ 
  and wf: P ⊢ (E, ws) ✓
  and sc: sequentially-consistent P (E, ws)
  shows legal-execution P  $\mathcal{E}$  (E, ws)
using E wf
apply(rule legal-executionI)
  apply(rule commit-for-sc-correct[OF assms])
apply clarify
apply(unfold o-apply)
apply(rule commit-for-sc-in- $\mathcal{E}$ [OF assms])
done

end

context jmm-consistent begin

theorem consistent:
  assumes E ∈  $\mathcal{E}$  P ⊢ (E, ws) ✓
  shows  $\exists E \in \mathcal{E}. \exists ws. \text{legal-execution } P\ \mathcal{E}\ (E, ws)$ 
proof -
  from  $\mathcal{E}$ -sequential-completion[OF assms, of 0]
  obtain E' ws' where E' ∈  $\mathcal{E}$  P ⊢ (E', ws') ✓ sequentially-consistent P (E', ws') by auto
  moreover hence legal-execution P  $\mathcal{E}$  (E', ws') by(rule SC-is-legal)
  ultimately show ?thesis by blast
qed

end

end

```

## 8.7 Non-speculative prefixes of executions

theory Non-Speculative imports

JMM-Spec

../Framework/FWLTS

begin

declare addr-locsI [simp]

### 8.7.1 Previously written values

fun w-value ::

'm prog  $\Rightarrow$  (('addr × addr-loc)  $\Rightarrow$  'addr val set)  $\Rightarrow$  ('addr, 'thread-id) obs-event action  
 $\Rightarrow$  (('addr × addr-loc)  $\Rightarrow$  'addr val set)

where

w-value P vs (NormalAction (WriteMem ad al v)) = vs((ad, al) := insert v (vs (ad, al)))  
 | w-value P vs (NormalAction (NewHeapElem ad hT)) =

$(\lambda(ad', al). \text{ if } ad = ad' \wedge al \in \text{addr-locs } P \ hT$   
 $\quad \text{ then insert } (\text{addr-loc-default } P \ hT \ al) \ (vs \ (ad, al))$   
 $\quad \text{ else } vs \ (ad', al))$   
 $| \ w\text{-value } P \ vs \ - = vs$

**lemma** *w-value-cases*:

**obtains**  $ad \ al \ v$  **where**  $x = \text{NormalAction } (\text{WriteMem } ad \ al \ v)$   
 $| \ ad \ hT$  **where**  $x = \text{NormalAction } (\text{NewHeapElem } ad \ hT)$   
 $| \ ad \ M \ vs \ v$  **where**  $x = \text{NormalAction } (\text{ExternalCall } ad \ M \ vs \ v)$   
 $| \ ad \ al \ v$  **where**  $x = \text{NormalAction } (\text{ReadMem } ad \ al \ v)$   
 $| \ t$  **where**  $x = \text{NormalAction } (\text{ThreadStart } t)$   
 $| \ t$  **where**  $x = \text{NormalAction } (\text{ThreadJoin } t)$   
 $| \ ad$  **where**  $x = \text{NormalAction } (\text{SyncLock } ad)$   
 $| \ ad$  **where**  $x = \text{NormalAction } (\text{SyncUnlock } ad)$   
 $| \ t$  **where**  $x = \text{NormalAction } (\text{ObsInterrupt } t)$   
 $| \ t$  **where**  $x = \text{NormalAction } (\text{ObsInterrupted } t)$   
 $| \ x = \text{InitialThreadAction}$   
 $| \ x = \text{ThreadFinishAction}$

**by** *pat-completeness*

**abbreviation** *w-values* ::

$'m \text{ prog} \Rightarrow ((\text{'addr} \times \text{addr-loc}) \Rightarrow \text{'addr val set}) \Rightarrow (\text{'addr}, \text{'thread-id}) \text{ obs-event action list}$   
 $\Rightarrow ((\text{'addr} \times \text{addr-loc}) \Rightarrow \text{'addr val set})$

**where** *w-values*  $P \equiv \text{foldl } (w\text{-value } P)$

**lemma** *in-w-valuesD*:

**assumes**  $w: v \in w\text{-values } P \ vs0 \ obs \ (ad, al)$   
**and**  $v: v \notin vs0 \ (ad, al)$   
**shows**  $\exists obs' \ wa \ obs''. \ obs = obs' @ wa \ \# \ obs'' \wedge \text{is-write-action } wa \wedge (ad, al) \in \text{action-loc-aux } P$   
 $wa \wedge$

$\text{value-written-aux } P \ wa \ al = v$

**(is ?concl obs)**

**using**  $w$

**proof**(*induction obs rule: rev-induct*)

**case Nil thus ?case using v by simp**

**next**

**case** (*snoc ob obs*)

**from** *snoc.IH* **show** *?case*

**proof**(*cases v ∈ w-values P vs0 obs (ad, al)*)

**case False thus ?thesis using**  $\langle v \in w\text{-values } P \ vs0 \ (obs @ [ob]) \ (ad, al) \rangle$

**by**(*cases ob rule: w-value-cases*)(*auto 4 4 intro: action-loc-aux-intros split: if-split-asm simp add:*

*addr-locs-def split: htype.split-asm*)

**qed fastforce**

**qed**

**lemma** *w-values-WriteMemD*:

**assumes**  $\text{NormalAction } (\text{WriteMem } ad \ al \ v) \in \text{set } obs$

**shows**  $v \in w\text{-values } P \ vs0 \ obs \ (ad, al)$

**using** *assms*

**apply**(*induct obs rule: rev-induct*)

**apply** *simp*

**apply** *clarsimp*

**apply**(*erule disjE*)

**apply** *clarsimp*

```

apply clarsimp
apply(case-tac x rule: w-value-cases)
apply auto
done

```

```

lemma w-values-new-actionD:
  assumes NormalAction (NewHeapElem ad hT)  $\in$  set obs (ad, al)  $\in$  action-loc-aux P (NormalAction
(NewHeapElem ad hT))
  shows addr-loc-default P hT al  $\in$  w-values P vs0 obs (ad, al)
using assms
apply(induct obs rule: rev-induct)
  apply simp
apply clarsimp
apply(rename-tac w' obs)
apply(case-tac w' rule: w-value-cases)
apply(auto simp add: split-beta)
done

```

```

lemma w-value-mono: vs0 adal  $\subseteq$  w-value P vs0 ob adal
by(cases ob rule: w-value-cases)(auto split: if-split-asm simp add: split-beta)

```

```

lemma w-values-mono: vs0 adal  $\subseteq$  w-values P vs0 obs adal
by(induct obs rule: rev-induct)(auto del: subsetI intro: w-value-mono subset-trans)

```

```

lemma w-value-greater: vs0  $\leq$  w-value P vs0 ob
by(rule le-funI)(rule w-value-mono)

```

```

lemma w-values-greater: vs0  $\leq$  w-values P vs0 obs
by(rule le-funI)(rule w-values-mono)

```

```

lemma w-values-eq-emptyD:
  assumes w-values P vs0 obs adal = {}
  and w  $\in$  set obs and is-write-action w and adal  $\in$  action-loc-aux P w
  shows False
using assms(4) assms(1-3)
apply(cases rule: action-loc-aux-cases)
apply(auto dest!: w-values-new-actionD[where ?vs0.0=vs0 and P=P] w-values-WriteMemD[where
?vs0.0=vs0 and P=P])
apply blast
done

```

### 8.7.2 Coinductive version of non-speculative prefixes

```

coinductive non-speculative ::
  'm prog  $\Rightarrow$  ('addr  $\times$  addr-loc  $\Rightarrow$  'addr val set)  $\Rightarrow$  ('addr, 'thread-id) obs-event action llist  $\Rightarrow$  bool
for P :: 'm prog
where
  LNil: non-speculative P vs LNil
  | LCons:
    [ case ob of NormalAction (ReadMem ad al v)  $\Rightarrow$  v  $\in$  vs (ad, al) | -  $\Rightarrow$  True;
      non-speculative P (w-value P vs ob) obs ]
     $\Rightarrow$  non-speculative P vs (LCons ob obs)

```

**inductive-simps** *non-speculative-simps* [*simp*]:  
*non-speculative P vs LNil*  
*non-speculative P vs (LCons ob obs)*

**lemma** *non-speculative-lappend*:

**assumes** *lfinite obs*  
**shows** *non-speculative P vs (lappend obs obs')  $\longleftrightarrow$*   
*non-speculative P vs obs  $\wedge$  non-speculative P (w-values P vs (list-of obs)) obs'*  
*(is ?concl vs obs)*  
**using** *assms*  
**proof**(*induct arbitrary: vs*)  
**case** *lfinite-LNil* **thus** *?case* **by** *simp*  
**next**  
**case** (*lfinite-LConsI obs ob*)  
**have** *?concl (w-value P vs ob) obs* **by** *fact*  
**thus** *?case* **using**  $\langle$ *lfinite obs* $\rangle$  **by** *simp*  
**qed**

**lemma**

**assumes** *non-speculative P vs obs*  
**shows** *non-speculative-ltake: non-speculative P vs (ltake n obs) (is ?thesis1)*  
**and** *non-speculative-ldrop: non-speculative P (w-values P vs (list-of (ltake n obs))) (ldrop n obs) (is ?thesis2)*  
**proof** –  
**note** *assms*  
**also have** *obs = lappend (ltake n obs) (ldrop n obs)* **by**(*simp add: lappend-ltake-ldrop*)  
**finally have** *?thesis1  $\wedge$  ?thesis2*  
**by**(*cases n*)(*simp-all add: non-speculative-lappend del: lappend-ltake-enat-ldropn*)  
**thus** *?thesis1 ?thesis2* **by** *blast+*  
**qed**

**lemma** *non-speculative-coinduct-append* [*consumes 1, case-names non-speculative, case-conclusion non-speculative LNil lappend*]:

**assumes** *major: X vs obs*  
**and** *step:  $\bigwedge$ vs obs. X vs obs*  
 $\implies$  *obs = LNil  $\vee$*   
 $(\exists$  *obs' obs''*. *obs = lappend obs' obs''  $\wedge$  obs'  $\neq$  LNil  $\wedge$  non-speculative P vs obs'  $\wedge$*   
 $($ *lfinite obs'  $\longrightarrow$  (X (w-values P vs (list-of obs')) obs''  $\vee$*   
 $\text{non-speculative P (w-values P vs (list-of obs')) obs''}))$   
*(is  $\bigwedge$ vs obs. -  $\implies$  -  $\vee$  ?step vs obs)*  
**shows** *non-speculative P vs obs*  
**proof** –  
**from** *major*  
**have**  $\exists$  *obs' obs''*. *obs = lappend (llist-of obs') obs''  $\wedge$  non-speculative P vs (llist-of obs')  $\wedge$*   
 $X$  *(w-values P vs obs') obs''*  
**by**(*auto intro: exI[where x=[]]*)  
**thus** *?thesis*  
**proof**(*coinduct*)  
**case** (*non-speculative vs obs*)  
**then obtain** *obs' obs''*  
**where** *obs: obs = lappend (llist-of obs') obs''*  
**and** *sc-obs': non-speculative P vs (llist-of obs')*  
**and** *X: X (w-values P vs obs') obs''* **by** *blast*

```

show ?case
proof(cases obs')
  case Nil
  with X have X vs obs'' by simp
  from step[OF this] show ?thesis
proof
  assume obs'' = LNil
  with Nil obs show ?thesis by simp
next
  assume ?step vs obs''
  then obtain obs''' obs''''
    where obs'': obs'' = lappend obs''' obs'''' and obs''' ≠ LNil
    and sc-obs''': non-speculative P vs obs'''
    and fin: lfinite obs''' ⇒ X (w-values P vs (list-of obs''')) obs'''' ∨
      non-speculative P (w-values P vs (list-of obs''')) obs''''
    by blast
  from ⟨obs''' ≠ LNil⟩ obtain ob obs''''' where obs''': obs''' = LCons ob obs'''''
    unfolding neq-LNil-conv by blast
  with Nil obs'' obs have concl1: obs = LCons ob (lappend obs''''' obs''') by simp
  have concl2: case ob of NormalAction (ReadMem ad al v) ⇒ v ∈ vs (ad, al) | - ⇒ True
    using sc-obs''' obs''' by simp

show ?thesis
proof(cases lfinite obs''')
  case False
  hence lappend obs''''' obs'''' = obs''''' using obs''' by (simp add: lappend-inf)
  hence non-speculative P (w-value P vs ob) (lappend obs''''' obs''')
    using sc-obs''' obs''' by simp
  with concl1 concl2 have ?LCons by blast
  thus ?thesis by simp
next
  case True
  with obs''' obtain obs'''''' where obs''''': obs''''' = llist-of obs''''''
    by simp(auto simp add: lfinite-eq-range-llist-of)
  from fin[OF True] have ?LCons
proof
  assume X: X (w-values P vs (list-of obs''')) obs''''
  hence X (w-values P (w-value P vs ob) obs''''') obs''''
    using obs'''''' obs''' by simp
  moreover from obs''''''
  have lappend obs'''''' obs'''' = lappend (llist-of obs''''') obs'''' by simp
  moreover have non-speculative P (w-value P vs ob) (llist-of obs''''')
    using sc-obs''' obs''' obs'''''' by simp
  ultimately show ?thesis using concl1 concl2 by blast
next
  assume non-speculative P (w-values P vs (list-of obs''')) obs''''
  with sc-obs''' obs'''''' obs'''
  have non-speculative P (w-value P vs ob) (lappend obs'''''' obs''')
    by (simp add: non-speculative-lappend)
  with concl1 concl2 show ?thesis by blast
qed
thus ?thesis by simp
qed
qed

```



```

next
  case (Cons ob obs'')
  hence obs = LCons ob (lappend (llist-of obs'') obs'')
  using obs by simp
  moreover from sc-obs' Cons
  have case ob of NormalAction (ReadMem ad al v)  $\Rightarrow v \in vs (ad, al) \mid - \Rightarrow True$ 
  and non-speculative P (w-value P vs ob) (llist-of obs'') by simp-all
  moreover from X Cons have X (w-values P (w-value P vs ob) obs'') obs'' by simp
  ultimately show ?thesis by blast
qed
qed
qed

lemma non-speculative-coinduct-append-wf
  [consumes 2, case-names non-speculative, case-conclusion non-speculative LNil lappend]:
  assumes major: X vs obs a
  and wf: wf R
  and step:  $\bigwedge vs obs a. X vs obs a \Rightarrow obs = LNil \vee$ 
     $(\exists obs' obs'' a'. obs = lappend obs' obs'' \wedge non-speculative P vs obs' \wedge (obs' = LNil \longrightarrow (a', a)$ 
 $\in R) \wedge$ 
     $(lfinite obs' \longrightarrow X (w-values P vs (list-of obs')) obs'' a' \vee$ 
     $non-speculative P (w-values P vs (list-of obs')) obs''))$ 
  (is  $\bigwedge vs obs a. - \Rightarrow - \vee ?step vs obs a$ )
  shows non-speculative P vs obs
proof -
  { fix vs obs a
    assume X vs obs a
    with wf
    have obs = LNil  $\vee (\exists obs' obs''. obs = lappend obs' obs'' \wedge obs' \neq LNil \wedge non-speculative P vs$ 
 $obs' \wedge$ 
     $(lfinite obs' \longrightarrow (\exists a. X (w-values P vs (list-of obs')) obs'' a) \vee$ 
     $non-speculative P (w-values P vs (list-of obs')) obs''))$ 
    (is  $- \vee ?step-concl vs obs$ )
    proof(induct a arbitrary: vs obs rule: wf-induct[consumes 1, case-names wf])
      case (wf a)
      note IH = wf.hyps[rule-format]
      from step[OF  $\langle X vs obs a \rangle$ ]
      show ?case
      proof
        assume obs = LNil thus ?thesis ..
      next
        assume ?step vs obs a
        then obtain obs' obs'' a'
          where obs: obs = lappend obs' obs''
          and sc-obs': non-speculative P vs obs'
          and decr: obs' = LNil  $\Rightarrow (a', a) \in R$ 
          and fin: lfinite obs'  $\Rightarrow$ 
             $X (w-values P vs (list-of obs')) obs'' a' \vee$ 
             $non-speculative P (w-values P vs (list-of obs')) obs''$ 
          by blast
        show ?case
        proof(cases obs' = LNil)
          case True

```

```

hence lfinite obs' by simp
from fin[OF this] show ?thesis
proof
  assume X: X (w-values P vs (list-of obs')) obs'' a'
  from True have (a', a) ∈ R by(rule decr)
  from IH[OF this X] show ?thesis
  proof
    assume obs'' = LNil
    with True obs have obs = LNil by simp
    thus ?thesis ..
  next
    assume ?step-concl (w-values P vs (list-of obs')) obs''
    hence ?step-concl vs obs using True obs by simp
    thus ?thesis ..
  qed
next
  assume non-speculative P (w-values P vs (list-of obs')) obs''
  thus ?thesis using obs True
  by cases(auto cong: action.case-cong obs-event.case-cong intro: exI[where x=LCons x LNil
for x])
qed
next
case False
with obs sc-obs' fin show ?thesis by auto
qed
qed }
note step' = this

from major show ?thesis
proof(coinduction arbitrary: vs obs a rule: non-speculative-coinduct-append)
  case (non-speculative vs obs)
  thus ?case by simp(rule step')
qed
qed

lemma non-speculative-nthI:
  (∧ i ad al v. [ enat i < llength obs; lnth obs i = NormalAction (ReadMem ad al v);
    non-speculative P vs (ltake (enat i) obs) ])
  ⇒ v ∈ w-values P vs (list-of (ltake (enat i) obs)) (ad, al)
  ⇒ non-speculative P vs obs
proof(coinduction arbitrary: vs obs rule: non-speculative.coinduct)
  case (non-speculative vs obs)
  hence nth:
    ∧ i ad al v. [ enat i < llength obs; lnth obs i = NormalAction (ReadMem ad al v);
      non-speculative P vs (ltake (enat i) obs) ]
    ⇒ v ∈ w-values P vs (list-of (ltake (enat i) obs)) (ad, al) by blast
  show ?case
  proof(cases obs)
    case LNil thus ?thesis by simp
  next
    case (LCons ob obs')
    { fix ad al v

```

```

assume  $ob = \text{NormalAction } (\text{ReadMem } ad \ al \ v)$ 
with  $nth[of \ 0 \ ad \ al \ v] \ LCons$ 
have  $v \in vs \ (ad, \ al) \ \mathbf{by}(\text{simp } add: \text{zero-enat-def}[\text{symmetric}]) \ }$ 
note  $base = this$ 
moreover {
  fix  $i \ ad \ al \ v$ 
  assume  $enat \ i < llength \ obs' \ lnth \ obs' \ i = \text{NormalAction } (\text{ReadMem } ad \ al \ v)$ 
  and  $\text{non-speculative } P \ (w\text{-value } P \ vs \ ob) \ (ltake \ (enat \ i) \ obs')$ 
  with  $LCons \ nth[of \ Suc \ i \ ad \ al \ v] \ base$ 
  have  $v \in w\text{-values } P \ (w\text{-value } P \ vs \ ob) \ (list\text{-of } (ltake \ (enat \ i) \ obs')) \ (ad, \ al)$ 
  by( $\text{clarsimp } simp \ add: \text{eSuc-enat}[\text{symmetric}] \ split: \text{obs-event.split } \text{action.split} \ }$ )
  ultimately have  $?LCons \ \mathbf{using} \ LCons \ \mathbf{by}(\text{simp } split: \text{action.split } \text{obs-event.split})$ 
  thus  $?thesis \ ..$ 
qed
qed

locale  $executions\text{-}sc\text{-}hb =$ 
   $executions\text{-}base \ \mathcal{E} \ P$ 
  for  $\mathcal{E} :: ('addr, 'thread\text{-}id) \text{ execution set}$ 
  and  $P :: 'm \text{ prog} +$ 
  assumes  $\mathcal{E}\text{-new-actions-for-fun}$ :
   $\llbracket E \in \mathcal{E}; a \in \text{new-actions-for } P \ E \ adal; a' \in \text{new-actions-for } P \ E \ adal \rrbracket \implies a = a'$ 
  and  $\mathcal{E}\text{-ex-new-action}$ :
   $\llbracket E \in \mathcal{E}; ra \in \text{read-actions } E; adal \in \text{action-loc } P \ E \ ra; \text{non-speculative } P \ (\lambda\cdot. \{\}) \ (ltake \ (enat \ ra) \ (lmap \ snd \ E)) \rrbracket$ 
   $\implies \exists wa. wa \in \text{new-actions-for } P \ E \ adal \wedge wa < ra$ 
begin

lemma  $\mathcal{E}\text{-new-same-addr-singleton}$ :
  assumes  $E: E \in \mathcal{E}$ 
  shows  $\exists a. \text{new-actions-for } P \ E \ adal \subseteq \{a\}$ 
by( $\text{blast } dest: \mathcal{E}\text{-new-actions-for-fun}[OF \ E]$ )

lemma  $\text{new-action-before-read}$ :
  assumes  $E: E \in \mathcal{E}$ 
  and  $ra: ra \in \text{read-actions } E$ 
  and  $adal: adal \in \text{action-loc } P \ E \ ra$ 
  and  $new: wa \in \text{new-actions-for } P \ E \ adal$ 
  and  $sc: \text{non-speculative } P \ (\lambda\cdot. \{\}) \ (ltake \ (enat \ ra) \ (lmap \ snd \ E))$ 
  shows  $wa < ra$ 
using  $\mathcal{E}\text{-new-same-addr-singleton}[OF \ E, \text{ of } adal] \ \mathcal{E}\text{-ex-new-action}[OF \ E \ ra \ adal \ sc] \ new$ 
by  $auto$ 

lemma  $\text{most-recent-write-exists}$ :
  assumes  $E: E \in \mathcal{E}$ 
  and  $ra: ra \in \text{read-actions } E$ 
  and  $sc: \text{non-speculative } P \ (\lambda\cdot. \{\}) \ (ltake \ (enat \ ra) \ (lmap \ snd \ E))$ 
  shows  $\exists wa. P, E \vdash ra \rightsquigarrow_{mrw} wa$ 
proof –
  from  $ra$  obtain  $ad \ al$  where
   $adal: (ad, \ al) \in \text{action-loc } P \ E \ ra$ 
  by( $\text{rule } \text{read-action-action-locE}$ )

define  $Q$  where  $Q = \{a. a \in \text{write-actions } E \wedge (ad, \ al) \in \text{action-loc } P \ E \ a \wedge E \vdash a \leq a \ ra\}$ 

```

```

let ?A = new-actions-for P E (ad, al)
let ?B = {a. a ∈ actions E ∧ (∃ v'. action-obs E a = NormalAction (WriteMem ad al v')) ∧ a ≤
ra}

have Q ⊆ ?A ∪ ?B unfolding Q-def
  by(auto elim!: write-actions.cases action-loc-aux-cases simp add: new-actions-for-def elim: ac-
tion-orderE)
moreover from  $\mathcal{E}$ -new-same-addr-singleton[OF E, of (ad, al)]
have finite ?A by(blast intro: finite-subset)
moreover have finite ?B by auto
ultimately have finQ: finite Q
  by(blast intro: finite-subset)

from  $\mathcal{E}$ -ex-new-action[OF E ra adal sc] ra obtain wa
  where wa: wa ∈ Q unfolding Q-def
  by(fastforce elim!: new-actionsE is-new-action.cases read-actions.cases intro: write-actionsI ac-
tion-orderI)

define wa' where wa' = Max-torder (action-order E) Q

from wa have Q ≠ {} Q ⊆ actions E by(auto simp add: Q-def)
with finQ have wa' ∈ Q unfolding wa'-def
  by(rule Max-torder-in-set[OF torder-action-order])
hence E ⊢ wa' ≤a ra wa' ∈ write-actions E
  and (ad, al) ∈ action-loc P E wa' by(simp-all add: Q-def)
with ra adal have P, E ⊢ ra  $\leadsto$ mrw wa'
proof
  fix wa''
  assume wa'': wa'' ∈ write-actions E (ad, al) ∈ action-loc P E wa''
  from ⟨wa'' ∈ write-actions E⟩ ra
  have ra ≠ wa'' by(auto dest: read-actions-not-write-actions)
  show E ⊢ wa'' ≤a wa' ∨ E ⊢ ra ≤a wa''
  proof(rule disjCI)
    assume ¬ E ⊢ ra ≤a wa''
    with total-onPD[OF total-action-order, of ra E wa'']
      ⟨ra ≠ wa''⟩ ⟨ra ∈ read-actions E⟩ ⟨wa'' ∈ write-actions E⟩
    have E ⊢ wa'' ≤a ra by simp
    with wa'' have wa'' ∈ Q by(simp add: Q-def)
    with finQ show E ⊢ wa'' ≤a wa'
      using ⟨Q ⊆ actions E⟩ unfolding wa'-def
      by(rule Max-torder-above[OF torder-action-order])
  qed
qed
thus ?thesis ..
qed

lemma mrw-before:
  assumes E: E ∈  $\mathcal{E}$ 
  and mrw: P, E ⊢ r  $\leadsto$ mrw w
  and sc: non-speculative P (λ-. {}) (ltake (enat r) (lmap snd E))
  shows w < r
using mrw read-actions-not-write-actions[of r E]
apply cases
apply(erule action-orderE)

```

```

apply(erule (1) new-action-before-read[OF E])
apply(simp add: new-actions-for-def)
apply(rule sc)
apply(cases w = r)
apply auto
done

```

```

lemma sequentially-consistent-most-recent-write-for:
  assumes E: E ∈  $\mathcal{E}$ 
  and sc: non-speculative P (λ-. {}) (lmap snd E)
  shows sequentially-consistent P (E, λr. THE w. P, E ⊢ r ∼mrw w)
proof(rule sequentially-consistentI)
  fix r
  assume r: r ∈ read-actions E
  from sc have sc': non-speculative P (λ-. {}) (ltake (enat r) (lmap snd E))
    by(rule non-speculative-ltake)
  from most-recent-write-exists[OF E r this]
  obtain w where P, E ⊢ r ∼mrw w ..
  thus P, E ⊢ r ∼mrw THE w. P, E ⊢ r ∼mrw w
    by(simp add: THE-most-recent-writeI)
qed

end

```

```

locale jmm-multithreaded = multithreaded-base +
  constrains final :: 'x ⇒ bool
  and r :: ('l, 'thread-id, 'x, 'm, 'w, ('addr, 'thread-id) obs-event action) semantics
  and convert-RA :: 'l released-locks ⇒ ('addr, 'thread-id) obs-event action list
  fixes P :: 'md prog

end

```

## 8.8 Sequentially consistent completion of executions in the JMM

```

theory SC-Completion
imports
  Non-Speculative
begin

```

### 8.8.1 Most recently written values

```

fun mrw-value ::
  'm prog ⇒ (('addr × addr-loc) → ('addr val × bool)) ⇒ ('addr, 'thread-id) obs-event action
  ⇒ (('addr × addr-loc) → ('addr val × bool))
where
  mrw-value P vs (NormalAction (WriteMem ad al v)) = vs((ad, al) ↦ (v, True))
| mrw-value P vs (NormalAction (NewHeapElem ad hT)) =
  (λ(ad', al). if ad = ad' ∧ al ∈ addr-locs P hT ∧ (case vs (ad, al) of None ⇒ True | Some (v, b)
  ⇒ ¬ b)
    then Some (addr-loc-default P hT al, False)
    else vs (ad', al))
| mrw-value P vs - = vs

```

**lemma** *mrw-value-cases*:

**obtains** *ad al v* **where**  $x = \text{NormalAction } (\text{WriteMem } ad \ al \ v)$   
 $|$  *ad hT* **where**  $x = \text{NormalAction } (\text{NewHeapElem } ad \ hT)$   
 $|$  *ad M vs v* **where**  $x = \text{NormalAction } (\text{ExternalCall } ad \ M \ vs \ v)$   
 $|$  *ad al v* **where**  $x = \text{NormalAction } (\text{ReadMem } ad \ al \ v)$   
 $|$  *t* **where**  $x = \text{NormalAction } (\text{ThreadStart } t)$   
 $|$  *t* **where**  $x = \text{NormalAction } (\text{ThreadJoin } t)$   
 $|$  *ad* **where**  $x = \text{NormalAction } (\text{SyncLock } ad)$   
 $|$  *ad* **where**  $x = \text{NormalAction } (\text{SyncUnlock } ad)$   
 $|$  *t* **where**  $x = \text{NormalAction } (\text{ObsInterrupt } t)$   
 $|$  *t* **where**  $x = \text{NormalAction } (\text{ObsInterrupted } t)$   
 $|$   $x = \text{InitialThreadAction}$   
 $|$   $x = \text{ThreadFinishAction}$

**by** *pat-completeness*

**abbreviation** *mrw-values* ::

$'m \text{ prog} \Rightarrow ((\text{'addr} \times \text{addr-loc}) \rightarrow (\text{'addr val} \times \text{bool})) \Rightarrow (\text{'addr}, \text{'thread-id}) \text{ obs-event action list}$   
 $\Rightarrow ((\text{'addr} \times \text{addr-loc}) \rightarrow (\text{'addr val} \times \text{bool}))$

**where** *mrw-values*  $P \equiv \text{foldl } (\text{mrw-value } P)$

**lemma** *mrw-values-eq-SomeD*:

**assumes** *mrw*: *mrw-values*  $P \text{ vs0 obs } (ad, al) = \lfloor (v, b) \rfloor$

**and** *vs0*  $(ad, al) = \lfloor (v, b) \rfloor \implies \exists wa. wa \in \text{set obs} \wedge \text{is-write-action } wa \wedge (ad, al) \in \text{action-loc-aux } P \ wa \wedge (b \longrightarrow \neg \text{is-new-action } wa)$

**shows**  $\exists \text{obs}' \ wa \ \text{obs}''. \text{obs} = \text{obs}' @ wa \# \text{obs}'' \wedge \text{is-write-action } wa \wedge (ad, al) \in \text{action-loc-aux } P \ wa \wedge$

$\text{value-written-aux } P \ wa \ al = v \wedge (\text{is-new-action } wa \longleftrightarrow \neg b) \wedge$

$(\forall ob \in \text{set obs}''. \text{is-write-action } ob \longrightarrow (ad, al) \in \text{action-loc-aux } P \ ob \longrightarrow \text{is-new-action } ob \wedge$

$b)$

**(is ?concl obs)**

**using** *assms*

**proof**(*induct obs rule: rev-induct*)

**case Nil thus ?case by simp**

**next**

**case** (*snoc ob obs*)

**note**  $mrw = \langle \text{mrw-values } P \text{ vs0 } (\text{obs} @ [ob]) \ (ad, al) = \lfloor (v, b) \rfloor \rangle$

**show** *?case*

**proof**(*cases is-write-action ob*  $\wedge (ad, al) \in \text{action-loc-aux } P \ ob \wedge (\text{is-new-action } ob \longrightarrow \neg b)$ )

**case True thus ?thesis using mrw**

**by**(*fastforce elim!: is-write-action.cases intro: action-loc-aux-intros split: if-split-asm*)

**next**

**case False**

**with mrw have** *mrw-values*  $P \text{ vs0 obs } (ad, al) = \lfloor (v, b) \rfloor$

**by**(*cases ob rule: mrw-value-cases*)(*auto split: if-split-asm simp add: addr-locs-def split: htype.split-asm*)

**moreover**

**{ assume** *vs0*  $(ad, al) = \lfloor (v, b) \rfloor$

**hence**  $\exists wa. wa \in \text{set } (\text{obs} @ [ob]) \wedge \text{is-write-action } wa \wedge (ad, al) \in \text{action-loc-aux } P \ wa \wedge (b \longrightarrow \neg \text{is-new-action } wa)$

**by**(*rule snoc*)

**with False have**  $\exists wa. wa \in \text{set obs} \wedge \text{is-write-action } wa \wedge (ad, al) \in \text{action-loc-aux } P \ wa \wedge (b \longrightarrow \neg \text{is-new-action } wa)$

**by auto }**

**ultimately have** *?concl obs by*(*rule snoc*)

```

    thus ?thesis using False mrw by fastforce
qed
qed

```

**lemma** *mrw-values-WriteMemD*:

```

  assumes NormalAction (WriteMem ad al v') ∈ set obs
  shows ∃ v. mrw-values P vs0 obs (ad, al) = Some (v, True)
using assms
apply(induct obs rule: rev-induct)
  apply simp
apply clarsimp
apply(erule disjE)
  apply clarsimp
apply clarsimp
apply(case-tac x rule: mrw-value-cases)
apply simp-all
done

```

**lemma** *mrw-values-new-actionD*:

```

  assumes w ∈ set obs is-new-action w adal ∈ action-loc-aux P w
  shows ∃ v b. mrw-values P vs0 obs adal = Some (v, b)
using assms
apply(induct obs rule: rev-induct)
  apply simp
apply clarsimp
apply(erule disjE)
  apply(fastforce simp add: split-beta elim!: action-loc-aux-cases is-new-action.cases)
apply clarsimp
apply(rename-tac w' obs' v b)
apply(case-tac w' rule: mrw-value-cases)
apply(auto simp add: split-beta)
done

```

**lemma** *mrw-value-dom-mono*:

```

  dom vs ⊆ dom (mrw-value P vs ob)
by(cases ob rule: mrw-value-cases) auto

```

**lemma** *mrw-values-dom-mono*:

```

  dom vs ⊆ dom (mrw-values P vs obs)
by(induct obs arbitrary: vs)(auto intro: subset-trans[OF mrw-value-dom-mono] del: subsetI)

```

**lemma** *mrw-values-eq-NoneD*:

```

  assumes mrw-values P vs0 obs adal = None
  and w ∈ set obs and is-write-action w and adal ∈ action-loc-aux P w
  shows False
using assms
apply −
apply(erule is-write-action.cases)
apply(fastforce dest: mrw-values-WriteMemD[where ?vs0.0=vs0 and P=P] mrw-values-new-actionD[where
?vs0.0=vs0] elim: action-loc-aux-cases)+
done

```

**lemma** *mrw-values-mrw*:

```

  assumes mrw: mrw-values P vs0 (map snd obs) (ad, al) = [(v, b)]

```

**and** *initial*:  $vs0\ (ad, al) = \lfloor (v, b) \rfloor \implies \exists wa. wa \in set\ (map\ snd\ obs) \wedge is\_write\_action\ wa \wedge (ad, al) \in action\_loc\_aux\ P\ wa \wedge (b \longrightarrow \neg is\_new\_action\ wa)$

**shows**  $\exists i. i < length\ obs \wedge P, llist\_of\ (obs\ @\ [(t, NormalAction\ (ReadMem\ ad\ al\ v))]) \vdash length\ obs \rightsquigarrow mrw\ i \wedge value\_written\ P\ (llist\_of\ obs)\ i\ (ad, al) = v$

**proof** –

**from** *mrw-values-eq-SomeD*[*OF mrw initial*]

**obtain** *obs'* *wa* *obs''* **where** *obs*:  $map\ snd\ obs = obs' @ wa \# obs''$

**and** *wa*: *is-write-action wa*

**and** *adal*:  $(ad, al) \in action\_loc\_aux\ P\ wa$

**and** *written*: *value-written-aux P wa al = v*

**and** *new*:  $is\_new\_action\ wa \longleftrightarrow \neg b$

**and** *last*:  $\bigwedge ob. \llbracket ob \in set\ obs''; is\_write\_action\ ob; (ad, al) \in action\_loc\_aux\ P\ ob \rrbracket \implies is\_new\_action\ ob \wedge b$

**by** *blast*

**let** *?i* = *length obs'*

**let** *?E* = *llist-of (obs @ [(t, NormalAction (ReadMem ad al v))])*

**from** *obs* **have** *len*:  $length\ (map\ snd\ obs) = Suc\ (length\ obs') + length\ obs''$  **by** *simp*

**hence** *?i* < *length obs* **by** *simp*

**moreover**

**hence** *obs-i*: *action-obs ?E ?i = wa* **using** *len obs*

**by**(*auto simp add: action-obs-def map-eq-append-conv*)

**have** *P, ?E* ⊢ *length obs*  $\rightsquigarrow mrw\ ?i$

**proof**(*rule most-recent-write-for.intros*)

**show** *length obs* ∈ *read-actions ?E*

**by**(*auto intro: read-actions.intros simp add: actions-def action-obs-def*)

**show**  $(ad, al) \in action\_loc\ P\ ?E\ (length\ obs)$

**by**(*simp add: action-obs-def lnth-llist-of*)

**show** *?E* ⊢ *length obs'* ≤<sub>a</sub> *length obs* **using** *len*

**by**–(*rule action-orderI, auto simp add: actions-def action-obs-def nth-append*)

**show** *?i* ∈ *write-actions ?E* **using** *len obs wa*

**by**–(*rule write-actions.intros, auto simp add: actions-def action-obs-def nth-append map-eq-append-conv*)

**show**  $(ad, al) \in action\_loc\ P\ ?E\ ?i$  **using** *obs-i adal* **by** *simp*

**fix** *wa'*

**assume** *wa'*: *wa' ∈ write-actions ?E*

**and** *adal'*:  $(ad, al) \in action\_loc\ P\ ?E\ wa'$

**from** *wa'* ⟨*?i* ∈ *write-actions ?E*⟩

**have** *wa' ∈ actions ?E ?i ∈ actions ?E* **by** *simp-all*

**hence** *?E* ⊢ *wa'* ≤<sub>a</sub> *?i*

**proof**(*rule action-orderI*)

**assume** *new-wa'*: *is-new-action (action-obs ?E wa')*

**and** *new-i*: *is-new-action (action-obs ?E ?i)*

**from** *new-i obs-i new* **have** *b*:  $\neg b$  **by** *simp*

**show** *wa' ≤ ?i*

**proof**(*rule ccontr*)

**assume**  $\neg ?thesis$

**hence** *?i* < *wa'* **by** *simp*

**hence** *snd (obs ! wa')* ∈ *set obs''* **using** *obs wa' unfolding in-set-conv-nth*

**by** –(*rule exI[where x=wa' – Suc (length obs')], auto elim!: write-actions.cases actionsE simp add: action-obs-def lnth-llist-of actions-def nth-append map-eq-append-conv nth-Cons' split: if-split-asm*)



```

moreover from  $wa'$  have is-write-action ( $snd\ (obs\ !\ wa')$ )
  by cases(auto simp add: action-obs-def nth-append actions-def split: if-split-asm)
moreover from  $adal'\ wa'$  have  $(ad, al) \in action-loc-aux\ P\ (snd\ (obs\ !\ wa'))$ 
  by(auto simp add: action-obs-def nth-append nth-Cons' actions-def split: if-split-asm elim!:
write-actions.cases)
  ultimately show False using last[of snd (obs ! wa')] b by simp
qed
next
assume  $new-wa': \neg is-new-action\ (action-obs\ ?E\ wa')$ 
with  $wa'\ adal'$  obtain  $v'$  where NormalAction (WriteMem  $ad\ al\ v'$ )  $\in set\ (map\ snd\ obs)$ 
  unfolding in-set-conv-nth
  by (fastforce elim!: write-actions.cases is-write-action.cases simp add: action-obs-def actions-def
nth-append split: if-split-asm intro!: exI[where x=wa'])
from mrw-values-WriteMemD[OF this, of P vs0] mrw have  $b$  by simp
with  $new\ obs-i$  have  $\neg is-new-action\ (action-obs\ ?E\ ?i)$  by simp
moreover
have  $wa' \leq ?i$ 
proof(rule ccontr)
  assume  $\neg ?thesis$ 
  hence  $?i < wa'$  by simp
  hence  $snd\ (obs\ !\ wa') \in set\ obs''$  using  $obs\ wa'$  unfolding in-set-conv-nth
    by  $\neg(rule\ exI[where\ x=wa' - Suc\ (length\ obs')],\ auto\ elim!: write-actions.cases\ ac-$ 
tionsE simp add: action-obs-def lnth-llist-of actions-def nth-append map-eq-append-conv nth-Cons'
split: if-split-asm)
  moreover from  $wa'$  have is-write-action ( $snd\ (obs\ !\ wa')$ )
    by cases(auto simp add: action-obs-def nth-append actions-def split: if-split-asm)
  moreover from  $adal'\ wa'$  have  $(ad, al) \in action-loc-aux\ P\ (snd\ (obs\ !\ wa'))$ 
    by(auto simp add: action-obs-def nth-append nth-Cons' actions-def split: if-split-asm elim!:
write-actions.cases)
  ultimately have is-new-action ( $snd\ (obs\ !\ wa')$ ) using last[of snd (obs ! wa')] by simp
  moreover from  $new-wa'\ wa'$  have  $\neg is-new-action\ (snd\ (obs\ !\ wa'))$ 
    by(auto elim!: write-actions.cases simp add: action-obs-def nth-append actions-def split:
if-split-asm)
  ultimately show False by contradiction
qed
ultimately
show  $\neg is-new-action\ (action-obs\ ?E\ ?i) \wedge wa' \leq ?i$  by blast
qed
thus  $?E \vdash wa' \leq a\ ?i \vee ?E \vdash length\ obs \leq a\ wa' ..$ 
qed
moreover from written  $\langle ?i < length\ obs \rangle\ obs-i$ 
have value-written  $P\ (llist-of\ obs)\ ?i\ (ad, al) = v$ 
  by(simp add: value-written-def action-obs-def nth-append)
ultimately show ?thesis by blast
qed

lemma mrw-values-no-write-unchanged:
  assumes no-write:  $\bigwedge w. \llbracket w \in set\ obs; is-write-action\ w; adal \in action-loc-aux\ P\ w \rrbracket$ 
   $\implies case\ vs\ adal\ of\ None \Rightarrow False \mid Some\ (v, b) \Rightarrow b \wedge is-new-action\ w$ 
  shows mrw-values  $P\ vs\ obs\ adal = vs\ adal$ 
using assms
proof(induct obs arbitrary: vs)
  case Nil show ?case by simp
next

```

```

case (Cons ob obs)
from Cons.prems[of ob]
have mrw-value P vs ob adal = vs adal
  apply(cases adal)
  apply(cases ob rule: mrw-value-cases, fastforce+)
  apply(auto simp add: addr-locs-def split: htype.split-asm)
  apply blast+
  done
moreover
have mrw-values P (mrw-value P vs ob) obs adal = mrw-value P vs ob adal
proof(rule Cons.hyps)
  fix w
  assume w ∈ set obs is-write-action w adal ∈ action-loc-aux P w
  with Cons.prems[of w] ⟨mrw-value P vs ob adal = vs adal⟩
  show case mrw-value P vs ob adal of None ⇒ False | [(v, b)] ⇒ b ∧ is-new-action w by simp
qed
ultimately show ?case by simp
qed

```

### 8.8.2 Coinductive version of sequentially consistent prefixes

```

coinductive ta-seq-consist ::
  'm prog ⇒ ('addr × addr-loc → 'addr val × bool) ⇒ ('addr, 'thread-id) obs-event action llist ⇒ bool
for P :: 'm prog
where
  LNil: ta-seq-consist P vs LNil
  | LCons:
    
$$\llbracket \text{case } ob \text{ of } NormalAction (ReadMem \text{ ad al } v) \Rightarrow \exists b. \text{ vs } (ad, al) = [(v, b)] \mid - \Rightarrow True; \\ \text{ta-seq-consist } P (mrw\text{-value } P \text{ vs } ob) \text{ obs} \rrbracket \\ \Rightarrow \text{ta-seq-consist } P \text{ vs } (LCons \text{ ob } obs)$$


```

**inductive-simps** *ta-seq-consist-simps* [*simp*]:

```

ta-seq-consist P vs LNil
ta-seq-consist P vs (LCons ob obs)

```

**lemma** *ta-seq-consist-lappend*:

**assumes** *lfinite obs*

**shows** *ta-seq-consist P vs (lappend obs obs') ⟷*

*ta-seq-consist P vs obs ∧ ta-seq-consist P (mrw-values P vs (list-of obs)) obs'*

(**is** *?concl vs obs*)

**using** *assms*

**proof**(*induct arbitrary: vs*)

**case** *lfinite-LNil* **thus** *?case* **by** *simp*

**next**

**case** (*lfinite-LConsI obs ob*)

**have** *?concl (mrw-value P vs ob) obs* **by** *fact*

**thus** *?case* **using** *⟨lfinite obs⟩* **by**(*simp split: action.split add: list-of-LCons*)

**qed**

**lemma**

**assumes** *ta-seq-consist P vs obs*

**shows** *ta-seq-consist-ltake: ta-seq-consist P vs (ltake n obs)* (**is** *?thesis1*)

**and** *ta-seq-consist-ldrop: ta-seq-consist P (mrw-values P vs (list-of (ltake n obs))) (ldrop n obs)* (**is** *?thesis2*)

**proof** –

**note** *assms*

**also have** *obs* = *lappend* (*ltake* *n* *obs*) (*ldrop* *n* *obs*) **by** (*simp* *add*: *lappend-ltake-ldrop*)

**finally have** *?thesis1* ∧ *?thesis2*

**by** (*cases* *n*) (*simp-all* *add*: *ta-seq-consist-lappend del*: *lappend-ltake-enat-ldropn*)

**thus** *?thesis1* *?thesis2* **by** *blast+*

**qed**

**lemma** *ta-seq-consist-coinduct-append* [*consumes* 1, *case-names* *ta-seq-consist*, *case-conclusion* *ta-seq-consist* *LNil lappend*]:

**assumes** *major*: *X* *vs* *obs*

**and** *step*:  $\bigwedge$  *vs* *obs*. *X* *vs* *obs*

$\implies$  *obs* = *LNil*  $\vee$

( $\exists$  *obs'* *obs''*. *obs* = *lappend* *obs'* *obs''* ∧ *obs'* ≠ *LNil* ∧ *ta-seq-consist* *P* *vs* *obs'* ∧  
 $(\text{lfinite } \text{obs}' \longrightarrow (X (\text{mrw-values } P \text{ vs } (\text{list-of } \text{obs}')) \text{ obs}'' \vee$   
 $\text{ta-seq-consist } P (\text{mrw-values } P \text{ vs } (\text{list-of } \text{obs}')) \text{ obs}''))$ )

(*is*  $\bigwedge$  *vs* *obs*.  $- \implies - \vee ?\text{step } \text{vs } \text{obs}$ )

**shows** *ta-seq-consist* *P* *vs* *obs*

**proof** –

**from** *major*

**have**  $\exists$  *obs'* *obs''*. *obs* = *lappend* (*llist-of* *obs'*) *obs''* ∧ *ta-seq-consist* *P* *vs* (*llist-of* *obs'*) ∧  
 $X (\text{mrw-values } P \text{ vs } \text{obs}') \text{ obs}''$

**by** (*auto* *intro*: *exI*[**where** *x*=[]])

**thus** *?thesis*

**proof** (*coinduct*)

**case** (*ta-seq-consist* *vs* *obs*)

**then obtain** *obs'* *obs''*

**where** *obs*: *obs* = *lappend* (*llist-of* *obs'*) *obs''*

**and** *sc-obs'*: *ta-seq-consist* *P* *vs* (*llist-of* *obs'*)

**and** *X*:  $X (\text{mrw-values } P \text{ vs } \text{obs}') \text{ obs}''$  **by** *blast*

**show** *?case*

**proof** (*cases* *obs'*)

**case** *Nil*

**with** *X* **have** *X* *vs* *obs''* **by** *simp*

**from** *step*[*OF* *this*] **show** *?thesis*

**proof**

**assume** *obs''* = *LNil*

**with** *Nil* *obs* **show** *?thesis* **by** *simp*

**next**

**assume** *?step* *vs* *obs''*

**then obtain** *obs'''* *obs''''*

**where** *obs''*: *obs''* = *lappend* *obs'''* *obs''''* **and** *obs'''* ≠ *LNil*

**and** *sc-obs'''*: *ta-seq-consist* *P* *vs* *obs'''*

**and** *fin*:  $\text{lfinite } \text{obs}''' \implies X (\text{mrw-values } P \text{ vs } (\text{list-of } \text{obs}''')) \text{ obs}'''' \vee$   
 $\text{ta-seq-consist } P (\text{mrw-values } P \text{ vs } (\text{list-of } \text{obs}''')) \text{ obs}''''$

**by** *blast*

**from**  $\langle \text{obs}''' \neq \text{LNil} \rangle$  **obtain** *ob* *obs''''* **where** *obs'''*: *obs'''* = *LCons* *ob* *obs''''*

**unfolding** *neq-LNil-conv* **by** *blast*

**with** *Nil* *obs''* *obs* **have** *concl1*: *obs* = *LCons* *ob* (*lappend* *obs''''* *obs''''*) **by** *simp*

**have** *concl2*: *case* *ob* of *NormalAction* (*ReadMem* *ad* *al* *v*)  $\Rightarrow \exists$  *b*. *vs* (*ad*, *al*) =  $\lfloor (v, b) \rfloor \mid - \Rightarrow$

*True*

**using** *sc-obs'''* *obs'''* **by** *simp*

```

show ?thesis
proof(cases lfinite obs''')
  case False
  hence lappend obs'''' obs''' = obs'''' using obs''' by(simp add: lappend-inf)
  hence ta-seq-consist P (mrw-value P vs ob) (lappend obs'''' obs''')
    using sc-obs''' obs''' by simp
  with concl1 concl2 have ?LCons by blast
  thus ?thesis by simp
next
case True
with obs''' obtain obs'''' where obs''': obs'''' = llist-of obs''''
  by simp(auto simp add: lfinite-eq-range-llist-of)
from fin[OF True] have ?LCons
proof
  assume X: X (mrw-values P vs (list-of obs''')) obs'''
  hence X (mrw-values P (mrw-value P vs ob) obs'''' obs''') obs'''
    using obs'''' obs''' by simp
  moreover from obs''''
  have lappend obs'''' obs''' = lappend (llist-of obs''') obs''' by simp
  moreover have ta-seq-consist P (mrw-value P vs ob) (llist-of obs''')
    using sc-obs''' obs''' obs'''' by simp
  ultimately show ?thesis using concl1 concl2 by blast
next
  assume ta-seq-consist P (mrw-values P vs (list-of obs''')) obs'''
  with sc-obs''' obs'''' obs'''
  have ta-seq-consist P (mrw-value P vs ob) (lappend obs'''' obs''')
    by(simp add: ta-seq-consist-lappend)
  with concl1 concl2 show ?thesis by blast
qed
thus ?thesis by simp
qed
qed
next
case (Cons ob obs'')
hence obs = LCons ob (lappend (llist-of obs'') obs'')
  using obs by simp
moreover from sc-obs' Cons
have case ob of NormalAction (ReadMem ad al v)  $\Rightarrow \exists b. vs (ad, al) = [(v, b)] \mid - \Rightarrow True$ 
  and ta-seq-consist P (mrw-value P vs ob) (llist-of obs'') by simp-all
moreover from X Cons have X (mrw-values P (mrw-value P vs ob) obs'') obs'' by simp
ultimately show ?thesis by blast
qed
qed
qed
lemma ta-seq-consist-coinduct-append-wf
[consumes 2, case-names ta-seq-consist, case-conclusion ta-seq-consist LNil lappend]:
assumes major: X vs obs a
and wf: wf R
and step:  $\bigwedge vs obs a. X vs obs a \Rightarrow obs = LNil \vee (\exists obs' obs'' a'. obs = lappend obs' obs'' \wedge ta-seq-consist P vs obs' \wedge (obs' = LNil \longrightarrow (a', a) \in R) \wedge$ 

$$(lfinite obs' \longrightarrow X (mrw-values P vs (list-of obs')) obs'' a' \vee$$


```

```

                                ta-seq-consist P (mrw-values P vs (list-of obs')) obs'')
(is  $\bigwedge vs\ obs\ a. - \implies - \vee ?step\ vs\ obs\ a$ )
shows ta-seq-consist P vs obs
proof -
{ fix vs obs a
  assume X vs obs a
  with wf
  have obs = LNil  $\vee (\exists obs'\ obs''.\ obs = lappend\ obs'\ obs'' \wedge obs' \neq LNil \wedge ta-seq-consist\ P\ vs\ obs')$ 
 $\wedge$ 
    (lfinite obs'  $\longrightarrow (\exists a.\ X\ (mrw-values\ P\ vs\ (list-of\ obs'))\ obs''\ a) \vee$ 
      ta-seq-consist P (mrw-values P vs (list-of obs')) obs'')
(is -  $\vee ?step-concl\ vs\ obs$ )
proof(induct a arbitrary: vs obs rule: wf-induct[consumes 1, case-names wf])
case (wf a)
note IH = wf.hyps[rule-format]
from step[OF  $\langle X\ vs\ obs\ a \rangle$ ]
show ?case
proof
  assume obs = LNil thus ?thesis ..
next
  assume ?step vs obs a
  then obtain obs' obs'' a'
    where obs: obs = lappend obs' obs''
    and sc-obs': ta-seq-consist P vs obs'
    and decr: obs' = LNil  $\implies (a', a) \in R$ 
    and fin: lfinite obs'  $\implies$ 
      X (mrw-values P vs (list-of obs')) obs'' a'  $\vee$ 
      ta-seq-consist P (mrw-values P vs (list-of obs')) obs''
    by blast
  show ?case
proof(cases obs' = LNil)
  case True
  hence lfinite obs' by simp
  from fin[OF this] show ?thesis
  proof
    assume X: X (mrw-values P vs (list-of obs')) obs'' a'
    from True have (a', a)  $\in R$  by(rule decr)
    from IH[OF this X] show ?thesis
    proof
      assume obs'' = LNil
      with True obs have obs = LNil by simp
      thus ?thesis ..
    next
      assume ?step-concl (mrw-values P vs (list-of obs')) obs''
      hence ?step-concl vs obs using True obs by simp
      thus ?thesis ..
    qed
  next
    assume ta-seq-consist P (mrw-values P vs (list-of obs')) obs''
    thus ?thesis using obs True
    by cases(auto cong: action.case-cong obs-event.case-cong intro: exI[where x=LCons x LNil
for x])
  qed
next

```

```

      case False
      with obs sc-obs' fin show ?thesis by auto
    qed
  qed
  qed }
note step' = this

from major show ?thesis
proof(coinduction arbitrary: vs obs a rule: ta-seq-consist-coinduct-append)
  case (ta-seq-consist vs obs a)
  thus ?case by simp(rule step')
qed
qed

lemma ta-seq-consist-nthI:
  ( $\bigwedge i \text{ ad al } v. \llbracket \text{enat } i < \text{llength obs}; \text{lnth obs } i = \text{NormalAction (ReadMem ad al } v) \rrbracket$ 
     $\text{ta-seq-consist } P \text{ vs } (\text{ltake (enat } i) \text{ obs}) \rrbracket$ 
 $\implies \exists b. \text{mrw-values } P \text{ vs } (\text{list-of (ltake (enat } i) \text{ obs)}) (ad, al) = \llbracket (v, b) \rrbracket$ 
 $\implies \text{ta-seq-consist } P \text{ vs obs}$ 
  )
proof(coinduction arbitrary: vs obs)
  case (ta-seq-consist vs obs)
  hence nth:
    ( $\bigwedge i \text{ ad al } v. \llbracket \text{enat } i < \text{llength obs}; \text{lnth obs } i = \text{NormalAction (ReadMem ad al } v) \rrbracket$ 
       $\text{ta-seq-consist } P \text{ vs } (\text{ltake (enat } i) \text{ obs}) \rrbracket$ 
 $\implies \exists b. \text{mrw-values } P \text{ vs } (\text{list-of (ltake (enat } i) \text{ obs)}) (ad, al) = \llbracket (v, b) \rrbracket$ 
    ) by blast
  show ?case
  proof(cases obs)
    case LNil thus ?thesis by simp
  next
    case (LCons ob obs')
    { fix ad al v
      assume ob = NormalAction (ReadMem ad al v)
      with nth[of 0 ad al v] LCons
      have  $\exists b. \text{vs } (ad, al) = \llbracket (v, b) \rrbracket$ 
      by(simp add: zero-enat-def[symmetric]) }
    note base = this
    moreover {
      fix i ad al v
      assume enat i < llength obs' lnth obs' i = NormalAction (ReadMem ad al v)
      and ta-seq-consist P (mrw-value P vs ob) (ltake (enat i) obs')
      with LCons nth[of Suc i ad al v] base
      have  $\exists b. \text{mrw-values } P \text{ (mrw-value } P \text{ vs ob) (list-of (ltake (enat } i) \text{ obs')) } (ad, al) = \llbracket (v, b) \rrbracket$ 
      by(clarsimp simp add: eSuc-enat[symmetric] split: obs-event.split action.split) }
    ultimately have ?LCons using LCons by(simp split: action.split obs-event.split)
    thus ?thesis ..
  }
qed
qed

lemma ta-seq-consist-into-non-speculative:
  ( $\llbracket \text{ta-seq-consist } P \text{ vs obs}; \forall adal. \text{set-option (vs adal)} \subseteq \text{vs' adal} \times UNIV \rrbracket$ 
 $\implies \text{non-speculative } P \text{ vs' obs}$ 
  )
proof(coinduction arbitrary: vs' obs vs)
  case (non-speculative vs' obs vs)
  thus ?case

```

```

  apply cases
  apply(auto split: action.split-asm obs-event.split-asm)
  apply(rule exI, erule conjI, auto)+
  done
qed

```

**lemma** *llist-of-list-of-append*:

*lfinite xs  $\implies$  llist-of (list-of xs @ ys) = lappend xs (llist-of ys)*

**unfolding** *lfinite-eq-range-llist-of* **by**(*clarsimp simp add: lappend-llist-of-llist-of*)

**lemma** *ta-seq-consist-most-recent-write-for*:

**assumes** *sc*: *ta-seq-consist P Map.empty (lmap snd E)*

**and** *read*: *r*  $\in$  *read-actions E*

**and** *new-actions-for-fun*:  $\bigwedge adal\ a\ a'. \llbracket a \in \text{new-actions-for } P\ E\ adal; a' \in \text{new-actions-for } P\ E\ adal \rrbracket \implies a = a'$

**shows**  $\exists i. P, E \vdash r \rightsquigarrow_{mrw} i \wedge i < r$

**proof** –

**from** *read* **obtain** *t v ad al*

**where** *nth-r*: *lnth E r* = (*t*, *NormalAction (ReadMem ad al v)*)

**and** *r*: *enat r* < *llength E*

**by**(*cases*)(*cases lnth E r*, *auto simp add: action-obs-def actions-def*)

**from** *nth-r r*

**have** *E-unfold*: *E* = *lappend (ltake (enat r) E) (LCons (t, NormalAction (ReadMem ad al v)) (ldropn (Suc r) E))*

**by** (*metis lappend-ltake-enat-ldropn ldropn-Suc-conv-ldropn*)

**from** *sc* **obtain** *b* **where** *sc'*: *ta-seq-consist P Map.empty (ltake (enat r) (lmap snd E))*

**and** *mrw'*: *mrw-values P Map.empty (map snd (list-of (ltake (enat r) E)))* (*ad*, *al*) =  $\lfloor (v, b) \rfloor$

**by**(*subst (asm) (3) E-unfold*)(*auto simp add: ta-seq-consist-lappend lmap-lappend-distrib*)

**from** *mrw-values-mrw*[*OF mrw'*, *of t*] *r*

**obtain** *E' w'*

**where** *E'*: *E'* = *llist-of (list-of (ltake (enat r) E) @ [(t, NormalAction (ReadMem ad al v))])*

**and** *v*: *v* = *value-written P (ltake (enat r) E) w' (ad, al)*

**and** *mrw''*: *P, E'  $\vdash r \rightsquigarrow_{mrw} w'$*

**and** *w'*: *w'* < *r* **by**(*fastforce simp add: length-list-of-conv-the-enat min-def split: if-split-asm*)

**from** *E' r* **have** *sim*: *ltake (enat (Suc r)) E'  $\approx$  ltake (enat (Suc r)) E*

**by**(*subst E-unfold*)(*simp add: ltake-lappend llist-of-list-of-append min-def, auto simp add: eSuc-enat[symmetric] zero-enat-def[symmetric] eq-into-sim-actions*)

**from** *nth-r* **have** *adal-r*: (*ad*, *al*)  $\in$  *action-loc P E r* **by**(*simp add: action-obs-def*)

**from** *E' r* **have** *nth-r'*: *lnth E' r* = (*t*, *NormalAction (ReadMem ad al v)*)

**by**(*auto simp add: nth-append length-list-of-conv-the-enat min-def*)

**with** *mrw'' w' r adal-r* **obtain** *E  $\vdash w' \leq_a r$  w'  $\in$  write-actions E* (*ad*, *al*)  $\in$  *action-loc P E w'*

**by** *cases*(*fastforce simp add: action-obs-def action-loc-change-prefix[OF sim[symmetric], simplified action-obs-def] intro: action-order-change-prefix[OF - sim] write-actions-change-prefix[OF - sim]*)

**with** *read adal-r* **have** *P, E  $\vdash r \rightsquigarrow_{mrw} w'$*

**proof**(*rule most-recent-write-for.intros*)

**fix** *wa'*

**assume** *write'*: *wa'  $\in$  write-actions E*

**and** *adal-wa'*: (*ad*, *al*)  $\in$  *action-loc P E wa'*

**show** *E  $\vdash wa' \leq_a w' \vee E \vdash r \leq_a wa'$*

**proof**(*cases r  $\leq$  wa'*)

**assume** *r  $\leq$  wa'*

```

show ?thesis
proof(cases is-new-action (action-obs E wa'))
  case False
  with  $\langle r \leq wa' \rangle$  have  $E \vdash r \leq_a wa'$  using read write'
  by(auto simp add: action-order-def elim!: read-actions.cases)
  thus ?thesis ..
next
case True
  with write' adal-wa' have  $wa' \in \text{new-actions-for } P \ E \ (ad, al)$ 
  by(simp add: new-actions-for-def)
  hence  $w' \notin \text{new-actions-for } P \ E \ (ad, al)$  using  $r \ w' \ \langle r \leq wa' \rangle$ 
  by(auto dest: new-actions-for-fun)
  with  $\langle w' \in \text{write-actions } E \rangle \ \langle (ad, al) \in \text{action-loc } P \ E \ w' \rangle$ 
  have  $\neg \text{is-new-action } (action-obs \ E \ w')$  by(simp add: new-actions-for-def)
  with write' True  $\langle w' \in \text{write-actions } E \rangle$  have  $E \vdash wa' \leq_a w'$  by(simp add: action-order-def)
  thus ?thesis ..
qed
next
assume  $\neg r \leq wa'$ 
  hence  $wa' < r$  by simp
  with write' adal-wa'
  have  $wa' \in \text{write-actions } E' \ (ad, al) \in \text{action-loc } P \ E' \ wa'$ 
  by(auto intro: write-actions-change-prefix[OF - sim[symmetric]] simp add: action-loc-change-prefix[OF
sim])
  from most-recent-write-recent[OF mrw'' - this] nth-r'
  have  $E' \vdash wa' \leq_a w' \vee E' \vdash r \leq_a wa'$  by(simp add: action-obs-def)
  thus ?thesis using  $\langle wa' < r \rangle \ w'$ 
  by(auto 4 3 del: disjCI intro: disjI1 disjI2 action-order-change-prefix[OF - sim])
qed
qed
with w' show ?thesis by blast
qed

lemma ta-seq-consist-mrw-before:
  assumes sc: ta-seq-consist P Map.empty (lmap snd E)
  and new-actions-for-fun:  $\bigwedge adal \ a \ a'. \llbracket a \in \text{new-actions-for } P \ E \ adal; a' \in \text{new-actions-for } P \ E \ adal \rrbracket \implies a = a'$ 
  and mrw:  $P, E \vdash r \rightsquigarrow_{mrw} w$ 
  shows  $w < r$ 
proof -
  from mrw have  $r \in \text{read-actions } E$  by cases
  with sc new-actions-for-fun obtain w' where  $P, E \vdash r \rightsquigarrow_{mrw} w' \ w' < r$ 
  by(auto dest: ta-seq-consist-most-recent-write-for)
  with mrw show ?thesis by(auto dest: most-recent-write-for-fun)
qed

lemma ta-seq-consist-imp-sequentially-consistent:
  assumes tsa-ok: thread-start-actions-ok E
  and new-actions-for-fun:  $\bigwedge adal \ a \ a'. \llbracket a \in \text{new-actions-for } P \ E \ adal; a' \in \text{new-actions-for } P \ E \ adal \rrbracket \implies a = a'$ 
  and seq: ta-seq-consist P Map.empty (lmap snd E)
  shows  $\exists ws. \text{sequentially-consistent } P \ (E, ws) \wedge P \vdash (E, ws) \checkmark$ 
proof(intro exI conjI)
  define ws where  $ws \ i = (THE \ w. \ P, E \vdash i \rightsquigarrow_{mrw} w)$  for i

```



```

from seq have ns: non-speculative  $P$  ( $\lambda\cdot$ .  $\{\}$ ) ( $\text{lmap}$  snd  $E$ )
  by(rule ta-seq-consist-into-non-speculative) simp
show sequentially-consistent  $P$  ( $E$ ,  $ws$ ) unfolding ws-def
proof(rule sequentially-consistentI)
  fix  $r$ 
  assume  $r \in \text{read-actions } E$ 
  with seq new-actions-for-fun
  obtain  $w$  where  $P, E \vdash r \rightsquigarrow_{\text{mrw}} w$  by(auto dest: ta-seq-consist-most-recent-write-for)
  thus  $P, E \vdash r \rightsquigarrow_{\text{mrw}} \text{THE } w$ .  $P, E \vdash r \rightsquigarrow_{\text{mrw}} w$  by(simp add: THE-most-recent-writeI)
qed

show  $P \vdash (E, ws) \checkmark$ 
proof(rule wf-execI)
  show is-write-seen  $P$   $E$   $ws$ 
  proof(rule is-write-seenI)
    fix  $a$   $ad$   $al$   $v$ 
    assume  $a: a \in \text{read-actions } E$ 
    and  $adal: \text{action-obs } E \ a = \text{NormalAction } (\text{ReadMem } ad \ al \ v)$ 
from ns have seq': non-speculative  $P$  ( $\lambda\cdot$ .  $\{\}$ ) ( $\text{ltake } (\text{enat } a) (\text{lmap } \text{snd } E)$ ) by(rule non-speculative-ltake)
from seq  $a$  seq new-actions-for-fun
obtain  $w$  where  $\text{mrw}: P, E \vdash a \rightsquigarrow_{\text{mrw}} w$ 
  and  $w < a$  by(auto dest: ta-seq-consist-most-recent-write-for)
hence  $w: ws \ a = w$  by(simp add: ws-def THE-most-recent-writeI)
with  $\text{mrw } adal$ 

show  $ws \ a \in \text{write-actions } E$ 
and  $(ad, al) \in \text{action-loc } P \ E \ (ws \ a)$ 
and  $\neg P, E \vdash a \leq_{hb} ws \ a$ 
  by(fastforce elim!: most-recent-write-for.cases dest: happens-before-into-action-order anti-
symPD[OF antisym-action-order] read-actions-not-write-actions)+

let ?between =  $\text{ltake } (\text{enat } (a - \text{Suc } w)) (\text{ldropn } (\text{Suc } w) \ E)$ 
let ?prefix =  $\text{ltake } (\text{enat } w) \ E$ 
let ?vs-prefix =  $\text{mrw-values } P \ \text{Map.empty } (\text{map } \text{snd } (\text{list-of } ?prefix))$ 

{ fix  $v'$ 
  assume new: is-new-action ( $\text{action-obs } E \ w$ )
  and  $vs': ?vs\text{-prefix } (ad, al) = \lfloor (v', \text{True}) \rfloor$ 
from  $\text{mrw-values-eq-SomeD}[OF \ vs']$ 
obtain  $obs' \ wa \ obs''$  where  $\text{split}: \text{map } \text{snd } (\text{list-of } ?prefix) = obs' @ wa \# obs''$ 
  and  $wa: \text{is-write-action } wa$ 
  and  $adal': (ad, al) \in \text{action-loc-aux } P \ wa$ 
  and  $\text{new-wa}: \neg \text{is-new-action } wa$  by blast
from split have  $\text{length } (\text{map } \text{snd } (\text{list-of } ?prefix)) = \text{Suc } (\text{length } obs' + \text{length } obs'')$  by simp
hence  $\text{len-prefix}: \text{llength } ?prefix = \text{enat } \dots$  by(simp add: length-list-of-conv-the-enat min-enat1-conv-enat)
with split have  $\text{nth } (\text{map } \text{snd } (\text{list-of } ?prefix)) (\text{length } obs') = wa$ 
  and  $\text{enat } (\text{length } obs') < \text{llength } ?prefix$  by simp-all
hence  $\text{snd } (\text{lnth } ?prefix (\text{length } obs')) = wa$  by(simp add: list-of-lmap[symmetric] del: list-of-lmap)
hence  $wa': \text{action-obs } E \ (\text{length } obs') = wa$  and  $\text{enat } (\text{length } obs') < \text{llength } E$ 
  using  $\langle \text{enat } (\text{length } obs') < \text{llength } ?prefix \rangle$  by(auto simp add: action-obs-def lnth-ltake)
  with  $wa$  have  $\text{length } obs' \in \text{write-actions } E$  by(auto intro: write-actions.intros simp add:
actions-def)
from most-recent-write-recent[OF mrw - this, of  $(ad, al)$ ]  $adal \ adal' \ wa'$ 
have  $E \vdash \text{length } obs' \leq a \ w \vee E \vdash a \leq a \ \text{length } obs'$  by simp

```

```

    hence False using new-wa new wa' adal len-prefix ⟨w < a⟩
    by(auto elim!: action-orderE simp add: min-enat1-conv-enat split: enat.split-asm)
  }
  hence mrw-value-w: mrw-value P ?vs-prefix (snd (lnth E w)) (ad, al) =
    [(value-written P E w (ad, al), ¬ is-new-action (action-obs E w))]
    using ⟨ws a ∈ write-actions E⟩ ⟨(ad, al) ∈ action-loc P E (ws a)⟩ w
    by(cases snd (lnth E w) rule: mrw-value-cases)(fastforce elim: write-actions.cases simp add:
value-written-def action-obs-def)+
    have mrw-values P (mrw-value P ?vs-prefix (snd (lnth E w))) (list-of (lmap snd ?between)) (ad,
al) = [(value-written P E w (ad, al), ¬ is-new-action (action-obs E w))]
    proof(subst mrw-values-no-write-unchanged)
      fix wa
      assume wa ∈ set (list-of (lmap snd ?between))
      and write-wa: is-write-action wa
      and adal-wa: (ad, al) ∈ action-loc-aux P wa
      hence wa: wa ∈ lset (lmap snd ?between) by simp
      from wa obtain i-wa where wa = lnth (lmap snd ?between) i-wa
      and i-wa: enat i-wa < llength (lmap snd ?between)
      unfolding lset-conv-lnth by blast
      moreover hence i-wa-len: enat (Suc (w + i-wa)) < llength E by(cases llength E) auto
      ultimately have wa': wa = action-obs E (Suc (w + i-wa))
      by(simp-all add: lnth-ltake action-obs-def ac-simps)
      with write-wa i-wa-len have Suc (w + i-wa) ∈ write-actions E
      by(auto intro: write-actions.intros simp add: actions-def)
      from most-recent-write-recent[OF mrw - this, of (ad, al)] adal adal-wa wa'
      have E ⊢ Suc (w + i-wa) ≤ a w ∨ E ⊢ a ≤ a Suc (w + i-wa) by(simp)
      hence is-new-action wa ∧ ¬ is-new-action (action-obs E w)
      using adal i-wa wa' by(auto elim: action-orderE)
      thus case (mrw-value P ?vs-prefix (snd (lnth E w)) (ad, al)) of None ⇒ False | Some (v, b) ⇒
b ∧ is-new-action wa
      unfolding mrw-value-w by simp
      qed(simp add: mrw-value-w)

    moreover

    from a have a ∈ actions E by simp
    hence enat a < llength E by(rule actionsE)
    with ⟨w < a⟩ have enat (a - Suc w) < llength E - enat (Suc w)
    by(cases llength E) simp-all
    hence E = lappend (lappend ?prefix (LCons (lnth E w) ?between)) (LCons (lnth (ldropn (Suc
w) E) (a - Suc w)) (ldropn (Suc (a - Suc w)) (ldropn (Suc w) E)))
    using ⟨w < a⟩ ⟨enat a < llength E⟩ unfolding lappend-assoc lappend-code
    apply(subst ldropn-Suc-conv-ldropn, simp)
    apply(subst lappend-ltake-enat-ldropn)
    apply(subst ldropn-Suc-conv-ldropn, simp add: less-trans[where y=enat a])
    by simp
    hence E': E = lappend (lappend ?prefix (LCons (lnth E w) ?between)) (LCons (lnth E a) (ldropn
(Suc a) E))
    using ⟨w < a⟩ ⟨enat a < llength E⟩ by simp

    from seq have ta-seq-consist P (mrw-values P Map.empty (list-of (lappend (lmap snd ?prefix)
(LCons (snd (lnth E w)) (lmap snd ?between)))) (lmap snd (LCons (lnth E a) (ldropn (Suc a) E))))
    by(subst (asm) E')(simp add: lmap-lappend-distrib ta-seq-consist-lappend)
    ultimately show value-written P E (ws a) (ad, al) = v using adal w

```

```

by(clarsimp simp add: action-obs-def list-of-lappend list-of-LCons)

show  $\neg P, E \vdash a \leq_{so} ws\ a$  using  $\langle w < a \rangle w\ adal$  by(auto elim!: action-orderE sync-orderE)

fix a'
assume a':  $a' \in write\_actions\ E$   $(ad, al) \in action\_loc\ P\ E\ a'$ 

{
  presume  $E \vdash ws\ a \leq_a a'\ E \vdash a' \leq_a a$ 
  with mrw adal a' have  $a' = ws\ a$  unfolding w
    by cases(fastforce dest: antisymPD[OF antisym-action-order] read-actions-not-write-actions
elim!: meta-allE[where x=a'])
  thus  $a' = ws\ a\ a' = ws\ a$  by -
next
  assume  $P, E \vdash ws\ a \leq_{hb} a'\ P, E \vdash a' \leq_{hb} a$ 
  thus  $E \vdash ws\ a \leq_a a'\ E \vdash a' \leq_a a$  using a' by(blast intro: happens-before-into-action-order)+
next
  assume is-volatile P al  $P, E \vdash ws\ a \leq_{so} a'\ P, E \vdash a' \leq_{so} a$ 
  thus  $E \vdash ws\ a \leq_a a'\ E \vdash a' \leq_a a$  by(auto elim: sync-orderE)
}
qed
qed(rule tsa-ok)
qed

```

### 8.8.3 Cut-and-update and sequentially consistent completion

```

inductive foldl-list-all2 ::
  ('b  $\Rightarrow$  'c  $\Rightarrow$  'a  $\Rightarrow$  'a)  $\Rightarrow$  ('b  $\Rightarrow$  'c  $\Rightarrow$  'a  $\Rightarrow$  bool)  $\Rightarrow$  ('b  $\Rightarrow$  'c  $\Rightarrow$  'a  $\Rightarrow$  bool)  $\Rightarrow$  'b list  $\Rightarrow$  'c list  $\Rightarrow$  'a
 $\Rightarrow$  bool
for f and P and Q
where
  foldl-list-all2 f P Q [] [] s
| [] Q x y s; P x y s  $\Longrightarrow$  foldl-list-all2 f P Q xs ys (f x y s)  $\Longrightarrow$  foldl-list-all2 f P Q (x # xs) (y # ys)
s

```

**inductive-simps** foldl-list-all2-simps [simp]:

```

foldl-list-all2 f P Q [] ys s
foldl-list-all2 f P Q xs [] s
foldl-list-all2 f P Q (x # xs) (y # ys) s

```

**inductive-simps** foldl-list-all2-Cons1:

```

foldl-list-all2 f P Q (x # xs) ys s

```

**inductive-simps** foldl-list-all2-Cons2:

```

foldl-list-all2 f P Q xs (y # ys) s

```

**definition** eq-upto-seq-inconsist ::

```

'm prog  $\Rightarrow$  ('addr, 'thread-id) obs-event action list  $\Rightarrow$  ('addr, 'thread-id) obs-event action list
 $\Rightarrow$  ('addr  $\times$  addr-loc  $\rightarrow$  'addr val  $\times$  bool)  $\Rightarrow$  bool

```

**where**

```

eq-upto-seq-inconsist P =
  foldl-list-all2 ( $\lambda ob\ ob'\ vs. mrw\_value\ P\ vs\ ob$ )
    ( $\lambda ob\ ob'\ vs. case\ ob\ of\ NormalAction\ (ReadMem\ ad\ al\ v) \Rightarrow \exists b. vs\ (ad, al) = Some$ )

```

$(v, b) \mid - \Rightarrow \text{True}$   
 $(\lambda ob\ ob' \ vs. \text{ if } (\text{case } ob \text{ of NormalAction } (\text{ReadMem } ad\ al\ v) \Rightarrow \exists b. \ vs\ (ad, al) = \text{Some}$   
 $(v, b) \mid - \Rightarrow \text{True}) \text{ then } ob = ob' \text{ else } ob \approx ob')$

**lemma** *eq-upto-seq-inconsist-simps:*

$eq\text{-upto-seq-inconsist } P \ []\ obs' \ vs \longleftrightarrow obs' = []$   
 $eq\text{-upto-seq-inconsist } P\ obs \ []\ vs \longleftrightarrow obs = []$   
 $eq\text{-upto-seq-inconsist } P\ (ob \# obs) (ob' \# obs') \ vs \longleftrightarrow$   
 $(\text{case } ob \text{ of NormalAction } (\text{ReadMem } ad\ al\ v) \Rightarrow$   
 $\text{ if } (\exists b. \ vs\ (ad, al) = [(v, b)])$   
 $\text{ then } ob = ob' \wedge eq\text{-upto-seq-inconsist } P\ obs\ obs' \ (\text{mrw-value } P\ vs\ ob)$   
 $\text{ else } ob \approx ob'$   
 $\mid - \Rightarrow ob = ob' \wedge eq\text{-upto-seq-inconsist } P\ obs\ obs' \ (\text{mrw-value } P\ vs\ ob))$

**by**(*auto simp add: eq-upto-seq-inconsist-def split: action.split obs-event.split*)

**lemma** *eq-upto-seq-inconsist-Cons1:*

$eq\text{-upto-seq-inconsist } P\ (ob \# obs) \ obs' \ vs \longleftrightarrow$   
 $(\exists ob' \ obs''. \ obs' = ob' \# obs'' \wedge$   
 $(\text{case } ob \text{ of NormalAction } (\text{ReadMem } ad\ al\ v) \Rightarrow$   
 $\text{ if } (\exists b. \ vs\ (ad, al) = [(v, b)])$   
 $\text{ then } ob' = ob \wedge eq\text{-upto-seq-inconsist } P\ obs\ obs'' \ (\text{mrw-value } P\ vs\ ob)$   
 $\text{ else } ob \approx ob'$   
 $\mid - \Rightarrow ob' = ob \wedge eq\text{-upto-seq-inconsist } P\ obs\ obs'' \ (\text{mrw-value } P\ vs\ ob)))$

**unfolding** *eq-upto-seq-inconsist-def*

**by**(*auto split: obs-event.split action.split simp add: foldl-list-all2-Cons1*)

**lemma** *eq-upto-seq-inconsist-appendD:*

**assumes**  $eq\text{-upto-seq-inconsist } P\ (obs \ @\ obs') \ obs'' \ vs$   
**and**  $ta\text{-seq-consist } P\ vs\ (l\text{list-of } obs)$   
**shows**  $\text{length } obs \leq \text{length } obs''$  (**is** ?thesis1)  
**and**  $\text{take } (\text{length } obs) \ obs'' = obs$  (**is** ?thesis2)  
**and**  $eq\text{-upto-seq-inconsist } P\ obs' \ (\text{drop } (\text{length } obs) \ obs'') \ (\text{mrw-values } P\ vs\ obs)$  (**is** ?thesis3)

**using** *assms*

**by**(*induct obs arbitrary: obs'' vs*)(*auto split: action.split-asm obs-event.split-asm simp add: eq-upto-seq-inconsist-Co*)

**lemma** *ta-seq-consist-imp-eq-upto-seq-inconsist-refl:*

$ta\text{-seq-consist } P\ vs\ (l\text{list-of } obs) \Longrightarrow eq\text{-upto-seq-inconsist } P\ obs\ obs\ vs$

**apply**(*induct obs arbitrary: vs*)

**apply**(*auto simp add: eq-upto-seq-inconsist-simps split: action.split obs-event.split*)

**done**

**context notes** *split-paired-Ex [simp del] eq-upto-seq-inconsist-simps [simp] begin*

**lemma** *eq-upto-seq-inconsist-appendI:*

$[eq\text{-upto-seq-inconsist } P\ obs\ OBS\ vs;$   
 $[ta\text{-seq-consist } P\ vs\ (l\text{list-of } obs)]] \Longrightarrow eq\text{-upto-seq-inconsist } P\ obs' \ OBS' \ (\text{mrw-values } P\ vs\ OBS)$   
 $\Longrightarrow eq\text{-upto-seq-inconsist } P\ (obs \ @\ obs') \ (OBS \ @\ OBS') \ vs$

**apply**(*induct obs arbitrary: vs OBS*)

**apply** *simp*

**apply**(*auto simp add: eq-upto-seq-inconsist-Cons1*)

**apply**(*simp split: action.split obs-event.split*)

**apply** *auto*

**done**

**lemma** *eq-upto-seq-inconsist-trans*:

```

  ⌊ eq-upto-seq-inconsist P obs obs' vs; eq-upto-seq-inconsist P obs' obs'' vs ⌋
  ⇒ eq-upto-seq-inconsist P obs obs'' vs
  apply(induction obs arbitrary: obs' obs'' vs)
  apply(clarsimp simp add: eq-upto-seq-inconsist-Cons1)+
  apply(auto split!: action.split obs-event.split if-split-asm)
done

```

**lemma** *eq-upto-seq-inconsist-append2*:

```

  ⌊ eq-upto-seq-inconsist P obs obs' vs; ¬ ta-seq-consist P vs (llist-of obs) ⌋
  ⇒ eq-upto-seq-inconsist P obs (obs' @ obs'') vs
  apply(induction obs arbitrary: obs' vs)
  apply(clarsimp simp add: eq-upto-seq-inconsist-Cons1)+
  apply(auto split!: action.split obs-event.split if-split-asm)
done

```

**end**

**context** *executions-sc-hb* **begin**

**lemma** *ta-seq-consist-mrwI*:

```

  assumes E: E ∈ ℰ
  and wf: P ⊢ (E, ws) ✓
  and mrw: ⋀a. ⌊ enat a < r; a ∈ read-actions E ⌋ ⇒ P, E ⊢ a ∼mrw ws a
  shows ta-seq-consist P Map.empty (lmap snd (ltake r E))

```

**proof**(rule *ta-seq-consist-nthI*)

```

  fix i ad al v
  assume i-len: enat i < llength (lmap snd (ltake r E))
  and E-i: lnth (lmap snd (ltake r E)) i = NormalAction (ReadMem ad al v)
  and sc: ta-seq-consist P Map.empty (ltake (enat i) (lmap snd (ltake r E)))
  from i-len have enat i < r by simp
  with sc have ta-seq-consist P Map.empty (ltake (enat i) (lmap snd E))
  by(simp add: min-def split: if-split-asm)
  hence ns: non-speculative P (λ-. { }) (ltake (enat i) (lmap snd E))
  by(rule ta-seq-consist-into-non-speculative) simp
  from i-len have i ∈ actions E by(simp add: actions-def)
  moreover from E-i i-len have obs-i: action-obs E i = NormalAction (ReadMem ad al v)
  by(simp add: action-obs-def lnth-ltake)
  ultimately have read: i ∈ read-actions E ..
  with i-len have mrw-i: P, E ⊢ i ∼mrw ws i by(auto intro: mrw)
  with E have ws i < i using ns by(rule mrw-before)

```

```

  from mrw-i obs-i obtain adal-w: (ad, al) ∈ action-loc P E (ws i)
  and adal-r: (ad, al) ∈ action-loc P E i
  and write: ws i ∈ write-actions E by cases auto

```

```

  from wf have is-write-seen P E ws by(rule wf-exec-is-write-seenD)
  from is-write-seenD[OF this read obs-i]
  have vw-v: value-written P E (ws i) (ad, al) = v by simp

```

```

  let ?vs = mrw-values P Map.empty (map snd (list-of (ltake (enat (ws i)) E)))

```

```

from  $\langle ws\ i < i \rangle$   $i$ -len have  $enat\ (ws\ i) < llength\ (ltake\ (enat\ i)\ E)$ 
  by(simp add: less-trans[where  $y=enat\ i$ ])
hence  $ltake\ (enat\ i)\ E = lappend\ (ltake\ (enat\ (ws\ i))\ (ltake\ (enat\ i)\ E))\ (LCons\ (lnth\ (ltake\ (enat\ i)\ E)\ (ws\ i))\ (ldropn\ (Suc\ (ws\ i))\ (ltake\ (enat\ i)\ E)))$ 
  by(simp only: ldropn-Suc-conv-ldropn lappend-ltake-enat-ldropn)
also have  $\dots = lappend\ (ltake\ (enat\ (ws\ i))\ E)\ (LCons\ (lnth\ E\ (ws\ i))\ (ldropn\ (Suc\ (ws\ i))\ (ltake\ (enat\ i)\ E)))$ 
  using  $\langle ws\ i < i \rangle$   $i$ -len  $\langle enat\ (ws\ i) < llength\ (ltake\ (enat\ i)\ E) \rangle$ 
  by (simp add: lnth-ltake)
finally have  $r\text{-}E: ltake\ (enat\ i)\ E = \dots$ 

have  $mrw\text{-}values\ P\ Map.empty\ (list\ of\ (ltake\ (enat\ i)\ (lmap\ snd\ (ltake\ r\ E))))\ (ad,\ al)$ 
   $= mrw\text{-}values\ P\ Map.empty\ (map\ snd\ (list\ of\ (ltake\ (enat\ i)\ E)))\ (ad,\ al)$ 
  using  $\langle enat\ i < r \rangle$  by(auto simp add: min-def)
also have  $\dots = mrw\text{-}values\ P\ (mrw\text{-}value\ P\ ?vs\ (snd\ (lnth\ E\ (ws\ i))))\ (map\ snd\ (list\ of\ (ldropn\ (Suc\ (ws\ i))\ (ltake\ (enat\ i)\ E))))\ (ad,\ al)$ 
  by(subst  $r\text{-}E$ )(simp add: list-of-lappend)
also have  $\dots = mrw\text{-}value\ P\ ?vs\ (snd\ (lnth\ E\ (ws\ i)))\ (ad,\ al)$ 
proof(rule  $mrw\text{-}values\text{-}no\text{-}write\text{-}unchanged$ )
  fix  $wa$ 
  assume  $wa: wa \in set\ (map\ snd\ (list\ of\ (ldropn\ (Suc\ (ws\ i))\ (ltake\ (enat\ i)\ E))))$ 
  and  $is\text{-}write\text{-}action\ wa\ (ad,\ al) \in action\text{-}loc\text{-}aux\ P\ wa$ 

from  $wa$  obtain  $w$  where  $w < length\ (map\ snd\ (list\ of\ (ldropn\ (Suc\ (ws\ i))\ (ltake\ (enat\ i)\ E))))$ 
  and  $map\ snd\ (list\ of\ (ldropn\ (Suc\ (ws\ i))\ (ltake\ (enat\ i)\ E)))\ !\ w = wa$ 
  unfolding  $in\text{-}set\text{-}conv\text{-}nth$  by blast
moreover hence  $Suc\ (ws\ i + w) < i$  (is  $?w < -$ ) using  $i$ -len
  by(cases  $llength\ E$ )(simp-all add: length-list-of-conv-the-enat)
ultimately have  $obs\text{-}w': action\text{-}obs\ E\ ?w = wa$  using  $i$ -len
  by(simp add: action-obs-def lnth-ltake less-trans[where  $y=enat\ i$ ] ac-simps)
from  $\langle ?w < i \rangle$   $i$ -len have  $?w \in actions\ E$ 
  by(simp add: actions-def less-trans[where  $y=enat\ i$ ])
with  $\langle is\text{-}write\text{-}action\ wa \rangle$   $obs\text{-}w'\ \langle (ad,\ al) \in action\text{-}loc\text{-}aux\ P\ wa \rangle$ 
have  $write': ?w \in write\text{-}actions\ E$ 
  and  $adal': (ad,\ al) \in action\text{-}loc\ P\ E\ ?w$ 
  by(auto intro: write-actions.intros)

from  $\langle ?w < i \rangle$   $\langle i \in read\text{-}actions\ E \rangle$   $\langle ?w \in actions\ E \rangle$ 
have  $E \vdash ?w \leq a\ i$  by(auto simp add: action-order-def elim: read-actions.cases)

from  $mrw\text{-}i\ adal\text{-}r\ write'\ adal'$ 
have  $E \vdash ?w \leq a\ ws\ i \vee E \vdash i \leq a\ ?w$  by(rule most-recent-write-recent)
hence  $E \vdash ?w \leq a\ ws\ i$ 
proof
  assume  $E \vdash i \leq a\ ?w$ 
  with  $\langle E \vdash ?w \leq a\ i \rangle$  have  $?w = i$  by(rule antisymPD[OF antisym-action-order])
  with  $write'\ read$  have  $False$  by(auto dest: read-actions-not-write-actions)
  thus  $?thesis$  ..
qed

from  $adal\text{-}w\ write$  have  $mrw\text{-}value\ P\ ?vs\ (snd\ (lnth\ E\ (ws\ i)))\ (ad,\ al) \neq None$ 
  by(cases  $snd\ (lnth\ E\ (ws\ i))$  rule:  $mrw\text{-}value\text{-}cases$ )
  (auto simp add: action-obs-def split: if-split-asm elim: write-actions.cases)
then obtain  $b\ v$  where  $vb: mrw\text{-}value\ P\ ?vs\ (snd\ (lnth\ E\ (ws\ i)))\ (ad,\ al) = Some\ (v,\ b)$  by auto

```

```

moreover
from  $\langle E \vdash ?w \leq_a ws \ i \rangle \text{ obs-}w'$ 
have  $is\text{-}new\text{-}action \ wa \ \neg \ is\text{-}new\text{-}action \ (action\text{-}obs \ E \ (ws \ i))$  by( $auto \ elim!:$   $action\text{-}orderE$ )
from  $\langle \neg \ is\text{-}new\text{-}action \ (action\text{-}obs \ E \ (ws \ i)) \rangle \text{ write } adal\text{-}w$ 
obtain  $v'$  where  $action\text{-}obs \ E \ (ws \ i) = NormalAction \ (WriteMem \ ad \ al \ v')$ 
  by( $auto \ elim!:$   $write\text{-}actions.cases \ is\text{-}write\text{-}action.cases$ )
with  $vb$  have  $b$  by( $simp \ add:$   $action\text{-}obs\text{-}def$ )
with  $\langle is\text{-}new\text{-}action \ wa \rangle vb$ 
  show  $case \ mrw\text{-}value \ P \ ?vs \ (snd \ (lth \ E \ (ws \ i))) \ (ad, \ al) \ of \ None \Rightarrow \ False \mid \lfloor (v, \ b) \rfloor \Rightarrow \ b \wedge$ 
 $is\text{-}new\text{-}action \ wa$  by  $simp$ 
qed
also {
  fix  $v$ 
  assume  $?vs \ (ad, \ al) = Some \ (v, \ True)$ 
  and  $is\text{-}new\text{-}action \ (action\text{-}obs \ E \ (ws \ i))$ 

  from  $mrw\text{-}values\text{-}eq\text{-}SomeD[OF \ this(1)]$ 
  obtain  $wa$  where  $wa \in set \ (map \ snd \ (list\text{-}of \ (ltake \ (enat \ (ws \ i)) \ E)))$ 
    and  $is\text{-}write\text{-}action \ wa$ 
    and  $(ad, \ al) \in action\text{-}loc\text{-}aux \ P \ wa$ 
    and  $\neg \ is\text{-}new\text{-}action \ wa$  by( $fastforce \ simp \ del:$   $set\text{-}map$ )
  moreover then obtain  $w$  where  $w: w < ws \ i$  and  $wa: wa = snd \ (lth \ E \ w)$ 
  unfolding  $in\text{-}set\text{-}conv\text{-}nth$  by( $cases \ llength \ E$ )( $auto \ simp \ add:$   $lth\text{-}ltake \ length\text{-}list\text{-}of\text{-}conv\text{-}the\text{-}enat$ )
  ultimately have  $w \in write\text{-}actions \ E \ action\text{-}obs \ E \ w = wa \ (ad, \ al) \in action\text{-}loc \ P \ E \ w$ 
    using  $\langle ws \ i \in write\text{-}actions \ E \rangle$ 
    by( $auto \ intro!:$   $write\text{-}actions.intros \ simp \ add:$   $actions\text{-}def \ less\text{-}trans$ [where  $y=enat \ (ws \ i)$ ]  $action\text{-}obs\text{-}def \ elim!:$   $write\text{-}actions.cases$ )
  with  $mrw\text{-}i \ adal\text{-}r$  have  $E \vdash w \leq_a ws \ i \vee E \vdash i \leq_a w$  by  $\neg$ ( $rule \ most\text{-}recent\text{-}write\text{-}recent$ )
  hence  $False$ 
  proof
    assume  $E \vdash w \leq_a ws \ i$ 
    moreover from  $\langle \neg \ is\text{-}new\text{-}action \ wa \rangle \langle is\text{-}new\text{-}action \ (action\text{-}obs \ E \ (ws \ i)) \rangle \text{ write } w \ wa \langle w \in$ 
 $write\text{-}actions \ E \rangle$ 
    have  $E \vdash ws \ i \leq_a w$  by( $auto \ simp \ add:$   $action\text{-}order\text{-}def \ action\text{-}obs\text{-}def$ )
    ultimately have  $w = ws \ i$  by( $rule \ antisymPD[OF \ antisym\text{-}action\text{-}order]$ )
    with  $\langle w < ws \ i \rangle$  show  $False$  by  $simp$ 
  next
    assume  $E \vdash i \leq_a w$ 
    moreover from  $\langle w \in write\text{-}actions \ E \rangle \langle w < ws \ i \rangle \langle ws \ i < i \rangle \text{ read}$ 
    have  $E \vdash w \leq_a i$  by( $auto \ simp \ add:$   $action\text{-}order\text{-}def \ elim:$   $read\text{-}actions.cases$ )
    ultimately have  $i = w$  by( $rule \ antisymPD[OF \ antisym\text{-}action\text{-}order]$ )
    with  $\langle w < ws \ i \rangle \langle ws \ i < i \rangle$  show  $False$  by  $simp$ 
  qed }
then obtain  $b$  where  $\dots = Some \ (v, \ b)$  using  $vw\text{-}v \text{ write } adal\text{-}w$ 
  apply( $atomize\text{-}elim$ )
  apply( $auto \ simp \ add:$   $action\text{-}obs\text{-}def \ value\text{-}written\text{-}def \ write\text{-}actions\text{-}iff$ )
  apply( $erule \ is\text{-}write\text{-}action.cases$ )
  apply  $auto$ 
  done
finally show  $\exists b. mrw\text{-}values \ P \ Map.empty \ (list\text{-}of \ (ltake \ (enat \ i) \ (lmap \ snd \ (ltake \ r \ E)))) \ (ad, \ al)$ 
 $= \lfloor (v, \ b) \rfloor$ 
  by  $blast$ 
qed

```

end

context *jmm-multithreaded* begin

**definition** *complete-sc* :: ('l, 'thread-id, 'x, 'm, 'w) state  $\Rightarrow$  ('addr  $\times$  addr-loc  $\rightarrow$  'addr val  $\times$  bool)  $\Rightarrow$  ('thread-id  $\times$  ('l, 'thread-id, 'x, 'm, 'w, ('addr, 'thread-id) obs-event action) thread-action) llist

where

*complete-sc* *s vs* = *unfold-llist*  
 $(\lambda(s, vs). \forall t \ ta \ s'. \neg s -t \triangleright ta \rightarrow s')$   
 $(\lambda(s, vs). \text{fst } (SOME ((t, ta), s'). s -t \triangleright ta \rightarrow s' \wedge ta\text{-seq-consist } P \text{ vs } (llist\text{-of } \{ta\}_o)))$   
 $(\lambda(s, vs). \text{let } ((t, ta), s') = SOME ((t, ta), s'). s -t \triangleright ta \rightarrow s' \wedge ta\text{-seq-consist } P \text{ vs } (llist\text{-of } \{ta\}_o)$   
 $\text{in } (s', mrw\text{-values } P \text{ vs } \{ta\}_o)$   
 $(s, vs)$

**definition** *sc-completion* :: ('l, 'thread-id, 'x, 'm, 'w) state  $\Rightarrow$  ('addr  $\times$  addr-loc  $\rightarrow$  'addr val  $\times$  bool)  $\Rightarrow$  bool

where

*sc-completion* *s vs*  $\longleftrightarrow$   
 $(\forall ttas \ s' \ t \ x \ ta \ x' \ m'. \\$   
 $s -\triangleright ttas \rightarrow * s' \rightarrow ta\text{-seq-consist } P \text{ vs } (llist\text{-of } (concat (map (\lambda(t, ta). \{ta\}_o) ttas))) \rightarrow$   
 $thr \ s' \ t = \lfloor (x, no\text{-wait-locks}) \rfloor \rightarrow t \vdash (x, shr \ s') -ta \rightarrow (x', m') \rightarrow actions\text{-ok } s' \ t \ ta \rightarrow$   
 $(\exists ta' \ x'' \ m''. t \vdash (x, shr \ s') -ta' \rightarrow (x'', m'') \wedge actions\text{-ok } s' \ t \ ta' \wedge$   
 $ta\text{-seq-consist } P (mrw\text{-values } P \text{ vs } (concat (map (\lambda(t, ta). \{ta\}_o) ttas))) (llist\text{-of } \{ta'\}_o)))$

**lemma** *sc-completionD*:

$\llbracket sc\text{-completion } s \text{ vs}; s -\triangleright ttas \rightarrow * s'; ta\text{-seq-consist } P \text{ vs } (llist\text{-of } (concat (map (\lambda(t, ta). \{ta\}_o) ttas))) \rrbracket$

$thr \ s' \ t = \lfloor (x, no\text{-wait-locks}) \rfloor; t \vdash (x, shr \ s') -ta \rightarrow (x', m'); actions\text{-ok } s' \ t \ ta \rrbracket$   
 $\implies \exists ta' \ x'' \ m''. t \vdash (x, shr \ s') -ta' \rightarrow (x'', m'') \wedge actions\text{-ok } s' \ t \ ta' \wedge$   
 $ta\text{-seq-consist } P (mrw\text{-values } P \text{ vs } (concat (map (\lambda(t, ta). \{ta\}_o) ttas))) (llist\text{-of } \{ta'\}_o)$

**unfolding** *sc-completion-def* by *blast*

**lemma** *sc-completionI*:

$(\bigwedge ttas \ s' \ t \ x \ ta \ x' \ m'. \\$   
 $\llbracket s -\triangleright ttas \rightarrow * s'; ta\text{-seq-consist } P \text{ vs } (llist\text{-of } (concat (map (\lambda(t, ta). \{ta\}_o) ttas))) \rrbracket;$   
 $thr \ s' \ t = \lfloor (x, no\text{-wait-locks}) \rfloor; t \vdash (x, shr \ s') -ta \rightarrow (x', m'); actions\text{-ok } s' \ t \ ta \rrbracket$   
 $\implies \exists ta' \ x'' \ m''. t \vdash (x, shr \ s') -ta' \rightarrow (x'', m'') \wedge actions\text{-ok } s' \ t \ ta' \wedge$   
 $ta\text{-seq-consist } P (mrw\text{-values } P \text{ vs } (concat (map (\lambda(t, ta). \{ta\}_o) ttas))) (llist\text{-of } \{ta'\}_o)$   
 $\implies sc\text{-completion } s \text{ vs}$

**unfolding** *sc-completion-def* by *blast*

**lemma** *sc-completion-shift*:

**assumes** *sc-c*: *sc-completion* *s vs*

**and**  $\tau Red$ :  $s -\triangleright ttas \rightarrow * s'$

**and** *sc*:  $ta\text{-seq-consist } P \text{ vs } (lconcat (lmap (\lambda(t, ta). llist\text{-of } \{ta\}_o) (llist\text{-of } ttas)))$

**shows** *sc-completion*  $s' (mrw\text{-values } P \text{ vs } (concat (map (\lambda(t, ta). \{ta\}_o) ttas)))$

**proof**(rule *sc-completionI*)

**fix**  $ttas' \ s'' \ t \ x \ ta \ x' \ m'$

**assume**  $\tau Red'$ :  $s' -\triangleright ttas' \rightarrow * s''$

**and** *sc'*:  $ta\text{-seq-consist } P (mrw\text{-values } P \text{ vs } (concat (map (\lambda(t, ta). \{ta\}_o) ttas))) (llist\text{-of } (concat (map (\lambda(t, ta). \{ta\}_o) ttas')))$

**and** *red*:  $thr \ s'' \ t = \lfloor (x, no\text{-wait-locks}) \rfloor \ t \vdash \langle x, shr \ s'' \rangle -ta \rightarrow \langle x', m' \rangle \ actions\text{-ok } s'' \ t \ ta$

**from**  $\tau Red \ \tau Red'$  **have**  $s -\triangleright ttas @ ttas' \rightarrow * s''$  **unfolding** *RedT-def* **by** (rule *rtrancl3p-trans*)



**moreover from**  $sc\ sc'$  **have**  $ta\text{-seq-consist}\ P\ vs\ (l\text{list-of}\ (\text{concat}\ (\text{map}\ (\lambda(t, ta). \llbracket ta \rrbracket_o) (ttas\ @\ ttas'))))$   
**apply**( $\text{simp add: lappend-llist-of-llist-of[symmetric] ta-seq-consist-lappend del: lappend-llist-of-llist-of}$ )  
**apply**( $\text{simp add: lconcat-llist-of[symmetric] lmap-llist-of[symmetric] llist.map-comp o-def split-def del: lmap-llist-of}$ )  
**done**  
**ultimately**  
**show**  $\exists ta' x'' m''. t \vdash \langle x, shr\ s' \rangle - ta' \rightarrow \langle x'', m'' \rangle \wedge \text{actions-ok}\ s''\ t\ ta' \wedge$   
 $ta\text{-seq-consist}\ P\ (\text{mrw-values}\ P\ (\text{mrw-values}\ P\ vs\ (\text{concat}\ (\text{map}\ (\lambda(t, ta). \llbracket ta \rrbracket_o) ttas)))\ (\text{concat}\$   
 $(\text{map}\ (\lambda(t, ta). \llbracket ta \rrbracket_o) ttas'))\ (l\text{list-of}\ \llbracket ta' \rrbracket_o)$   
**using** **red unfolding**  $\text{foldl-append[symmetric] concat-append[symmetric] map-append[symmetric]}$   
**by**( $\text{rule sc-completionD[OF sc-c]}$ )  
**qed**

**lemma** *complete-sc-in-Runs*:

**assumes**  $\text{cau: sc-completion}\ s\ vs$

**and**  $ta\text{-seq-consist-convert-RA: } \bigwedge vs\ ln. ta\text{-seq-consist}\ P\ vs\ (l\text{list-of}\ (\text{convert-RA}\ ln))$

**shows**  $\text{mthr.Runs}\ s\ (\text{complete-sc}\ s\ vs)$

**proof** –

**let**  $?ttas' = \lambda ttas' :: ('thread\text{-id} \times ('l, 'thread\text{-id}, 'x, 'm, 'w, ('addr, 'thread\text{-id})\ \text{obs-event}\ \text{action})\ \text{thread-action})$   
 $\text{list.}$

$\text{concat}\ (\text{map}\ (\lambda(t, ta). \llbracket ta \rrbracket_o) ttas')$

**let**  $?vs\ ttas' = \text{mrw-values}\ P\ vs\ (?ttas'\ ttas')$

**define**  $s'\ vs'$

**and**  $ttas :: ('thread\text{-id} \times ('l, 'thread\text{-id}, 'x, 'm, 'w, ('addr, 'thread\text{-id})\ \text{obs-event}\ \text{action})\ \text{thread-action})$

$\text{list}$

**where**  $s' = s$  **and**  $vs' = vs$  **and**  $ttas = []$

**hence**  $s \rightarrow^* ttas \rightarrow^* s'$   $ta\text{-seq-consist}\ P\ vs\ (l\text{list-of}\ (?ttas'\ ttas))$  **by** *auto*

**hence**  $\text{mthr.Runs}\ s'\ (\text{complete-sc}\ s'\ (?vs\ ttas'))$

**proof**(*coinduction arbitrary: s' ttas rule: mthr.Runs.coinduct*)

**case** ( $\text{Runs}\ s'\ ttas'$ )

**note**  $\text{Red} = \langle s \rightarrow^* ttas' \rightarrow^* s' \rangle$

**and**  $sc = \langle ta\text{-seq-consist}\ P\ vs\ (l\text{list-of}\ (?ttas'\ ttas')) \rangle$

**show**  $?case$

**proof**( $\text{cases } \exists t'\ ta'\ s''. s' - t' \triangleright ta' \rightarrow s''$ )

**case** *False*

**hence**  $?Stuck$  **by**( $\text{simp add: complete-sc-def}$ )

**thus**  $?thesis\ ..$

**next**

**case** *True*

**let**  $?proceed = \lambda((t', ta'), s''). s' - t' \triangleright ta' \rightarrow s'' \wedge ta\text{-seq-consist}\ P\ (?vs\ ttas')\ (l\text{list-of}\ \llbracket ta' \rrbracket_o)$

**from** *True* **obtain**  $t'\ ta'\ s''$  **where**  $\text{red: } s' - t' \triangleright ta' \rightarrow s''$  **by**(*auto*)

**then obtain**  $ta''\ s'''$  **where**  $s' - t' \triangleright ta'' \rightarrow s'''$

**and**  $ta\text{-seq-consist}\ P\ (?vs\ ttas')\ (l\text{list-of}\ \llbracket ta'' \rrbracket_o)$

**proof**(*cases*)

**case** ( $\text{redT-normal}\ x\ x'\ m'$ )

**note**  $\text{red} = \langle t' \vdash \langle x, shr\ s' \rangle - ta' \rightarrow \langle x', m' \rangle \rangle$

**and**  $ts''t' = \langle thr\ s'\ t' = \lfloor (x, \text{no-wait-locks}) \rfloor \rangle$

**and**  $\text{aok} = \langle \text{actions-ok}\ s'\ t'\ ta' \rangle$

**and**  $s'' = \langle \text{redT-upd}\ s'\ t'\ ta'\ x'\ m'\ s'' \rangle$

**from**  $\text{sc-completionD[OF cau Red sc ts''t' red aok]}$

**obtain**  $ta''\ x''\ m''$  **where**  $\text{red}': t' \vdash \langle x, shr\ s' \rangle - ta'' \rightarrow \langle x'', m'' \rangle$

**and**  $\text{aok}': \text{actions-ok}\ s'\ t'\ ta''$

```

    and sc': ta-seq-consist P (?vs ttas') (llist-of {ta''}_o) by blast
    from redT-updWs-total obtain ws' where redT-updWs t' (wset s') {ta''}_w ws' ..
    then obtain s''' where redT-upd s' t' ta'' x'' m'' s''' by fastforce
    with red' ts''t' aok' have s' -t'▷ta''→ s''' ..
    thus thesis using sc' by(rule that)
  next
    case redT-acquire
    thus thesis by(simp add: that[OF red] ta-seq-consist-convert-RA)
  qed
  hence ?proceed ((t', ta''), s''') using Red by(auto)
  hence *: ?proceed (Eps ?proceed) by(rule someI)
  moreover from Red * have s -▷ttas' @ [fst (Eps ?proceed)]→* snd (Eps ?proceed)
    by(auto simp add: split-beta RedT-def intro: rtrancl3p-step)
  moreover from True
    have complete-sc s' (?vs ttas') = LCons (fst (Eps ?proceed)) (complete-sc (snd (Eps ?proceed))
      (?vs (ttas' @ [fst (Eps ?proceed)])))
    unfolding complete-sc-def by(simp add: split-def)
  moreover from sc <?proceed (Eps ?proceed)>
    have ta-seq-consist P vs (llist-of (?ttas' (ttas' @ [fst (Eps ?proceed)])))
    unfolding map-append concat-append lappend-llist-of-llist-of[symmetric]
    by(subst ta-seq-consist-lappend)(auto simp add: split-def)
  ultimately have ?Step
    by(fastforce intro: exI[where x=ttas' @ [fst (Eps ?proceed)]] simp del: split-paired-Ex)
  thus ?thesis by simp
  qed
  qed
  thus ?thesis by(simp add: s'-def ttas-def)
  qed

lemma complete-sc-ta-seq-consist:
  assumes cau: sc-completion s vs
  and ta-seq-consist-convert-RA: ∧vs ln. ta-seq-consist P vs (llist-of (convert-RA ln))
  shows ta-seq-consist P vs (lconcat (lmap (λ(t, ta). llist-of {ta}_o) (complete-sc s vs)))
proof -
  define vs' where vs' = vs
  let ?obs = λttas'. lconcat (lmap (λ(t, ta). llist-of {ta}_o) ttas)
  define obs where obs = ?obs (complete-sc s vs)
  define a where a = complete-sc s vs'
  let ?ttas' = λttas'::('l, 'thread-id, 'x, 'm, 'w, ('addr, 'thread-id) obs-event action) thread-action)
  list.
    concat (map (λ(t, ta). {ta}_o) ttas')
  let ?vs = λttas'. mrw-values P vs (?ttas' ttas')
  from vs'-def obs-def
  have s -▷[]→* s ta-seq-consist P vs (llist-of (?ttas' [])) vs' = ?vs [] by(auto)
  hence ∃ s' ttas'. obs = ?obs (complete-sc s' vs') ∧ s -▷ttas'→* s' ∧
    ta-seq-consist P vs (llist-of (?ttas' ttas')) ∧ vs' = ?vs ttas' ∧
    a = complete-sc s' vs'
    unfolding obs-def vs'-def a-def by metis
  moreover have wf (inv-image {(m, n). m < n} (llength ∘ ltakeWhile (λ(t, ta). {ta}_o = [])))
    (is wf ?R) by(rule wf-inv-image)(rule wellorder-class.wf)
  ultimately show ta-seq-consist P vs' obs
  proof(coinduct vs' obs a rule: ta-seq-consist-coinduct-append-wf)
    case (ta-seq-consist vs' obs a)
    then obtain s' ttas' where obs-def: obs = ?obs (complete-sc s' (?vs ttas'))

```

and *Red*:  $s \rightarrow^* ttas' \rightarrow^* s'$   
 and *sc*:  $ta\text{-seq-consist } P \text{ vs } (l\text{list-of } (?ttas' \text{ } ttas'))$   
 and *vs'-def*:  $vs' = ?vs \text{ } ttas'$   
 and *a-def*:  $a = \text{complete-sc } s' \text{ vs' by blast}$

show *?case*

proof(*cases*  $\exists t' \text{ } ta' \text{ } s''. s' - t' \triangleright ta' \rightarrow s''$ )

case *False*

hence *obs* = *LNil* **unfolding** *obs-def* *complete-sc-def* **by** *simp*

hence *?LNil* **unfolding** *obs-def* **by** *auto*

thus *?thesis* ..

next

case *True*

let *?proceed* =  $\lambda((t', ta'), s''). s' - t' \triangleright ta' \rightarrow s'' \wedge ta\text{-seq-consist } P \text{ } (?vs \text{ } ttas') (l\text{list-of } \llbracket ta' \rrbracket_o)$

let *?tta* = *fst* (*Eps* *?proceed*)

let *?s'* = *snd* (*Eps* *?proceed*)

from *True* **obtain**  $t' \text{ } ta' \text{ } s''$  **where** *red*:  $s' - t' \triangleright ta' \rightarrow s''$  **by** *blast*

then **obtain**  $ta'' \text{ } s'''$  **where**  $s' - t' \triangleright ta'' \rightarrow s'''$

and  $ta\text{-seq-consist } P \text{ } (?vs \text{ } ttas') (l\text{list-of } \llbracket ta'' \rrbracket_o)$

proof(*cases*)

case (*redT-normal*  $x \text{ } x' \text{ } m'$ )

**note** *red* =  $\langle t' \vdash \langle x, \text{shr } s' \rangle - ta' \rightarrow \langle x', m' \rangle \rangle$

and  $ts''t' = \langle \text{thr } s' \text{ } t' = \lfloor (x, \text{no-wait-locks}) \rfloor \rangle$

and *aok* =  $\langle \text{actions-ok } s' \text{ } t' \text{ } ta' \rangle$

and  $s''' = \langle \text{redT-upd } s' \text{ } t' \text{ } ta' \text{ } x' \text{ } m' \text{ } s'' \rangle$

from *sc-completionD*[*OF* *cau Red sc ts''t' red aok*]

**obtain**  $ta'' \text{ } x'' \text{ } m''$  **where** *red'*:  $t' \vdash \langle x, \text{shr } s' \rangle - ta'' \rightarrow \langle x'', m'' \rangle$

and *aok'*:  $\text{actions-ok } s' \text{ } t' \text{ } ta''$

and *sc'*:  $ta\text{-seq-consist } P \text{ } (?vs \text{ } ttas') (l\text{list-of } \llbracket ta'' \rrbracket_o)$  **by** *blast*

from *redT-updWs-total* **obtain**  $ws'$  **where** *redT-updWs*  $t' (wset \text{ } s') \llbracket ta'' \rrbracket_w ws' ..$

then **obtain**  $s'''$  **where** *redT-upd*  $s' \text{ } t' \text{ } ta'' \text{ } x'' \text{ } m'' \text{ } s'''$  **by** *fastforce*

with *red'*  $ts''t'$  *aok'* **have**  $s' - t' \triangleright ta'' \rightarrow s''' ..$

thus *thesis* **using** *sc'* **by**(*rule that*)

next

case *redT-acquire*

thus *thesis* **by**(*simp add: that*[*OF red*] *ta-seq-consist-convert-RA*)

qed

hence *?proceed*  $((t', ta''), s''')$  **by** *auto*

hence *?proceed* (*Eps* *?proceed*) **by**(*rule someI*)

show *?thesis*

proof(*cases* *obs* = *LNil*)

case *True* **thus** *?thesis* ..

next

case *False*

from *True*

**have** *csc-unfold*:  $\text{complete-sc } s' \text{ } (?vs \text{ } ttas') = L\text{Cons } ?tta (\text{complete-sc } ?s' \text{ } (?vs \text{ } (ttas' @ \llbracket ?tta \rrbracket)))$

**unfolding** *complete-sc-def* **by**(*simp add: split-def*)

**hence** *obs* =  $\text{lappend } (l\text{list-of } \llbracket \text{snd } ?tta \rrbracket_o) \text{ } (?obs (\text{complete-sc } ?s' \text{ } (?vs \text{ } (ttas' @ \llbracket ?tta \rrbracket))))$

**using** *obs-def* **by**(*simp add: split-beta*)

**moreover** **have**  $ta\text{-seq-consist } P \text{ vs' } (l\text{list-of } \llbracket \text{snd } ?tta \rrbracket_o)$

**using**  $\langle ?proceed \text{ } (Eps \text{ } ?proceed) \rangle \text{ vs'-def}$  **by**(*clarsimp simp add: split-beta*)

**moreover** {

**assume**  $l\text{list-of } \llbracket \text{snd } ?tta \rrbracket_o = L\text{Nil}$

**moreover from** *obs-def*  $\langle \text{obs} \neq \text{LNil} \rangle$   
**have** *lfinite* (*ltakeWhile* ( $\lambda(t, ta). \llbracket ta \rrbracket_o = []$ ) (*complete-sc*  $s' (\text{?vs } ttas')$ ))  
**unfolding** *lfinite-ltakeWhile* **by**(*fastforce simp add: split-def lconcat-eq-LNil*)  
**ultimately have** (*complete-sc*  $?s' (\text{?vs } (ttas' @ [\text{?tta}])), a \in ?R$ )  
**unfolding** *a-def vs'-def csc-unfold*  
**by**(*clarsimp simp add: split-def llist-of-eq-LNil-conv*)(*auto simp add: lfinite-eq-range-llist-of*)  
**}**  
**moreover have** *?obs* (*complete-sc*  $?s' (\text{?vs } (ttas' @ [\text{?tta}]))) = ?obs (*complete-sc*  $?s' (\text{mrw-values } P \text{ vs' } (\text{list-of } (\text{llist-of } \llbracket \text{snd ?tta} \rrbracket_o)))$ ))  
**unfolding** *vs'-def* **by**(*simp add: split-def*)  
**moreover from**  $\langle ?\text{proceed } (Eps \text{ ?proceed}) \rangle \text{ Red}$   
**have**  $s \rightarrow ttas' @ [\text{?tta}] \rightarrow^* ?s' \text{ by } (\text{auto simp add: RedT-def split-def intro: rtrancl3p-step})$   
**moreover from**  $sc \langle ?\text{proceed } (Eps \text{ ?proceed}) \rangle$   
**have** *ta-seq-consist*  $P \text{ vs } (\text{llist-of } (?ttas' (ttas' @ [\text{?tta}])))$   
**by**(*clarsimp simp add: split-def ta-seq-consist-lappend lappend-llist-of-llist-of[symmetric] simp del: lappend-llist-of-llist-of*)  
**moreover have** *mrw-values*  $P \text{ vs' } (\text{list-of } (\text{llist-of } \llbracket \text{snd ?tta} \rrbracket_o)) = ?vs$  ( $ttas' @ [\text{?tta}]$ )  
**unfolding** *vs'-def* **by**(*simp add: split-def*)  
**moreover have** *complete-sc*  $?s' (\text{?vs } (ttas' @ [\text{?tta}]))) = \text{complete-sc } ?s' (\text{mrw-values } P \text{ vs' } (\text{list-of } (\text{llist-of } \llbracket \text{snd ?tta} \rrbracket_o)))$   
**unfolding** *vs'-def* **by**(*simp add: split-def*)  
**ultimately have** *?lappend* **by** *blast*  
**thus** *?thesis* ..  
**qed**  
**qed**  
**qed**  
**qed**$

**lemma** *sequential-completion-Runs*:

**assumes** *sc-completion*  $s \text{ vs}$   
**and**  $\bigwedge \text{vs } ln. \text{ ta-seq-consist } P \text{ vs } (\text{llist-of } (\text{convert-RA } ln))$   
**shows**  $\exists ttas. \text{ mthr.Runs } s \text{ ttas} \wedge \text{ ta-seq-consist } P \text{ vs } (\text{lconcat } (\text{lmap } (\lambda(t, ta). \llbracket ta \rrbracket_o) \text{ ttas}))$   
**using** *complete-sc-ta-seq-consist[OF assms] complete-sc-in-Runs[OF assms]*  
**by** *blast*

**definition** *cut-and-update*  $:: ('l, 'thread-id, 'x, 'm, 'w) \text{ state} \Rightarrow ('addr \times \text{addr-loc} \rightarrow 'addr \text{ val} \times \text{bool}) \Rightarrow \text{bool}$

**where**

*cut-and-update*  $s \text{ vs} \longleftrightarrow$   
 $(\forall ttas \ s' \ t \ x \ ta \ x' \ m'. \quad$   
 $s \rightarrow ttas \rightarrow^* s' \longrightarrow \text{ta-seq-consist } P \text{ vs } (\text{llist-of } (\text{concat } (\text{map } (\lambda(t, ta). \llbracket ta \rrbracket_o) \text{ ttas}))) \longrightarrow$   
 $\text{thr } s' \ t = [(x, \text{no-wait-locks})] \longrightarrow t \vdash (x, \text{shr } s') \rightarrow ta \rightarrow (x', m') \longrightarrow \text{actions-ok } s' \ t \ ta \longrightarrow$   
 $(\exists ta' \ x'' \ m''. t \vdash (x, \text{shr } s') \rightarrow ta' \rightarrow (x'', m'') \wedge \text{actions-ok } s' \ t \ ta' \wedge$   
 $\text{ta-seq-consist } P (\text{mrw-values } P \text{ vs } (\text{concat } (\text{map } (\lambda(t, ta). \llbracket ta \rrbracket_o) \text{ ttas}))) (\text{llist-of } \llbracket ta' \rrbracket_o)$   
 $\wedge$   
 $\text{eq-upto-seq-inconsist } P \llbracket ta \rrbracket_o \llbracket ta' \rrbracket_o (\text{mrw-values } P \text{ vs } (\text{concat } (\text{map } (\lambda(t, ta). \llbracket ta \rrbracket_o) \text{ ttas}))))))$

**lemma** *cut-and-updateI[?intro?]*:

$(\bigwedge ttas \ s' \ t \ x \ ta \ x' \ m'. \quad$   
 $\llbracket s \rightarrow ttas \rightarrow^* s'; \text{ ta-seq-consist } P \text{ vs } (\text{llist-of } (\text{concat } (\text{map } (\lambda(t, ta). \llbracket ta \rrbracket_o) \text{ ttas})))$   
 $\text{thr } s' \ t = [(x, \text{no-wait-locks})]; t \vdash (x, \text{shr } s') \rightarrow ta \rightarrow (x', m'); \text{actions-ok } s' \ t \ ta \rrbracket$   
 $\implies \exists ta' \ x'' \ m''. t \vdash (x, \text{shr } s') \rightarrow ta' \rightarrow (x'', m'') \wedge \text{actions-ok } s' \ t \ ta' \wedge$

$\llbracket ta' \rrbracket_o \wedge$   
 $ta\text{-seq-consist } P \text{ (mrw-values } P \text{ vs (concat (map } (\lambda(t, ta). \llbracket ta \rrbracket_o) \text{ ttas))) (llist-of}$   
 $eq\text{-upto-seq-inconsist } P \llbracket ta \rrbracket_o \llbracket ta' \rrbracket_o \text{ (mrw-values } P \text{ vs (concat (map } (\lambda(t, ta). \llbracket ta \rrbracket_o) \text{ ttas)))}$   
 $\implies cut\text{-and-update } s \text{ vs}$   
**unfolding** *cut-and-update-def* **by** *blast*

**lemma** *cut-and-updateD*:

$\llbracket cut\text{-and-update } s \text{ vs}; s \multimap ttas \rightarrow^* s'; ta\text{-seq-consist } P \text{ vs (llist-of (concat (map } (\lambda(t, ta). \llbracket ta \rrbracket_o) \text{ ttas)))}$   
 $thr \ s' \ t = \lfloor (x, no\text{-wait-locks}) \rfloor; t \vdash (x, shr \ s') \text{-} ta \rightarrow (x', m'); actions\text{-ok } s' \ t \ ta \rrbracket$   
 $\implies \exists ta' \ x'' \ m''. t \vdash (x, shr \ s') \text{-} ta' \rightarrow (x'', m'') \wedge actions\text{-ok } s' \ t \ ta' \wedge$   
 $ta\text{-seq-consist } P \text{ (mrw-values } P \text{ vs (concat (map } (\lambda(t, ta). \llbracket ta \rrbracket_o) \text{ ttas))) (llist-of } \llbracket ta' \rrbracket_o$   
 $\wedge$   
 $eq\text{-upto-seq-inconsist } P \llbracket ta \rrbracket_o \llbracket ta' \rrbracket_o \text{ (mrw-values } P \text{ vs (concat (map } (\lambda(t, ta). \llbracket ta \rrbracket_o) \text{ ttas)))}$   
**unfolding** *cut-and-update-def* **by** *blast*

**lemma** *cut-and-update-imp-sc-completion*:

$cut\text{-and-update } s \text{ vs} \implies sc\text{-completion } s \text{ vs}$   
**apply**(*rule sc-completionI*)  
**apply**(*drule* (5) *cut-and-updateD*)  
**apply** *blast*  
**done**

**lemma** *sequential-completion*:

**assumes** *cut-and-update*:  $cut\text{-and-update } s \text{ vs}$   
**and** *ta-seq-consist-convert-RA*:  $\bigwedge vs \ ln. ta\text{-seq-consist } P \text{ vs (llist-of (convert-RA } ln))$   
**and** *Red*:  $s \multimap ttas \rightarrow^* s'$   
**and** *sc*:  $ta\text{-seq-consist } P \text{ vs (llist-of (concat (map } (\lambda(t, ta). \llbracket ta \rrbracket_o) \text{ ttas)))}$   
**and** *red*:  $s' \text{-} t \rightarrow ta \rightarrow s''$   
**shows**  
 $\exists ta' \ ttas'. mthr.Runs \ s' \ (LCons \ (t, ta') \ ttas') \wedge$   
 $ta\text{-seq-consist } P \text{ vs (lconcat (lmap } (\lambda(t, ta). \llbracket ta \rrbracket_o) \text{ (lappend (llist-of } ttas) \ (LCons \ (t, ta') \ ttas')))) \wedge$   
 $eq\text{-upto-seq-inconsist } P \llbracket ta \rrbracket_o \llbracket ta' \rrbracket_o \text{ (mrw-values } P \text{ vs (concat (map } (\lambda(t, ta). \llbracket ta \rrbracket_o) \text{ ttas)))}$

**proof** –

**from** *red* **obtain**  $ta' \ s'''$   
**where**  $red': redT \ s' \ (t, ta') \ s'''$   
**and**  $sc': ta\text{-seq-consist } P \text{ vs (lconcat (lmap } (\lambda(t, ta). \llbracket ta \rrbracket_o) \text{ (lappend (llist-of } ttas) \ (LCons \ (t, ta') \ LNil))))$   
**and**  $eq: eq\text{-upto-seq-inconsist } P \llbracket ta \rrbracket_o \llbracket ta' \rrbracket_o \text{ (mrw-values } P \text{ vs (concat (map } (\lambda(t, ta). \llbracket ta \rrbracket_o) \text{ ttas)))}$   
**proof** *cases*  
**case** (*redT-normal*  $x \ x' \ m'$ )  
**note**  $ts't = \langle thr \ s' \ t = \lfloor (x, no\text{-wait-locks}) \rfloor \rangle$   
**and**  $red = \langle t \vdash \langle x, shr \ s' \rangle \text{-} ta \rightarrow \langle x', m' \rangle \rangle$   
**and**  $aok = \langle actions\text{-ok } s' \ t \ ta \rangle$   
**and**  $s'' = \langle redT\text{-upd } s' \ t \ ta \ x' \ m' \ s'' \rangle$   
**from** *cut-and-updateD*[*OF cut-and-update, OF Red sc ts't red aok*]  
**obtain**  $ta' \ x'' \ m''$  **where**  $red: t \vdash \langle x, shr \ s' \rangle \text{-} ta' \rightarrow \langle x'', m'' \rangle$   
**and**  $sc': ta\text{-seq-consist } P \text{ (mrw-values } P \text{ vs (concat (map } (\lambda(t, y). \llbracket y \rrbracket_o) \text{ ttas))) (llist-of } \llbracket ta' \rrbracket_o$   
**and**  $eq: eq\text{-upto-seq-inconsist } P \llbracket ta \rrbracket_o \llbracket ta' \rrbracket_o \text{ (mrw-values } P \text{ vs (concat (map } (\lambda(t, ta). \llbracket ta \rrbracket_o) \text{ ttas)))}$

```

    and aok: actions-ok s' t ta' by blast
  obtain ws''' where redT-updWs t (wset s')  $\{ta\}_w$  ws'''
    using redT-updWs-total ..
  then obtain s''' where s''': redT-upd s' t ta' x'' m'' s''' by fastforce
  with red  $\langle thr s' t = \lfloor (x, no-wait-locks) \rfloor \rangle$  aok have s'  $\rightarrow ta' \rightarrow s'''$  by (rule redT.redT-normal)
  moreover from sc sc'
  have ta-seq-consist P vs (lconcat (lmap ( $\lambda(t, ta). llist-of \{ta\}_o$ ) (lappend (llist-of ttas) (LCons (t, ta') LNil))))
    by (auto simp add: lmap-lappend-distrib ta-seq-consist-lappend split-def lconcat-llist-of[symmetric]
      o-def list-of-lconcat)
  ultimately show thesis using eq by (rule that)
next
case (redT-acquire x ln n)
  hence ta-seq-consist P vs (lconcat (lmap ( $\lambda(t, ta). llist-of \{ta\}_o$ ) (lappend (llist-of ttas) (LCons (t, ta) LNil))))
    and eq-upto-seq-inconsist P  $\{ta\}_o \{ta\}_o$  (mrw-values P vs (concat (map ( $\lambda(t, ta). \{ta\}_o$ ) ttas)))
    using sc
    by (simp-all add: lmap-lappend-distrib ta-seq-consist-lappend split-def lconcat-llist-of[symmetric]
      o-def list-of-lconcat ta-seq-consist-convert-RA ta-seq-consist-imp-eq-upto-seq-inconsist-refl)
  with red show thesis by (rule that)
qed

```

Now, find a sequentially consistent completion from  $s'''$  onwards.

```

from Red red' have Red': s  $\rightarrow ttas @ [(t, ta')] \rightarrow^* s'''$ 
  unfolding RedT-def by (auto intro: rtrancl3p-step)

from sc sc'
have ta-seq-consist P vs (lconcat (lmap ( $\lambda(t, ta). llist-of \{ta\}_o$ ) (llist-of (ttas @ [(t, ta')]))))
  by (simp add: o-def split-def lappend-llist-of-llist-of[symmetric])
with cut-and-update-imp-sc-completion[OF cut-and-update] Red'
have sc-completion s''' (mrw-values P vs (concat (map ( $\lambda(t, ta). \{ta\}_o$ ) (ttas @ [(t, ta')]))))
  by (rule sc-completion-shift)
from sequential-completion-Runs[OF this ta-seq-consist-convert-RA]
obtain ttas' where  $\tau$ Runs: mthr.Runs s''' ttas'
  and sc'': ta-seq-consist P (mrw-values P vs (concat (map ( $\lambda(t, ta). \{ta\}_o$ ) (ttas @ [(t, ta')]))))
    (lconcat (lmap ( $\lambda(t, ta). llist-of \{ta\}_o$ ) ttas'))

  by blast
from red'  $\tau$ Runs have mthr.Runs s' (LCons (t, ta') ttas') ..
moreover from sc sc' sc''
have ta-seq-consist P vs (lconcat (lmap ( $\lambda(t, ta). llist-of \{ta\}_o$ ) (lappend (llist-of ttas) (LCons (t, ta') ttas'))))
  unfolding lmap-lappend-distrib lconcat-lappend by (simp add: o-def ta-seq-consist-lappend split-def
    list-of-lconcat)
  ultimately show ?thesis using eq by blast
qed

end

end

```

## 8.9 Happens-before consistent completion of executions in the JMM

**theory** *HB-Completion* **imports**

*Non-Speculative*

**begin**

**coinductive** *ta-hb-consistent* :: 'm prog  $\Rightarrow$  ('thread-id  $\times$  ('addr, 'thread-id) obs-event action) list  $\Rightarrow$  ('thread-id  $\times$  ('addr, 'thread-id) obs-event action) llist  $\Rightarrow$  bool

**for** *P* :: 'm prog

**where**

*LNil*: *ta-hb-consistent* *P* *obs* *LNil*

| *LCons*:

$\llbracket$  *ta-hb-consistent* *P* (*obs* @ [*ob*]) *obs'*;

case *ob* of (*t*, *NormalAction* (*ReadMem* *ad* *al* *v*))

$\Rightarrow (\exists w. w \in \text{write-actions } (\text{llist-of } (\text{obs} @ [\text{ob}]))) \wedge (ad, al) \in \text{action-loc } P (\text{llist-of } (\text{obs} @ [\text{ob}])))$

*w*  $\wedge$

value-written *P* (*llist-of* (*obs* @ [*ob*])) *w* (*ad*, *al*) = *v*  $\wedge$

*P*, *llist-of* (*obs* @ [*ob*])  $\vdash w \leq_{hb} \text{length } \text{obs} \wedge$

$(\forall w' \in \text{write-actions } (\text{llist-of } (\text{obs} @ [\text{ob}]))) . (ad, al) \in \text{action-loc } P (\text{llist-of } (\text{obs} @ [\text{ob}]))) w'$

$\longrightarrow$

$(P, \text{llist-of } (\text{obs} @ [\text{ob}]) \vdash w \leq_{hb} w' \wedge P, \text{llist-of } (\text{obs} @ [\text{ob}]) \vdash w' \leq_{hb} \text{length } \text{obs} \vee$

*is-volatile* *P* *al*  $\wedge P, \text{llist-of } (\text{obs} @ [\text{ob}]) \vdash w \leq_{so} w' \wedge P, \text{llist-of } (\text{obs} @ [\text{ob}]) \vdash w' \leq_{so}$

*length obs*)  $\longrightarrow$

*w'* = *w*)

| -  $\Rightarrow$  *True*  $\rrbracket$

$\Rightarrow$  *ta-hb-consistent* *P* *obs* (*LCons* *ob* *obs'*)

**inductive-simps** *ta-hb-consistent-LNil* [*simp*]:

*ta-hb-consistent* *P* *obs* *LNil*

**inductive-simps** *ta-hb-consistent-LCons*:

*ta-hb-consistent* *P* *obs* (*LCons* *ob* *obs'*)

**lemma** *ta-hb-consistent-into-non-speculative*:

*ta-hb-consistent* *P* *obs0* *obs*

$\Rightarrow$  *non-speculative* *P* (*w-values* *P* ( $\lambda\cdot$ .  $\{\}$ ) (*map snd obs0*)) (*lmap snd obs*)

**proof**(*coinduction arbitrary: obs0 obs*)

case (*non-speculative* *obs0 obs*)

let *?vs* = *w-values* *P* ( $\lambda\cdot$ .  $\{\}$ ) (*map snd obs0*)

let *?CH* =  $\lambda vs \text{ obs}'. \exists \text{ obs0 obs. } vs = \text{w-values } P (\lambda\cdot. \{\}) (\text{map snd obs0}) \wedge \text{obs}' = \text{lmap snd obs} \wedge$   
*ta-hb-consistent* *P* *obs0* *obs*

**from** *non-speculative* **show** *?case*

**proof**(*cases*)

case *LNil* hence *?LNil* by *simp*

thus *?thesis* ..

**next**

case (*LCons* *tob* *obs''*)

**note** *obs* =  $\langle \text{obs} = \text{LCons } \text{tob } \text{obs}'' \rangle$

**obtain** *t ob* **where** *tob*: *tob* = (*t*, *ob*) **by**(*cases tob*)

**from**  $\langle \text{ta-hb-consistent } P (\text{obs0} @ [\text{tob}]) \text{ obs}'' \rangle$  *tob obs*

**have** *?CH* (*w-value* *P* *?vs* *ob*) (*lmap snd obs''*) **by**(*auto intro! exI*)

**moreover** {

fix *ad al v*

```

assume ob: ob = NormalAction (ReadMem ad al v)
with LCons tob obtain w where w: w ∈ write-actions (llist-of (obs0 @ [tob]))
  and adal: (ad, al) ∈ action-loc P (llist-of (obs0 @ [tob])) w
  and v: value-written P (llist-of (obs0 @ [tob])) w (ad, al) = v by auto
from w obtain is-write-action (action-obs (llist-of (obs0 @ [tob])) w)
  and w-actions: w ∈ actions (llist-of (obs0 @ [tob])) by cases
hence v ∈ ?vs (ad, al)
proof(cases)
  case (WriteMem ad' al' v')
  hence NormalAction (WriteMem ad al v) ∈ set (map snd obs0)
    using adal ob tob v w-actions unfolding in-set-conv-nth
    by(auto simp add: action-obs-def nth-append value-written.simps actions-def cong: conj-cong
split: if-split-asm)
    thus ?thesis by(rule w-values-WriteMemD)
  next
  case (NewHeapElem ad' hT)
  hence NormalAction (NewHeapElem ad hT) ∈ set (map snd obs0)
    using adal ob tob v w-actions unfolding in-set-conv-nth
    by(auto simp add: action-obs-def nth-append value-written.simps actions-def cong: conj-cong
split: if-split-asm)
    thus ?thesis using NewHeapElem adal unfolding v[symmetric]
    by(fastforce simp add: value-written.simps intro!: w-values-new-actionD intro: rev-image-eqI)
  qed }
hence case ob of NormalAction (ReadMem ad al v) ⇒ v ∈ ?vs (ad, al) | - ⇒ True
  by(simp split: action.split obs-event.split)
ultimately have ?LCons using obs tob by simp
thus ?thesis ..
qed
qed

```

```

lemma ta-hb-consistent-lappendI:
  assumes hb1: ta-hb-consistent P E E'
  and hb2: ta-hb-consistent P (E @ list-of E') E''
  and fin: lfinite E'
  shows ta-hb-consistent P E (lappend E' E'')
using fin hb1 hb2
proof(induction arbitrary: E)
  case lfinite-LNil thus ?case by simp
next
  case (lfinite-LConsI E' tob)
  from ⟨ta-hb-consistent P E (LCons tob E')⟩
  have ta-hb-consistent P (E @ [tob]) E' by cases
  moreover from ⟨ta-hb-consistent P (E @ list-of (LCons tob E')) E''⟩ ⟨lfinite E'⟩
  have ta-hb-consistent P ((E @ [tob]) @ list-of E') E'' by simp
  ultimately have ta-hb-consistent P (E @ [tob]) (lappend E' E'') by(rule lfinite-LConsI.IH)
  thus ?case unfolding lappend-code apply(rule ta-hb-consistent.LCons)
    using ⟨ta-hb-consistent P E (LCons tob E')⟩
    by cases (simp split: prod.split-asm action.split-asm obs-event.split-asm)
qed

```

```

lemma ta-hb-consistent-coinduct-append
  [consumes 1, case-names ta-hb-consistent, case-conclusion ta-hb-consistent LNil lappend]:
  assumes major: X E tobs
  and step:  $\bigwedge E$  tobs. X E tobs

```



```

⇒ tobs = LNil ∨
  (∃ tobs' tobs''. tobs = lappend tobs' tobs'' ∧ tobs' ≠ LNil ∧ ta-hb-consistent P E tobs' ∧
    (lfinite tobs' ⇒ (X (E @ list-of tobs') tobs'' ∨
      ta-hb-consistent P (E @ list-of tobs') tobs'')))
(is ∧ E tobs. - ⇒ - ∨ ?step E tobs)
shows ta-hb-consistent P E tobs
proof -
  from major
  have ∃ tobs' tobs''. tobs = lappend (llist-of tobs') tobs'' ∧ ta-hb-consistent P E (llist-of tobs') ∧
    X (E @ tobs') tobs''
  by(auto intro: exI[where x=[]])
  thus ?thesis
  proof(coinduct)
    case (ta-hb-consistent E tobs)
    then obtain tobs' tobs''
      where tobs: tobs = lappend (llist-of tobs') tobs''
      and hb-tobs': ta-hb-consistent P E (llist-of tobs')
      and X: X (E @ tobs') tobs'' by blast

  show ?case
  proof(cases tobs')
    case Nil
    with X have X E tobs'' by simp
    from step[OF this] show ?thesis
    proof
      assume tobs'' = LNil
      with Nil tobs show ?thesis by simp
    next
      assume ?step E tobs''
      then obtain tobs''' tobs''''
        where tobs'': tobs'' = lappend tobs''' tobs'''' and tobs''' ≠ LNil
        and sc-obs''': ta-hb-consistent P E tobs'''
        and fin: lfinite tobs''' ⇒ X (E @ list-of tobs''') tobs'''' ∨
          ta-hb-consistent P (E @ list-of tobs''') tobs''''

        by blast
      from ⟨tobs''' ≠ LNil⟩ obtain t ob tobs''''' where tobs''': tobs''' = LCons (t, ob) tobs'''''
        unfolding neq-LNil-conv by auto
      with Nil tobs'' tobs have concl1: tobs = LCons (t, ob) (lappend tobs''''' tobs''') by simp

      have ?LCons
      proof(cases lfinite tobs''')
        case False
        hence lappend tobs''''' tobs'''' = tobs'''' using tobs''' by(simp add: lappend-inf)
        hence ta-hb-consistent P (E @ [(t, ob)]) (lappend tobs''''' tobs''')
          using sc-obs''' tobs''' by(simp add: ta-hb-consistent-LCons)
        with concl1 show ?LCons apply(simp)
          using sc-obs'''[unfolded tobs'''] by cases simp
      next
        case True
        with tobs''' obtain tobs'''''' where tobs''''': tobs''''' = llist-of tobs''''''
          by simp(auto simp add: lfinite-eq-range-llist-of)
        from fn[OF True]
        have ta-hb-consistent P (E @ [(t, ob)]) (llist-of tobs''''') ∧ X (E @ (t, ob) # tobs''''') tobs'''''

```

```

      ta-hb-consistent P (E @ [(t, ob)]) (lappend (llist-of tobs''''') tobs''')
proof
  assume X: X (E @ list-of tobs''') tobs'''
  hence X (E @ (t, ob) # tobs''''') tobs''' using tobs'''' tobs''' by simp
  moreover have ta-hb-consistent P (E @ [(t, ob)]) (llist-of tobs''''')
    using sc-obs''' tobs''' tobs'''' by (simp add: ta-hb-consistent-LCons)
  ultimately show ?thesis by simp
next
  assume ta-hb-consistent P (E @ list-of tobs''') tobs'''
  with sc-obs''' tobs'''' tobs'''
  have ta-hb-consistent P (E @ [(t, ob)]) (lappend (llist-of tobs''''') tobs''')
    by (simp add: ta-hb-consistent-LCons ta-hb-consistent-lappendI)
  thus ?thesis ..
qed
hence (( $\exists$  tobs' tobs''. lappend (llist-of tobs''''') tobs'''' = lappend (llist-of tobs') tobs''  $\wedge$ 
      ta-hb-consistent P (E @ [(t, ob)]) (llist-of tobs')  $\wedge$  X (E @ (t, ob) # tobs')
tobs')  $\vee$ 
      ta-hb-consistent P (E @ [(t, ob)]) (lappend (llist-of tobs''''') tobs'''))
  by auto
  thus ?LCons using concl1 tobs'''' apply (simp)
    using sc-obs'''[unfolded tobs'''] by cases simp
qed
thus ?thesis ..
qed
next
  case (Cons tob tobs''')
  with X tobs hb-tobs' show ?thesis by (auto simp add: ta-hb-consistent-LCons)
qed
qed
qed

```

**lemma** *ta-hb-consistent-coinduct-append-wf*  
 [consumes 2, case-names ta-hb-consistent, case-conclusion ta-hb-consistent LNil lappend]:  
 assumes major: X E obs a  
 and wf: wf R  
 and step:  $\bigwedge E \text{ obs } a. X E \text{ obs } a$   
 $\implies \text{obs} = \text{LNil} \vee$   
 $(\exists \text{obs}' \text{obs}'' a'. \text{obs} = \text{lappend obs}' \text{obs}'' \wedge \text{ta-hb-consistent P E obs}' \wedge (\text{obs}' = \text{LNil} \longrightarrow (a', a)$   
 $\in R) \wedge$   
 $(\text{lfinite obs}' \longrightarrow X (E @ \text{list-of obs}') \text{obs}'' a' \vee$   
 $\text{ta-hb-consistent P (E @ list-of obs}') \text{obs}''))$   
 (is  $\bigwedge E \text{ obs } a. - \implies - \vee ?\text{step E obs } a$ )  
 shows ta-hb-consistent P E obs

**proof** –  
 { fix E obs a  
 assume X E obs a  
 with wf  
 have obs = LNil  $\vee$  ( $\exists \text{obs}' \text{obs}''.$  obs = lappend obs' obs''  $\wedge$  obs'  $\neq$  LNil  $\wedge$  ta-hb-consistent P E  
 obs'  $\wedge$   
 $(\text{lfinite obs}' \longrightarrow (\exists a. X (E @ \text{list-of obs}') \text{obs}'' a) \vee$   
 $\text{ta-hb-consistent P (E @ list-of obs}') \text{obs}'))$   
 (is  $-\vee ?\text{step-concl E obs}$ )  
 proof (induction a arbitrary: E obs rule: wf-induct[consumes 1, case-names wf])  
 case (wf a)

```

note  $IH = wf.IH[rule-format]$ 
from  $step[OF \langle X \ E \ obs \ a \rangle]$ 
show  $?case$ 
proof
  assume  $obs = LNil$  thus  $?thesis ..$ 
next
  assume  $?step \ E \ obs \ a$ 
  then obtain  $obs' \ obs'' \ a'$ 
    where  $obs: obs = lappend \ obs' \ obs''$ 
    and  $sc\text{-}obs': ta\text{-}hb\text{-}consistent \ P \ E \ obs'$ 
    and  $decr: obs' = LNil \implies (a', a) \in R$ 
    and  $fin: lfinite \ obs' \implies$ 
       $X \ (E \ @ \ list\text{-}of \ obs') \ obs'' \ a' \vee$ 
       $ta\text{-}hb\text{-}consistent \ P \ (E \ @ \ list\text{-}of \ obs') \ obs''$ 
    by  $blast$ 
  show  $?case$ 
  proof( $cases \ obs' = LNil$ )
    case  $True$ 
    hence  $lfinite \ obs'$  by  $simp$ 
    from  $fin[OF \ this]$  show  $?thesis$ 
    proof
      assume  $X: X \ (E \ @ \ list\text{-}of \ obs') \ obs'' \ a'$ 
      from  $True$  have  $(a', a) \in R$  by( $rule \ decr$ )
      from  $IH[OF \ this \ X]$  show  $?thesis$ 
      proof
        assume  $obs'' = LNil$ 
        with  $True \ obs$  have  $obs = LNil$  by  $simp$ 
        thus  $?thesis ..$ 
      next
        assume  $?step\text{-}concl \ (E \ @ \ list\text{-}of \ obs') \ obs''$ 
        hence  $?step\text{-}concl \ E \ obs$  using  $True \ obs$  by  $simp$ 
        thus  $?thesis ..$ 
      qed
    next
      assume  $ta\text{-}hb\text{-}consistent \ P \ (E \ @ \ list\text{-}of \ obs') \ obs''$ 
      thus  $?thesis$  using  $obs \ True$ 
        by  $cases \ (auto \ 4 \ 3 \ cong: \ action.case\text{-}cong \ obs\text{-}event.case\text{-}cong \ intro: \ exI[where \ x=LCons$ 
 $x \ LNil \ for \ x] \ simp \ add: \ ta\text{-}hb\text{-}consistent\text{-}LCons)$ 
      qed
    next
      case  $False$ 
      with  $obs \ sc\text{-}obs' \ fin$  show  $?thesis$  by  $auto$ 
    qed
  qed
qed }
note  $step' = this$ 

from  $major$  show  $?thesis$ 
proof( $coinduction \ arbitrary: \ E \ obs \ a \ rule: \ ta\text{-}hb\text{-}consistent\text{-}coinduct\text{-}append$ )
  case  $(ta\text{-}hb\text{-}consistent \ E \ obs)$ 
  thus  $?case$  by  $simp(rule \ step')$ 
qed
qed

```

**lemma** *ta-hb-consistent-lappendD2*:

**assumes** *hb*: *ta-hb-consistent* *P E* (*lappend E' E''*)

**and** *fin*: *lfinite E'*

**shows** *ta-hb-consistent* *P (E @ list-of E') E''*

**using** *fin hb*

**by**(*induct arbitrary: E*)(*fastforce simp add: ta-hb-consistent-LCons*)+

**lemma** *ta-hb-consistent-Read-hb*:

**fixes** *E E'* **defines** *E''*  $\equiv$  *lappend (llist-of E') E*

**assumes** *hb*: *ta-hb-consistent* *P E' E*

**and** *tsa*: *thread-start-actions-ok E''*

**and** *E''*: *is-write-seen* *P (llist-of E') ws'*

**and** *new-actions-for-fun*:

$\bigwedge w w' \text{ adal. } \llbracket w \in \text{new-actions-for } P E'' \text{ adal};$   
 $w' \in \text{new-actions-for } P E'' \text{ adal} \rrbracket \implies w = w'$

**shows**  $\exists ws. P \vdash (E'', ws) \checkmark \wedge (\forall n. n \in \text{read-actions } E'' \longrightarrow \text{length } E' \leq n \longrightarrow P, E'' \vdash ws \ n \leq_{hb} n) \wedge$

$(\forall n. n < \text{length } E' \longrightarrow ws \ n = ws' \ n)$

**proof**(*intro exI conjI strip*)

**let** *?P* =

$\lambda n w. \text{case } \text{lnth } E'' \ n \text{ of}$

$(t, \text{NormalAction } (\text{ReadMem } ad \ al \ v)) \Rightarrow$

$(w \in \text{write-actions } E'' \wedge (ad, al) \in \text{action-loc } P E'' w \wedge \text{value-written } P E'' w (ad, al) = v \wedge$   
 $P, E'' \vdash w \leq_{hb} n \wedge$

$(\forall w' \in \text{write-actions } E''. (ad, al) \in \text{action-loc } P E'' w' \longrightarrow$

$(P, E'' \vdash w \leq_{hb} w' \wedge P, E'' \vdash w' \leq_{hb} n \vee$

$\text{is-volatile } P \ al \wedge P, E'' \vdash w \leq_{so} w' \wedge P, E'' \vdash w' \leq_{so} n) \longrightarrow$

$w' = w))$

**let** *?ws* =  $\lambda n. \text{if } n < \text{length } E' \text{ then } ws' \ n \text{ else } Eps \ ( ?P \ n)$

**have**  $\bigwedge n. n < \text{length } E' \implies ?ws \ n = ws' \ n$  **by** *simp*

**moreover**

**have**  $P \vdash (E'', ?ws) \checkmark \wedge$

$(\forall n \ ad \ al \ v. n \in \text{read-actions } E'' \longrightarrow \text{length } E' \leq n \longrightarrow \text{action-obs } E'' \ n = \text{NormalAction}$

$(\text{ReadMem } ad \ al \ v) \longrightarrow P, E'' \vdash ?ws \ n \leq_{hb} n)$

**proof**(*intro conjI wf-execI strip is-write-seenI*)

**fix** *a' ad al v*

**assume** *read*: *a'*  $\in$  *read-actions E''*

**and** *aobs*: *action-obs E'' a' = NormalAction (ReadMem ad al v)*

**then obtain** *t* **where** *a'*: *enat a' < llength E''*

**and** *lnth''*: *lnth E'' a' = (t, NormalAction (ReadMem ad al v))*

**by**(*cases*)(*cases lnth E'' a', clarsimp simp add: actions-def action-obs-def*)

**have** *?ws a'*  $\in$  *write-actions E''*  $\wedge$

$(ad, al) \in \text{action-loc } P E'' \ ( ?ws \ a') \wedge$

$\text{value-written } P E'' \ ( ?ws \ a') \ (ad, al) = v \wedge$

$(\text{length } E' \leq a' \longrightarrow P, E'' \vdash ?ws \ a' \leq_{hb} a') \wedge$

$\neg P, E'' \vdash a' \leq_{hb} ?ws \ a' \wedge$

$(\text{is-volatile } P \ al \longrightarrow \neg P, E'' \vdash a' \leq_{so} ?ws \ a') \wedge$

$(\forall a''. a'' \in \text{write-actions } E'' \longrightarrow (ad, al) \in \text{action-loc } P E'' a'' \longrightarrow$

$(P, E'' \vdash ?ws \ a' \leq_{hb} a'' \wedge P, E'' \vdash a'' \leq_{hb} a' \vee \text{is-volatile } P \ al \wedge P, E'' \vdash ?ws \ a' \leq_{so} a'' \wedge$

$P, E'' \vdash a'' \leq_{so} a')$

$\longrightarrow a'' = ?ws \ a')$

**proof**(*cases a' < length E', safe del: notI disjE conjE*)

```

assume  $a'-E'$ :  $a' < \text{length } E'$ 
with  $\text{read } a\text{obs}$  have  $a'$ :  $a' \in \text{read-actions } (\text{llist-of } E')$ 
  and  $a\text{obs}'$ :  $\text{action-obs } (\text{llist-of } E') \ a' = \text{NormalAction } (\text{ReadMem } ad \ al \ v)$ 
  by( $\text{auto simp add: } E''\text{-def action-obs-def lnth-lappend1 actions-def elim: read-actions.cases intro:}$ 
 $\text{read-actions.intros}$ )
  have  $\text{sim}$ :  $\text{ltake } (\text{enat } (\text{length } E')) \ (\text{llist-of } E') \ [\approx] \ \text{ltake } (\text{enat } (\text{length } E')) \ (\text{lappend } (\text{llist-of } E'))$ 
 $E)$ 
    by( $\text{rule eq-into-sim-actions}$ )( $\text{simp add: ltake-all ltake-lappend1}$ )
from  $\text{tsa}$  have  $\text{tsa}'$ :  $\text{thread-start-actions-ok } (\text{llist-of } E')$ 
  by( $\text{rule thread-start-actions-ok-prefix}$ )( $\text{simp add: } E''\text{-def lprefix-lappend}$ )

from  $\text{is-write-seenD}[OF \ E'' \ a' \ a\text{obs}] \ a'-E'$ 
show  $?ws \ a' \in \text{write-actions } E''$ 
  and  $(ad, al) \in \text{action-loc } P \ E'' \ (?ws \ a')$ 
  and  $\text{value-written } P \ E'' \ (?ws \ a') \ (ad, al) = v$ 
  and  $\neg P, E'' \vdash a' \leq_{hb} ?ws \ a'$ 
  and  $\text{is-volatile } P \ al \implies \neg P, E'' \vdash a' \leq_{so} ?ws \ a'$ 
  by( $\text{auto elim!: write-actions.cases intro!: write-actions.intros simp add: } E''\text{-def lnth-lappend1}$ 
 $\text{actions-def action-obs-def value-written-def enat-less-enat-plusI dest: happens-before-change-prefix}[OF$ 
 $- \text{tsa}' \ \text{sim}[\text{symmetric}]] \ \text{sync-order-change-prefix}[OF - \text{sim}[\text{symmetric}]]$ )

{ assume  $\text{length } E' \leq a'$ 
  thus  $P, E'' \vdash ?ws \ a' \leq_{hb} a'$  using  $a'-E'$  by  $\text{simp}$  }

{ fix  $w$ 
  assume  $w$ :  $w \in \text{write-actions } E'' \ (ad, al) \in \text{action-loc } P \ E'' \ w$ 
  and  $\text{hbso}$ :  $P, E'' \vdash ?ws \ a' \leq_{hb} w \wedge P, E'' \vdash w \leq_{hb} a' \vee \text{is-volatile } P \ al \wedge P, E'' \vdash ?ws \ a' \leq_{so}$ 
 $w \wedge P, E'' \vdash w \leq_{so} a'$ 
  show  $w = ?ws \ a'$ 
  proof( $\text{cases } w < \text{length } E'$ )
    case  $\text{True}$ 
    with  $\text{is-write-seenD}[OF \ E'' \ a' \ a\text{obs}] \ a'-E' \ w \ \text{hbso}$  show  $?thesis$ 
    by( $\text{auto } 4 \ 3 \ \text{elim!: write-actions.cases intro!: write-actions.intros simp add: } E''\text{-def}$ 
 $\text{lnth-lappend1 actions-def action-obs-def value-written-def enat-less-enat-plusI dest: happens-before-change-prefix}[OF$ 
 $- \text{tsa}[\text{unfolded } E''\text{-def}] \ \text{sim}] \ \text{happens-before-change-prefix}[OF - \text{tsa}' \ \text{sim}[\text{symmetric}]] \ \text{sync-order-change-prefix}[OF$ 
 $- \text{sim}, \text{simplified}] \ \text{sync-order-change-prefix}[OF - \text{sim}[\text{symmetric}], \text{simplified}] \ \text{bspec}[\text{where } x=w])$ 
    next
    case  $\text{False}$ 
    from  $\text{hbso}$  have  $E'' \vdash w \leq_a a'$  by( $\text{auto intro: happens-before-into-action-order elim:}$ 
 $\text{sync-orderE}$ )
    moreover from  $w(1) \ \text{read}$  have  $w \neq a'$  by( $\text{auto dest: read-actions-not-write-actions}$ )
    ultimately have  $\text{new-w}$ :  $\text{is-new-action } (\text{action-obs } E'' \ w)$  using  $\text{False } a\text{obs } a'-E'$ 
    by( $\text{cases rule: action-orderE}$ )  $\text{auto}$ 
    moreover from  $\text{hbso } a'-E'$  have  $E'' \vdash ws' \ a' \leq_a w$ 
    by( $\text{auto intro: happens-before-into-action-order elim: sync-orderE}$ )
    hence  $\text{new-a'}$ :  $\text{is-new-action } (\text{action-obs } E'' \ (?ws \ a'))$  using  $\text{new-w } a'-E'$ 
    by( $\text{cases rule: action-orderE}$ )  $\text{auto}$ 
    ultimately have  $w \in \text{new-actions-for } P \ E'' \ (ad, al) \ ?ws \ a' \in \text{new-actions-for } P \ E'' \ (ad, al)$ 
    using  $w \ \text{is-write-seenD}[OF \ E'' \ a' \ a\text{obs}] \ a'-E'$ 
    by( $\text{auto simp add: new-actions-for-def actions-def action-obs-def lnth-lappend1 } E''\text{-def}$ 
 $\text{enat-less-enat-plusI elim!: write-actions.cases}$ )
    thus  $?thesis$  by( $\text{rule new-actions-for-fun}$ )
  qed }
next

```

```

assume  $\neg a' < \text{length } E'$ 
hence  $a'-E': \text{length } E' \leq a'$  by simp
define  $a$  where  $a = a' - \text{length } E'$ 
with  $a' a'-E'$  have  $a: \text{enat } a < \text{llength } E$ 
  by(simp add: E''-def) (metis enat-add-mono le-add-diff-inverse plus-enat-simps(1))

from  $a\text{-def } a\text{obs } \text{lnth}'' a'-E'$ 
have  $a\text{obs}: \text{action-obs } E a = \text{NormalAction } (\text{ReadMem } ad \ al \ v)$ 
  and  $\text{lnth}: \text{lnth } E a = (t, \text{NormalAction } (\text{ReadMem } ad \ al \ v))$ 
  by(simp-all add: E''-def lnth-lappend2 action-obs-def)

define  $E'''$  where  $E''' = \text{lappend } (\text{llist-of } E') (\text{ltake } (\text{enat } a) \ E)$ 
let  $?E'' = \text{lappend } E''' (\text{LCons } (t, \text{NormalAction } (\text{ReadMem } ad \ al \ v)) \ \text{LNil})$ 

note hb also
have  $E\text{-unfold1}: E = \text{lappend } (\text{ltake } (\text{enat } a) \ E) (\text{ldropn } a \ E)$  by simp
also have  $E\text{-unfold2}: \text{ldropn } a \ E = \text{LCons } (t, \text{NormalAction } (\text{ReadMem } ad \ al \ v)) (\text{ldropn } (\text{Suc } a) \ E)$ 
  using  $a \ \text{lnth}$  by (metis ldropsn-Suc-conv-ldropsn)
finally
have  $ta\text{-hb-consistent } P \ (E' @ \text{list-of } (\text{ltake } (\text{enat } a) \ E))$ 
  ( $\text{LCons } (t, \text{NormalAction } (\text{ReadMem } ad \ al \ v)) (\text{ldropn } (\text{Suc } a) \ E)$ )
  by(rule ta-hb-consistent-lappendD2) simp
with  $a \ a'-E' \ a\text{-def}$  obtain  $w$  where  $w: w \in \text{write-actions } ?E''$ 
  and  $adal\text{-}w: (ad, al) \in \text{action-loc } P \ ?E'' \ w$ 
  and  $\text{written}: \text{value-written } P \ ?E'' \ w \ (ad, al) = v$ 
  and  $hb: P, ?E'' \vdash w \leq_{hb} a'$ 
  and in-between-so:
   $\bigwedge w'. [\ w' \in \text{write-actions } ?E''; (ad, al) \in \text{action-loc } P \ ?E'' \ w';$ 
     $\text{is-volatile } P \ al; P, ?E'' \vdash w \leq_{so} w'; P, ?E'' \vdash w' \leq_{so} a' \ ]$ 
   $\implies w' = w$ 
  and in-between-hb:
   $\bigwedge w'. [\ w' \in \text{write-actions } ?E''; (ad, al) \in \text{action-loc } P \ ?E'' \ w';$ 
     $P, ?E'' \vdash w \leq_{hb} w'; P, ?E'' \vdash w' \leq_{hb} a' \ ]$ 
   $\implies w' = w$ 
  by(auto simp add: ta-hb-consistent-LCons length-list-of-conv-the-enat min-def lnth-ltake lappend-llist-of-llist-of[symmetric] E'''-def lappend-assoc simp del: lappend-llist-of-llist-of nth-list-of split-if-splits)

from  $a' \ a'-E' \ a$ 
have  $eq: \text{ltake } (\text{enat } (\text{Suc } a')) \ ?E'' = \text{ltake } (\text{enat } (\text{Suc } a')) \ E''$  (is  $?lhs = ?rhs$ )
  unfolding  $E''\text{-def } E'''\text{-def lappend-assoc}$ 
  apply(subst (2) E-unfold1)
  apply(subst E-unfold2)
  apply(subst (1 2) ltake-lappend2)
  apply(simp)
  apply(rule arg-cong) back
  apply(subst (1 2) ltake-lappend2)
  apply(simp add: min-def)
  apply (metis Suc-diff-le a-def le-Suc-eq order-le-less)
  apply(rule arg-cong) back
  apply(auto simp add: min-def a-def)
  apply(auto simp add: eSuc-enat[symmetric] zero-enat-def[symmetric])
done

```

hence *sim*: ?lhs [≈] ?rhs **by**(rule eq-into-sim-actions)  
 from *tsa* have *tsa'*: thread-start-actions-ok ?E'' **unfolding** E''-def E'''-def lappend-assoc  
**by**(rule thread-start-actions-ok-prefix)(subst (2) E-unfold1, simp add: E-unfold2)  
  
 from *w a a'* a-def a'-E' **have** *w-a'*: *w* < Suc *a'*  
**by** cases(simp add: actions-def E'''-def min-def zero-enat-def eSuc-enat split: if-split-asm)  
  
 from *w sim* **have** *w* ∈ write-actions E'' **by**(rule write-actions-change-prefix)(simp add: *w-a'*)  
 moreover  
 from *adal-w* action-loc-change-prefix[OF *sim*, of *w P*] *w-a'*  
 have (ad, al) ∈ action-loc P E'' *w* **by** simp  
 moreover  
 from *written value-written-change-prefix*[OF eq, of *w P*] *w-a'*  
 have value-written P E'' *w* (ad, al) = *v* **by** simp  
 moreover  
 from *hb tsa sim* **have** P, E'' ⊢ *w* ≤<sub>hb</sub> *a'* **by**(rule happens-before-change-prefix)(simp-all add:  
*w-a'*)  
 moreover {  
 fix *w'*  
 assume *w'*: *w'* ∈ write-actions E''  
 and *adal*: (ad, al) ∈ action-loc P E'' *w'*  
 and *hbso*: P, E'' ⊢ *w* ≤<sub>hb</sub> *w'* ∧ P, E'' ⊢ *w'* ≤<sub>hb</sub> *a'* ∨ is-volatile P al ∧ P, E'' ⊢ *w* ≤<sub>so</sub> *w'* ∧  
 P, E'' ⊢ *w' ≤<sub>so</sub> a'*  
 (is ?hbso E'')  
 from *hbso* **have** *ao*: E'' ⊢ *w* ≤<sub>a</sub> *w'* E'' ⊢ *w' ≤<sub>a</sub> a'*  
**by**(auto dest: happens-before-into-action-order elim: sync-orderE)  
 have *w' = w*  
**proof**(cases is-new-action (action-obs E'' *w'*))  
 case True  
 hence *w' ∈ new-actions-for P E''* (ad, al) **using** *w'* *adal* **by**(simp add: new-actions-for-def)  
 moreover from *ao* True **have** is-new-action (action-obs E'' *w*) **by**(cases rule: action-orderE)  
 simp-all  
 with ⟨*w* ∈ write-actions E''⟩ ⟨(ad, al) ∈ action-loc P E'' *w*⟩  
 have *w* ∈ new-actions-for P E'' (ad, al) **by**(simp add: new-actions-for-def)  
 ultimately show *w' = w* **by**(rule new-actions-for-fun)  
 next  
 case False  
 with *ao* **have** *w' ≤ a'* **by**(auto elim: action-orderE)  
 hence *w'-a*: enat *w' < enat* (Suc *a'*) **by** simp  
 with *hbso w-a'* **have** ?hbso ?E''  
**by**(auto 4 3 elim: happens-before-change-prefix[OF - *tsa'* *sim*[symmetric]] sync-order-change-prefix[OF  
 - *sim*[symmetric]] del: disjCI intro: disjI1 disjI2)  
 moreover from *w' < a'* a' a lnth a'-E' **have** *w' ∈ write-actions ?E''*  
**by**(cases)(cases *w' < a'*, auto intro!: write-actions.intros simp add: E'''-def actions-def  
 action-obs-def lnth-lappend min-def zero-enat-def eSuc-enat lnth-ltake a-def E''-def not-le not-less)  
 moreover from *adal <w' ≤ a'> a a' lnth w' a'-E'* **have** (ad, al) ∈ action-loc P ?E'' *w'*  
**by**(cases *w' < a'*)(cases *w' < length E'*, auto simp add: E'''-def action-obs-def lnth-lappend  
 lappend-assoc[symmetric] min-def lnth-ltake less-trans[where *y=enat a*] a-def E''-def lnth-ltake elim:  
 write-actions.cases)  
 ultimately show *w' = w* **by**(blast dest: in-between-so in-between-hb)  
 qed }  
 ultimately have ?P *a' w* **using** a'-E' lnth **unfolding** E''-def a-def **by**(simp add: lnth-lappend)  
 hence P: ?P *a'* (Eps (?P *a'*)) **by**(rule someI[where P=?P *a'*])

```

from  $P \text{ lnh}'' a' - E'$ 
show  $?ws\ a' \in \text{write-actions } E''$ 
  and  $(ad, al) \in \text{action-loc } P\ E''\ (?ws\ a')$ 
  and  $\text{value-written } P\ E''\ (?ws\ a')\ (ad, al) = v$ 
  and  $P, E'' \vdash ?ws\ a' \leq_{hb} a'$  by simp-all

show  $\neg P, E'' \vdash a' \leq_{hb} ?ws\ a'$ 
proof
  assume  $P, E'' \vdash a' \leq_{hb} ?ws\ a'$ 
  with  $\langle P, E'' \vdash ?ws\ a' \leq_{hb} a' \rangle$  have  $a' = ?ws\ a'$ 
    by (blast dest: antisymPD[OF antisym-action-order] happens-before-into-action-order)
  with  $\langle ?ws\ a' \in \text{write-actions } E'' \rangle$  show False
    by (auto dest: read-actions-not-write-actions)
qed

show  $\neg P, E'' \vdash a' \leq_{so} ?ws\ a'$ 
proof
  assume  $P, E'' \vdash a' \leq_{so} ?ws\ a'$ 
  hence  $E'' \vdash a' \leq_a ?ws\ a'$  by (blast elim: sync-orderE)
  with  $\langle P, E'' \vdash ?ws\ a' \leq_{hb} a' \rangle$  have  $a' = ?ws\ a'$ 
    by (blast dest: antisymPD[OF antisym-action-order] happens-before-into-action-order)
  with  $\langle ?ws\ a' \in \text{write-actions } E'' \rangle$  show False
    by (auto dest: read-actions-not-write-actions)
qed

fix  $a''$ 
assume  $a'' \in \text{write-actions } E''\ (ad, al) \in \text{action-loc } P\ E''\ a''$ 
  and  $P, E'' \vdash ?ws\ a' \leq_{hb} a'' \wedge P, E'' \vdash a'' \leq_{hb} a' \vee$ 
     $\text{is-volatile } P\ al \wedge P, E'' \vdash ?ws\ a' \leq_{so} a'' \wedge P, E'' \vdash a'' \leq_{so} a'$ 
  thus  $a'' = ?ws\ a'$  using  $\text{lnh}'' P\ a' - E'$  by  $\neg(\text{erule } \text{disjE}, \text{clarsimp+})$ 
qed
thus  $?ws\ a' \in \text{write-actions } E''$ 
  and  $(ad, al) \in \text{action-loc } P\ E''\ (?ws\ a')$ 
  and  $\text{value-written } P\ E''\ (?ws\ a')\ (ad, al) = v$ 
  and  $\text{length } E' \leq a' \implies P, E'' \vdash ?ws\ a' \leq_{hb} a'$ 
  and  $\neg P, E'' \vdash a' \leq_{hb} ?ws\ a'$ 
  and  $\text{is-volatile } P\ al \implies \neg P, E'' \vdash a' \leq_{so} ?ws\ a'$ 
  and  $\bigwedge a''. [\![ a'' \in \text{write-actions } E''; (ad, al) \in \text{action-loc } P\ E''\ a''; P, E'' \vdash ?ws\ a' \leq_{hb} a''; P, E'' \vdash a'' \leq_{hb} a' ]\!] \implies a'' = ?ws\ a'$ 
  and  $\bigwedge a''. [\![ a'' \in \text{write-actions } E''; (ad, al) \in \text{action-loc } P\ E''\ a''; \text{is-volatile } P\ al; P, E'' \vdash ?ws\ a' \leq_{so} a''; P, E'' \vdash a'' \leq_{so} a' ]\!] \implies a'' = ?ws\ a'$ 
  by blast+
qed(assumption|rule tsa)+
thus  $P \vdash (E'', ?ws) \checkmark$ 
  and  $\bigwedge n. [\![ n \in \text{read-actions } E''; \text{length } E' \leq n ]\!] \implies P, E'' \vdash ?ws\ n \leq_{hb} n$ 
  by (blast elim: read-actions.cases intro: read-actions.intros)+

fix  $n$ 
assume  $n < \text{length } E'$ 
thus  $?ws\ n = ws'\ n$  by simp
qed

```

**lemma** *ta-hb-consistent-not-ReadI*:

$(\bigwedge t\ ad\ al\ v. (t, \text{NormalAction } (\text{ReadMem } ad\ al\ v)) \notin \text{lset } E) \implies \text{ta-hb-consistent } P\ E'\ E$



**proof**(*coinduction arbitrary*:  $E' E$ )  
**case** (*ta-hb-consistent*  $E' E$ )  
**thus** ?*case* **by**(*cases*  $E$ )(*auto split*: *action.split obs-event.split, blast*)  
**qed**

**context** *jmm-multithreaded* **begin**

**definition** *complete-hb* ::  $('l, 'thread-id, 'x, 'm, 'w)$  *state*  $\Rightarrow ('thread-id \times ('addr, 'thread-id)$  *obs-event action*) *list*

$\Rightarrow ('thread-id \times ('l, 'thread-id, 'x, 'm, 'w, ('addr, 'thread-id)$  *obs-event action*) *thread-action*) *llist*

**where**

*complete-hb*  $s E = \text{unfold-llist}$

$(\lambda(s, E). \forall t \text{ ta } s'. \neg s -t \triangleright \text{ta} \rightarrow s')$

$(\lambda(s, E). \text{fst} (\text{SOME} ((t, \text{ta}), s'). s -t \triangleright \text{ta} \rightarrow s' \wedge \text{ta-hb-consistent } P E (\text{llist-of} (\text{map} (\text{Pair } t) \llbracket \text{ta} \rrbracket_o))))$

$(\lambda(s, E). \text{let} ((t, \text{ta}), s') = \text{SOME} ((t, \text{ta}), s'). s -t \triangleright \text{ta} \rightarrow s' \wedge \text{ta-hb-consistent } P E (\text{llist-of} (\text{map} (\text{Pair } t) \llbracket \text{ta} \rrbracket_o)))$

$\text{in } (s', E @ \text{map} (\text{Pair } t) \llbracket \text{ta} \rrbracket_o))$

$(s, E)$

**definition** *hb-completion* ::

$('l, 'thread-id, 'x, 'm, 'w)$  *state*  $\Rightarrow ('thread-id \times ('addr, 'thread-id)$  *obs-event action*) *list*  $\Rightarrow \text{bool}$

**where**

*hb-completion*  $s E \longleftrightarrow$

$(\forall \text{ttas } s' t x \text{ ta } x' m' i.$

$s -\triangleright \text{ttas} \rightarrow * s' \longrightarrow$

$\text{non-speculative } P (\text{w-values } P (\lambda-. \{\}) (\text{map } \text{snd } E)) (\text{llist-of} (\text{concat} (\text{map} (\lambda(t, \text{ta}). \llbracket \text{ta} \rrbracket_o) \text{ttas}))) \longrightarrow$

$\text{thr } s' t = \lfloor (x, \text{no-wait-locks}) \rfloor \longrightarrow t \vdash (x, \text{shr } s') -\text{ta} \rightarrow (x', m') \longrightarrow \text{actions-ok } s' t \text{ ta} \longrightarrow$

$\text{non-speculative } P (\text{w-values } P (\text{w-values } P (\lambda-. \{\}) (\text{map } \text{snd } E)) (\text{concat} (\text{map} (\lambda(t, \text{ta}). \llbracket \text{ta} \rrbracket_o) \text{ttas}))) (\text{llist-of} (\text{take } i \llbracket \text{ta} \rrbracket_o)) \longrightarrow$

$(\exists \text{ta}' x'' m''. t \vdash (x, \text{shr } s') -\text{ta}' \rightarrow (x'', m'') \wedge \text{actions-ok } s' t \text{ ta}' \wedge$

$\text{take } i \llbracket \text{ta}' \rrbracket_o = \text{take } i \llbracket \text{ta} \rrbracket_o \wedge$

$\text{ta-hb-consistent } P$

$(E @ \text{concat} (\text{map} (\lambda(t, \text{ta}). \text{map} (\text{Pair } t) \llbracket \text{ta} \rrbracket_o) \text{ttas}) @ \text{map} (\text{Pair } t) (\text{take } i \llbracket \text{ta} \rrbracket_o))$

$(\text{llist-of} (\text{map} (\text{Pair } t) (\text{drop } i \llbracket \text{ta}' \rrbracket_o))) \wedge$

$(i < \text{length } \llbracket \text{ta} \rrbracket_o \longrightarrow i < \text{length } \llbracket \text{ta}' \rrbracket_o) \wedge$

$(\text{if } \exists \text{ad al } v. \llbracket \text{ta} \rrbracket_o ! i = \text{NormalAction } (\text{ReadMem ad al } v) \text{ then sim-action else}$

$(=)) (\llbracket \text{ta} \rrbracket_o ! i) (\llbracket \text{ta}' \rrbracket_o ! i))$

**lemma** *hb-completionD*:

$\llbracket \text{hb-completion } s E; s -\triangleright \text{ttas} \rightarrow * s';$

$\text{non-speculative } P (\text{w-values } P (\lambda-. \{\}) (\text{map } \text{snd } E)) (\text{llist-of} (\text{concat} (\text{map} (\lambda(t, \text{ta}). \llbracket \text{ta} \rrbracket_o) \text{ttas})))$ ;

$\text{thr } s' t = \lfloor (x, \text{no-wait-locks}) \rfloor; t \vdash (x, \text{shr } s') -\text{ta} \rightarrow (x', m'); \text{actions-ok } s' t \text{ ta};$

$\text{non-speculative } P (\text{w-values } P (\text{w-values } P (\lambda-. \{\}) (\text{map } \text{snd } E)) (\text{concat} (\text{map} (\lambda(t, \text{ta}). \llbracket \text{ta} \rrbracket_o) \text{ttas}))) (\text{llist-of} (\text{take } i \llbracket \text{ta} \rrbracket_o)) \llbracket$

$\implies \exists \text{ta}' x'' m''. t \vdash (x, \text{shr } s') -\text{ta}' \rightarrow (x'', m'') \wedge \text{actions-ok } s' t \text{ ta}' \wedge$

$\text{take } i \llbracket \text{ta}' \rrbracket_o = \text{take } i \llbracket \text{ta} \rrbracket_o \wedge$

$\text{ta-hb-consistent } P (E @ \text{concat} (\text{map} (\lambda(t, \text{ta}). \text{map} (\text{Pair } t) \llbracket \text{ta} \rrbracket_o) \text{ttas}) @ \text{map} (\text{Pair } t) (\text{take } i \llbracket \text{ta} \rrbracket_o))$

$(\text{llist-of} (\text{map} (\text{Pair } t) (\text{drop } i \llbracket \text{ta}' \rrbracket_o))) \wedge$

$(i < \text{length } \llbracket \text{ta} \rrbracket_o \longrightarrow i < \text{length } \llbracket \text{ta}' \rrbracket_o) \wedge$

(if  $\exists ad\ al\ v. \llbracket ta \rrbracket_o ! i = NormalAction\ (ReadMem\ ad\ al\ v)$  then *sim-action* else  $(=)$ )  
 $(\llbracket ta \rrbracket_o ! i) (\llbracket ta' \rrbracket_o ! i)$   
**unfolding** *hb-completion-def* **by** *blast*

**lemma** *hb-completionI* [intro?]:

( $\wedge ttas\ s'\ t\ x\ ta\ x'\ m'\ i.$   
 $\llbracket s \rightarrow^* ttas \rightarrow^* s';\ non-speculative\ P\ (w-values\ P\ (\lambda-. \{\})\ (map\ snd\ E))\ (llist-of\ (concat\ (map\ (\lambda(t, ta). \llbracket ta \rrbracket_o) ttas)))$ ;  
 $thr\ s'\ t = \lfloor (x, no-wait-locks) \rfloor; t \vdash (x, shr\ s') -ta \rightarrow (x', m'); actions-ok\ s'\ t\ ta$ ;  
 $non-speculative\ P\ (w-values\ P\ (w-values\ P\ (\lambda-. \{\})\ (map\ snd\ E))\ (concat\ (map\ (\lambda(t, ta). \llbracket ta \rrbracket_o) ttas)))\ (llist-of\ (take\ i\ \llbracket ta \rrbracket_o))$ )  
 $\implies \exists ta'\ x''\ m''. t \vdash (x, shr\ s') -ta' \rightarrow (x'', m'') \wedge actions-ok\ s'\ t\ ta' \wedge take\ i\ \llbracket ta' \rrbracket_o = take\ i\ \llbracket ta \rrbracket_o \wedge$   
 $ta-hb-consistent\ P\ (E @ concat\ (map\ (\lambda(t, ta). map\ (Pair\ t)\ \llbracket ta \rrbracket_o) ttas) @ map\ (Pair\ t)\ (take\ i\ \llbracket ta \rrbracket_o))\ (llist-of\ (map\ (Pair\ t)\ (drop\ i\ \llbracket ta' \rrbracket_o))) \wedge$   
 $(i < length\ \llbracket ta \rrbracket_o \longrightarrow i < length\ \llbracket ta' \rrbracket_o) \wedge$   
 $(if\ \exists ad\ al\ v. \llbracket ta \rrbracket_o ! i = NormalAction\ (ReadMem\ ad\ al\ v)$  then *sim-action* else  $(=)$ )  
 $(\llbracket ta \rrbracket_o ! i) (\llbracket ta' \rrbracket_o ! i)$ )  
 $\implies hb-completion\ s\ E$

**unfolding** *hb-completion-def* **by** *blast*

**lemma** *hb-completion-shift*:

**assumes** *hb-c*: *hb-completion* *s* *E*  
**and**  $\tau Red$ :  $s \rightarrow^* ttas \rightarrow^* s'$   
**and** *sc*: *non-speculative* *P* (*w-values* *P* ( $\lambda-. \{\}$ ) (*map* *snd* *E*)) (*llist-of* (*concat* (*map* ( $\lambda(t, ta). \llbracket ta \rrbracket_o$ ) *ttas*)))  
**(is** *non-speculative* - *?vs* -)  
**shows** *hb-completion* *s'* (*E* @ (*concat* (*map* ( $\lambda(t, ta). map\ (Pair\ t)\ \llbracket ta \rrbracket_o$ ) *ttas*)))  
**(is** *hb-completion* - *?E*)  
**proof**(*rule* *hb-completionI*)  
**fix** *ttas'* *s''* *t* *x* *ta* *x'* *m'* *i*  
**assume**  $\tau Red'$ :  $s' \rightarrow^* ttas' \rightarrow^* s''$   
**and** *sc'*: *non-speculative* *P* (*w-values* *P* ( $\lambda-. \{\}$ ) (*map* *snd* *?E*)) (*llist-of* (*concat* (*map* ( $\lambda(t, ta). \llbracket ta \rrbracket_o$ ) *ttas'*)))  
**and** *red*:  $thr\ s''\ t = \lfloor (x, no-wait-locks) \rfloor\ t \vdash \langle x, shr\ s'' \rangle -ta \rightarrow \langle x', m' \rangle\ actions-ok\ s''\ t\ ta$   
**and** *ns*: *non-speculative* *P* (*w-values* *P* (*w-values* *P* ( $\lambda-. \{\}$ ) (*map* *snd* *?E*)) (*concat* (*map* ( $\lambda(t, ta). \llbracket ta \rrbracket_o$ ) *ttas'*))) (*llist-of* (*take* *i*  $\llbracket ta \rrbracket_o$ ))  
**from**  $\tau Red\ \tau Red'$  **have**  $s \rightarrow^* ttas @ ttas' \rightarrow^* s''$  **unfolding** *RedT-def* **by**(*rule* *rtrancl3p-trans*)  
**moreover from** *sc* *sc'* **have** *non-speculative* *P* *?vs* (*llist-of* (*concat* (*map* ( $\lambda(t, ta). \llbracket ta \rrbracket_o$ ) *ttas* @ *ttas'*)))  
**unfolding** *map-append* *concat-append* *lappend-llist-of-llist-of*[*symmetric*] *map-concat*  
**by**(*simp* *add*: *non-speculative-lappend* *o-def* *split-def* *del*: *lappend-llist-of-llist-of*)  
**ultimately**  
**show**  $\exists ta'\ x''\ m''. t \vdash \langle x, shr\ s'' \rangle -ta' \rightarrow \langle x'', m'' \rangle \wedge actions-ok\ s''\ t\ ta' \wedge take\ i\ \llbracket ta' \rrbracket_o = take\ i\ \llbracket ta \rrbracket_o \wedge$   
 $ta-hb-consistent\ P\ (?E @ concat\ (map\ (\lambda(t, ta). map\ (Pair\ t)\ \llbracket ta \rrbracket_o) ttas') @ map\ (Pair\ t)\ (take\ i\ \llbracket ta \rrbracket_o))$   
 $(llist-of\ (map\ (Pair\ t)\ (drop\ i\ \llbracket ta' \rrbracket_o))) \wedge$   
 $(i < length\ \llbracket ta \rrbracket_o \longrightarrow i < length\ \llbracket ta' \rrbracket_o) \wedge$   
 $(if\ \exists ad\ al\ v. \llbracket ta \rrbracket_o ! i = NormalAction\ (ReadMem\ ad\ al\ v)$  then *sim-action* else  $(=)$ ) ( $\llbracket ta \rrbracket_o !$   
 $i) (\llbracket ta' \rrbracket_o ! i)$   
**using** *red* *ns* **unfolding** *append-assoc*  
**apply**(*subst* (2) *append-assoc*[*symmetric*])  
**unfolding** *concat-append*[*symmetric*] *map-append*[*symmetric*] *foldr-append*[*symmetric*]

by(rule hb-completionD[OF hb-c])(simp-all add: map-concat o-def split-def)  
qed

lemma hb-completion-shift1:

assumes hb-c: hb-completion s E  
and Red:  $s \multimap ta \rightarrow s'$   
and sc: non-speculative P (w-values P ( $\lambda\cdot$ .  $\{\}$ ) (map snd E)) (llist-of  $\{ta\}_o$ )  
shows hb-completion s' (E @ map (Pair t)  $\{ta\}_o$ )  
using hb-completion-shift[OF hb-c, of [(t, ta)] s'] Red sc  
by(simp add: RedT-def rtrancl3p-Cons rtrancl3p-Nil del: split-paired-Ex)

lemma complete-hb-in-Runs:

assumes hb-c: hb-completion s E  
and ta-hb-consistent-convert-RA:  $\bigwedge t E \ln. ta\text{-hb-consistent } P E (l\text{list-of } (map (Pair t) (convert\text{-}RA\ ln)))$   
shows mthr.Runs s (complete-hb s E)  
using hb-c  
proof(coinduction arbitrary: s E)  
case (Runs s E)  
let  $?P = \lambda((t, ta), s'). s \multimap ta \rightarrow s' \wedge ta\text{-hb-consistent } P E (l\text{list-of } (map (Pair t) \{ta\}_o))$   
show ?case  
proof(cases  $\exists t ta s'. s \multimap ta \rightarrow s'$ )  
case False  
then have ?Stuck by(simp add: complete-hb-def)  
thus ?thesis ..  
next  
case True  
let  $?t = fst (fst (Eps ?P))$  and  $?ta = snd (fst (Eps ?P))$  and  $?s' = snd (Eps ?P)$   
from True obtain t ta s' where red:  $s \multimap ta \rightarrow s'$  by blast  
hence  $\exists x. ?P x$   
proof(cases)  
case (redT-normal x x' m')  
from hb-completionD[OF Runs - -  $\langle thr\ s\ t = \lfloor(x, no\text{-}wait\text{-}locks)\rfloor \rangle \langle t \vdash \langle x, shr\ s \rangle \multimap ta \rightarrow \langle x', m' \rangle$   
 $\langle actions\text{-}ok\ s\ t \rangle$ , of  $\lfloor\rfloor\ 0\rfloor$   
obtain  $ta' x'' m''$  where  $t \vdash \langle x, shr\ s \rangle \multimap ta' \rightarrow \langle x'', m'' \rangle$   
and actions-ok s t ta' ta-hb-consistent P E (llist-of (map (Pair t)  $\{ta'\}_o$ ))  
by fastforce  
moreover obtain  $ws'$  where redT-updWs t (wset s)  $\{ta'\}_w ws'$  by (metis redT-updWs-total)  
ultimately show ?thesis using  $\langle thr\ s\ t = \lfloor(x, no\text{-}wait\text{-}locks)\rfloor \rangle$   
by(cases ta')(auto intro!: exI redT.redT-normal)  
next  
case (redT-acquire x n ln)  
thus ?thesis using ta-hb-consistent-convert-RA[of E t ln]  
by(auto intro!: exI redT.redT-acquire)  
qed  
hence ?P (Eps ?P) by(rule someI-ex)  
hence red:  $s \multimap ?t ?ta \rightarrow ?s'$   
and hb: ta-hb-consistent P E (llist-of (map (Pair ?t)  $\{?ta\}_o$ ))  
by(simp-all add: split-beta)  
moreover  
from ta-hb-consistent-into-non-speculative[OF hb]  
have non-speculative P (w-values P ( $\lambda\cdot$ .  $\{\}$ ) (map snd E)) (llist-of  $\{?ta\}_o$ ) by(simp add: o-def)  
with Runs red have hb-completion ?s' (E @ map (Pair ?t)  $\{?ta\}_o$ ) by(rule hb-completion-shift1)  
ultimately have ?Step using True

```

    unfolding complete-hb-def by(fastforce simp del: split-paired-Ex simp add: split-def)
  thus ?thesis ..
qed
qed

lemma complete-hb-ta-hb-consistent:
  assumes hb-completion s E
  and ta-hb-consistent-convert-RA:  $\bigwedge E t ln. ta-hb-consistent P E (l\text{list-of } (\text{map } (Pair t) (\text{convert-RA } ln)))$ 
  shows ta-hb-consistent P E (lconcat (lmap ( $\lambda(t, ta). l\text{list-of } (\text{map } (Pair t) \{\!\!\{ta\}\!\!\}_o)$ ) (complete-hb s E)))
  (is ta-hb-consistent - - (?obs (complete-hb s E)))
proof -
  define obs a where obs = ?obs (complete-hb s E) and a = complete-hb s E
  with <hb-completion s E> have  $\exists s. hb-completion s E \wedge obs = ?obs (complete-hb s E) \wedge a = complete-hb s E$  by blast
  moreover have wf (inv-image  $\{(m, n). m < n\}$  (llength  $\circ$  ltakeWhile ( $\lambda(t, ta). \{\!\!\{ta\}\!\!\}_o = []$ )))
    (is wf ?R) by(rule wf-inv-image)(rule wellorder-class.wf)
  ultimately show ta-hb-consistent P E obs
  proof(coinduct E obs a rule: ta-hb-consistent-coinduct-append-wf)
    case (ta-hb-consistent E obs a)
    then obtain s where hb-c: hb-completion s E
      and obs: obs = lconcat (lmap ( $\lambda(t, ta). l\text{list-of } (\text{map } (Pair t) \{\!\!\{ta\}\!\!\}_o)$ ) (complete-hb s E))
      and a: a = complete-hb s E
      by blast
    let ?P =  $\lambda((t, ta), s'). s -t>ta \rightarrow s' \wedge ta-hb-consistent P E (l\text{list-of } (\text{map } (Pair t) \{\!\!\{ta\}\!\!\}_o))$ 
    show ?case
    proof(cases  $\exists t ta s'. s -t>ta \rightarrow s'$ )
      case False
      with obs have ?LNil by(simp add: complete-hb-def)
      thus ?thesis ..
    next
      case True
      let ?t = fst (fst (Eps ?P)) and ?ta = snd (fst (Eps ?P)) and ?s' = snd (Eps ?P)
      from True obtain t ta s' where red:  $s -t>ta \rightarrow s'$  by blast
      hence  $\exists x. ?P x$ 
      proof(cases)
        case (redT-normal x x' m')
        from hb-completionD[OF hb-c - - <thr s t = [(x, no-wait-locks)]> <t ⊢ <x, shr s> -ta→ <x', m'>>
          <actions-ok s t ta>, of [] 0]
        obtain ta' x'' m'' where  $t \vdash \langle x, shr s \rangle -ta' \rightarrow \langle x'', m'' \rangle$ 
          and actions-ok s t ta' ta-hb-consistent P E (llist-of (map (Pair t)  $\{\!\!\{ta'\}\!\!\}_o$ ))
          by fastforce
        moreover obtain ws' where redT-updWs t (wset s)  $\{\!\!\{ta'\}\!\!\}_w ws'$  by (metis redT-updWs-total)
        ultimately show ?thesis using <thr s t = [(x, no-wait-locks)]>
          by(cases ta')(auto intro!: exI redT.redT-normal)
      next
        case (redT-acquire x n ln)
        thus ?thesis using ta-hb-consistent-convert-RA[of E t ln]
          by(auto intro!: exI redT.redT-acquire)
      qed
    hence ?P (Eps ?P) by(rule someI-ex)
    hence red':  $s -?t>?ta \rightarrow ?s'$ 
      and hb: ta-hb-consistent P E (llist-of (map (Pair ?t)  $\{\!\!\{?ta\}\!\!\}_o$ ))

```

```

    by(simp-all add: split-beta)
  moreover
  from ta-hb-consistent-into-non-speculative[OF hb]
  have non-speculative P (w-values P (λ-. {})) (map snd E) (llist-of {?ta}_o) by(simp add: o-def)
  with hb-c red' have hb-c': hb-completion ?s' (E @ map (Pair ?t) {?ta}_o)
    by(rule hb-completion-shift1)
  show ?thesis
  proof(cases lnull obs)
    case True thus ?thesis unfolding lnull-def by simp
  next
    case False
    have eq: (∀ t ta s'. ¬ s -t>ta→ s') = False using True by auto
    { assume {?ta}_o = []
      moreover from obs False
      have lfinite (ltakeWhile (λ(t, ta). {?ta}_o = [])) (complete-hb s E)
        unfolding lfinite-ltakeWhile by(fastforce simp add: split-def lconcat-eq-LNil)
      ultimately have (complete-hb ?s' (E @ map (Pair ?t) {?ta}_o), a) ∈ ?R
        using red unfolding a complete-hb-def
        apply(subst (2) unfold-llist.code)
        apply(subst (asm) unfold-llist.code)
        apply(auto simp add: split-beta simp del: split-paired-Ex split-paired-All split: if-split-asm)
        apply(auto simp add: lfinite-eq-range-llist-of)
        done }
    hence ?lappend using red hb hb-c' unfolding obs complete-hb-def
      apply(subst unfold-llist.code)
      apply(simp add: split-beta eq del: split-paired-Ex split-paired-All split del: if-split)
      apply(intro exI conjI impI refl disjI1|rule refl|assumption|simp-all add: llist-of-eq-LNil-conv)+
      done
    thus ?thesis ..
  qed
qed
qed
qed
qed

lemma hb-completion-Runs:
  assumes hb-completion s E
  and ∧E t ln. ta-hb-consistent P E (llist-of (map (Pair t) (convert-RA ln)))
  shows ∃ ttas. mthr.Runs s ttas ∧ ta-hb-consistent P E (lconcat (lmap (λ(t, ta). llist-of (map (Pair
t) {?ta}_o)) ttas))
  using complete-hb-in-Runs[OF assms] complete-hb-ta-hb-consistent[OF assms]
  by blast

end

end

```

## 8.10 Locales for heap operations with set of allocated addresses

```

theory JMM-Heap
imports
  ../Common/WellForm
  SC-Completion
  HB-Completion

```

**begin**

**definition**  $w\text{-}addr s :: ('addr \times addr\text{-}loc \Rightarrow 'addr\text{ val set}) \Rightarrow 'addr\text{ set}$   
**where**  $w\text{-}addr s\ vs = \{a. \exists adal. Addr\ a \in vs\ adal\}$

**lemma**  $w\text{-}addr s\text{-}empty$  [simp]:  $w\text{-}addr s\ (\lambda\_. \{\}) = \{\}$   
**by**(simp add:  $w\text{-}addr s\text{-}def$ )

**locale**  $allocated\text{-}heap\text{-}base = heap\text{-}base +$   
**constrains**  $addr2thread\text{-}id :: ('addr :: addr) \Rightarrow 'thread\text{-}id$   
**and**  $thread\text{-}id2addr :: 'thread\text{-}id \Rightarrow 'addr$   
**and**  $spurious\text{-}wakeups :: bool$   
**and**  $empty\text{-}heap :: 'heap$   
**and**  $allocate :: 'heap \Rightarrow htype \Rightarrow ('heap \times 'addr)\text{ set}$   
**and**  $typeof\text{-}addr :: 'heap \Rightarrow 'addr \rightarrow htype$   
**and**  $heap\text{-}read :: 'heap \Rightarrow 'addr \Rightarrow addr\text{-}loc \Rightarrow 'addr\text{ val} \Rightarrow bool$   
**and**  $heap\text{-}write :: 'heap \Rightarrow 'addr \Rightarrow addr\text{-}loc \Rightarrow 'addr\text{ val} \Rightarrow 'heap \Rightarrow bool$   
**fixes**  $allocated :: 'heap \Rightarrow 'addr\text{ set}$

**locale**  $allocated\text{-}heap =$   
 $allocated\text{-}heap\text{-}base +$   
 $heap +$   
**constrains**  $addr2thread\text{-}id :: ('addr :: addr) \Rightarrow 'thread\text{-}id$   
**and**  $thread\text{-}id2addr :: 'thread\text{-}id \Rightarrow 'addr$   
**and**  $spurious\text{-}wakeups :: bool$   
**and**  $empty\text{-}heap :: 'heap$   
**and**  $allocate :: 'heap \Rightarrow htype \Rightarrow ('heap \times 'addr)\text{ set}$   
**and**  $typeof\text{-}addr :: 'heap \Rightarrow 'addr \rightarrow htype$   
**and**  $heap\text{-}read :: 'heap \Rightarrow 'addr \Rightarrow addr\text{-}loc \Rightarrow 'addr\text{ val} \Rightarrow bool$   
**and**  $heap\text{-}write :: 'heap \Rightarrow 'addr \Rightarrow addr\text{-}loc \Rightarrow 'addr\text{ val} \Rightarrow 'heap \Rightarrow bool$   
**and**  $allocated :: 'heap \Rightarrow 'addr\text{ set}$   
**and**  $P :: 'm\text{ prog}$

**assumes**  $allocated\text{-}empty$ :  $allocated\text{ empty}\text{-}heap = \{\}$   
**and**  $allocate\text{-}allocatedD$ :  
 $(h', a) \in allocate\ h\ hT \implies allocated\ h' = insert\ a\ (allocated\ h) \wedge a \notin allocated\ h$   
**and**  $heap\text{-}write\text{-}allocated\text{-}same$ :  
 $heap\text{-}write\ h\ a\ al\ v\ h' \implies allocated\ h' = allocated\ h$

**begin**

**lemma**  $allocate\text{-}allocated\text{-}mono$ :  $(h', a) \in allocate\ h\ C \implies allocated\ h \subseteq allocated\ h'$   
**by**(simp-all add:  $allocate\text{-}allocatedD$ )

**lemma**

**shows**  $start\text{-}addr s\text{-}allocated$ :  $allocated\ start\text{-}heap = set\ start\text{-}addr s$   
**and**  $distinct\text{-}start\text{-}addr s'$ :  $distinct\ start\text{-}addr s$

**proof** –

**{ fix**  $h\ ads\ b$  **and**  $xs :: cname\ list$   
**let**  $?start\text{-}addr s\ h\ ads\ b\ xs = fst\ (snd\ (foldl\ create\text{-}initial\text{-}object\ (h, ads, b)\ xs))$   
**let**  $?start\text{-}heap\ h\ ads\ b\ xs = fst\ (foldl\ create\text{-}initial\text{-}object\ (h, ads, b)\ xs)$   
**assume**  $allocated\ h = set\ ads$   
**hence**  $allocated\ (?start\text{-}heap\ h\ ads\ b\ xs) = set\ (?start\text{-}addr s\ h\ ads\ b\ xs) \wedge$   
 $(distinct\ ads \longrightarrow distinct\ (?start\text{-}addr s\ h\ ads\ b\ xs))$   
**(is**  $?concl\ xs\ h\ ads\ b)$

```

proof(induct xs arbitrary: h ads b)
  case Nil thus ?case by auto
next
  case (Cons x xs)
  note ads = ⟨allocated h = set ads⟩
  show ?case
  proof(cases b ∧ allocate h (Class-type x) ≠ {})
    case False thus ?thesis using ads
      by(simp add: create-initial-object-simps zip-append1)
  next
  case [simp]: True
  then obtain h' a'
    where h'a': (SOME ha. ha ∈ allocate h (Class-type x)) = (h', a')
    and new-obj: (h', a') ∈ allocate h (Class-type x)
    by(cases (SOME ha. ha ∈ allocate h (Class-type x)) (auto simp del: True dest: allocate-Eps))

    from new-obj have allocated h' = insert a' (allocated h) a' ∉ allocated h
      by(auto dest: allocate-allocatedD)
    with ads have allocated h' = set (ads @ [a']) by auto
    hence ?concl xs h' (ads @ [a']) True by(rule Cons)
    moreover have a' ∉ set ads using ⟨a' ∉ allocated h⟩ ads by blast
    ultimately show ?thesis by(simp add: create-initial-object-simps new-obj h'a')
  qed
qed }
from this[of empty-heap [] True initialization-list]
show allocated start-heap = set start-addrs
  and distinct-start-addrs: distinct start-addrs
  unfolding start-heap-def start-addrs-def start-heap-data-def
  by(auto simp add: allocated-empty)
qed

lemma w-addrs-start-heap-obs: w-addrs (w-values P vs (map NormalAction start-heap-obs))  $\subseteq$  w-addrs
vs
proof –
  { fix xs
    let ?NewObj =  $\lambda a C. \text{NewHeapElem } a \text{ (Class-type } C \text{)} :: ('addr, 'thread-id) \text{ obs-event}$ 
    let ?start-heap-obs xs = map ( $\lambda(C, a). ?\text{NewObj } a C$ ) xs
    have w-addrs (w-values P vs (map NormalAction (?start-heap-obs xs)))  $\subseteq$  w-addrs vs
      (is ?concl xs)
    proof(induct xs arbitrary: vs)
      case Nil thus ?case by simp
    next
      case (Cons x xs)
      have w-addrs (w-values P vs (map NormalAction (map ( $\lambda(C, a). ?\text{NewObj } a C$ ) (x # xs))))
        = w-addrs (w-values P (w-value P vs (NormalAction (?NewObj (snd x) (fst x)))) (map Nor-
malAction (map ( $\lambda(C, a). ?\text{NewObj } a C$ ) xs)))
        by(simp add: split-beta)
      also have  $\dots \subseteq$  w-addrs (w-value P vs (NormalAction (?NewObj (snd x) (fst x)))) by(rule Cons)
      also have  $\dots \subseteq$  w-addrs vs
        by(auto simp add: w-addrs-def default-val-not-Addr Addr-not-default-val)
      finally show ?case .
    qed }
  thus ?thesis by(simp add: start-heap-obs-def)
qed

```

**end**

**context** *heap-base* **begin**

**lemma** *addr-loc-default-conf*:

$P \vdash \text{class-type-of } CTn \text{ has } F:T \text{ (fm) in } C$   
 $\implies P, h \vdash \text{addr-loc-default } P \ CTn \ (CField \ C \ F) : \leq T$

**apply**(*cases CTn*)

**apply** *simp*

**apply**(*frule has-field-decl-above*)

**apply** *simp*

**done**

**definition** *vs-conf* :: '*m prog*  $\Rightarrow$  '*heap*  $\Rightarrow$  ('*addr*  $\times$  *addr-loc*  $\Rightarrow$  '*addr val set*)  $\Rightarrow$  *bool*

**where** *vs-conf* *P h vs*  $\longleftrightarrow (\forall ad \ al \ v. v \in vs \ (ad, al) \longrightarrow (\exists T. P, h \vdash ad@al : T \wedge P, h \vdash v : \leq T))$

**lemma** *vs-confI*:

$(\bigwedge ad \ al \ v. v \in vs \ (ad, al) \implies \exists T. P, h \vdash ad@al : T \wedge P, h \vdash v : \leq T) \implies vs-conf \ P \ h \ vs$

**unfolding** *vs-conf-def* **by** *blast*

**lemma** *vs-confD*:

$\llbracket vs-conf \ P \ h \ vs; v \in vs \ (ad, al) \rrbracket \implies \exists T. P, h \vdash ad@al : T \wedge P, h \vdash v : \leq T$

**unfolding** *vs-conf-def* **by** *blast*

**lemma** *vs-conf-insert-iff*:

$vs-conf \ P \ h \ (vs((ad, al) := insert \ v \ (vs \ (ad, al))))$   
 $\longleftrightarrow vs-conf \ P \ h \ vs \wedge (\exists T. P, h \vdash ad@al : T \wedge P, h \vdash v : \leq T)$

**by**(*auto 4 3 elim: vs-confD intro: vs-confI split: if-split-asm*)

**end**

**context** *heap* **begin**

**lemma** *vs-conf-hext*:  $\llbracket vs-conf \ P \ h \ vs; h \trianglelefteq h' \rrbracket \implies vs-conf \ P \ h' \ vs$

**by**(*blast intro!: vs-confI intro: conf-hext addr-loc-type-hext-mono dest: vs-confD*)

**lemma** *vs-conf-allocate*:

$\llbracket vs-conf \ P \ h \ vs; (h', a) \in allocate \ h \ hT; is-htype \ P \ hT \rrbracket$   
 $\implies vs-conf \ P \ h' \ (w-value \ P \ vs \ (NormalAction \ (NewHeapElem \ a \ hT)))$

**apply**(*drule vs-conf-hext*)

**apply**(*erule hext-allocate*)

**apply**(*auto intro!: vs-confI simp add: addr-locs-def split: if-split-asm htype.split-asm*)

**apply**(*auto 3 3 intro: addr-loc-type.intros defval-conf dest: allocate-SomeD elim: has-field-is-class vs-confD*)

**apply**(*rule exI conjI addr-loc-type.intros|drule allocate-SomeD|erule has-field-is-class|simp*)**+**

**done**

**end**

*heap-read-typeable* must not be defined in *heap-conf-base* (where it should be) because this would lead to duplicate definitions of *heap-read-typeable* in contexts where *heap-conf-base* is imported twice with different parameters, e.g., *P* and *J2JVM P* in *J-JVM-heap-conf-read*.

**context** *heap-base* **begin**



**definition** *heap-read-typeable* :: ('heap  $\Rightarrow$  bool)  $\Rightarrow$  'm prog  $\Rightarrow$  bool  
**where** *heap-read-typeable hconf*  $P \longleftrightarrow (\forall h \text{ ad al } v \ T. \text{ hconf } h \longrightarrow P, h \vdash \text{ ad@al} : T \longrightarrow P, h \vdash v : \leq T \longrightarrow \text{heap-read } h \text{ ad al } v)$

**lemma** *heap-read-typeableI*:

$(\bigwedge h \text{ ad al } v \ T. \llbracket P, h \vdash \text{ ad@al} : T; P, h \vdash v : \leq T; \text{ hconf } h \rrbracket \Longrightarrow \text{heap-read } h \text{ ad al } v) \Longrightarrow \text{heap-read-typeable hconf } P$

**unfolding** *heap-read-typeable-def* **by** *blast*

**lemma** *heap-read-typeableD*:

$\llbracket \text{heap-read-typeable hconf } P; P, h \vdash \text{ ad@al} : T; P, h \vdash v : \leq T; \text{ hconf } h \rrbracket \Longrightarrow \text{heap-read } h \text{ ad al } v$

**unfolding** *heap-read-typeable-def* **by** *blast*

**end**

**context** *heap-base* **begin**

**definition** *heap-read-typed* :: 'm prog  $\Rightarrow$  'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool

**where** *heap-read-typed*  $P \ h \ \text{ad al } v \longleftrightarrow \text{heap-read } h \ \text{ad al } v \wedge (\forall T. P, h \vdash \text{ ad@al} : T \longrightarrow P, h \vdash v : \leq T)$

**lemma** *heap-read-typedI*:

$\llbracket \text{heap-read } h \ \text{ad al } v; \bigwedge T. P, h \vdash \text{ ad@al} : T \Longrightarrow P, h \vdash v : \leq T \rrbracket \Longrightarrow \text{heap-read-typed } P \ h \ \text{ad al } v$

**unfolding** *heap-read-typed-def* **by** *blast*

**lemma** *heap-read-typed-into-heap-read*:

*heap-read-typed*  $P \ h \ \text{ad al } v \Longrightarrow \text{heap-read } h \ \text{ad al } v$

**unfolding** *heap-read-typed-def* **by** *blast*

**lemma** *heap-read-typed-typed*:

$\llbracket \text{heap-read-typed } P \ h \ \text{ad al } v; P, h \vdash \text{ ad@al} : T \rrbracket \Longrightarrow P, h \vdash v : \leq T$

**unfolding** *heap-read-typed-def* **by** *blast*

**end**

**context** *heap-conf* **begin**

**lemma** *heap-conf-read-heap-read-typed*:

*heap-conf-read* *addr2thread-id* *thread-id2addr* *empty-heap* *allocate* *typeof-addr* (*heap-read-typed*  $P$ )  
*heap-write* *hconf*  $P$

**proof**

**fix**  $h \ a \ \text{al } v \ T$

**assume** *heap-read-typed*  $P \ h \ a \ \text{al } v \ P, h \vdash a@al : T$

**thus**  $P, h \vdash v : \leq T$  **by**(*rule heap-read-typed-typed*)

**qed**

**end**

**context** *heap* **begin**

**lemma** *start-addr-dom-w-values*:

**assumes** *wf*: *wf-syscls*  $P$

**and**  $a: a \in \text{set start-addr}$

**and** *adal*:  $P, \text{start-heap} \vdash a@al : T$

```

shows  $w\text{-values } P (\lambda\cdot. \{\}) (\text{map } \text{NormalAction } \text{start-heap-obs}) (a, al) \neq \{\}$ 
proof –
  from  $a$  obtain  $CTn$  where  $CTn: \text{NewHeapElem } a \text{ } CTn \in \text{set } \text{start-heap-obs}$ 
    unfolding  $\text{in-set-start-addr-conv-NewHeapElem ..}$ 
  then obtain  $\text{obs obs' where } \text{obs: start-heap-obs} = \text{obs} @ \text{NewHeapElem } a \text{ } CTn \# \text{obs' by}(\text{auto dest:}$ 
     $\text{split-list})$ 
  have  $w\text{-value } P (\text{w-values } P (\lambda\cdot. \{\}) (\text{map } \text{NormalAction } \text{obs})) (\text{NormalAction } (\text{NewHeapElem } a$ 
     $CTn)) (a, al) \neq \{\}$ 
  proof  $(\text{cases } CTn)$ 
    case  $[\text{simp}]: (\text{Class-type } C)$ 
    with  $wf \text{ } CTn$  have  $\text{typeof-addr start-heap } a = \lfloor \text{Class-type } C \rfloor$ 
      by  $(\text{auto intro: NewHeapElem-start-heap-obsD})$ 
    with  $\text{adal show ?thesis by cases auto}$ 
  next
    case  $[\text{simp}]: (\text{Array-type } T \text{ } n)$ 
    with  $wf \text{ } CTn$  have  $\text{typeof-addr start-heap } a = \lfloor \text{Array-type } T \text{ } n \rfloor$ 
      by  $(\text{auto dest: NewHeapElem-start-heap-obsD})$ 
    with  $\text{adal show ?thesis by cases(auto dest: has-field-decl-above)}$ 
  qed
  moreover have  $w\text{-value } P (\text{w-values } P (\lambda\cdot. \{\}) (\text{map } \text{NormalAction } \text{obs})) (\text{NormalAction } (\text{NewHeapElem}$ 
     $a \text{ } CTn :: ('addr, 'thread-id) \text{obs-event}))$ 
     $(a, al) \subseteq w\text{-values } P (\lambda\cdot. \{\}) (\text{map } \text{NormalAction } \text{start-heap-obs}) (a, al)$ 
    by  $(\text{simp add: obs del: w-value.simps})(\text{rule w-values-mono})$ 
  ultimately show ?thesis by blast
qed

end

end

```

## 8.11 Combination of locales for heap operations and interleaving

**theory** *JMM-Framework*

**imports**

*JMM-Heap*

*../Framework/FWInitFinLift*

*../Common/WellForm*

**begin**

**lemma** *enat-plus-eq-enat-conv*: — Move to Extended\_Nat

$\text{enat } m + n = \text{enat } k \longleftrightarrow k \geq m \wedge n = \text{enat } (k - m)$

**by**  $(\text{cases } n) \text{ auto}$

**declare** *convert-new-thread-action-id*  $[\text{simp}]$

**context** *heap* **begin**

**lemma** *init-fin-lift-state-start-state*:

$\text{init-fin-lift-state } s (\text{start-state } f \text{ } P \text{ } C \text{ } M \text{ } vs) = \text{start-state } (\lambda C \text{ } M \text{ } Ts \text{ } T \text{ } \text{meth } vs. (s, f \text{ } C \text{ } M \text{ } Ts \text{ } T \text{ } \text{meth}$

$vs)) \text{ } P \text{ } C \text{ } M \text{ } vs$

**by**  $(\text{simp add: start-state-def init-fin-lift-state-def split-beta fun-eq-iff})$

**lemma** *non-speculative-start-heap-obs*:

*non-speculative P vs (llist-of (map snd (lift-start-obs start-tid start-heap-obs)))*  
**apply**(rule *non-speculative-nthI*)  
**using** *start-heap-obs-not-Read*  
**by**(*clarsimp simp add: lift-start-obs-def lnth-LCons o-def eSuc-enat[symmetric] in-set-conv-nth split: nat.split-asm*)

**lemma** *ta-seq-consist-start-heap-obs*:

*ta-seq-consist P Map.empty (llist-of (map snd (lift-start-obs start-tid start-heap-obs)))*  
**using** *start-heap-obs-not-Read*  
**by**(*auto intro: ta-seq-consist-nthI simp add: lift-start-obs-def o-def lnth-LCons in-set-conv-nth split: nat.split-asm*)

**end**

**context** *allocated-heap* **begin**

**lemma** *w-addr-start-heap-obs*:

*w-addr (w-values P vs (map snd (lift-start-obs start-tid start-heap-obs)))  $\subseteq$  w-addr vs*  
**by**(*simp add: lift-start-obs-def o-def w-addr-start-heap-obs*)

**end**

**context** *heap* **begin**

**lemma** *w-values-start-heap-obs-typeable*:

**assumes** *wf: wf-syscls P*  
**and** *mrws: v  $\in$  w-values P ( $\lambda\cdot$ . { }) (map snd (lift-start-obs start-tid start-heap-obs)) (ad, al)*  
**shows**  $\exists T. P, \text{start-heap} \vdash \text{ad}@al : T \wedge P, \text{start-heap} \vdash v : \leq T$

**proof** –

**from** *in-w-valuesD[OF mrws]*

**obtain** *obs' wa obs''*

**where** *eq: map snd (lift-start-obs start-tid start-heap-obs) = obs' @ wa # obs''*

**and** *is-write-action wa*

**and** *adal: (ad, al)  $\in$  action-loc-aux P wa*

**and** *vwa: value-written-aux P wa al = v*

**by** *blast*

**from**  $\langle \text{is-write-action } wa \rangle$  **show** *?thesis*

**proof** *cases*

**case** (*WriteMem ad' al' v'*)

**with** *vwa adal eq* **have** *WriteMem ad al v  $\in$  set start-heap-obs*

**by**(*auto simp add: map-eq-append-conv Cons-eq-append-conv lift-start-obs-def*)

**thus** *?thesis* **by**(rule *start-heap-write-typeable*)

**next**

**case** (*NewHeapElem ad' hT*)

**with** *vwa adal eq* **have** *NewHeapElem ad hT  $\in$  set start-heap-obs*

**by**(*auto simp add: map-eq-append-conv Cons-eq-append-conv lift-start-obs-def*)

**hence** *typeof-addr start-heap ad =  $\lfloor hT \rfloor$*

**by**(rule *NewHeapElem-start-heap-obsD[OF wf]*)

**thus** *?thesis* **using** *adal vwa NewHeapElem*

**apply**(*cases hT*)

**apply**(*auto intro!: addr-loc-type.intros dest: has-field-decl-above*)

**apply**(*frule has-field-decl-above*)

**apply**(*auto intro!: addr-loc-type.intros dest: has-field-decl-above*)

done  
qed  
qed

**lemma** *start-state-vs-conf*:

*wf-syscls*  $P \implies vs\text{-}conf\ P\ start\text{-}heap\ (w\text{-}values\ P\ (\lambda\cdot. \{\})\ (map\ snd\ (lift\text{-}start\text{-}obs\ start\text{-}tid\ start\text{-}heap\text{-}obs)))$   
**by**(*rule* *vs-confI*)(*rule* *w-values-start-heap-obs-typeable*)

**end**

### 8.11.1 JMM traces for Jinja semantics

**context** *multithreaded-base* **begin**

**inductive-set**  $\mathcal{E} :: ('l, 't, 'x, 'm, 'w)\ state \Rightarrow ('t \times 'o)\ llist\ set$   
**for**  $\sigma :: ('l, 't, 'x, 'm, 'w)\ state$

**where**

*mtthr.Runs*  $\sigma\ E'$   
 $\implies lconcat\ (lmap\ (\lambda(t, ta).\ llist\text{-}of\ (map\ (Pair\ t)\ \{\!\!\{ta\}\!\!\}_o))\ E') \in \mathcal{E}\ \sigma$

**lemma** *actions- $\mathcal{E}E\text{-}aux$* :

**fixes**  $\sigma\ E'$   
**defines**  $E == lconcat\ (lmap\ (\lambda(t, ta).\ llist\text{-}of\ (map\ (Pair\ t)\ \{\!\!\{ta\}\!\!\}_o))\ E')$   
**assumes** *mtthr*: *mtthr.Runs*  $\sigma\ E'$   
**and**  $a$ : *enat*  $a < llength\ E$   
**obtains**  $m\ n\ t\ ta$   
**where**  $lnth\ E\ a = (t, \{\!\!\{ta\}\!\!\}_o ! n)$   
**and**  $n < length\ \{\!\!\{ta\}\!\!\}_o$  **and** *enat*  $m < llength\ E'$   
**and**  $a = (\sum i < m.\ length\ \{\!\!\{snd\ (lnth\ E'\ i)\}\!\!\}_o) + n$   
**and**  $lnth\ E'\ m = (t, ta)$

**proof** –

**from** *lnth-lconcat-conv*[*OF*  $a[unfolded\ E\text{-}def], folded\ E\text{-}def]$

**obtain**  $m\ n$

**where**  $lnth\ E\ a = lnth\ (lnth\ (lmap\ (\lambda(t, ta).\ llist\text{-}of\ (map\ (Pair\ t)\ \{\!\!\{ta\}\!\!\}_o))\ E')\ m)\ n$   
**and** *enat*  $n < llength\ (lnth\ (lmap\ (\lambda(t, ta).\ llist\text{-}of\ (map\ (Pair\ t)\ \{\!\!\{ta\}\!\!\}_o))\ E')\ m)$   
**and** *enat*  $m < llength\ (lmap\ (\lambda(t, ta).\ llist\text{-}of\ (map\ (Pair\ t)\ \{\!\!\{ta\}\!\!\}_o))\ E')$   
**and** *enat*  $a = (\sum i < m.\ llength\ (lnth\ (lmap\ (\lambda(t, ta).\ llist\text{-}of\ (map\ (Pair\ t)\ \{\!\!\{ta\}\!\!\}_o))\ E')\ i)) + \text{enat}\ n$

$n$

**by** *blast*

**moreover**

**obtain**  $t\ ta$  **where**  $lnth\ E'\ m = (t, ta)$  **by**(*cases*  $lnth\ E'\ m$ )

**ultimately have** *E-a*:  $lnth\ E\ a = (t, \{\!\!\{ta\}\!\!\}_o ! n)$

**and**  $n$ :  $n < length\ \{\!\!\{ta\}\!\!\}_o$

**and**  $m$ : *enat*  $m < llength\ E'$

**and**  $a$ : *enat*  $a = (\sum i < m.\ llength\ (lnth\ (lmap\ (\lambda(t, ta).\ llist\text{-}of\ (map\ (Pair\ t)\ \{\!\!\{ta\}\!\!\}_o))\ E')\ i)) +$

*enat*  $n$

**by**(*simp-all*)

**note**  $a$

**also have**  $(\sum i < m.\ llength\ (lnth\ (lmap\ (\lambda(t, ta).\ llist\text{-}of\ (map\ (Pair\ t)\ \{\!\!\{ta\}\!\!\}_o))\ E')\ i)) =$   
 $sum\ (\text{enat} \circ (\lambda i.\ length\ \{\!\!\{snd\ (lnth\ E'\ i)\}\!\!\}_o))\ \{..<m\}$

**using**  $m$  **by**(*simp add: less-trans*[**where**  $y = \text{enat}\ m$ ] *split-beta*)

**also have**  $\dots = \text{enat}\ (\sum i < m.\ length\ \{\!\!\{snd\ (lnth\ E'\ i)\}\!\!\}_o)$

**by**(*subst sum-comp-morphism*)(*simp-all add: zero-enat-def*)

**finally have**  $a$ :  $a = (\sum i < m.\ length\ \{\!\!\{snd\ (lnth\ E'\ i)\}\!\!\}_o) + n$  **by** *simp*

with  $E$ -a  $n$  **show** *thesis* **using**  $\langle \text{lnth } E' \ m = (t, ta) \rangle$  **by**(*rule that*)  
**qed**

**lemma** *actions- $\mathcal{E}E$* :

**assumes**  $E$ :  $E \in \mathcal{E} \ \sigma$   
**and**  $a$ :  $\text{enat } a < \text{llength } E$   
**obtains**  $E' \ m \ n \ t \ ta$   
**where**  $E = \text{lconcat } (\text{lmap } (\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \ \{\!\!\{ta\}\!\!\}_o)) \ E')$   
**and**  $\text{mthr.Runs } \sigma \ E'$   
**and**  $\text{lnth } E \ a = (t, \{\!\!\{ta\}\!\!\}_o \ ! \ n)$   
**and**  $n < \text{length } \{\!\!\{ta\}\!\!\}_o$  **and**  $\text{enat } m < \text{llength } E'$   
**and**  $a = (\sum i < m. \text{length } \{\!\!\{\text{snd } (\text{lnth } E' \ i)\}\!\!\}_o) + n$   
**and**  $\text{lnth } E' \ m = (t, ta)$

**proof** –

**from**  $E$  **obtain**  $E' \ ws$   
**where**  $E$ :  $E = \text{lconcat } (\text{lmap } (\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \ \{\!\!\{ta\}\!\!\}_o)) \ E')$   
**and**  $\text{mthr.Runs } \sigma \ E'$  **by**(*rule  $\mathcal{E}$ .cases*) *blast*  
**from**  $\langle \text{mthr.Runs } \sigma \ E' \rangle$   $a[\text{unfolded } E]$   
**show** *?thesis*  
**by**(*rule actions- $\mathcal{E}E$ -aux*)(*fold*  $E$ , *rule that*[*OF*  $E \ \langle \text{mthr.Runs } \sigma \ E' \rangle$ ])

**qed**

**end**

**context**  $\tau$ *multithreaded-wf* **begin**

Alternative characterisation for  $\mathcal{E}$

**lemma**  *$\mathcal{E}$ -conv-Runs*:

$\mathcal{E} \ \sigma = \text{lconcat } (\text{lmap } (\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \ \{\!\!\{ta\}\!\!\}_o)) \ \text{'llist-of-tllist' } \{E. \text{mthr.}\tau\text{Runs } \sigma \ E\})$   
*(is ?lhs = ?rhs)*

**proof**(*intro equalityI subsetI*)

**fix**  $E$   
**assume**  $E \in ?rhs$   
**then obtain**  $E'$  **where**  $E$ :  $E = \text{lconcat } (\text{lmap } (\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \ \{\!\!\{ta\}\!\!\}_o)) \ (\text{llist-of-tllist } E'))$   
**and**  $\tau\text{Runs}$ :  $\text{mthr.}\tau\text{Runs } \sigma \ E'$  **by**(*blast*)  
**obtain**  $E''$  **where**  $E'$ :  $E' = \text{tmap } (\lambda(\text{tls}, s', \text{tl}, s''). \text{tl}) \ (\text{case-sum } (\lambda(\text{tls}, s'). \lfloor s' \rfloor) \ \text{Map.empty}) \ E''$   
**and**  $\tau\text{Runs}'$ :  $\text{mthr.}\tau\text{Runs-table2 } \sigma \ E''$   
**using**  $\tau\text{Runs}$  **by**(*rule mthr.}\tau\text{Runs-into-}\tau\text{Runs-table2}*)  
**have**  $\text{mthr.Runs } \sigma \ (\text{lconcat } (\text{lappend } (\text{lmap } (\lambda(\text{tls}, s, \text{tl}, s'). \text{llist-of } (\text{tls} @ [\text{tl}])) \ (\text{llist-of-tllist } E'')) \ (\text{LCons } (\text{case terminal } E'' \ \text{of } \text{Inl } (\text{tls}, s') \Rightarrow \text{llist-of } \text{tls} \mid \text{Inr } \text{tls} \Rightarrow \text{tls})$

$\text{LNil}))$

*(is mthr.Runs - ?E'')*

**using**  $\tau\text{Runs}'$  **by**(*rule mthr.}\tau\text{Runs-table2-into-Runs}*)

**moreover**

**let**  $?tail = \lambda E''. \text{case terminal } E'' \ \text{of } \text{Inl } (\text{tls}, s') \Rightarrow \text{llist-of } \text{tls} \mid \text{Inr } \text{tls} \Rightarrow \text{tls}$

**{**

**have**  $E = \text{lconcat } (\text{lfilter } (\lambda xs. \neg \text{lnull } xs) \ (\text{lmap } (\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \ \{\!\!\{ta\}\!\!\}_o)) \ (\text{llist-of-tllist } E'))$

**unfolding**  $E$  **by**(*simp add: lconcat-lfilter-neq-LNil*)

**also have**  $\dots = \text{lconcat } (\text{lmap } (\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \ \{\!\!\{ta\}\!\!\}_o)) \ (\text{lmap } (\lambda(\text{tls}, s', \text{tta}, s''). \text{tta}) \ (\text{lfilter } (\lambda(\text{tls}, s', (t, ta), s''). \{\!\!\{ta\}\!\!\}_o \neq []) \ (\text{llist-of-tllist } E'))))$

**by**(*simp add: E' lfilter-lmap llist.map-comp o-def split-def*)

**also**

```

from  $\langle mthr.\tauRuns\text{-}table2\ \sigma\ E'' \rangle$ 
have  $lmap\ (\lambda(tls, s', tta, s'').\ tta)\ (lfilter\ (\lambda(tls, s', (t, ta), s'').\ \{ta\}_o \neq [])\ (l\text{-}list\text{-}of\text{-}tlist\ E'')) =$ 
 $lfilter\ (\lambda(t, ta).\ \{ta\}_o \neq [])\ (lconcat\ (lappend\ (lmap\ (\lambda(tls, s, tl, s').\ l\text{-}list\text{-}of\ (tls\ @\ [tl]))$ 
 $(l\text{-}list\text{-}of\text{-}tlist\ E''))\ (LCons\ (?tail\ E'')\ LNil)))$ 
 $(is\ ?lhs\ \sigma\ E'' = ?rhs\ \sigma\ E'')$ 
proof(coinduction arbitrary:  $\sigma\ E''$  rule:  $l\text{-}list.coinduct\text{-}strong$ )
case ( $Eq\text{-}l\text{-}list\ \sigma\ E''$ )
have  $?lnull$ 
by(cases  $lfinite\ (l\text{-}list\text{-}of\text{-}tlist\ E'')$ )(fastforce split:  $sum.split\text{-}asm\ simp\ add: split\text{-}beta\ lset\text{-}lconcat\text{-}lfinite$ 
 $lappend\text{-}inf\ mthr.silent\text{-}move2\text{-}def\ dest: mthr.\tauRuns\text{-}table2\text{-}silentsD[OF\ Eq\text{-}l\text{-}list]\ mthr.\tauRuns\text{-}table2\text{-}terminal\text{-}silentD$ 
 $Eq\text{-}l\text{-}list]\ mthr.\tauRuns\text{-}table2\text{-}terminal\text{-}inf\text{-}stepD[OF\ Eq\text{-}l\text{-}list]\ m\tau move\text{-}silentD\ inf\text{-}step\text{-}silentD\ silent\text{-}moves2\text{-}silentD$ 
 $split: sum.split\text{-}asm$ ) +
moreover
have  $?LCons$ 
proof(intro impI conjI)
assume  $lhs': \neg\ lnull\ (lmap\ (\lambda(tls, s', tta, s'').\ tta)\ (lfilter\ (\lambda(tls, s', (t, ta), s'').\ \{ta\}_o \neq [])$ 
 $(l\text{-}list\text{-}of\text{-}tlist\ E'')))$ 
 $(is\ \neg\ lnull\ ?lhs')$ 
and  $\neg\ lnull\ (lfilter\ (\lambda(t, ta).\ \{ta\}_o \neq [])\ (lconcat\ (lappend\ (lmap\ (\lambda(tls, s, tl, s').\ l\text{-}list\text{-}of\ (tls\ @\ [tl]))$ 
 $[tl]))\ (l\text{-}list\text{-}of\text{-}tlist\ E''))\ (LCons\ (case\ terminal\ E''\ of\ Inl\ (tls, s') \Rightarrow l\text{-}list\text{-}of\ tls\ |\ Inr\ tls \Rightarrow tls)\ LNil)))$ 
 $(is\ \neg\ lnull\ ?rhs')$ 

note  $\tauRuns' = \langle mthr.\tauRuns\text{-}table2\ \sigma\ E'' \rangle$ 
from  $lhs'$  obtain  $tl\ tls'$  where  $?lhs\ \sigma\ E'' = LCons\ tl\ tls'$ 
by(auto simp only: not-lnull-conv)
then obtain  $tls\ s'\ s''\ tlstlss'$ 
where  $tls': tls' = lmap\ (\lambda(tls, s', tta, s'').\ tta)\ tlstlss'$ 
and  $filter: lfilter\ (\lambda(tls, s', (t, ta), s'').\ obs\text{-}a\ ta \neq [])\ (l\text{-}list\text{-}of\text{-}tlist\ E'') = LCons\ (tls, s', tl,$ 
 $s'')\ tlstlss'$ 
using  $lhs'$  by(fastforce simp add:  $lmap\text{-}eq\text{-}LCons\text{-}conv$ )
from  $lfilter\text{-}eq\text{-}LConsD[OF\ filter]$ 
obtain  $us\ vs$  where  $eq: l\text{-}list\text{-}of\text{-}tlist\ E'' = lappend\ us\ (LCons\ (tls, s', tl, s'')\ vs)$ 
and  $fin: lfinite\ us$ 
and  $empty: \forall (tls, s', (t, ta), s'') \in lset\ us.\ obs\text{-}a\ ta = []$ 
and  $neq\text{-}empty: obs\text{-}a\ (snd\ tl) \neq []$ 
and  $tlstlss': tlstlss' = lfilter\ (\lambda(tls, s', (t, ta), s'').\ obs\text{-}a\ ta \neq [])\ vs$ 
by(auto simp add: split-beta)
from  $eq$  obtain  $E'''$  where  $E'': E'' = lappendt\ us\ E'''$ 
and  $eq': l\text{-}list\text{-}of\text{-}tlist\ E''' = LCons\ (tls, s', tl, s'')\ vs$ 
and  $terminal: terminal\ E''' = terminal\ E''$ 
unfolding  $l\text{-}list\text{-}of\text{-}tlist\text{-}eq\text{-}lappend\text{-}conv$  by auto
from  $\tauRuns'\ fin\ E''$  obtain  $\sigma'$  where  $\tauRuns'': mthr.\tauRuns\text{-}table2\ \sigma'\ E'''$ 
by(auto dest:  $mthr.\tauRuns\text{-}table2\text{-}lappendtD$ )
then obtain  $\sigma''\ E''''$  where  $mthr.\tauRuns\text{-}table2\ \sigma''\ E''''\ E''' = TCons\ (tls, s', tl, s'')\ E''''$ 
using  $eq'$  by cases auto
moreover from  $\tauRuns'\ E''\ fin$ 
have  $\forall (tls, s, tl, s') \in lset\ us.\ \forall (t, ta) \in set\ tls.\ ta = \varepsilon$ 
by(fastforce dest:  $mthr.\tauRuns\text{-}table2\text{-}silentsD\ m\tau move\text{-}silentD\ simp\ add: mthr.silent\text{-}move2\text{-}def$ )
hence  $lfilter\ (\lambda(t, ta).\ obs\text{-}a\ ta \neq [])\ (lconcat\ (lmap\ (\lambda(tls, s, tl, s').\ l\text{-}list\text{-}of\ (tls\ @\ [tl]))\ us)) =$ 
 $LNil$ 
using empty by(auto simp add:  $lfilter\text{-}empty\text{-}conv\ lset\text{-}lconcat\text{-}lfinite\ split\text{-}beta$ )
moreover from  $\tauRuns''\ eq'$  have  $snd\ 'set\ tls \subseteq \{\varepsilon\}$ 
by(cases)(fastforce dest:  $silent\text{-}moves2\text{-}silentD$ ) +
hence  $[(t, ta) \leftarrow tls.\ obs\text{-}a\ ta \neq []] = []$ 

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    by(auto simp add: filter-empty-conv split-beta)
  ultimately
  show lhd ?lhs' = lhd ?rhs'
    and ( $\exists \sigma E''$ . ltl ?lhs' = lmap ( $\lambda(tls, s', tta, s'')$ ). tta) (lfilter ( $\lambda(tls, s', (t, ta), s'')$ ).  $\{ta\}_o \neq []$ )
    (llist-of-tllist E'')  $\wedge$ 
    ltl ?rhs' = lfilter ( $\lambda(t, ta)$ .  $\{ta\}_o \neq []$ ) (lconcat (lappend (lmap ( $\lambda(tls, s, tl, s')$ ). llist-of (tls @
    [tl])) (llist-of-tllist E'')) (LCons (case terminal E'' of Inl (tls, s')  $\Rightarrow$  llist-of tls | Inr tls  $\Rightarrow$  tls) LNil)))
   $\wedge$ 
     $\tau trsys.\tau Runs$ -table2 redT m $\tau$ move  $\sigma E'$ )  $\vee$ 
    ltl ?lhs' = ltl ?rhs'
  using lhs' E'' fin tls' tssstls' filter eq' neq-empty
  by(auto simp add: lmap-lappend-distrib lappend-assoc split-beta filter-empty-conv simp del:
  split-paired-Ex)
  qed
  ultimately show ?case ..
  qed
  also have lmap ( $\lambda(t, ta)$ . llist-of (map (Pair t) (obs-a ta))) ... = lfilter ( $\lambda obs$ .  $\neg lnull\ obs$ ) (lmap
  ( $\lambda(t, ta)$ . llist-of (map (Pair t) (obs-a ta))) (lconcat (lappend (lmap ( $\lambda(tls, s, tl, s')$ ). llist-of (tls @
  [tl])) (llist-of-tllist E'')) (LCons (?tail E'') LNil))))
  unfolding lfilter-lmap by(simp add: o-def split-def llist-of-eq-LNil-conv)
  finally have E = lconcat (lmap ( $\lambda(t, ta)$ . llist-of (map (Pair t)  $\{ta\}_o$ )) ?E''')
  by(simp add: lconcat-lfilter-neq-LNil) }
  ultimately show E  $\in$  ?lhs by(blast intro:  $\mathcal{E}$ .intros)
next
fix E
assume E  $\in$  ?lhs
then obtain E' where E: E = lconcat (lmap ( $\lambda(t, ta)$ . llist-of (map (Pair t) (obs-a ta))) E')
  and Runs: mthr.Runs  $\sigma E'$  by(blast elim:  $\mathcal{E}$ .cases)
from Runs obtain E'' where E': E' = lmap ( $\lambda(s, tl, s')$ . tl) E''
  and Runs': mthr.Runs-table  $\sigma E''$  by(rule mthr.Runs-into-Runs-table)
have mthr. $\tau$ Runs  $\sigma$  (tmap ( $\lambda(s, tl, s')$ . tl) id (tfilter None ( $\lambda(s, tl, s')$ .  $\neg m\tau move\ s\ tl\ s'$ ) (tllist-of-llist
(Some (llast (LCons  $\sigma$  (lmap ( $\lambda(s, tl, s')$ . s') E'')))) E''))))
  (is mthr. $\tau$ Runs - ?E'')
  using Runs' by(rule mthr.Runs-table-into- $\tau$ Runs)
moreover
have ( $\lambda(s, (t, ta), s')$ . obs-a ta  $\neq []$ ) = ( $\lambda(s, (t, ta), s')$ . obs-a ta  $\neq [] \wedge \neg m\tau move\ s\ (t, ta)\ s'$ )
  by(rule ext)(auto dest: m $\tau$ move-silentD)
hence E = lconcat (lmap ( $\lambda(t, ta)$ . llist-of (map (Pair t) (obs-a ta))) (llist-of-tllist ?E''))
  unfolding E E'
  by(subst (1 2) lconcat-lfilter-neq-LNil[symmetric])(simp add: lfilter-lmap lfilter-lfilter o-def split-def)
  ultimately show E  $\in$  ?rhs by(blast)
qed
end

```

Running threads have been started before

**definition** Status-no-wait-locks :: ( $'l, 't, status \times 'x$ ) thread-info  $\Rightarrow$  bool

where

Status-no-wait-locks ts  $\longleftrightarrow$   
 $(\forall t\ status\ x\ ln.$  ts t =  $\lfloor((status, x), ln)\rfloor \longrightarrow status \neq Running \longrightarrow ln = no-wait-locks)$

**lemma** Status-no-wait-locks-PreStartD:

$\bigwedge ln.$   $\llbracket Status-no-wait-locks\ ts; ts\ t = \lfloor((PreStart, x), ln)\rfloor \rrbracket \Longrightarrow ln = no-wait-locks$

unfolding Status-no-wait-locks-def by blast

**lemma** *Status-no-wait-locks-FinishedD*:

$\bigwedge ln. \llbracket \text{Status-no-wait-locks } ts; ts \ t = \lfloor ((\text{Finished}, x), ln) \rfloor \rrbracket \implies ln = \text{no-wait-locks}$   
**unfolding** *Status-no-wait-locks-def* **by** *blast*

**lemma** *Status-no-wait-locksI*:

$(\bigwedge t \ status \ x \ ln. \llbracket ts \ t = \lfloor ((\text{status}, x), ln) \rfloor; \text{status} = \text{PreStart} \vee \text{status} = \text{Finished} \rrbracket \implies ln = \text{no-wait-locks})$

$\implies \text{Status-no-wait-locks } ts$

**unfolding** *Status-no-wait-locks-def*

**apply** *clarify*

**apply**(*case-tac status*)

**apply** *auto*

**done**

**context** *heap-base* **begin**

**lemma** *Status-no-wait-locks-start-state*:

*Status-no-wait-locks (thr (init-fin-lift-state status (start-state f P C M vs)))*

**by**(*clarsimp simp add: Status-no-wait-locks-def init-fin-lift-state-def start-state-def split-beta*)

**end**

**context** *multithreaded-base* **begin**

**lemma** *init-fin-preserve-Status-no-wait-locks*:

**assumes** *ok: Status-no-wait-locks (thr s)*

**and** *redT: multithreaded-base.redT init-fin-final init-fin (map NormalAction  $\circ$  convert-RA) s tta s'*

**shows** *Status-no-wait-locks (thr s')*

**using** *redT*

**proof**(*cases rule: multithreaded-base.redT.cases[consumes 1, case-names redT-normal redT-acquire]*)

**case** *redT-acquire*

**with** *ok* **show** *?thesis*

**by**(*auto intro!: Status-no-wait-locksI dest: Status-no-wait-locks-PreStartD Status-no-wait-locks-FinishedD split: if-split-asm*)

**next**

**case** *redT-normal*

**show** *?thesis*

**proof**(*rule Status-no-wait-locksI*)

**fix** *t' status' x' ln'*

**assume** *tst': thr s' t' =  $\lfloor ((\text{status}', x'), ln') \rfloor$*

**and** *status: status' = PreStart  $\vee$  status' = Finished*

**show** *ln' = no-wait-locks*

**proof**(*cases thr s t'*)

**case** *None*

**with** *redT-normal tst'* **show** *?thesis*

**by**(*fastforce elim!: init-fin.cases dest: redT-updTs-new-thread simp add: final-thread.actions-ok-iff*

*split: if-split-asm*)

**next**

**case** (*Some sxln*)

**obtain** *status'' x'' ln''*

**where** [*simp*]: *sxln = ((status'', x''), ln'')* **by**(*cases sxln*) *auto*

**show** *?thesis*

**proof**(*cases fst tta = t'*)



```

    case True
    with redT-normal tst' status show ?thesis by(auto simp add: expand-funfun-eq fun-eq-iff)
next
case False
with tst' redT-normal Some status have status'' = status' ln'' = ln'
  by(force dest: redT-updTs-Some simp add: final-thread.actions-ok-iff)+
with ok Some status show ?thesis
  by(auto dest: Status-no-wait-locks-PreStartD Status-no-wait-locks-FinishedD)
qed
qed
qed
qed

lemma init-fin-Running-InitialThreadAction:
  assumes redT: multithreaded-base.redT init-fin-final init-fin (map NormalAction  $\circ$  convert-RA) s tta
  s'
  and not-running:  $\bigwedge x \ln. \text{thr } s \ t \neq \lfloor ((\text{Running}, x), \ln) \rfloor$ 
  and running:  $\text{thr } s' \ t = \lfloor ((\text{Running}, x'), \ln') \rfloor$ 
  shows tta = (t,  $\{ \text{InitialThreadAction} \}$ )
using redT
proof(cases rule: multithreaded-base.redT.cases[consumes 1, case-names redT-normal redT-acquire])
  case redT-acquire
  with running not-running show ?thesis by(auto split: if-split-asm)
next
case redT-normal
show ?thesis
proof(cases thr s t)
  case None
  with redT-normal running not-running show ?thesis
  by(fastforce simp add: final-thread.actions-ok-iff elim: init-fin.cases dest: redT-updTs-new-thread
split: if-split-asm)
  next
  case (Some a)
  with redT-normal running not-running show ?thesis
  apply(cases a)
  apply(auto simp add: final-thread.actions-ok-iff split: if-split-asm elim: init-fin.cases)
  apply((drule (1) redT-updTs-Some)?, fastforce)+
  done
qed
qed

end

context if-multithreaded begin

lemma init-fin-Trsys-preserve-Status-no-wait-locks:
  assumes ok: Status-no-wait-locks (thr s)
  and Trsys: if.mthr.Trsys s ttas s'
  shows Status-no-wait-locks (thr s')
using Trsys ok
by(induct)(blast dest: init-fin-preserve-Status-no-wait-locks)+

lemma init-fin-Trsys-Running-InitialThreadAction:
  assumes redT: if.mthr.Trsys s ttas s'

```

```

and not-running:  $\bigwedge x \ln. \text{thr } s \ t \neq \lfloor ((\text{Running}, x), \ln) \rfloor$ 
and running:  $\text{thr } s' \ t = \lfloor ((\text{Running}, x'), \ln') \rfloor$ 
shows  $(t, \llbracket \text{InitialThreadAction} \rrbracket) \in \text{set } ttas$ 
using redT not-running running
proof(induct arbitrary: x' ln')
  case rtrancl3p-refl thus ?case by(fastforce)
next
  case (rtrancl3p-step s ttas s' tta s') thus ?case
    by(cases  $\exists x \ln. \text{thr } s' \ t = \lfloor ((\text{Running}, x), \ln) \rfloor$ )(fastforce dest: init-fin-Running-InitialThreadAction)+
qed

```

**end**

```

locale heap-multithreaded-base =
  heap-base
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  +
  mthr: multithreaded-base final r convert-RA
  for addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
  and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
  and spurious-wakeups :: bool
  and empty-heap :: 'heap
  and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
  and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype
  and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
  and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
  and final :: 'x  $\Rightarrow$  bool
  and r :: ('addr, 'thread-id, 'x, 'heap, 'addr, ('addr, 'thread-id) obs-event) semantics ( $- \vdash - \dashrightarrow -$ 
    [50,0,0,50] 80)
  and convert-RA :: 'addr released-locks  $\Rightarrow$  ('addr, 'thread-id) obs-event list

```

**sublocale** *heap-multithreaded-base* < *mthr: if-multithreaded-base final r convert-RA*

.

**context** *heap-multithreaded-base* **begin**

**abbreviation**  $\mathcal{E}\text{-start}$  ::

```

  (cname  $\Rightarrow$  mname  $\Rightarrow$  ty list  $\Rightarrow$  ty  $\Rightarrow$  'md  $\Rightarrow$  'addr val list  $\Rightarrow$  'x)
   $\Rightarrow$  'md prog  $\Rightarrow$  cname  $\Rightarrow$  mname  $\Rightarrow$  'addr val list  $\Rightarrow$  status
   $\Rightarrow$  ('thread-id  $\times$  ('addr, 'thread-id) obs-event action) llist set

```

**where**

```

   $\mathcal{E}\text{-start } f \ P \ C \ M \ \text{vs } \text{status} \equiv$ 
  lappend (llist-of (lift-start-obs start-tid start-heap-obs)) '
  mthr.if.ℰ (init-fin-lift-state status (start-state f P C M vs))

```

**end**

```

locale heap-multithreaded =
  heap-multithreaded-base
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write

```

```

  final r convert-RA
+
heap
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  P
+
mthr: multithreaded final r convert-RA

for addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and final :: 'x ⇒ bool
and r :: ('addr, 'thread-id, 'x, 'heap, 'addr, ('addr, 'thread-id) obs-event) semantics (- ⊢ - → -
[50,0,0,50] 80)
and convert-RA :: 'addr released-locks ⇒ ('addr, 'thread-id) obs-event list
and P :: 'md prog

sublocale heap-multithreaded < mthr: if-multithreaded final r convert-RA
by(unfold-locales)

sublocale heap-multithreaded < if: jmm-multithreaded
  mthr.init-fin-final mthr.init-fin map NormalAction o convert-RA P
.

context heap-multithreaded begin

lemma thread-start-actions-ok-init-fin-RedT:
  assumes Red: mthr.if.RedT (init-fin-lift-state status (start-state f P C M vs)) ttas s'
    (is mthr.if.RedT ?start-state - -)
  shows thread-start-actions-ok (llist-of (lift-start-obs start-tid start-heap-obs @ concat (map (λ(t, ta).
map (Pair t) {ta}_o) ttas)))
    (is thread-start-actions-ok (llist-of (?obs-prefix @ ?E')))
proof(rule thread-start-actions-okI)
  let ?E = llist-of (?obs-prefix @ ?E')
  fix a
  assume a: a ∈ actions ?E
  and new: ¬ is-new-action (action-obs ?E a)
  show ∃ i ≤ a. action-obs ?E i = InitialThreadAction ∧ action-tid ?E i = action-tid ?E a
proof(cases action-tid ?E a = start-tid)
  case True thus ?thesis
    by(auto simp add: lift-start-obs-def action-tid-def action-obs-def)
next
  case False
  let ?a = a - length ?obs-prefix

  from False have a-len: a ≥ length ?obs-prefix
  by(rule contrapos-np)(auto simp add: lift-start-obs-def action-tid-def lnth-LCons nth-append split:

```

```

nat.split)
  hence [simp]: action-tid ?E a = action-tid (llist-of ?E') ?a action-obs ?E a = action-obs (llist-of
    ?E') ?a
    by(simp-all add: action-tid-def nth-append action-obs-def)

  from False have not-running:  $\bigwedge x \ln. \text{thr } ?\text{start-state } (\text{action-tid } (\text{llist-of } ?E') ?a) \neq \lfloor ((\text{Running}, x), \ln) \rfloor$ 
    by(auto simp add: start-state-def split-beta init-fin-lift-state-def split: if-split-asm)

  from a a-len have ?a < length ?E' by(simp add: actions-def)
  from nth-concat-conv[OF this]
  obtain m n where E'-a: ?E' ! ?a = ( $\lambda(t, ta). (t, \llbracket ta \rrbracket_o ! n)$ ) (ttas ! m)
    and n: n < length  $\llbracket \text{snd } (ttas ! m) \rrbracket_o$ 
    and m: m < length ttas
    and a-conv: ?a = ( $\sum i < m. \text{length } (\text{map } (\lambda(t, ta). \text{map } (\text{Pair } t) \llbracket ta \rrbracket_o) \text{ttas } ! i)$ ) + n
    by(clarsimp simp add: split-def)

  from Red obtain s'' s''' where Red1: mthr.if.RedT ?start-state (take m ttas) s''
    and red: mthr.if.RedT s'' (ttas ! m) s'''
    and Red2: mthr.if.RedT s''' (drop (Suc m) ttas) s'
  unfolding mthr.if.RedT-def
  by(subst (asm) (4) id-take-nth-drop[OF m])(blast elim: rtrancl3p-appendE rtrancl3p-converseE)

  from E'-a m n have [simp]: action-tid (llist-of ?E') ?a = fst (ttas ! m)
    by(simp add: action-tid-def split-def)

  from red obtain status x ln where tst: thr s'' (fst (ttas ! m)) =  $\lfloor ((\text{status}, x), \ln) \rfloor$  by cases auto
  show ?thesis
  proof(cases status = PreStart  $\vee$  status = Finished)
    case True
    from Red1 have Status-no-wait-locks (thr s'')
      unfolding mthr.if.RedT-def
      by(rule mthr.init-fin-Trsys-preserve-Status-no-wait-locks[OF Status-no-wait-locks-start-state])
    with True tst have ln = no-wait-locks
      by(auto dest: Status-no-wait-locks-PreStartD Status-no-wait-locks-FinishedD)
    with red tst True have  $\llbracket \text{snd } (ttas ! m) \rrbracket_o = [\text{InitialThreadAction}]$  by(cases) auto
    hence action-obs ?E a = InitialThreadAction using a-conv n a-len E'-a
      by(simp add: action-obs-def nth-append split-beta)
    thus ?thesis by(auto)
  next
    case False
    hence status = Running by(cases status) auto
    with tst mthr.init-fin-Trsys-Running-InitialThreadAction[OF Red1[unfolded mthr.if.RedT-def]
not-running]
    have (fst (ttas ! m),  $\llbracket \text{InitialThreadAction} \rrbracket$ )  $\in \text{set } (\text{take } m \text{ttas})$ 
      using E'-a by(auto simp add: action-tid-def split-beta)
    then obtain i where i: i < m
      and nth-i: ttas ! i = (fst (ttas ! m),  $\llbracket \text{InitialThreadAction} \rrbracket$ )
      unfolding in-set-conv-nth by auto

    let ?i' = length (concat (map ( $\lambda(t, ta). \text{map } (\text{Pair } t) \llbracket ta \rrbracket_o$ ) (take i ttas)))
    let ?i = length ?obs-prefix + ?i'

    from i m nth-i

```

```

have ?i' < length (concat (map (λ(t, ta). map (Pair t) {ta}_o) (take m ttas)))
  apply(simp add: length-concat o-def split-beta)
  apply(subst (6) id-take-nth-drop[where i=i])
  apply(simp-all add: take-map[symmetric] min-def)
done
also from m have ... ≤ ?a unfolding a-conv
  by(simp add: length-concat sum-list-sum-nth min-def split-def atLeast0LessThan)
finally have ?i < a using a-len by simp
moreover
from i m nth-i have ?i' < length ?E'
  apply(simp add: length-concat o-def split-def)
  apply(subst (7) id-take-nth-drop[where i=i])
  apply(simp-all add: take-map[symmetric])
done
from nth-i i E'-a a-conv m
have lnth ?E ?i = (fst (ttas ! m), InitialThreadAction)
by(simp add: lift-start-obs-def nth-append length-concat o-def split-def)(rule nth-concat-eqI[where
k=0 and i=i], simp-all add: take-map o-def split-def)
ultimately show ?thesis using E'-a
  by(cases ttas ! m)(auto simp add: action-obs-def action-tid-def nth-append intro!: exI[where
x=?i])
qed
qed
qed

```

**lemma** *thread-start-actions-ok-init-fin:*

```

assumes E: E ∈ mthr.if.ℰ (init-fin-lift-state status (start-state f P C M vs))
shows thread-start-actions-ok (lappend (llist-of (lift-start-obs start-tid start-heap-obs)) E)
(is thread-start-actions-ok ?E)
proof(rule thread-start-actions-okI)
  let ?start-heap-obs = lift-start-obs start-tid start-heap-obs
  let ?start-state = init-fin-lift-state status (start-state f P C M vs)
  fix a
  assume a: a ∈ actions ?E
  and a-new: ¬ is-new-action (action-obs ?E a)
  show ∃ i. i ≤ a ∧ action-obs ?E i = InitialThreadAction ∧ action-tid ?E i = action-tid ?E a
  proof(cases action-tid ?E a = start-tid)
    case True thus ?thesis
      by(auto simp add: lift-start-obs-def action-tid-def action-obs-def)
    next
    case False
      let ?a = a - length ?start-heap-obs
      from False have a ≥ length ?start-heap-obs
        by(rule contrapos-np)(auto simp add: lift-start-obs-def action-tid-def lnth-LCons lnth-lappend1
split: nat.split)
      hence [simp]: action-tid ?E a = action-tid E ?a action-obs ?E a = action-obs E ?a
        by(simp-all add: action-tid-def lnth-lappend2 action-obs-def)
      from False have not-running: ∧ x ln. thr ?start-state (action-tid E ?a) ≠ [((Running, x), ln)]
        by(auto simp add: start-state-def split-beta init-fin-lift-state-def split: if-split-asm)

```

```

from  $E$  obtain  $E'$  where  $E'$ :  $E = \text{lconcat} (\text{lmap} (\lambda(t, ta). \text{llist-of} (\text{map} (\text{Pair } t) \{\!\!|ta|\!\!|_o)) E')$ 
  and  $\tau\text{Runs}$ :  $\text{mthr.if.mthr.Runs } ?\text{start-state } E' \text{ by}(\text{rule mthr.if.}\mathcal{E}.\text{cases})$ 
from  $a \ E' \ \langle a \geq \text{length } ?\text{start-heap-obs} \rangle$ 
have  $\text{enat-}a$ :  $\text{enat } ?a < \text{llength} (\text{lconcat} (\text{lmap} (\lambda(t, ta). \text{llist-of} (\text{map} (\text{Pair } t) \{\!\!|ta|\!\!|_o)) E'))$ 
  by( $\text{cases llength} (\text{lconcat} (\text{lmap} (\lambda(t, ta). \text{llist-of} (\text{map} (\text{Pair } t) \{\!\!|ta|\!\!|_o)) E'))(\text{auto simp add:}$ 
actions-def)
  with  $\tau\text{Runs}$  obtain  $m \ n \ t \ ta$ 
  where  $a\text{-obs}$ :  $\text{lnth} (\text{lconcat} (\text{lmap} (\lambda(t, ta). \text{llist-of} (\text{map} (\text{Pair } t) \{\!\!|ta|\!\!|_o)) E')) (a - \text{length}$ 
     $?\text{start-heap-obs}) = (t, \{\!\!|ta|\!\!|_o ! n)$ 
  and  $n$ :  $n < \text{length } \{\!\!|ta|\!\!|_o$ 
  and  $m$ :  $\text{enat } m < \text{llength } E'$ 
  and  $a\text{-conv}$ :  $?a = (\sum i < m. \text{length } \{\!\!|\text{snd} (\text{lnth } E' i)|\!\!|_o) + n$ 
  and  $E'\text{-}m$ :  $\text{lnth } E' m = (t, ta)$ 
  by( $\text{rule mthr.if.actions-}\mathcal{E}E\text{-aux}$ )
from  $a\text{-obs}$  have [simp]:  $\text{action-tid } E \ ?a = t \ \text{action-obs } E \ ?a = \{\!\!|ta|\!\!|_o ! n$ 
  by( $\text{simp-all add: } E' \ \text{action-tid-def action-obs-def}$ )

let  $?E' = \text{ldropn} (\text{Suc } m) E'$ 
let  $?m\text{-}E' = \text{ltake} (\text{enat } m) E'$ 
have  $E'\text{-unfold}$ :  $E' = \text{lappend} (\text{ltake} (\text{enat } m) E') (\text{LCons} (\text{lnth } E' m) ?E')$ 
  unfolding  $\text{ldropn-Suc-conv-ldropn}[OF \ m]$  by simp
hence  $\text{mthr.if.mthr.Runs } ?\text{start-state} (\text{lappend } ?m\text{-}E' (\text{LCons} (\text{lnth } E' m) ?E'))$ 
  using  $\tau\text{Runs}$  by simp
then obtain  $\sigma'$  where  $\sigma\text{-}\sigma'$ :  $\text{mthr.if.mthr.Trsys } ?\text{start-state} (\text{list-of } ?m\text{-}E') \ \sigma'$ 
  and  $\tau\text{Runs}'$ :  $\text{mthr.if.mthr.Runs } \sigma' (\text{LCons} (\text{lnth } E' m) ?E')$ 
  by( $\text{rule mthr.if.mthr.Runs-lappendE}$ ) simp
from  $\tau\text{Runs}'$  obtain  $\sigma'''$  where  $\text{red-}a$ :  $\text{mthr.if.redT } \sigma' (t, ta) \ \sigma'''$ 
  and  $\tau\text{Runs}''$ :  $\text{mthr.if.mthr.Runs } \sigma''' \ ?E'$ 
  unfolding  $E'\text{-}m$  by cases
from  $\text{red-}a$  obtain  $\text{status } x \ \text{ln}$  where  $\text{tst: thr } \sigma' \ t = [((\text{status}, x), \text{ln})]$  by cases auto
show  $?thesis$ 
proof( $\text{cases status} = \text{PreStart} \vee \text{status} = \text{Finished}$ )
  case True
  have  $\text{Status-no-wait-locks} (\text{thr } \sigma')$ 
  by( $\text{rule mthr.init-fin-Trsys-preserve-Status-no-wait-locks}[OF \ \sigma\text{-}\sigma'](\text{rule Status-no-wait-locks-start-state})$ 
  with  $\text{True tst}$  have  $\text{ln} = \text{no-wait-locks}$ 
  by( $\text{auto dest: Status-no-wait-locks-PreStartD Status-no-wait-locks-FinishedD}$ )
  with  $\text{red-}a \ \text{tst} \ \text{True}$  have  $\{\!\!|ta|\!\!|_o = [\text{InitialThreadAction}]$  by(cases auto)
  hence  $\text{action-obs } E \ ?a = \text{InitialThreadAction}$  using  $a\text{-obs } n$  unfolding  $E'$ 
  by( $\text{simp add: action-obs-def}$ )
  thus  $?thesis$  by(auto)
next
  case False
  hence  $\text{status} = \text{Running}$  by( $\text{cases status}$ ) auto
  with  $\text{tst mthr.init-fin-Trsys-Running-InitialThreadAction}[OF \ \sigma\text{-}\sigma' \ \text{not-running}]$ 
  have  $(\text{action-tid } E \ ?a, \{\!\!|\text{InitialThreadAction}|\!\!|) \in \text{set} (\text{list-of} (\text{ltake} (\text{enat } m) E'))$ 
  using  $a\text{-obs } E'$  by( $\text{auto simp add: action-tid-def}$ )
  then obtain  $i$  where  $i < m$   $\text{enat } i < \text{llength } E'$ 
  and  $\text{nth-}i$ :  $\text{lnth } E' i = (\text{action-tid } E \ ?a, \{\!\!|\text{InitialThreadAction}|\!\!|)$ 
  unfolding  $\text{in-set-conv-nth}$ 
  by( $\text{cases llength } E')(\text{auto simp add: length-list-of-conv-the-enat lnth-ltake})$ 

let  $?i' = \sum i < i. \text{length } \{\!\!|\text{snd} (\text{lnth } E' i)|\!\!|_o$ 

```

let  $?i = \text{length } ?\text{start-heap-obs} + ?i'$

from  $\langle i < m \rangle$  have  $(\sum i < m. \text{length } \llbracket \text{snd } (\text{lnth } E' i) \rrbracket_o) = ?i' + (\sum i = i..< m. \text{length } \llbracket \text{snd } (\text{lnth } E' i) \rrbracket_o)$

unfolding *atLeast0LessThan*[*symmetric*] by (*subst sum.atLeastLessThan-concat*) *simp-all*

hence  $?i' \leq ?a$  unfolding *a-conv* by *simp*

hence  $?i \leq a$  using  $\langle a \geq \text{length } ?\text{start-heap-obs} \rangle$  by *arith*

from  $\langle ?i' \leq ?a \rangle$  have *enat*  $?i' < \text{llength } E$  using *enat-a*  $E'$

by (*simp add: le-less-trans* [where  $y = \text{enat } ?a$ ])

from *lnth-lconcat-conv* [*OF this* [*unfolded*  $E'$ ], *folded*  $E'$ ]

obtain  $k \ l$

where *nth-i'*:  $\text{lnth } E \ ?i' = \text{lnth } (\text{lmap } (\lambda(t, ta). \text{lList-of } (\text{map } (\text{Pair } t) \llbracket ta \rrbracket_o)) \ E') \ k) \ l$

and  $l: l < \text{length } \llbracket \text{snd } (\text{lnth } E' k) \rrbracket_o$

and  $k: \text{enat } k < \text{llength } E'$

and *i-conv*:  $\text{enat } ?i' = (\sum i < k. \text{llength } (\text{lnth } (\text{lmap } (\lambda(t, ta). \text{lList-of } (\text{map } (\text{Pair } t) \llbracket ta \rrbracket_o)) \ E') \ i)) + \text{enat } l$

by (*fastforce simp add: split-beta*)

have  $(\sum i < k. \text{llength } (\text{lnth } (\text{lmap } (\lambda(t, ta). \text{lList-of } (\text{map } (\text{Pair } t) \llbracket ta \rrbracket_o)) \ E') \ i)) =$   
 $(\sum i < k. (\text{enat } \circ (\lambda i. \text{length } \llbracket \text{snd } (\text{lnth } E' i) \rrbracket_o)) \ i)$

by (*rule sum.cong*) (*simp-all add: less-trans* [where  $y = \text{enat } k$ ] *split-beta*  $k$ )

also have  $\dots = \text{enat } (\sum i < k. \text{length } \llbracket \text{snd } (\text{lnth } E' i) \rrbracket_o)$

by (*rule sum-comp-morphism*) (*simp-all add: zero-enat-def*)

finally have *i-conv*:  $?i' = (\sum i < k. \text{length } \llbracket \text{snd } (\text{lnth } E' i) \rrbracket_o) + l$  using *i-conv* by *simp*

have [*simp*]:  $i = k$

proof (*rule ccontr*)

assume  $i \neq k$

thus *False* unfolding *neq-iff*

proof

assume  $i < k$

hence  $(\sum i < k. \text{length } \llbracket \text{snd } (\text{lnth } E' i) \rrbracket_o) =$

$(\sum i < i. \text{length } \llbracket \text{snd } (\text{lnth } E' i) \rrbracket_o) + (\sum i = i..< k. \text{length } \llbracket \text{snd } (\text{lnth } E' i) \rrbracket_o)$

unfolding *atLeast0LessThan*[*symmetric*] by (*subst sum.atLeastLessThan-concat*) *simp-all*

with *i-conv* have  $(\sum i = i..< k. \text{length } \llbracket \text{snd } (\text{lnth } E' i) \rrbracket_o) = l \ l = 0$  by *simp-all*

moreover have  $(\sum i = i..< k. \text{length } \llbracket \text{snd } (\text{lnth } E' i) \rrbracket_o) \geq \text{length } \llbracket \text{snd } (\text{lnth } E' i) \rrbracket_o$

by (*subst sum.atLeast-Suc-lessThan* [*OF*  $\langle i < k \rangle$ ]) *simp*

ultimately show *False* using *nth-i* by *simp*

next

assume  $k < i$

hence  $?i' = (\sum i < k. \text{length } \llbracket \text{snd } (\text{lnth } E' i) \rrbracket_o) + (\sum i = k..< i. \text{length } \llbracket \text{snd } (\text{lnth } E' i) \rrbracket_o)$

unfolding *atLeast0LessThan*[*symmetric*] by (*subst sum.atLeastLessThan-concat*) *simp-all*

with *i-conv* have  $(\sum i = k..< i. \text{length } \llbracket \text{snd } (\text{lnth } E' i) \rrbracket_o) = l$  by *simp*

moreover have  $(\sum i = k..< i. \text{length } \llbracket \text{snd } (\text{lnth } E' i) \rrbracket_o) \geq \text{length } \llbracket \text{snd } (\text{lnth } E' k) \rrbracket_o$

by (*subst sum.atLeast-Suc-lessThan* [*OF*  $\langle k < i \rangle$ ]) *simp*

ultimately show *False* using  $l$  by *simp*

qed

qed

with  $l$  *nth-i* have [*simp*]:  $l = 0$  by *simp*

hence  $\text{lnth } E \ ?i' = (\text{action-tid } E \ ?a, \text{InitialThreadAction})$

using *nth-i* *nth-i'*  $k$  by *simp*

```

    with ⟨?i ≤ a⟩ show ?thesis
    by(auto simp add: action-tid-def action-obs-def lnth-lappend2)
  qed
  qed
  qed

```

end

In the subsequent locales, *convert-RA* refers to *convert-RA* and is no longer a parameter!

**lemma** *convert-RA-not-write*:

```

  ∧ln. ob ∈ set (convert-RA ln) ⇒ ¬ is-write-action (NormalAction ob)
by(auto simp add: convert-RA-def)

```

**lemma** *ta-seq-consist-convert-RA*:

```

  fixes ln shows
    ta-seq-consist P vs (llist-of ((map NormalAction ∘ convert-RA) ln))
proof(rule ta-seq-consist-nthI)
  fix i ad al v
  assume enat i < llength (llist-of ((map NormalAction ∘ convert-RA) ln :: ('b, 'c) obs-event action list))
  and lnth (llist-of ((map NormalAction ∘ convert-RA) ln :: ('b, 'c) obs-event action list)) i =
    NormalAction (ReadMem ad al v)
  hence ReadMem ad al v ∈ set (convert-RA ln :: ('b, 'c) obs-event list)
  by(auto simp add: in-set-conv-nth)
  hence False by(auto simp add: convert-RA-def)
  thus ∃ b. mrw-values P vs (list-of (ltake (enat i) (llist-of ((map NormalAction ∘ convert-RA) ln))))
    (ad, al) = [(v, b)] ..
qed

```

**lemma** *ta-hb-consistent-convert-RA*:

```

  ∧ln. ta-hb-consistent P E (llist-of (map (Pair t) ((map NormalAction ∘ convert-RA) ln)))
by(rule ta-hb-consistent-not-ReadI)(auto simp add: convert-RA-def)

```

**locale** *allocated-multithreaded* =

```

  allocated-heap
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  allocated
  P
  +
  mthr: multithreaded final r convert-RA

```

```

for addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and allocated :: 'heap ⇒ 'addr set

```



```

and final :: 'x ⇒ bool
and r :: ('addr, 'thread-id, 'x, 'heap, 'addr, ('addr, 'thread-id) obs-event) semantics (- ⊢ - → -
[50,0,0,50] 80)
and P :: 'md prog
+
assumes red-allocated-mono:  $t \vdash (x, m) \rightarrow ta \rightarrow (x', m') \implies \text{allocated } m \subseteq \text{allocated } m'$ 
and red-New-allocatedD:
 $\llbracket t \vdash (x, m) \rightarrow ta \rightarrow (x', m'); \text{NewHeapElem } ad \ C\text{Tn} \in \text{set } \{ta\}_o \rrbracket$ 
 $\implies ad \in \text{allocated } m' \wedge ad \notin \text{allocated } m$ 
and red-allocated-NewD:
 $\llbracket t \vdash (x, m) \rightarrow ta \rightarrow (x', m'); ad \in \text{allocated } m'; ad \notin \text{allocated } m \rrbracket$ 
 $\implies \exists C\text{Tn}. \text{NewHeapElem } ad \ C\text{Tn} \in \text{set } \{ta\}_o$ 
and red-New-same-addr-same:
 $\llbracket t \vdash (x, m) \rightarrow ta \rightarrow (x', m');$ 
 $\{ta\}_o ! i = \text{NewHeapElem } a \ C\text{Tn}; i < \text{length } \{ta\}_o;$ 
 $\{ta\}_o ! j = \text{NewHeapElem } a \ C\text{Tn}'; j < \text{length } \{ta\}_o \rrbracket$ 
 $\implies i = j$ 

```

```

sublocale allocated-multithreaded < heap-multithreaded
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate type-of-addr heap-read heap-write
  final r convert-RA P
by(unfold-locales)

```

```

context allocated-multithreaded begin

```

```

lemma redT-allocated-mono:
  assumes mthr.redT  $\sigma \ (t, ta) \ \sigma'$ 
  shows  $\text{allocated } (\text{shr } \sigma) \subseteq \text{allocated } (\text{shr } \sigma')$ 
using assms
by cases(auto dest: red-allocated-mono del: subsetI)

```

```

lemma RedT-allocated-mono:
  assumes mthr.RedT  $\sigma \ ttas \ \sigma'$ 
  shows  $\text{allocated } (\text{shr } \sigma) \subseteq \text{allocated } (\text{shr } \sigma')$ 
using assms unfolding mthr.RedT-def
by induct(auto dest!: redT-allocated-mono intro: subset-trans del: subsetI)

```

```

lemma init-fin-allocated-mono:
   $t \vdash (x, m) \rightarrow ta \rightarrow i \ (x', m') \implies \text{allocated } m \subseteq \text{allocated } m'$ 
by(cases rule: mthr.init-fin.cases)(auto dest: red-allocated-mono)

```

```

lemma init-fin-redT-allocated-mono:
  assumes mthr.if.redT  $\sigma \ (t, ta) \ \sigma'$ 
  shows  $\text{allocated } (\text{shr } \sigma) \subseteq \text{allocated } (\text{shr } \sigma')$ 
using assms
by cases(auto dest: init-fin-allocated-mono del: subsetI)

```

```

lemma init-fin-RedT-allocated-mono:
  assumes mthr.if.RedT  $\sigma \ ttas \ \sigma'$ 
  shows  $\text{allocated } (\text{shr } \sigma) \subseteq \text{allocated } (\text{shr } \sigma')$ 
using assms unfolding mthr.if.RedT-def

```

by *induct*(*auto dest!*: *init-fin-redT-allocated-mono intro: subset-trans del: subsetI*)

**lemma** *init-fin-red-New-allocatedD*:

**assumes**  $t \vdash (x, m) -ta \rightarrow i (x', m')$  *NormalAction* (*NewHeapElem ad CTn*)  $\in \text{set } \{ta\}_o$   
**shows**  $ad \in \text{allocated } m' \wedge ad \notin \text{allocated } m$

**using** *assms*

**by** *cases*(*auto dest: red-New-allocatedD*)

**lemma** *init-fin-red-allocated-NewD*:

**assumes**  $t \vdash (x, m) -ta \rightarrow i (x', m')$   $ad \in \text{allocated } m' \wedge ad \notin \text{allocated } m$   
**shows**  $\exists CTn. \text{NormalAction } (\text{NewHeapElem } ad \ CTn) \in \text{set } \{ta\}_o$

**using** *assms*

**by**(*cases*)(*auto dest!*: *red-allocated-NewD*)

**lemma** *init-fin-red-New-same-addr-same*:

**assumes**  $t \vdash (x, m) -ta \rightarrow i (x', m')$   
**and**  $\{ta\}_o ! i = \text{NormalAction } (\text{NewHeapElem } a \ CTn) \ i < \text{length } \{ta\}_o$   
**and**  $\{ta\}_o ! j = \text{NormalAction } (\text{NewHeapElem } a \ CTn') \ j < \text{length } \{ta\}_o$   
**shows**  $i = j$

**using** *assms*

**by** *cases*(*auto dest: red-New-same-addr-same*)

**lemma** *init-fin-redT-allocated-NewHeapElemD*:

**assumes** *mthr.if.redT s (t, ta) s'*  
**and**  $ad \in \text{allocated } (\text{shr } s')$   
**and**  $ad \notin \text{allocated } (\text{shr } s)$   
**shows**  $\exists CTn. \text{NormalAction } (\text{NewHeapElem } ad \ CTn) \in \text{set } \{ta\}_o$

**using** *assms*

**by**(*cases*)(*auto dest: init-fin-red-allocated-NewD*)

**lemma** *init-fin-RedT-allocated-NewHeapElemD*:

**assumes** *mthr.if.RedT s ttas s'*  
**and**  $ad \in \text{allocated } (\text{shr } s')$   
**and**  $ad \notin \text{allocated } (\text{shr } s)$   
**shows**  $\exists t \ ta \ CTn. (t, ta) \in \text{set } ttas \wedge \text{NormalAction } (\text{NewHeapElem } ad \ CTn) \in \text{set } \{ta\}_o$

**using** *assms*

**proof**(*induct rule: mthr.if.RedT-induct'*)

**case refl thus ?case by simp**

**next**

**case** (*step ttas s' t ta s'*) **thus ?case**

**by**(*cases ad ∈ allocated (shr s')*)(*fastforce simp del: split-paired-Ex dest: init-fin-redT-allocated-NewHeapElemD*)

**qed**

**lemma** *ℰ-new-actions-for-unique*:

**assumes** *E: E ∈ ℰ-start f P C M vs status*  
**and** *a: a ∈ new-actions-for P E adal*  
**and** *a': a' ∈ new-actions-for P E adal*  
**shows**  $a = a'$

**using** *a a'*

**proof**(*induct a a' rule: wlog-linorder-le*)

**case symmetry thus ?case by simp**

**next**

**case** (*le a a'*)

**note**  $a = \langle a \in \text{new-actions-for } P \ E \ \text{adal} \rangle$

```

and  $a' = \langle a' \in \text{new-actions-for } P \ E \ \text{adal} \rangle$ 
and  $a - a' = \langle a \leq a' \rangle$ 
obtain  $ad \ al$  where  $adal$ :  $adal = (ad, al)$  by  $(\text{cases } adal)$ 

let  $?init\text{-}obs = \text{lift-start-obs } start\text{-}tid \ start\text{-}heap\text{-}obs$ 
let  $?start\text{-}state = \text{init-fin-lift-state } status \ (start\text{-}state \ f \ P \ C \ M \ vs)$ 

have  $distinct$ :  $distinct \ (\text{filter } (\lambda obs. \exists a \ CThn. \ obs = \text{NormalAction } (\text{NewHeapElem } a \ CThn)) \ (\text{map}$ 
 $\text{snd } ?init\text{-}obs))$ 
unfolding  $start\text{-}heap\text{-}obs\text{-}def$ 
by  $(\text{fastforce } intro: \text{inj-onI } intro!: \text{distinct-filter simp add: distinct-map distinct-zipI1 distinct-initialization-list})$ 

from  $start\text{-}addrs\text{-}allocated$ 
have  $dom\text{-}start\text{-}state$ :  $\{a. \exists CThn. \text{NormalAction } (\text{NewHeapElem } a \ CThn) \in \text{snd } ' \text{set } ?init\text{-}obs\} \subseteq$ 
 $allocated \ (\text{shr } ?start\text{-}state)$ 
by  $(\text{fastforce simp add: init-fin-lift-state-conv-simps shr-start-state dest: NewHeapElem-start-heap-obs-start-addrsD}$ 
 $\text{subsetD})$ 

show  $?case$ 
proof  $(\text{cases } a' < \text{length } ?init\text{-}obs)$ 
case  $True$ 
with  $a' \ adal \ E$  obtain  $t\text{-}a' \ CThn\text{-}a'$ 
where  $CThn\text{-}a'$ :  $?init\text{-}obs ! a' = (t\text{-}a', \text{NormalAction } (\text{NewHeapElem } ad \ CThn\text{-}a'))$ 
by  $(\text{cases } ?init\text{-}obs ! a')(\text{fastforce elim!: is-new-action.cases action-loc-aux-cases simp add: ac-}$ 
 $\text{tion-obs-def lnth-lappend1 new-actions-for-def }) +$ 
from  $True \ a\text{-}a'$  have  $len\text{-}a$ :  $a < \text{length } ?init\text{-}obs$  by  $\text{simp}$ 
with  $a \ adal \ E$  obtain  $t\text{-}a \ CThn\text{-}a$ 
where  $CThn\text{-}a$ :  $?init\text{-}obs ! a = (t\text{-}a, \text{NormalAction } (\text{NewHeapElem } ad \ CThn\text{-}a))$ 
by  $(\text{cases } ?init\text{-}obs ! a)(\text{fastforce elim!: is-new-action.cases action-loc-aux-cases simp add: ac-}$ 
 $\text{tion-obs-def lnth-lappend1 new-actions-for-def }) +$ 
from  $CThn\text{-}a \ CThn\text{-}a' \ True \ len\text{-}a$ 
have  $\text{NormalAction } (\text{NewHeapElem } ad \ CThn\text{-}a') \in \text{snd } ' \text{set } ?init\text{-}obs$ 
and  $\text{NormalAction } (\text{NewHeapElem } ad \ CThn\text{-}a) \in \text{snd } ' \text{set } ?init\text{-}obs$  unfolding  $set\text{-}conv\text{-}nth$ 
by  $(\text{fastforce } intro: \text{rev-image-eqI}) +$ 
hence  $[simp]$ :  $CThn\text{-}a' = CThn\text{-}a$  using  $distinct\text{-}start\text{-}addrs'$ 
by  $(\text{auto simp add: in-set-conv-nth distinct-conv-nth start-heap-obs-def start-addrs-def}) \text{blast}$ 
from  $distinct\text{-}filterD[OF \ distinct, \text{ of } a' \ a \ \text{NormalAction } (\text{NewHeapElem } ad \ CThn\text{-}a)] \ len\text{-}a \ True$ 
 $CThn\text{-}a \ CThn\text{-}a'$ 
show  $a = a'$  by  $\text{simp}$ 
next
case  $False$ 
obtain  $n$  where  $n$ :  $\text{length } ?init\text{-}obs = n$  by  $\text{blast}$ 
with  $False$  have  $n \leq a'$  by  $\text{simp}$ 

from  $E$  obtain  $E''$  where  $E$ :  $E = \text{lappend } (l\text{list-of } ?init\text{-}obs) \ E''$ 
and  $E''$ :  $E'' \in \text{mthr.if.}\mathcal{E} \ ?start\text{-}state$  by  $\text{auto}$ 
from  $E''$  obtain  $E'$  where  $E'$ :  $E'' = \text{lconcat } (l\text{map } (\lambda(t, ta). \text{l\text{list-of } (map } (Pair \ t) \ \{\{ta\}_o\})) \ E')$ 
and  $\tau\text{Runs}$ :  $\text{mthr.if.mthr.Runs } ?start\text{-}state \ E'$  by  $(\text{rule } \text{mthr.if.}\mathcal{E}.\text{cases})$ 

from  $E \ E'' \ a' \ n \ \langle n \leq a' \rangle \ adal$  have  $a'$ :  $a' - n \in \text{new-actions-for } P \ E'' \ adal$ 
by  $(\text{auto simp add: new-actions-for-def lnth-lappend2 action-obs-def actions-lappend elim: actionsE})$ 

from  $a'$  have  $a' - n \in \text{actions } E''$  by  $(\text{auto elim: new-actionsE})$ 
hence  $\text{enat } (a' - n) < \text{llength } E''$  by  $(\text{rule } \text{actionsE})$ 

```

**with**  $\tauRuns$  **obtain**  $a'-m$   $a'-n$   $t-a'$   $ta-a'$   
**where**  $E-a'$ :  $lnth\ E''\ (a' - n) = (t-a', \{ta-a'\}_o ! a'-n)$   
**and**  $a'-n$ :  $a'-n < length\ \{ta-a'\}_o$  **and**  $a'-m$ :  $enat\ a'-m < llength\ E'$   
**and**  $a'-conv$ :  $a' - n = (\sum i < a'-m. length\ \{snd\ (lnth\ E'\ i)\}_o) + a'-n$   
**and**  $E'-a'-m$ :  $lnth\ E'\ a'-m = (t-a', ta-a')$   
**unfolding**  $E'$  **by**  $(rule\ mthr.if.actions-\mathcal{E}E-aux)$

**from**  $a'$  **have**  $is-new-action\ (action-obs\ E''\ (a' - n))$   
**and**  $(ad, al) \in action-loc\ P\ E''\ (a' - n)$   
**unfolding**  $adal$  **by**  $(auto\ elim: new-actionsE)$   
**then obtain**  $CTn'$   
**where**  $action-obs\ E''\ (a' - n) = NormalAction\ (NewHeapElem\ ad\ CTn')$   
**by**  $cases(fastforce)+$   
**hence**  $New-ta-a'$ :  $\{ta-a'\}_o ! a'-n = NormalAction\ (NewHeapElem\ ad\ CTn')$   
**using**  $E-a'\ a'-n$  **unfolding**  $action-obs-def$  **by**  $simp$

**show**  $?thesis$   
**proof**  $(cases\ a < n)$   
**case**  $True$   
**with**  $a\ adal\ E\ n$  **obtain**  $t-a\ CTn-a$  **where**  $?init-obs ! a = (t-a, NormalAction\ (NewHeapElem\ ad\ CTn-a))$   
**by**  $(cases\ ?init-obs ! a)(fastforce\ elim!: is-new-action.cases\ simp\ add: action-obs-def\ lnth-lappend1\ new-actions-for-def)+$

**with**  $subsetD[OF\ dom-start-state, of\ ad]\ n\ True$   
**have**  $a-shr-\sigma$ :  $ad \in allocated\ (shr\ ?start-state)$   
**by**  $(fastforce\ simp\ add: set-conv-nth\ intro: rev-image-eqI)$

**have**  $E'-unfold'$ :  $E' = lappend\ (ltake\ (enat\ a'-m)\ E')\ (LCons\ (lnth\ E'\ a'-m)\ (ldropn\ (Suc\ a'-m)\ E'))$   
**unfolding**  $ldropn-Suc-conv-ldropn[OF\ a'-m]$  **by**  $simp$   
**hence**  $mthr.if.mthr.Runs\ ?start-state\ (lappend\ (ltake\ (enat\ a'-m)\ E')\ (LCons\ (lnth\ E'\ a'-m)\ (ldropn\ (Suc\ a'-m)\ E')))$   
**using**  $\tauRuns$  **by**  $simp$

**then obtain**  $\sigma'$   
**where**  $\sigma-\sigma'$ :  $mthr.if.mthr.Trsys\ ?start-state\ (list-of\ (ltake\ (enat\ a'-m)\ E'))\ \sigma'$   
**and**  $\tauRuns'$ :  $mthr.if.mthr.Runs\ \sigma'\ (LCons\ (lnth\ E'\ a'-m)\ (ldropn\ (Suc\ a'-m)\ E'))$   
**by**  $(rule\ mthr.if.mthr.Runs-lappendE)\ simp$   
**from**  $\tauRuns'$  **obtain**  $\sigma''$   
**where**  $red-a'$ :  $mthr.if.redT\ \sigma'\ (t-a', ta-a')\ \sigma''$   
**and**  $\tauRuns''$ :  $mthr.if.mthr.Runs\ \sigma''\ (ldropn\ (Suc\ a'-m)\ E')$   
**unfolding**  $E'-a'-m$  **by**  $cases$   
**from**  $New-ta-a'\ a'-n$  **have**  $NormalAction\ (NewHeapElem\ ad\ CTn') \in set\ \{ta-a'\}_o$   
**unfolding**  $in-set-conv-nth$  **by**  $blast$   
**with**  $red-a'$  **obtain**  $x-a'\ x'-a'\ m'-a'$   
**where**  $red'-a'$ :  $mthr.init-fin\ t-a'\ (x-a', shr\ \sigma')\ ta-a'\ (x'-a', m'-a')$   
**and**  $\sigma'''$ :  $redT-upd\ \sigma'\ t-a'\ ta-a'\ x'-a'\ m'-a'\ \sigma''$   
**and**  $ts-t-a'$ :  $thr\ \sigma'\ t-a' = \lfloor (x-a', no-wait-locks) \rfloor$   
**by**  $cases\ auto$   
**from**  $red'-a'\ \langle NormalAction\ (NewHeapElem\ ad\ CTn') \in set\ \{ta-a'\}_o \rangle$   
**obtain**  $ta'-a'\ X-a'\ X'-a'$   
**where**  $x-a'$ :  $x-a' = (Running, X-a')$   
**and**  $x'-a'$ :  $x'-a' = (Running, X'-a')$

and  $ta-a'$ :  $ta-a' = \text{convert-TA-initial } (\text{convert-obs-initial } ta'-a')$   
 and  $red''-a'$ :  $t-a' \vdash \langle X-a', \text{shr } \sigma' \rangle \rightarrow ta'-a' \rightarrow \langle X'-a', m'-a' \rangle$   
 by *cases fastforce+*

from  $ta-a'$  *New-ta-a'*  $a'-n$  **have**  $\text{New-ta}'-a'$ :  $\{ta'-a'\}_o ! a'-n = \text{NewHeapElem ad CTn}'$   
 and  $a'-n'$ :  $a'-n < \text{length } \{ta'-a'\}_o$  **by** *auto*  
 hence  $\text{NewHeapElem ad CTn}' \in \text{set } \{ta'-a'\}_o$  **unfolding** *in-set-conv-nth* **by** *blast*  
 with  $red''-a'$  **have**  $\text{allocated-ad}'$ :  $ad \notin \text{allocated } (\text{shr } \sigma')$   
 by(*auto dest: red-New-allocatedD*)

**have**  $\text{allocated } (\text{shr } ?\text{start-state}) \subseteq \text{allocated } (\text{shr } \sigma')$   
 using  $\sigma-\sigma'$  **unfolding** *mthr.if.RedT-def[symmetric]* **by**(*rule init-fin-RedT-allocated-mono*)  
 hence *False* **using**  $\text{allocated-ad}'$   $a\text{-shr-}\sigma$  **by** *blast*  
 thus *?thesis ..*

**next**

**case** *False*  
 hence  $n \leq a$  **by** *simp*

from  $E E''$   $a$   $n < n \leq a$  **ad al** **have**  $a: a - n \in \text{new-actions-for } P E''$  **ad al**  
 by(*auto simp add: new-actions-for-def lnth-lappend2 action-obs-def actions-lappend elim: actionsE*)

from  $a$  **have**  $a - n \in \text{actions } E''$  **by**(*auto elim: new-actionsE*)  
 hence  $\text{enat } (a - n) < \text{length } E''$  **by**(*rule actionsE*)

with  $\tau\text{Runs}$  **obtain**  $a-m$   $a-n$   $t-a$   $ta-a$   
 where  $E-a$ :  $\text{lnth } E'' (a - n) = (t-a, \{ta-a\}_o ! a-n)$   
 and  $a-n$ :  $a-n < \text{length } \{ta-a\}_o$  **and**  $a-m$ :  $\text{enat } a-m < \text{length } E'$   
 and  $a\text{-conv}$ :  $a - n = (\sum i < a-m. \text{length } \{snd (\text{lnth } E' i)\}_o) + a-n$   
 and  $E'-a-m$ :  $\text{lnth } E' a-m = (t-a, ta-a)$   
 unfolding  $E'$  **by**(*rule mthr.if.actions- $\mathcal{E}E\text{-aux}$* )

from  $a$  **have** *is-new-action* ( $\text{action-obs } E'' (a - n)$ )  
 and  $(ad, al) \in \text{action-loc } P E'' (a - n)$   
 unfolding *adal* **by**(*auto elim: new-actionsE*)  
 then **obtain**  $CTn$  **where**  $\text{action-obs } E'' (a - n) = \text{NormalAction } (\text{NewHeapElem ad CTn})$   
 by *cases(fastforce)+*  
 hence  $\text{New-ta-a}$ :  $\{ta-a\}_o ! a-n = \text{NormalAction } (\text{NewHeapElem ad CTn})$   
 using  $E-a$   $a-n$  **unfolding** *action-obs-def* **by** *simp*

let  $?E' = \text{ldroptn } (\text{Suc } a-m) E'$

**have**  $E'\text{-unfold}$ :  $E' = \text{lappend } (\text{ltake } (\text{enat } a-m) E') (LCons (\text{lnth } E' a-m) ?E')$   
 unfolding *ldroptn-Suc-conv-ldroptn[OF a-m]* **by** *simp*  
 hence *mthr.if.mthr.Runs*  $?start\text{-state}$  ( $\text{lappend } (\text{ltake } (\text{enat } a-m) E') (LCons (\text{lnth } E' a-m) ?E')$ )  
 using  $\tau\text{Runs}$  **by** *simp*  
 then **obtain**  $\sigma'$  **where**  $\sigma-\sigma'$ : *mthr.if.mthr.Trsys*  $?start\text{-state}$  ( $\text{list-of } (\text{ltake } (\text{enat } a-m) E')$ )  $\sigma'$   
 and  $\tau\text{Runs}'$ : *mthr.if.mthr.Runs*  $\sigma' (LCons (\text{lnth } E' a-m) ?E')$   
 by(*rule mthr.if.mthr.Runs-lappendE*) *simp*  
 from  $\tau\text{Runs}'$  **obtain**  $\sigma''$   
 where  $red-a$ : *mthr.if.redT*  $\sigma' (t-a, ta-a) \sigma''$   
 and  $\tau\text{Runs}''$ : *mthr.if.mthr.Runs*  $\sigma'' ?E'$   
 unfolding  $E'-a-m$  **by** *cases*  
 from  $\text{New-ta-a}$   $a-n$  **have**  $\text{NormalAction } (\text{NewHeapElem ad CTn}) \in \text{set } \{ta-a\}_o$

**unfolding** *in-set-conv-nth* **by** *blast*  
**with** *red-a* **obtain**  $x\text{-}a\ x'\text{-}a\ m'\text{-}a$   
**where**  $\text{red}'\text{-}a$ :  $\text{mthr.init-fin } t\text{-}a\ (x\text{-}a, \text{shr } \sigma')\ ta\text{-}a\ (x'\text{-}a, m'\text{-}a)$   
**and**  $\sigma''$ :  $\text{redT-upd } \sigma'\ t\text{-}a\ ta\text{-}a\ x'\text{-}a\ m'\text{-}a\ \sigma''$   
**and**  $\text{ts-t-a}$ :  $\text{thr } \sigma'\ t\text{-}a = \lfloor (x\text{-}a, \text{no-wait-locks}) \rfloor$   
**by** *cases auto*  
**from**  $\text{red}'\text{-}a\ \langle \text{NormalAction } (\text{NewHeapElem ad } C\text{Tn}) \in \text{set } \{ta\text{-}a\}_o \rangle$   
**obtain**  $ta'\text{-}a\ X\text{-}a\ X'\text{-}a$   
**where**  $x\text{-}a$ :  $x\text{-}a = (\text{Running}, X\text{-}a)$   
**and**  $x'\text{-}a$ :  $x'\text{-}a = (\text{Running}, X'\text{-}a)$   
**and**  $ta\text{-}a$ :  $ta\text{-}a = \text{convert-TA-initial } (\text{convert-obs-initial } ta'\text{-}a)$   
**and**  $\text{red}''\text{-}a$ :  $t\text{-}a \vdash (X\text{-}a, \text{shr } \sigma') - ta'\text{-}a \rightarrow (X'\text{-}a, m'\text{-}a)$   
**by** *cases fastforce+*  
**from**  $ta\text{-}a\ \text{New-ta-a}\ a\text{-}n$  **have**  $\text{New-ta}'\text{-}a$ :  $\{ta'\text{-}a\}_o ! a\text{-}n = \text{NewHeapElem ad } C\text{Tn}$   
**and**  $a\text{-}n'$ :  $a\text{-}n < \text{length } \{ta'\text{-}a\}_o$  **by** *auto*  
**hence**  $\text{NewHeapElem ad } C\text{Tn} \in \text{set } \{ta'\text{-}a\}_o$  **unfolding** *in-set-conv-nth* **by** *blast*  
**with**  $\text{red}''\text{-}a$  **have**  $\text{allocated-}m'\text{-}a\text{-}ad$ :  $ad \in \text{allocated } m'\text{-}a$   
**by**(*auto dest: red-New-allocatedD*)

**have**  $a\text{-}m \leq a'\text{-}m$   
**proof**(*rule ccontr*)  
**assume**  $\neg ?thesis$   
**hence**  $a'\text{-}m < a\text{-}m$  **by** *simp*  
**hence**  $(\sum i < a\text{-}m. \text{length } \{snd\ (lnth\ E'\ i)\}_o) = (\sum i < a'\text{-}m. \text{length } \{snd\ (lnth\ E'\ i)\}_o) + (\sum i = a'\text{-}m..<a\text{-}m. \text{length } \{snd\ (lnth\ E'\ i)\}_o)$   
**by**(*simp add: sum-upto-add-nat*)  
**hence**  $a' - n < a - n$  **using**  $\langle a'\text{-}m < a\text{-}m \rangle\ a'\text{-}n\ E'\text{-}a'\text{-}m$  **unfolding**  $a\text{-}conv\ a'\text{-}conv$   
**by**(*subst (asm) sum.atLeast-Suc-lessThan*) *simp-all*  
**with**  $a\text{-}a'$  **show** *False* **by** *simp*  
**qed**

**have**  $a'\text{-}less$ :  $a' - n < (a - n) - a\text{-}n + \text{length } \{ta\text{-}a\}_o$   
**proof**(*rule ccontr*)  
**assume**  $\neg ?thesis$   
**hence**  $a'\text{-}greater$ :  $(a - n) - a\text{-}n + \text{length } \{ta\text{-}a\}_o \leq a' - n$  **by** *simp*

**have**  $a\text{-}m < a'\text{-}m$   
**proof**(*rule ccontr*)  
**assume**  $\neg ?thesis$   
**with**  $\langle a\text{-}m \leq a'\text{-}m \rangle$  **have**  $a\text{-}m = a'\text{-}m$  **by** *simp*  
**with**  $a'\text{-}greater\ a\text{-}n\ a'\text{-}n\ E'\text{-}a'\text{-}m\ E'\text{-}a\text{-}m$  **show** *False*  
**unfolding**  $a\text{-}conv\ a'\text{-}conv$  **by** *simp*  
**qed**  
**hence**  $a'\text{-}m\text{-}a\text{-}m$ :  $\text{enat } (a'\text{-}m - \text{Suc } a\text{-}m) < \text{length } ?E'$  **using**  $a'\text{-}m$   
**by**(*cases llength E'*) *simp-all*  
**from**  $\langle a\text{-}m < a'\text{-}m \rangle\ a'\text{-}m\ E'\text{-}a'\text{-}m$   
**have**  $E'\text{-}a'\text{-}m'$ :  $\text{lnth } ?E' (a'\text{-}m - \text{Suc } a\text{-}m) = (t\text{-}a', ta\text{-}a')$  **by** *simp*

**have**  $E'\text{-}unfold'$ :  $?E' = \text{lappend } (\text{ltake } (\text{enat } (a'\text{-}m - \text{Suc } a\text{-}m))\ ?E')\ (\text{LCons } (\text{lnth } ?E' (a'\text{-}m - \text{Suc } a\text{-}m))\ (\text{ldropn } (\text{Suc } (a'\text{-}m - \text{Suc } a\text{-}m))\ ?E'))$   
**unfolding**  $\text{ldropn-Suc-conv-ldropn}[OF\ a'\text{-}m\text{-}a\text{-}m]\ \text{lappend-ltake-enat-ldropn} \dots$   
**hence**  $\text{mthr.if.mthr.Runs } \sigma'' (\text{lappend } (\text{ltake } (\text{enat } (a'\text{-}m - \text{Suc } a\text{-}m))\ ?E')\ (\text{LCons } (\text{lnth } ?E' (a'\text{-}m - \text{Suc } a\text{-}m))\ (\text{ldropn } (\text{Suc } (a'\text{-}m - \text{Suc } a\text{-}m))\ ?E')))$   
**using**  $\tau\text{Runs''}$  **by** *simp*

then obtain  $\sigma'''$   
 where  $\sigma''\text{-}\sigma''': \text{mthr.if.mthr.Trsys } \sigma'' \text{ (list-of (ltake (enat (a'-m - Suc a-m)) ?E')) } \sigma'''$   
 and  $\tau\text{Runs}''': \text{mthr.if.mthr.Runs } \sigma''' \text{ (LCons (lnth ?E' (a'-m - Suc a-m)) (ldropn (Suc (a'-m - Suc a-m)) ?E'))}$   
 by(rule mthr.if.mthr.Runs-lappendE) simp  
 from  $\tau\text{Runs}'''$  obtain  $\sigma''''$   
 where  $\text{red-a}': \text{mthr.if.redT } \sigma''' \text{ (t-a', ta-a')} \sigma''''$   
 and  $\tau\text{Runs}''': \text{mthr.if.mthr.Runs } \sigma'''' \text{ (ldropn (Suc (a'-m - Suc a-m)) ?E')}$   
 unfolding  $E'\text{-a'-m'}$  by cases  
 from  $\text{New-ta-a' a'-n}$  have  $\text{NormalAction (NewHeapElem ad CTn')} \in \text{set } \llbracket \text{ta-a'} \rrbracket_o$   
 unfolding in-set-conv-nth by blast  
 with  $\text{red-a'}$  obtain  $x\text{-a' } x'\text{-a' m'-a'}$   
 where  $\text{red}'\text{-a}': \text{mthr.init-fin t-a' (x-a', shr } \sigma''') \text{ ta-a' (x'-a', m'-a')}$   
 and  $\sigma''''': \text{redT-upd } \sigma''' \text{ t-a' ta-a' x'-a' m'-a' } \sigma'''''$   
 and  $\text{ts-t-a'}: \text{thr } \sigma''' \text{ t-a' = } \lfloor (x\text{-a'}, \text{no-wait-locks}) \rfloor$   
 by cases auto  
 from  $\text{red}'\text{-a' } \langle \text{NormalAction (NewHeapElem ad CTn')} \in \text{set } \llbracket \text{ta-a'} \rrbracket_o \rangle$   
 obtain  $\text{ta}'\text{-a' } X\text{-a' } X'\text{-a'}$   
 where  $x\text{-a'}: x\text{-a'} = (\text{Running}, X\text{-a'})$   
 and  $x'\text{-a'}: x'\text{-a'} = (\text{Running}, X'\text{-a'})$   
 and  $\text{ta-a'}: \text{ta-a'} = \text{convert-TA-initial (convert-obs-initial ta-a')}$   
 and  $\text{red}''\text{-a'}: \text{t-a'} \vdash (X\text{-a'}, \text{shr } \sigma''') \text{ -ta-a' } \rightarrow (X'\text{-a'}, m'\text{-a'})$   
 by cases fastforce+  
 from  $\text{ta-a' New-ta-a' a'-n}$  have  $\text{New-ta-a'}: \llbracket \text{ta}'\text{-a'} \rrbracket_o ! a'\text{-n} = \text{NewHeapElem ad CTn'}$   
 and  $a'\text{-n'}: a'\text{-n} < \text{length } \llbracket \text{ta}'\text{-a'} \rrbracket_o$  by auto  
 hence  $\text{NewHeapElem ad CTn'} \in \text{set } \llbracket \text{ta}'\text{-a'} \rrbracket_o$  unfolding in-set-conv-nth by blast  
 with  $\text{red}''\text{-a'}$  have  $\text{allocated-ad'}: \text{ad} \notin \text{allocated (shr } \sigma''')$   
 by(auto dest: red-New-allocatedD)  
  
 have  $\text{allocated m'-a} = \text{allocated (shr } \sigma'')$  using  $\sigma'''$  by auto  
 also have  $\dots \subseteq \text{allocated (shr } \sigma''')$   
 using  $\sigma''\text{-}\sigma'''$  unfolding mthr.if.RedT-def[symmetric] by(rule init-fin-RedT-allocated-mono)  
 finally have  $\text{ad} \in \text{allocated (shr } \sigma''')$  using  $\text{allocated-m'-a-ad}$  by blast  
 with  $\text{allocated-ad'}$  show False by contradiction  
 qed  
  
 from  $\langle a\text{-m} \leq a'\text{-m} \rangle$  have [simp]:  $a\text{-m} = a'\text{-m}$   
 proof(rule le-antisym)  
 show  $a'\text{-m} \leq a\text{-m}$   
 proof(rule ccontr)  
 assume  $\neg ?thesis$   
 hence  $a\text{-m} < a'\text{-m}$  by simp  
 hence  $(\sum i < a'\text{-m}. \text{length } \llbracket \text{snd (lnth E' i)} \rrbracket_o) = (\sum i < a\text{-m}. \text{length } \llbracket \text{snd (lnth E' i)} \rrbracket_o) + (\sum i = a\text{-m}..<a'\text{-m}. \text{length } \llbracket \text{snd (lnth E' i)} \rrbracket_o)$   
 by(simp add: sum-upto-add-nat)  
 with  $a'\text{-less } \langle a\text{-m} < a'\text{-m} \rangle E'\text{-a-m a-n a'-n}$  show False  
 unfolding  $a'\text{-conv a-conv}$  by(subst (asm) sum.atLeast-Suc-lessThan) simp-all  
 qed  
 qed  
 with  $E'\text{-a-m } E'\text{-a'-m}$  have [simp]:  $\text{t-a'} = \text{t-a ta-a'} = \text{ta-a}$  by simp-all  
 from  $\text{New-ta-a' a'-n ta-a}$  have  $a'\text{-n'}: a'\text{-n} < \text{length } \llbracket \text{ta}'\text{-a'} \rrbracket_o$   
 and  $\text{New-ta-a'}: \llbracket \text{ta}'\text{-a'} \rrbracket_o ! a'\text{-n} = \text{NewHeapElem ad CTn'}$  by auto  
 with  $\text{red}''\text{-a New-ta-a a-n'}$  have  $a'\text{-n} = a\text{-n}$   
 by(auto dest: red-New-same-addr-same)

```

    with  $\langle a-m = a'-m \rangle$  have  $a - n = a' - n$  unfolding  $a\text{-conv}$   $a'\text{-conv}$  by simp
    thus ?thesis using  $\langle n \leq a \rangle \langle n \leq a' \rangle$  by simp
  qed
qed
qed
end

```

Knowledge of addresses of a multithreaded state

```

fun ka-Val :: 'addr val  $\Rightarrow$  'addr set
where
  ka-Val (Addr a) = {a}
| ka-Val - = {}

fun new-obs-addr :: ('addr, 'thread-id) obs-event  $\Rightarrow$  'addr set
where
  new-obs-addr (ReadMem ad al (Addr ad')) = {ad'}
| new-obs-addr (NewHeapElem ad hT) = {ad}
| new-obs-addr - = {}

lemma new-obs-addr-cases[consumes 1, case-names ReadMem NewHeapElem, cases set]:
  assumes ad  $\in$  new-obs-addr ob
  obtains ad' al where ob = ReadMem ad' al (Addr ad)
  | CTn where ob = NewHeapElem ad CTn
using assms
by(cases ob rule: new-obs-addr.cases) auto

definition new-obs-addrs :: ('addr, 'thread-id) obs-event list  $\Rightarrow$  'addr set
where
  new-obs-addrs obs =  $\bigcup$  (new-obs-addr ' set obs)

fun new-obs-addr-if :: ('addr, 'thread-id) obs-event action  $\Rightarrow$  'addr set
where
  new-obs-addr-if (NormalAction a) = new-obs-addr a
| new-obs-addr-if - = {}

definition new-obs-addrs-if :: ('addr, 'thread-id) obs-event action list  $\Rightarrow$  'addr set
where
  new-obs-addrs-if obs =  $\bigcup$  (new-obs-addr-if ' set obs)

lemma ka-Val-subset-new-obs-Addr-ReadMem:
  ka-Val v  $\subseteq$  new-obs-addr (ReadMem ad al v)
by(cases v) simp-all

lemma typeof-ka: typeof v  $\neq$  None  $\implies$  ka-Val v = {}
by(cases v) simp-all

lemma ka-Val-undefined-value [simp]:
  ka-Val undefined-value = {}
apply(cases undefined-value :: 'a val)
apply(bestsimp simp add: undefined-value-not-Addr dest: subst)+
done

locale known-addrs-base =

```



**fixes** *known-addr* :: 't  $\Rightarrow$  'x  $\Rightarrow$  'addr set  
**begin**

**definition** *known-addr*-thr :: ('l, 't, 'x) thread-info  $\Rightarrow$  'addr set  
**where** *known-addr*-thr *ts* = ( $\bigcup t \in \text{dom } ts. \text{known-addr } t \text{ (fst (the (ts t)))}$ )

**definition** *known-addr*-state :: ('l, 't, 'x, 'm, 'w) state  $\Rightarrow$  'addr set  
**where** *known-addr*-state *s* = *known-addr*-thr (thr *s*)

**lemma** *known-addr*-state-simps [simp]:  
*known-addr*-state (*ls*, (*ts*, *m*), *ws*) = *known-addr*-thr *ts*  
**by**(simp add: *known-addr*-state-def)

**lemma** *known-addr*-thr-cases[consumes 1, case-names *known-addr*, cases set: *known-addr*-thr]:  
**assumes** *ad*  $\in$  *known-addr*-thr *ts*  
**and**  $\bigwedge t x \text{ ln. } \llbracket ts \ t = \llbracket (x, \text{ln}) \rrbracket; \text{ad} \in \text{known-addr } t \ x \rrbracket \Longrightarrow \text{thesis}$   
**shows** *thesis*  
**using** *assms*  
**by**(auto simp add: *known-addr*-thr-def ran-def)

**lemma** *known-addr*-stateI:  
 $\bigwedge \text{ln. } \llbracket \text{ad} \in \text{known-addr } t \ x; \text{thr } s \ t = \llbracket (x, \text{ln}) \rrbracket \rrbracket \Longrightarrow \text{ad} \in \text{known-addr-state } s$   
**by**(fastforce simp add: *known-addr*-state-def *known-addr*-thr-def intro: rev-bexI)

**fun** *known-addr*-if :: 't  $\Rightarrow$  status  $\times$  'x  $\Rightarrow$  'addr set  
**where** *known-addr*-if *t* (*s*, *x*) = *known-addr* *t* *x*

**end**

**locale** *if-known-addr*-base =  
*known-addr*-base *known-addr*  
+  
*multithreaded*-base final *r* convert-RA  
**for** *known-addr* :: 't  $\Rightarrow$  'x  $\Rightarrow$  'addr set  
**and** final :: 'x  $\Rightarrow$  bool  
**and** *r* :: ('addr, 't, 'x, 'heap, 'addr, 'obs) semantics ( $\vdash - \dashrightarrow - [50, 0, 0, 50]$  80)  
**and** convert-RA :: 'addr released-locks  $\Rightarrow$  'obs list

**sublocale** *if-known-addr*-base < if: *known-addr*-base *known-addr*-if .

**locale** *known-addr* =  
*allocated-multithreaded*  
*addr2thread-id thread-id2addr*  
*spurious-wakeups*  
*empty-heap allocate typeof-addr heap-read heap-write*  
*allocated*  
*final r*  
*P*  
+  
*if-known-addr*-base *known-addr* final *r* convert-RA  
**for** *addr2thread-id* :: ('addr :: addr)  $\Rightarrow$  'thread-id  
**and** *thread-id2addr* :: 'thread-id  $\Rightarrow$  'addr  
**and** *spurious-wakeups* :: bool

```

and empty-heap :: 'heap
and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype
and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
and allocated :: 'heap  $\Rightarrow$  'addr set
and known-addr :: 'thread-id  $\Rightarrow$  'x  $\Rightarrow$  'addr set
and final :: 'x  $\Rightarrow$  bool
and r :: ('addr, 'thread-id, 'x, 'heap, 'addr, ('addr, 'thread-id) obs-event) semantics (-  $\vdash$  -  $\dashrightarrow$  -
[50,0,0,50] 80)
and P :: 'md prog
+
assumes red-known-addr-new:
   $t \vdash \langle x, m \rangle -ta\rightarrow \langle x', m' \rangle$ 
 $\implies \text{known-addr } t \ x' \subseteq \text{known-addr } t \ x \cup \text{new-obs-addr } \llbracket ta \rrbracket_o$ 
and red-known-addr-new-thread:
   $\llbracket t \vdash \langle x, m \rangle -ta\rightarrow \langle x', m' \rangle; \text{NewThread } t' \ x'' \ m'' \in \text{set } \llbracket ta \rrbracket_t \rrbracket$ 
 $\implies \text{known-addr } t' \ x'' \subseteq \text{known-addr } t \ x$ 
and red-read-knows-addr:
   $\llbracket t \vdash \langle x, m \rangle -ta\rightarrow \langle x', m' \rangle; \text{ReadMem } ad \ al \ v \in \text{set } \llbracket ta \rrbracket_o \rrbracket$ 
 $\implies ad \in \text{known-addr } t \ x$ 
and red-write-knows-addr:
   $\llbracket t \vdash \langle x, m \rangle -ta\rightarrow \langle x', m' \rangle; \llbracket ta \rrbracket_o ! n = \text{WriteMem } ad \ al \ (\text{Addr } ad'); n < \text{length } \llbracket ta \rrbracket_o \rrbracket$ 
 $\implies ad' \in \text{known-addr } t \ x \vee ad' \in \text{new-obs-addr } (\text{take } n \ \llbracket ta \rrbracket_o)$ 
  — second possibility necessary for heap-clone
begin

notation mthr.redT-syntax1 (-  $\dashrightarrow$  - [50,0,0,50] 80)

lemma if-red-known-addr-new:
  assumes  $t \vdash \langle x, m \rangle -ta\rightarrow i \langle x', m' \rangle$ 
  shows  $\text{known-addr-if } t \ x' \subseteq \text{known-addr-if } t \ x \cup \text{new-obs-addr-if } \llbracket ta \rrbracket_o$ 
using assms
by cases(auto dest!: red-known-addr-new simp add: new-obs-addr-if-def new-obs-addr-def)

lemma if-red-known-addr-new-thread:
  assumes  $t \vdash \langle x, m \rangle -ta\rightarrow i \langle x', m' \rangle \text{NewThread } t' \ x'' \ m'' \in \text{set } \llbracket ta \rrbracket_t$ 
  shows  $\text{known-addr-if } t' \ x'' \subseteq \text{known-addr-if } t \ x$ 
using assms
by cases(fastforce dest: red-known-addr-new-thread)+

lemma if-red-read-knows-addr:
  assumes  $t \vdash \langle x, m \rangle -ta\rightarrow i \langle x', m' \rangle \text{NormalAction } (\text{ReadMem } ad \ al \ v) \in \text{set } \llbracket ta \rrbracket_o$ 
  shows  $ad \in \text{known-addr-if } t \ x$ 
using assms
by cases(fastforce dest: red-read-knows-addr)+

lemma if-red-write-knows-addr:
  assumes  $t \vdash \langle x, m \rangle -ta\rightarrow i \langle x', m' \rangle$ 
and  $\llbracket ta \rrbracket_o ! n = \text{NormalAction } (\text{WriteMem } ad \ al \ (\text{Addr } ad')) \ n < \text{length } \llbracket ta \rrbracket_o$ 
  shows  $ad' \in \text{known-addr-if } t \ x \vee ad' \in \text{new-obs-addr-if } (\text{take } n \ \llbracket ta \rrbracket_o)$ 
using assms
by cases(auto dest: red-write-knows-addr simp add: new-obs-addr-if-def new-obs-addr-def take-map)

```

```

lemma if-redT-known-addr-new:
  assumes redT: mthr.if.redT s (t, ta) s'
  shows if.known-addr-state s'  $\subseteq$  if.known-addr-state s  $\cup$  new-obs-addr-if  $\{ta\}_o$ 
using redT
proof(cases)
  case redT-acquire thus ?thesis
    by(cases s)(fastforce simp add: if.known-addr-thr-def split: if-split-asm intro: rev-bexI)
next
  case (redT-normal x x' m)
  note red =  $\langle t \vdash (x, \text{shr } s) -ta \rightarrow i (x', m) \rangle$ 
  show ?thesis
  proof
    fix ad
    assume ad  $\in$  if.known-addr-state s'
    hence ad  $\in$  if.known-addr-thr (thr s') by(simp add: if.known-addr-state-def)
    then obtain t' x'' ln'' where ts't': thr s' t' =  $\lfloor (x'', \text{ln}'') \rfloor$ 
      and ad: ad  $\in$  known-addr-if t' x''
      by(rule if.known-addr-thr-cases)
    show ad  $\in$  if.known-addr-state s  $\cup$  new-obs-addr-if  $\{ta\}_o$ 
    proof(cases thr s t')
      case None
      with redT-normal  $\langle \text{thr } s' t' = \lfloor (x'', \text{ln}'') \rfloor \rangle$ 
      obtain m'' where NewThread t' x'' m''  $\in$  set  $\{ta\}_t$ 
        by(fastforce dest: redT-updTs-new-thread split: if-split-asm)
      with red have known-addr-if t' x''  $\subseteq$  known-addr-if t x by(rule if-red-known-addr-new-thread)
      also have  $\dots \subseteq$  known-addr-if t x  $\cup$  new-obs-addr-if  $\{ta\}_o$  by simp
      finally have ad  $\in$  known-addr-if t x  $\cup$  new-obs-addr-if  $\{ta\}_o$  using ad by blast
      thus ?thesis using  $\langle \text{thr } s t = \lfloor (x, \text{no-wait-locks}) \rfloor \rangle$  by(blast intro: if.known-addr-stateI)
    next
      case (Some xln)
      show ?thesis
      proof(cases t = t')
        case True
        with redT-normal ts't' if-red-known-addr-new[OF red] ad
        have ad  $\in$  known-addr-if t x  $\cup$  new-obs-addr-if  $\{ta\}_o$  by auto
        thus ?thesis using  $\langle \text{thr } s t = \lfloor (x, \text{no-wait-locks}) \rfloor \rangle$  by(blast intro: if.known-addr-stateI)
      next
        case False
        with ts't' redT-normal ad Some show ?thesis
        by(fastforce dest: redT-updTs-Some[where ts=thr s and t=t'] intro: if.known-addr-stateI)
      qed
    qed
  qed
qed

```

```

lemma if-redT-read-knows-addr:
  assumes redT: mthr.if.redT s (t, ta) s'
  and read: NormalAction (ReadMem ad al v)  $\in$  set  $\{ta\}_o$ 
  shows ad  $\in$  if.known-addr-state s
using redT
proof(cases)
  case redT-acquire thus ?thesis using read by auto
next
  case (redT-normal x x' m')

```

**with** *if-red-read-knows-addr*[*OF*  $\langle t \vdash (x, \text{shr } s) \rightarrow i(x', m') \rangle$  read]  
**show** *?thesis*  
**by**(*auto simp add: if-known-addr-state-def if-known-addr-thr-def intro: beI*[**where**  $x=t$ ])  
**qed**

**lemma** *init-fin-redT-known-addr-subset*:

**assumes** *mthr.if.redT*  $s (t, ta) s'$   
**shows** *if-known-addr-state*  $s' \subseteq \text{if-known-addr-state } s \cup \text{known-addr-if } t (\text{fst } (\text{the } (\text{thr } s' t)))$   
**using** *assms*  
**apply**(*cases*)  
**apply**(*rule subsetI*)  
**apply**(*clarsimp simp add: if-known-addr-thr-def split: if-split-asm*)  
**apply**(*rename-tac status x status' x' m' a ws' t'' status'' x'' ln'*)  
**apply**(*case-tac thr s t''*)  
**apply**(*drule (2) redT-updTs-new-thread*)  
**apply** *clarsimp*  
**apply**(*drule (1) if-red-known-addr-new-thread*)  
**apply** *simp*  
**apply**(*drule (1) subsetD*)  
**apply**(*rule-tac x=(status, x) in if-known-addr-stateI*)  
**apply**(*simp*)  
**apply** *simp*  
**apply**(*frule-tac t=t'' in redT-updTs-Some, assumption*)  
**apply** *clarsimp*  
**apply**(*rule-tac x=(status'', x'') in if-known-addr-stateI*)  
**apply** *simp*  
**apply** *simp*  
**apply**(*auto simp add: if-known-addr-state-def if-known-addr-thr-def split: if-split-asm*)  
**done**

**lemma** *w-values-no-write-unchanged*:

**assumes** *no-write*:  $\bigwedge w. \llbracket w \in \text{set obs}; \text{is-write-action } w; \text{adal} \in \text{action-loc-aux } P w \rrbracket \implies \text{False}$   
**shows** *w-values*  $P \text{ vs obs adal} = \text{vs adal}$   
**using** *assms*  
**proof**(*induct obs arbitrary: vs*)  
**case** *Nil* **show** *?case by simp*  
**next**  
**case** (*Cons ob obs*)  
**from** *Cons.prem*[*of ob*]  
**have** *w-value*  $P \text{ vs ob adal} = \text{vs adal}$   
**by**(*cases adal*)(*cases ob rule: w-value-cases, auto simp add: addr-locs-def split: htype.split-asm, blast+*)  
**moreover**  
**have** *w-values*  $P (w\text{-value } P \text{ vs ob}) \text{ obs adal} = w\text{-value } P \text{ vs ob adal}$   
**proof**(*rule Cons.hyps*)  
**fix**  $w$   
**assume**  $w \in \text{set obs is-write-action } w \text{ adal} \in \text{action-loc-aux } P w$   
**with** *Cons.prem*[*of w*]  $\langle w\text{-value } P \text{ vs ob adal} = \text{vs adal} \rangle$   
**show** *False by simp*  
**qed**  
**ultimately show** *?case by simp*  
**qed**

**lemma** *redT-non-speculative-known-addr-allocated*:

```

assumes red: mthr.if.redT s (t, ta) s'
and tasc: non-speculative P vs (llist-of  $\{ta\}_o$ )
and ka: if.known-addr-state s  $\subseteq$  allocated (shr s)
and vs: w-addrs vs  $\subseteq$  allocated (shr s)
shows if.known-addr-state s'  $\subseteq$  allocated (shr s') (is ?thesis1)
and w-addrs (w-values P vs  $\{ta\}_o$ )  $\subseteq$  allocated (shr s') (is ?thesis2)
proof –
  have ?thesis1  $\wedge$  ?thesis2 using red
  proof(cases)
    case (redT-acquire x ln n)
      hence if.known-addr-state s' = if.known-addr-state s
      by(auto 4 4 simp add: if.known-addr-state-def if.known-addr-thr-def split: if-split-asm dest:
        bspec)
      also note ka
      also from redT-acquire have shr s = shr s' by simp
      finally have if.known-addr-state s'  $\subseteq$  allocated (shr s') .
      moreover have w-values P vs  $\{ta\}_o$  = vs using redT-acquire
      by(fastforce intro!: w-values-no-write-unchanged del: equalityI dest: convert-RA-not-write)
      ultimately show ?thesis using vs by(simp add:  $\langle shr\ s = shr\ s' \rangle$ )
  next
    case (redT-normal x x' m')
      note red =  $\langle t \vdash (x, shr\ s) \rightarrow ta \rightarrow i (x', m') \rangle$ 
      and tst =  $\langle thr\ s\ t = \lfloor (x, no-wait-locks) \rfloor \rangle$ 
      have allocated-subset: allocated (shr s)  $\subseteq$  allocated (shr s')
      using  $\langle mthr.if.redT\ s\ (t, ta)\ s' \rangle$  by(rule init-fin-redT-allocated-mono)
      with vs have vs': w-addrs vs  $\subseteq$  allocated (shr s') by blast
      { fix obs obs'
        assume  $\{ta\}_o = obs @ obs'$ 
        moreover with tasc have non-speculative P vs (llist-of obs)
        by(simp add: lappend-llist-of-llist-of[symmetric] non-speculative-lappend del: lappend-llist-of-llist-of)
        ultimately have w-addrs (w-values P vs obs)  $\cup$  new-obs-addr-if obs  $\subseteq$  allocated (shr s')
        (is ?concl obs)
      }
      proof(induct obs arbitrary: obs' rule: rev-induct)
        case Nil thus ?case using vs' by(simp add: new-obs-addr-if-def)
      next
        case (snoc ob obs)
        note ta =  $\langle \{ta\}_o = (obs @ [ob]) @ obs' \rangle$ 
        note tasc =  $\langle non-speculative\ P\ vs\ (l\text{list-of}\ (obs @ [ob])) \rangle$ 
        from snoc have IH: ?concl obs
        by(simp add: lappend-llist-of-llist-of[symmetric] non-speculative-lappend del: lappend-llist-of-llist-of)
        hence ?concl (obs @ [ob])
        proof(cases ob rule: mrw-value-cases)
          case (1 ad' al v)
            note ob =  $\langle ob = NormalAction\ (WriteMem\ ad'\ al\ v) \rangle$ 
            with ta have Write:  $\{ta\}_o ! length\ obs = NormalAction\ (WriteMem\ ad'\ al\ v)$  by simp
            show ?thesis
          proof
            fix ad''
            assume ad''  $\in$  w-addrs (w-values P vs (obs @ [ob]))  $\cup$  new-obs-addr-if (obs @ [ob])
            hence ad''  $\in$  w-addrs (w-values P vs obs)  $\cup$  new-obs-addr-if obs  $\vee v = Addr\ ad''$ 
            by(auto simp add: ob w-addrs-def ran-def new-obs-addr-if-def split: if-split-asm)
            thus ad''  $\in$  allocated (shr s')
          proof
            assume ad''  $\in$  w-addrs (w-values P vs obs)  $\cup$  new-obs-addr-if obs

```

```

    also note IH finally show ?thesis .
  next
    assume v: v = Addr ad''
    with Write have  $\{ta\}_o ! \text{length } obs = \text{NormalAction } (\text{WriteMem } ad' \text{ al } (\text{Addr } ad''))$  by
simp
    with red have ad'' ∈ known-addr-if t x ∨ ad'' ∈ new-obs-addr-if (take (length obs)
 $\{ta\}_o$ )
    by(rule if-red-write-knows-addr)(simp add: ta)
    thus ?thesis
  proof
    assume ad'' ∈ known-addr-if t x
    hence ad'' ∈ if.known-addr-state s using tst by(rule if.known-addr-stateI)
    with ka allocated-subset show ?thesis by blast
  next
    assume ad'' ∈ new-obs-addr-if (take (length obs)  $\{ta\}_o$ )
    with ta have ad'' ∈ new-obs-addr-if obs by simp
    with IH show ?thesis by blast
  qed
qed
qed
next
case (2 ad hT)

hence ob: ob = NormalAction (NewHeapElem ad hT) by simp
hence w-addr (w-values P vs (obs @ [ob])) ⊆ w-addr (w-values P vs obs)
  by(cases hT)(auto simp add: w-addr-def default-val-not-Addr Addr-not-default-val)
moreover from ob ta have NormalAction (NewHeapElem ad hT) ∈ set  $\{ta\}_o$  by simp
from init-fin-red-New-allocatedD[OF red this] have ad ∈ allocated m' ..
with redT-normal have ad ∈ allocated (shr s') by auto
ultimately show ?thesis using IH ob by(auto simp add: new-obs-addr-if-def)
next
case (4 ad al v)
note ob = ⟨ob = NormalAction (ReadMem ad al v)⟩
{ fix ad'
  assume v: v = Addr ad'
  with tasc ob have mrw: Addr ad' ∈ w-values P vs obs (ad, al)
  by(auto simp add: lappend-llist-of-llist-of[symmetric] non-speculative-lappend simp del:
lappend-llist-of-llist-of)
  hence ad' ∈ w-addr (w-values P vs obs)
  by(auto simp add: w-addr-def)
  with IH have ad' ∈ allocated (shr s') by blast }
with ob IH show ?thesis by(cases v)(simp-all add: new-obs-addr-if-def)
qed(simp-all add: new-obs-addr-if-def)
thus ?case by simp
qed }
note this[of  $\{ta\}_o$  []]
moreover have if.known-addr-state s' ⊆ if.known-addr-state s ∪ new-obs-addr-if  $\{ta\}_o$ 
  using ⟨mthr.if.redT s (t, ta) s'⟩ by(rule if-redT-known-addr-new)
ultimately show ?thesis using ka allocated-subset by blast
qed
thus ?thesis1 ?thesis2 by simp-all
qed

```

**lemma** *RedT-non-speculative-known-addr-allocated*:

**assumes** *red*: *mthr.if.RedT s ttas s'*  
**and** *tasc*: *non-speculative P vs (llist-of (concat (map ( $\lambda(t, ta). \llbracket ta \rrbracket_o$ ) ttas)))*  
**and** *ka*: *if.known-addr-state s  $\subseteq$  allocated (shr s)*  
**and** *vs*: *w-addr vs  $\subseteq$  allocated (shr s)*  
**shows** *if.known-addr-state s'  $\subseteq$  allocated (shr s') (is ?thesis1 s')*  
**and** *w-addr (w-values P vs (concat (map ( $\lambda(t, ta). \llbracket ta \rrbracket_o$ ) ttas)))  $\subseteq$  allocated (shr s') (is ?thesis2 s' ttas)*  
**proof** –  
**from** *red tasc* **have** *?thesis1 s'  $\wedge$  ?thesis2 s' ttas*  
**proof**(*induct rule: mthr.if.RedT-induct'*)  
**case** *refl* **thus** *?case* **using** *ka vs* **by** *simp*  
**next**  
**case** (*step ttas s' t ta s''*)  
**hence** *non-speculative P (w-values P vs (concat (map ( $\lambda(t, ta). \llbracket ta \rrbracket_o$ ) ttas))) (llist-of  $\llbracket ta \rrbracket_o$ )*  
**and** *?thesis1 s' ?thesis2 s' ttas*  
**by**(*simp-all add: lappend-llist-of-llist-of[symmetric] non-speculative-lappend del: lappend-llist-of-llist-of*)  
**from** *redT-non-speculative-known-addr-allocated[OF  $\langle mthr.if.redT s' (t, ta) s'' \rangle$  this]*  
**show** *?case* **by** *simp*  
**qed**  
**thus** *?thesis1 s' ?thesis2 s' ttas* **by** *simp-all*  
**qed**

**lemma** *read-ex-NewHeapElem [consumes 5, case-names start Red]*:

**assumes** *RedT*: *mthr.if.RedT (init-fin-lift-state status (start-state f P C M vs)) ttas s*  
**and** *red*: *mthr.if.redT s (t, ta) s'*  
**and** *read*: *NormalAction (ReadMem ad al v)  $\in$  set  $\llbracket ta \rrbracket_o$*   
**and** *sc*: *non-speculative P ( $\lambda-. \{\}$ ) (llist-of (map snd (lift-start-obs start-tid start-heap-obs) @ concat (map ( $\lambda(t, ta). \llbracket ta \rrbracket_o$ ) ttas)))*  
**and** *known*: *known-addr start-tid (f (fst (method P C M)) M (fst (snd (method P C M))) (fst (snd (snd (method P C M)))) (the (snd (snd (snd (method P C M)))))) vs)  $\subseteq$  allocated start-heap*  
**obtains** (*start*) *CTn* **where** *NewHeapElem ad CTn  $\in$  set start-heap-obs*  
 $|$  (*Red*) *ttas' s'' t' ta' s''' ttas'' CTn*  
**where** *mthr.if.RedT (init-fin-lift-state status (start-state f P C M vs)) ttas' s''*  
**and** *mthr.if.redT s'' (t', ta') s'''*  
**and** *mthr.if.RedT s''' ttas'' s*  
**and** *ttas = ttas' @ (t', ta') # ttas''*  
**and** *NormalAction (NewHeapElem ad CTn)  $\in$  set  $\llbracket ta' \rrbracket_o$*

**proof** –

**let** *?start-state = init-fin-lift-state status (start-state f P C M vs)*  
**let** *?obs-prefix = lift-start-obs start-tid start-heap-obs*  
**let** *?vs-start = w-values P ( $\lambda-. \{\}$ ) (map snd ?obs-prefix)*  
**from** *sc* **have** *non-speculative P (w-values P ( $\lambda-. \{\}$ ) (map snd (lift-start-obs start-tid start-heap-obs))) (llist-of (concat (map ( $\lambda(t, ta). \llbracket ta \rrbracket_o$ ) ttas)))*  
**by**(*simp add: non-speculative-lappend lappend-llist-of-llist-of[symmetric] del: lappend-llist-of-llist-of*)  
**with** *RedT* **have** *if.known-addr-state s  $\subseteq$  allocated (shr s)*  
**proof**(*rule RedT-non-speculative-known-addr-allocated*)  
**show** *if.known-addr-state ?start-state  $\subseteq$  allocated (shr ?start-state)*  
**using** *known*  
**by**(*auto simp add: if.known-addr-state-def if.known-addr-thr-def start-state-def init-fin-lift-state-def split-beta split: if-split-asm*)  
**have** *w-addr ?vs-start  $\subseteq$  w-addr ( $\lambda-. \{\}$ )* **by**(*rule w-addr-lift-start-heap-obs*)

```

    thus  $w\text{-}addr s \text{ ?}vs\text{-}start \subseteq allocated (shr \text{ ?}start\text{-}state)$  by simp
  qed
also from red read obtain  $x\text{-}ra \ x'\text{-}ra \ m'\text{-}ra$ 
  where  $red'\text{-}ra: t \vdash (x\text{-}ra, shr \ s) \text{ --}ta\text{--}\rightarrow i \ (x'\text{-}ra, m'\text{-}ra)$ 
  and  $s': redT\text{-}upd \ s \ t \ ta \ x'\text{-}ra \ m'\text{-}ra \ s'$ 
  and  $ts\text{-}t: thr \ s \ t = \lfloor (x\text{-}ra, no\text{-}wait\text{-}locks) \rfloor$ 
  by cases auto
from  $red'\text{-}ra$  read
have  $ad \in known\text{-}addrs\text{-}if \ t \ x\text{-}ra$  by(rule if-red-read-knows-addr)
hence  $ad \in if.known\text{-}addrs\text{-}state \ s$  using  $ts\text{-}t$  by(rule if.known-addrs-stateI)
finally have  $ad \in allocated (shr \ s)$  .

show ?thesis
proof(cases ad ∈ allocated start-heap)
  case True
  then obtain  $C\text{Th}$  where  $NewHeapElem \ ad \ C\text{Th} \in set \ start\text{-}heap\text{-}obs$ 
  unfolding start-addrs-allocated by(blast dest: start-addrs-NewHeapElem-start-heap-obsD)
  thus ?thesis by(rule start)
next
  case False
  hence  $ad \notin allocated (shr \text{ ?}start\text{-}state)$  by(simp add: start-state-def split-beta shr-init-fin-lift-state)
  with  $RedT \langle ad \in allocated (shr \ s) \rangle$  obtain  $t' \ ta' \ C\text{Th}$ 
  where  $tta: (t', ta') \in set \ ttas$ 
  and  $new: NormalAction (NewHeapElem \ ad \ C\text{Th}) \in set \ \{\!\{ta'\}\!\}_o$ 
  by(blast dest: init-fin-RedT-allocated-NewHeapElemD)
  from  $tta$  obtain  $ttas' \ ttas''$  where  $ttas: ttas = ttas' @ (t', ta') \# \ ttas''$  by(auto dest: split-list)
  with  $RedT$  obtain  $s'' \ s'''$ 
  where  $mthr.if.RedT \text{ ?}start\text{-}state \ ttas' \ s''$ 
  and  $mthr.if.redT \ s'' \ (t', ta') \ s'''$ 
  and  $mthr.if.RedT \ s''' \ ttas'' \ s$ 
  unfolding  $mthr.if.RedT\text{-}def$  by(auto elim!: rtrancl3p-appendE dest!: converse-rtrancl3p-step)
  thus thesis using  $ttas \ new$  by(rule Red)
  qed
  qed
end

locale known-addrs-typing =
  known-addrs
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  allocated known-addrs
  final r P
for  $addr2thread\text{-}id :: ('addr :: addr) \Rightarrow 'thread\text{-}id$ 
and  $thread\text{-}id2addr :: 'thread\text{-}id \Rightarrow 'addr$ 
and  $spurious\text{-}wakeups :: bool$ 
and  $empty\text{-}heap :: 'heap$ 
and  $allocate :: 'heap \Rightarrow htype \Rightarrow ('heap \times 'addr) \ set$ 
and  $typeof\text{-}addr :: 'heap \Rightarrow 'addr \rightarrow htype$ 
and  $heap\text{-}read :: 'heap \Rightarrow 'addr \Rightarrow addr\text{-}loc \Rightarrow 'addr \ val \Rightarrow bool$ 
and  $heap\text{-}write :: 'heap \Rightarrow 'addr \Rightarrow addr\text{-}loc \Rightarrow 'addr \ val \Rightarrow 'heap \Rightarrow bool$ 
and  $allocated :: 'heap \Rightarrow 'addr \ set$ 
and  $known\text{-}addrs :: 'thread\text{-}id \Rightarrow 'x \Rightarrow 'addr \ set$ 

```



```

and final :: 'x ⇒ bool
and r :: ('addr, 'thread-id, 'x, 'heap, 'addr, ('addr, 'thread-id) obs-event) semantics (- ⊢ - → -
[50,0,0,50] 80)
and wfx :: 'thread-id ⇒ 'x ⇒ 'heap ⇒ bool
and P :: 'md prog
+
assumes wfs-non-speculative-invar:
  ⌊ t ⊢ (x, m) -ta→ (x', m'); wfx t x m;
    vs-conf P m vs; non-speculative P vs (llist-of (map NormalAction {ta}_o)) ⌋
  ⇒ wfx t x' m'
and wfs-non-speculative-spawn:
  ⌊ t ⊢ (x, m) -ta→ (x', m'); wfx t x m;
    vs-conf P m vs; non-speculative P vs (llist-of (map NormalAction {ta}_o));
    NewThread t'' x'' m'' ∈ set {ta}_t ⌋
  ⇒ wfx t'' x'' m''
and wfs-non-speculative-other:
  ⌊ t ⊢ (x, m) -ta→ (x', m'); wfx t x m;
    vs-conf P m vs; non-speculative P vs (llist-of (map NormalAction {ta}_o));
    wfx t'' x'' m ⌋
  ⇒ wfx t'' x'' m'
and wfs-non-speculative-vs-conf:
  ⌊ t ⊢ (x, m) -ta→ (x', m'); wfx t x m;
    vs-conf P m vs; non-speculative P vs (llist-of (take n (map NormalAction {ta}_o))) ⌋
  ⇒ vs-conf P m' (w-values P vs (take n (map NormalAction {ta}_o)))
and red-read-typeable:
  ⌊ t ⊢ (x, m) -ta→ (x', m'); wfx t x m; ReadMem ad al v ∈ set {ta}_o ⌋
  ⇒ ∃ T. P, m ⊢ ad@al : T
and red-NewHeapElemD:
  ⌊ t ⊢ (x, m) -ta→ (x', m'); wfx t x m; NewHeapElem ad hT ∈ set {ta}_o ⌋
  ⇒ typeof-addr m' ad = [hT]
and red-hext-incr:
  ⌊ t ⊢ (x, m) -ta→ (x', m'); wfx t x m;
    vs-conf P m vs; non-speculative P vs (llist-of (map NormalAction {ta}_o)) ⌋
  ⇒ m ≤ m'
begin

lemma redT-wfs-non-speculative-invar:
  assumes redT: mthr.redT s (t, ta) s'
  and wfx: ts-ok wfx (thr s) (shr s)
  and vs: vs-conf P (shr s) vs
  and ns: non-speculative P vs (llist-of (map NormalAction {ta}_o))
  shows ts-ok wfx (thr s') (shr s')
using redT
proof(cases)
  case (redT-normal x x' m')
  with vs wfx ns show ?thesis
    apply(clarsimp intro!: ts-okI split: if-split-asm)
    apply(erule wfs-non-speculative-invar, auto dest: ts-okD)
    apply(rename-tac t' x' ln ws')
    apply(case-tac thr s t')
    apply(frule (2) redT-updT-s-new-thread, clarify)
    apply(frule (1) mthr.new-thread-memory)
  apply(auto intro: wfs-non-speculative-other wfs-non-speculative-spawn dest: ts-okD simp add: redT-updT-s-Some)
  done

```

**next**

**case** (*redT-acquire*  $x \text{ ln } n$ )  
**thus** *?thesis* **using** *wfx* **by**(*auto intro!*: *ts-okI dest: ts-okD split: if-split-asm*)

**qed**

**lemma** *redT-wfs-non-speculative-vs-conf*:

**assumes** *redT*: *mthr.redT*  $s \ (t, ta) \ s'$   
**and** *wfx*: *ts-ok wfx* (*thr*  $s$ ) (*shr*  $s$ )  
**and** *conf*: *vs-conf*  $P \ (shr \ s) \ vs$   
**and** *ns*: *non-speculative*  $P \ vs \ (l\text{list-of } (take \ n \ (map \ NormalAction \ \{ta\}_o)))$   
**shows** *vs-conf*  $P \ (shr \ s') \ (w\text{-values } P \ vs \ (take \ n \ (map \ NormalAction \ \{ta\}_o)))$

**using** *redT*

**proof**(*cases*)

**case** (*redT-normal*  $x \ x' \ m'$ )  
**thus** *?thesis* **using** *ns conf wfx* **by**(*auto dest: wfs-non-speculative-vs-conf ts-okD*)

**next**

**case** (*redT-acquire*  $x \ l \ ln$ )  
**have** *w-values*  $P \ vs \ (take \ n \ (map \ NormalAction \ (convert\text{-}RA \ ln :: ('addr, 'thread\text{-}id) \ obs\text{-}event \ list)))$   
 $= \ vs$   
**by**(*fastforce dest: in-set-takeD simp add: convert-RA-not-write intro!: w-values-no-write-unchanged*  
*del: equalityI*)

**thus** *?thesis* **using** *conf redT-acquire* **by**(*auto*)

**qed**

**lemma** *if-redT-non-speculative-invar*:

**assumes** *red*: *mthr.if.redT*  $s \ (t, ta) \ s'$   
**and** *ts-ok*: *ts-ok* (*init-fin-lift wfx*) (*thr*  $s$ ) (*shr*  $s$ )  
**and** *sc*: *non-speculative*  $P \ vs \ (l\text{list-of } \{ta\}_o)$   
**and** *vs*: *vs-conf*  $P \ (shr \ s) \ vs$   
**shows** *ts-ok* (*init-fin-lift wfx*) (*thr*  $s'$ ) (*shr*  $s'$ )

**proof** –

**let**  $?s = \lambda s. (locks \ s, (\lambda t. map\text{-}option \ (\lambda((status, x), ln). (x, ln)) \ (thr \ s \ t), shr \ s), wset \ s, interrupts \ s)$

**from** *ts-ok* **have** *ts-ok'*: *ts-ok wfx* (*thr*  $(?s \ s)$ ) (*shr*  $(?s \ s)$ ) **by**(*auto intro!: ts-okI dest: ts-okD*)

**from** *vs* **have** *vs'*: *vs-conf*  $P \ (shr \ (?s \ s)) \ vs$  **by** *simp*

**from** *red* **show** *?thesis*

**proof**(*cases*)

**case** (*redT-normal*  $x \ x' \ m$ )  
**note**  $tst = \langle thr \ s \ t = \lfloor (x, no\text{-}wait\text{-}locks) \rfloor \rangle$   
**from**  $\langle t \vdash (x, shr \ s) -ta \rightarrow i \ (x', m) \rangle$   
**show** *?thesis*  
**proof**(*cases*)  
**case** (*NormalAction*  $X \ TA \ X'$ )  
**from**  $\langle ta = convert\text{-}TA\text{-}initial \ (convert\text{-}obs\text{-}initial \ TA) \rangle \langle mthr.if.actions\text{-}ok \ s \ t \ ta \rangle$   
**have** *mthr.actions-ok*  $(?s \ s) \ t \ TA$   
**by**(*auto elim: rev-iffD1[OF - thread-oks-ts-change] cond-action-oks-final-change*)

**with** *tst NormalAction*  $\langle redT\text{-}upd \ s \ t \ ta \ x' \ m \ s' \rangle$  **have** *mthr.redT*  $(?s \ s) \ (t, TA) \ (?s \ s')$

**using** *map-redT-updT*s[*of snd thr s \{ta\}\_t*]

**by**(*auto intro!: mthr.redT.intros simp add: split-def map-prod-def o-def fun-eq-iff*)

**moreover** **note** *ts-ok' vs'*

**moreover** **from**  $\langle ta = convert\text{-}TA\text{-}initial \ (convert\text{-}obs\text{-}initial \ TA) \rangle$  **have**  $\{ta\}_o = map \ NormalAc\text{-}$

```

tion  $\llbracket TA \rrbracket_o$  by(auto)
  with sc have non-speculative  $P$  vs (llist-of (map NormalAction  $\llbracket TA \rrbracket_o$ )) by simp
  ultimately have ts-ok wfx (thr (?s s')) (shr (?s s'))
    by(auto dest: redT-wfs-non-speculative-invar)
  thus ?thesis using  $\langle \llbracket ta \rrbracket_o = \text{map NormalAction } \llbracket TA \rrbracket_o \rangle$  by(auto intro!: ts-okI dest: ts-okD)
next
  case InitialThreadAction
  with redT-normal ts-ok' vs show ?thesis
    by(auto 4 3 intro!: ts-okI dest: ts-okD split: if-split-asm)
next
  case ThreadFinishAction
  with redT-normal ts-ok' vs show ?thesis
    by(auto 4 3 intro!: ts-okI dest: ts-okD split: if-split-asm)
qed
next
  case (redT-acquire x ln l)
  thus ?thesis using vs ts-ok by(auto 4 3 intro!: ts-okI dest: ts-okD split: if-split-asm)
qed
qed

lemma if-redT-non-speculative-vs-conf:
  assumes red: mthr.if.redT s (t, ta) s'
  and ts-ok: ts-ok (init-fin-lift wfx) (thr s) (shr s)
  and sc: non-speculative P vs (llist-of (take n  $\llbracket ta \rrbracket_o$ ))
  and vs: vs-conf P (shr s) vs
  shows vs-conf P (shr s') (w-values P vs (take n  $\llbracket ta \rrbracket_o$ ))
proof -
  let ?s =  $\lambda s. (\text{locks } s, (\lambda t. \text{map-option } (\lambda((\text{status}, x), \text{ln}). (x, \text{ln})) (\text{thr } s \ t), \text{shr } s), \text{wset } s, \text{interrupts } s)$ 
  from ts-ok have ts-ok': ts-ok wfx (thr (?s s)) (shr (?s s)) by(auto intro!: ts-okI dest: ts-okD)
  from vs have vs': vs-conf P (shr (?s s)) vs by simp

  from red show ?thesis
proof(cases)
  case (redT-normal x x' m)
  note tst =  $\langle \text{thr } s \ t = \lfloor (x, \text{no-wait-locks}) \rfloor \rangle$ 
  from  $\langle t \vdash (x, \text{shr } s) \text{ --ta--> } i (x', m) \rangle$ 
  show ?thesis
proof(cases)
  case (NormalAction X TA X')
  from  $\langle ta = \text{convert-TA-initial } (\text{convert-obs-initial } TA) \rangle \langle \text{mthr.if.actions-ok } s \ t \ ta \rangle$ 
  have mthr.actions-ok (?s s) t TA
    by(auto elim: rev-iffD1[OF - thread-oks-ts-change] cond-action-oks-final-change)

  with tst NormalAction  $\langle \text{redT-upd } s \ t \ ta \ x' \ m \ s' \rangle$  have mthr.redT (?s s) (t, TA) (?s s')
    using map-redT-updTs[of snd thr s  $\llbracket ta \rrbracket_t$ ]
    by(auto intro!: mthr.redT.intros simp add: split-def map-prod-def o-def fun-eq-iff)
  moreover note ts-ok' vs'
  moreover from  $\langle ta = \text{convert-TA-initial } (\text{convert-obs-initial } TA) \rangle$  have  $\llbracket ta \rrbracket_o = \text{map NormalAction } \llbracket TA \rrbracket_o$  by(auto)
  with sc have non-speculative P vs (llist-of (take n (map NormalAction  $\llbracket TA \rrbracket_o$ ))) by simp
  ultimately have vs-conf P (shr (?s s')) (w-values P vs (take n (map NormalAction  $\llbracket TA \rrbracket_o$ )))
    by(auto dest: redT-wfs-non-speculative-vs-conf)

```

```

    thus ?thesis using ⟨{ta}o = map NormalAction {TA}o⟩ by (auto)
  next
    case InitialThreadAction
    with redT-normal vs show ?thesis by (auto simp add: take-Cons')
  next
    case ThreadFinishAction
    with redT-normal vs show ?thesis by (auto simp add: take-Cons')
  qed
next
  case (redT-acquire x l ln)
  have w-values P vs (take n (map NormalAction (convert-RA ln :: ('addr, 'thread-id) obs-event
list))) = vs
  by (fastforce simp add: convert-RA-not-write take-Cons' dest: in-set-takeD intro!: w-values-no-write-unchanged
del: equalityI)
  thus ?thesis using vs redT-acquire by auto
  qed
qed

lemma if-RedT-non-speculative-invar:
  assumes red: mthr.if.RedT s ttas s'
  and tsok: ts-ok (init-fin-lift wfx) (thr s) (shr s)
  and sc: non-speculative P vs (llist-of (concat (map (λ(t, ta). {ta}o) ttas)))
  and vs: vs-conf P (shr s) vs
  shows ts-ok (init-fin-lift wfx) (thr s') (shr s') (is ?thesis1)
  and vs-conf P (shr s') (w-values P vs (concat (map (λ(t, ta). {ta}o) ttas))) (is ?thesis2)
using red tsok sc vs unfolding mthr.if.RedT-def
proof (induct arbitrary: vs rule: rtrancl3p-converse-induct')
  case refl
  case 1 thus ?case by -
  case 2 thus ?case by simp
next
  case (step s tta s' ttas)
  obtain t ta where tta: tta = (t, ta) by (cases tta)

  case 1
  hence sc1: non-speculative P vs (llist-of {ta}o)
  and sc2: non-speculative P (w-values P vs {ta}o) (llist-of (concat (map (λ(t, ta). {ta}o) ttas)))
  unfolding lconcat-llist-of[symmetric] lmap-llist-of[symmetric] llist.map-comp o-def llist-of.simps
  llist.map(2) lconcat-LCons tta
  by (simp-all add: non-speculative-lappend list-of-lconcat o-def)
  from if-redT-non-speculative-invar[OF step(2)[unfolded tta] - sc1] if-redT-non-speculative-vs-conf[OF
step(2)[unfolded tta], where vs = vs and n=length {ta}o] 1 step.hyps(3)[of w-values P vs {ta}o] sc2
  sc1
  show ?case by simp

  case 2
  hence sc1: non-speculative P vs (llist-of {ta}o)
  and sc2: non-speculative P (w-values P vs {ta}o) (llist-of (concat (map (λ(t, ta). {ta}o) ttas)))
  unfolding lconcat-llist-of[symmetric] lmap-llist-of[symmetric] llist.map-comp o-def llist-of.simps
  llist.map(2) lconcat-LCons tta
  by (simp-all add: non-speculative-lappend list-of-lconcat o-def)
  from if-redT-non-speculative-invar[OF step(2)[unfolded tta] - sc1] if-redT-non-speculative-vs-conf[OF
step(2)[unfolded tta], where vs = vs and n=length {ta}o] 2 step.hyps(4)[of w-values P vs {ta}o] sc2
  sc1

```

**show** ?case **by**(simp add: tta o-def)  
**qed**

**lemma** *init-fin-hext-incr*:  
**assumes**  $t \vdash (x, m) -ta \rightarrow i (x', m')$   
**and** *init-fin-lift wfx*  $t \ x \ m$   
**and** *non-speculative*  $P \ vs \ (l\text{list-of } \{ta\}_o)$   
**and** *vs-conf*  $P \ m \ vs$   
**shows**  $m \sqsubseteq m'$   
**using** *assms*  
**by**(cases)(auto intro: red-hext-incr)

**lemma** *init-fin-redT-hext-incr*:  
**assumes** *mthr.if.redT*  $s \ (t, ta) \ s'$   
**and** *ts-ok* (*init-fin-lift wfx*) (*thr*  $s$ ) (*shr*  $s$ )  
**and** *non-speculative*  $P \ vs \ (l\text{list-of } \{ta\}_o)$   
**and** *vs-conf*  $P \ (shr \ s) \ vs$   
**shows**  $shr \ s \sqsubseteq shr \ s'$   
**using** *assms*  
**by**(cases)(auto dest: *init-fin-hext-incr ts-okD*)

**lemma** *init-fin-RedT-hext-incr*:  
**assumes** *mthr.if.RedT*  $s \ ttas \ s'$   
**and** *ts-ok* (*init-fin-lift wfx*) (*thr*  $s$ ) (*shr*  $s$ )  
**and** *sc*: *non-speculative*  $P \ vs \ (l\text{list-of } (concat \ (map \ (\lambda(t, ta). \{ta\}_o) \ ttas)))$   
**and** *vs*: *vs-conf*  $P \ (shr \ s) \ vs$   
**shows**  $shr \ s \sqsubseteq shr \ s'$   
**using** *assms*  
**proof**(*induction rule: mthr.if.RedT-induct'*)  
**case** *refl* **thus** ?case **by** *simp*  
**next**  
**case** (*step*  $ttas \ s' \ t \ ta \ s''$ )  
**note**  $ts-ok = \langle ts-ok \ (init-fin-lift \ wfx) \ (thr \ s) \ (shr \ s) \rangle$   
**from**  $\langle non-speculative \ P \ vs \ (l\text{list-of } (concat \ (map \ (\lambda(t, ta). \{ta\}_o) \ (ttas \ @ \ [(t, ta)])))) \rangle$   
**have**  $ns$ : *non-speculative*  $P \ vs \ (l\text{list-of } (concat \ (map \ (\lambda(t, ta). \{ta\}_o) \ ttas)))$   
**and**  $ns'$ : *non-speculative*  $P \ (w-values \ P \ vs \ (concat \ (map \ (\lambda(t, ta). \{ta\}_o) \ ttas))) \ (l\text{list-of } \{ta\}_o)$   
**by**(simp-all add: *lappend-llist-of-llist-of[symmetric] non-speculative-lappend del: lappend-llist-of-llist-of*)  
**from**  $ts-ok \ ns$  **have**  $shr \ s \sqsubseteq shr \ s'$   
**using**  $\langle vs-conf \ P \ (shr \ s) \ vs \rangle$  **by**(rule *step.IH*)  
**also** **have**  $ts-ok \ (init-fin-lift \ wfx) \ (thr \ s') \ (shr \ s')$   
**using**  $\langle mthr.if.RedT \ s \ ttas \ s' \rangle \ ts-ok \ ns \ \langle vs-conf \ P \ (shr \ s) \ vs \rangle$   
**by**(rule *if-RedT-non-speculative-invar*)  
**with**  $\langle mthr.if.redT \ s' \ (t, ta) \ s'' \rangle$   
**have**  $\dots \sqsubseteq shr \ s''$  **using**  $ns'$   
**proof**(rule *init-fin-redT-hext-incr*)  
**from**  $\langle mthr.if.RedT \ s \ ttas \ s' \rangle \ ts-ok \ ns \ \langle vs-conf \ P \ (shr \ s) \ vs \rangle$   
**show**  $vs-conf \ P \ (shr \ s') \ (w-values \ P \ vs \ (concat \ (map \ (\lambda(t, ta). \{ta\}_o) \ ttas)))$   
**by**(rule *if-RedT-non-speculative-invar*)  
**qed**  
**finally** **show** ?case .  
**qed**

**lemma** *init-fin-red-read-typeable*:  
**assumes**  $t \vdash (x, m) -ta \rightarrow i (x', m')$

```

and init-fin-lift wfx t x m NormalAction (ReadMem ad al v)  $\in$  set  $\{ta\}_o$ 
shows  $\exists T. P, m \vdash ad@al : T$ 
using assms
by cases(auto dest: red-read-typeable)

lemma Ex-new-action-for:
  assumes wf: wf-syscls P
  and wfx-start: ts-ok wfx (thr (start-state f P C M vs)) start-heap
  and ka: known-addr start-tid (f (fst (method P C M)) M (fst (snd (method P C M))) (fst (snd (snd (method P C M)))))) (the (snd (snd (snd (method P C M)))))) vs)  $\subseteq$  allocated start-heap
  and E: E  $\in$   $\mathcal{E}$ -start f P C M vs status
  and read: ra  $\in$  read-actions E
  and aloc: adal  $\in$  action-loc P E ra
  and sc: non-speculative P ( $\lambda\cdot. \{\}$ ) (ltake (enat ra) (lmap snd E))
  shows  $\exists wa. wa \in$  new-actions-for P E adal  $\wedge$  wa  $<$  ra
proof –
  let ?obs-prefix = lift-start-obs start-tid start-heap-obs
  let ?start-state = init-fin-lift-state status (start-state f P C M vs)

  from start-state-vs-conf[OF wf]
  have vs-conf-start: vs-conf P start-heap (w-values P ( $\lambda\cdot. \{\}$ ) (map NormalAction start-heap-obs))
    by(simp add: lift-start-obs-def o-def)

  obtain ad al where adal: adal = (ad, al) by(cases adal)
  with read aloc obtain v where ra: action-obs E ra = NormalAction (ReadMem ad al v)
    and ra-len: enat ra  $<$  llength E
    by(cases lnth E ra)(auto elim!: read-actions.cases actionsE)

  from E obtain E'' where E: E = lappend (llist-of ?obs-prefix) E''
    and E'': E''  $\in$  mthr.if. $\mathcal{E}$  ?start-state by(auto)
  from E'' obtain E' where E': E'' = lconcat (lmap ( $\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \{ta\}_o))$  E')
    and  $\tau$ Runs: mthr.if.mthr.Runs ?start-state E' by(rule mthr.if. $\mathcal{E}$ .cases)

  have ra-len': length ?obs-prefix  $\leq$  ra
  proof(rule ccontr)
    assume  $\neg$  ?thesis
    hence ra  $<$  length ?obs-prefix by simp
    moreover with ra ra-len E obtain ra' ad al v
      where start-heap-obs ! ra' = ReadMem ad al v ra'  $<$  length start-heap-obs
      by(cases ra)(auto simp add: lnth-LCons lnth-lappend1 action-obs-def lift-start-obs-def)
      ultimately have ReadMem ad al v  $\in$  set start-heap-obs unfolding in-set-conv-nth by blast
      thus False by(simp add: start-heap-obs-not-Read)
    qed
  let ?n = length ?obs-prefix
  from ra ra-len ra-len' E have enat (ra – ?n)  $<$  llength E''
    and ra-obs: action-obs E'' (ra – ?n) = NormalAction (ReadMem ad al v)
    by(cases llength E'', auto simp add: action-obs-def lnth-lappend2)

  from  $\tau$ Runs (enat (ra – ?n)  $<$  llength E'') obtain ra-m ra-n t-ra ta-ra
    where E-ra: lnth E'' (ra – ?n) = (t-ra,  $\{ta-ra\}_o$  ! ra-n)
    and ra-n: ra-n  $<$  length  $\{ta-ra\}_o$  and ra-m: enat ra-m  $<$  llength E'
    and ra-conv: ra – ?n = ( $\sum i < ra-m. \text{length } \{snd (lnth E' i)\}_o$ ) + ra-n
    and E'-ra-m: lnth E' ra-m = (t-ra, ta-ra)
    unfolding E' by(rule mthr.if.actions- $\mathcal{E}$  E-aux)

```

**let**  $?E' = \text{ldropn } (\text{Suc } \text{ra-m}) \ E'$   
**have**  $E'\text{-unfold}$ :  $E' = \text{lappend } (\text{ltake } (\text{enat } \text{ra-m}) \ E') \ (LCons \ (\text{lnth } E' \ \text{ra-m}) \ ?E')$   
**unfolding**  $\text{ldropn-Suc-conv-ldropn}[OF \ \text{ra-m}]$  **by**  $\text{simp}$   
**hence**  $\text{mthr.if.mthr.Runs } ?\text{start-state} \ (\text{lappend } (\text{ltake } (\text{enat } \text{ra-m}) \ E') \ (LCons \ (\text{lnth } E' \ \text{ra-m}) \ ?E'))$   
**using**  $\tau\text{Runs}$  **by**  $\text{simp}$   
**then obtain**  $\sigma'$  **where**  $\sigma\text{-}\sigma'$ :  $\text{mthr.if.mthr.Trsys } ?\text{start-state} \ (\text{list-of } (\text{ltake } (\text{enat } \text{ra-m}) \ E')) \ \sigma'$   
**and**  $\tau\text{Runs}'$ :  $\text{mthr.if.mthr.Runs } \sigma' \ (LCons \ (\text{lnth } E' \ \text{ra-m}) \ ?E')$   
**by**(rule  $\text{mthr.if.mthr.Runs-lappendE}$ )  $\text{simp}$   
**from**  $\tau\text{Runs}'$  **obtain**  $\sigma''$  **where**  $\text{red-ra}$ :  $\text{mthr.if.redT } \sigma' \ (t\text{-ra}, \text{ta-ra}) \ \sigma''$   
**and**  $\tau\text{Runs}''$ :  $\text{mthr.if.mthr.Runs } \sigma'' \ ?E'$   
**unfolding**  $E'\text{-ra-m}$  **by**  $\text{cases}$   
  
**from**  $E\text{-ra } \text{ra-n } \text{ra-obs}$  **have**  $\text{NormalAction } (\text{ReadMem } \text{ad } \text{al } v) \in \text{set } \{\text{ta-ra}\}_o$   
**by**(auto  $\text{simp add: action-obs-def in-set-conv-nth}$ )  
**with**  $\text{red-ra}$  **obtain**  $x\text{-ra } x'\text{-ra } m'\text{-ra}$   
**where**  $\text{red}'\text{-ra}$ :  $\text{mthr.init-fin } t\text{-ra} \ (x\text{-ra}, \text{shr } \sigma') \ \text{ta-ra} \ (x'\text{-ra}, m'\text{-ra})$   
**and**  $\sigma''$ :  $\text{redT-upd } \sigma' \ t\text{-ra} \ \text{ta-ra} \ x'\text{-ra} \ m'\text{-ra} \ \sigma''$   
**and**  $\text{ts-t-a}$ :  $\text{thr } \sigma' \ t\text{-ra} = \lfloor (x\text{-ra}, \text{no-wait-locks}) \rfloor$   
**by**  $\text{cases auto}$   
**from**  $\text{red}'\text{-ra}$   $\langle \text{NormalAction } (\text{ReadMem } \text{ad } \text{al } v) \in \text{set } \{\text{ta-ra}\}_o \rangle$   
**obtain**  $\text{ta}'\text{-ra } X\text{-ra } X'\text{-ra}$   
**where**  $x\text{-ra}$ :  $x\text{-ra} = (\text{Running}, X\text{-ra})$   
**and**  $x'\text{-ra}$ :  $x'\text{-ra} = (\text{Running}, X'\text{-ra})$   
**and**  $\text{ta-ra}$ :  $\text{ta-ra} = \text{convert-TA-initial } (\text{convert-obs-initial } \text{ta}'\text{-ra})$   
**and**  $\text{red}''\text{-ra}$ :  $t\text{-ra} \vdash (X\text{-ra}, \text{shr } \sigma') \text{--ta}'\text{-ra} \rightarrow (X'\text{-ra}, m'\text{-ra})$   
**by**  $\text{cases fastforce+}$   
  
**from**  $\langle \text{NormalAction } (\text{ReadMem } \text{ad } \text{al } v) \in \text{set } \{\text{ta-ra}\}_o \rangle \ \text{ta-ra}$   
**have**  $\text{ReadMem } \text{ad } \text{al } v \in \text{set } \{\text{ta}'\text{-ra}\}_o$  **by**  $\text{auto}$   
  
**from**  $\text{wfx-start}$  **have**  $\text{wfx-start}$ :  $\text{ts-ok } (\text{init-fin-lift } \text{wfx}) \ (\text{thr } ?\text{start-state}) \ (\text{shr } ?\text{start-state})$   
**by**( $\text{simp add: start-state-def split-beta}$ )  
  
**from**  $\text{sc ra-len}'$   
**have**  $\text{non-speculative } P \ (w\text{-values } P \ (\lambda\text{-}. \{\}) \ (\text{map } \text{snd } ?\text{obs-prefix}))$   
 $(\text{lmap } \text{snd } (\text{ltake } (\text{enat } (\text{ra} - ?n)) \ (\text{lconcat } (\text{lmap } (\lambda(t, \text{ta}). \text{llist-of } (\text{map } (\text{Pair } t) \ \{\text{ta}\}_o)) \ E'))))$   
**unfolding**  $E \ E'$  **by**( $\text{simp add: ltake-lappend2 lmap-lappend-distrib non-speculative-lappend}$ )  
**also note**  $\text{ra-conv}$  **also note**  $\text{plus-enat-simps}(1)[\text{symmetric}]$   
**also have**  $\text{enat } (\sum i < \text{ra-m}. \text{length } \{\text{snd } (\text{lnth } E' \ i)\}_o) = (\sum i < \text{ra-m}. \text{enat } (\text{length } \{\text{snd } (\text{lnth } E' \ i)\}_o))$   
**by**( $\text{subst sum-comp-morphism}[\text{symmetric}](\text{simp-all add: zero-enat-def})$ )  
**also have**  $\dots = (\sum i < \text{ra-m}. \text{length } (\text{lnth } (\text{lmap } (\lambda(t, \text{ta}). \text{llist-of } (\text{map } (\text{Pair } t) \ \{\text{ta}\}_o)) \ E') \ i))$   
**using**  $\text{ra-m}$  **by**-(rule  $\text{sum.cong}[OF \ \text{refl}]$ ,  $\text{simp add: le-less-trans}[\text{where } y = \text{enat } \text{ra-m}] \ \text{split-beta}$ )  
**also note**  $\text{ltake-plus-conv-lappend}$  **also note**  $\text{lconcat-ltake}[\text{symmetric}]$   
**also note**  $\text{lmap-lappend-distrib}$   
**also note**  $\text{non-speculative-lappend}$   
**finally have**  $\text{non-speculative } P \ (w\text{-values } P \ (\lambda\text{-}. \{\}) \ (\text{map } \text{snd } ?\text{obs-prefix})) \ (\text{lmap } \text{snd } (\text{lconcat } (\text{lmap } (\lambda(t, \text{ta}). \text{llist-of } (\text{map } (\text{Pair } t) \ \{\text{ta}\}_o)) \ (\text{llist-of } (\text{list-of } (\text{ltake } (\text{enat } \text{ra-m}) \ E'))))))$   
**by**( $\text{simp add: split-def}$ )  
**hence**  $\text{sc}'$ :  $\text{non-speculative } P \ (w\text{-values } P \ (\lambda\text{-}. \{\}) \ (\text{map } \text{snd } ?\text{obs-prefix})) \ (\text{llist-of } (\text{concat } (\text{map } (\lambda(t, \text{ta}). \{\text{ta}\}_o) \ (\text{list-of } (\text{ltake } (\text{enat } \text{ra-m}) \ E'))))))$   
**unfolding**  $\text{lmap-lconcat llist.map-comp o-def lconcat-llist-of}[\text{symmetric}] \ \text{lmap-llist-of}[\text{symmetric}]$

```

    by(simp add: split-beta o-def)

  from vs-conf-start have vs-conf-start: vs-conf P (shr ?start-state) (w-values P (λ-. {})) (map snd
    ?obs-prefix))
    by(simp add: init-fin-lift-state-conv-simps start-state-def split-beta lift-start-obs-def o-def)
  with σ-σ' wfx-start sc' have ts-ok (init-fin-lift wfx) (thr σ') (shr σ')
    unfolding mthr.if.RedT-def[symmetric] by(rule if-RedT-non-speculative-invar)
  with ts-t-a have wfx t-ra X-ra (shr σ') unfolding x-ra by(auto dest: ts-okD)

  with red''-ra ⟨ReadMem ad al v ∈ set {ta'-ra}_o⟩
  obtain T' where type-adal: P,shr σ' ⊢ ad@al : T' by(auto dest: red-read-typeable)

  from sc ra-len' have non-speculative P (λ-. {}) (llist-of (map snd ?obs-prefix))
    unfolding E by(simp add: ltake-lappend2 lmap-lappend-distrib non-speculative-lappend)
  with sc' have sc'': non-speculative P (λ-. {}) (llist-of (map snd (lift-start-obs start-tid start-heap-obs)
    @ concat (map (λ(t, ta). {ta}_o) (list-of (ltake (enat ra-m) E'))))))
    by(simp add: lappend-llist-of-llist-of[symmetric] non-speculative-lappend del: lappend-llist-of)

  from σ-σ' red-ra ⟨NormalAction (ReadMem ad al v) ∈ set {ta-ra}_o⟩ sc'' ka
  show ∃ wa. wa ∈ new-actions-for P E adal ∧ wa < ra
    unfolding mthr.if.RedT-def[symmetric]
  proof(cases rule: read-ex-NewHeapElem)
    case (start CTn)
    then obtain n where n: start-heap-obs ! n = NewHeapElem ad CTn
      and len: n < length start-heap-obs
      unfolding in-set-conv-nth by blast
    from len have Suc n ∈ actions E unfolding E by(simp add: actions-def enat-less-enat-plusI)
    moreover
    from σ-σ' have hext: start-heap ≤ shr σ' unfolding mthr.if.RedT-def[symmetric]
      using wfx-start sc' vs-conf-start
    by(auto dest!: init-fin-RedT-hext-incr simp add: start-state-def split-beta init-fin-lift-state-conv-simps)

    from start have typeof-addr start-heap ad = ⌊CTn⌋
      by(auto dest: NewHeapElem-start-heap-obsD[OF wf])
    with hext have typeof-addr (shr σ') ad = ⌊CTn⌋ by(rule typeof-addr-hext-mono)
    with type-adal have adal ∈ action-loc P E (Suc n) using n len unfolding E adal
      by cases(auto simp add: action-obs-def lnth-lappend1 lift-start-obs-def)
    moreover have is-new-action (action-obs E (Suc n)) using n len unfolding E
      by(simp add: action-obs-def lnth-lappend1 lift-start-obs-def)
    ultimately have Suc n ∈ new-actions-for P E adal by(rule new-actionsI)
    moreover have Suc n < ra using ra-len' len by(simp)
    ultimately show ?thesis by blast
  next
    case (Red ttas' s'' t' ta' s''' ttas'' CTn)

    from ⟨NormalAction (NewHeapElem ad CTn) ∈ set {ta'}_o⟩
    obtain obs obs' where obs: {ta'}_o = obs @ NormalAction (NewHeapElem ad CTn) # obs'
      by(auto dest: split-list)

    let ?wa = ?n + length (concat (map (λ(t, ta). {ta}_o) ttas')) + length obs
    have enat (length (concat (map (λ(t, ta). {ta}_o) ttas')) + length obs) < enat (length (concat (map
      (λ(t, ta). {ta}_o) (ttas' @ [(t', ta')]))))
      using obs by simp
    also have ... = llength (lconcat (lmap llist-of (lmap (λ(t, ta). {ta}_o) (llist-of (ttas' @ [(t', ta')])))))

```



by(simp del: map-map map-append add: lconcat-llist-of)  
 also have ... ≤ llength (lconcat (lmap (λ(t, ta). llist-of {ta}\_o) (llist-of (ttas' @ (t', ta') # ttas'))))  
 by(auto simp add: o-def split-def intro: lprefix-llist-ofI intro!: lprefix-lconcatI lprefix-llength-le)  
 also note len-less = calculation  
 have ... ≤ (∑ i<ra-m. llength (lnth (lmap (λ(t, ta). llist-of {ta}\_o) E') i))  
 unfolding ⟨list-of (ltake (enat ra-m) E') = ttas' @ (t', ta') # ttas''⟩[symmetric]  
 by(simp add: ltake-lmap[symmetric] lconcat-ltake del: ltake-lmap)  
 also have ... = enat (∑ i<ra-m. length {snd (lnth E' i)}\_o) **using** ra-m  
 by(subst sum-comp-morphism[symmetric, **where** h=enat])(auto intro: sum.cong simp add:  
 zero-enat-def less-trans[**where** y=enat ra-m] split-beta)  
 also have ... ≤ enat (ra - ?n) **unfolding** ra-conv **by** simp  
 finally have enat-length: enat (length (concat (map (λ(t, ta). {ta}\_o) ttas')) + length obs) < enat  
 (ra - length (lift-start-obs start-tid start-heap-obs)) .  
 then have wa-ra: ?wa < ra **by** simp  
 with ra-len have ?wa ∈ actions E **by**(cases llength E)(simp-all add: actions-def)  
 moreover  
 from ⟨mthr.if.redT s'' (t', ta') s'''⟩ ⟨NormalAction (NewHeapElem ad CTn) ∈ set {ta'}\_o⟩  
 obtain x-wa x-wa' **where** ts''t': thr s'' t' = [(x-wa, no-wait-locks)]  
 and red-wa: mthr.init-fin t' (x-wa, shr s'') ta' (x-wa', shr s''')  
 by(cases) fastforce+  
  
 from sc'  
 have ns: non-speculative P (w-values P (λ-. { }) (map snd ?obs-prefix)) (llist-of (concat (map (λ(t,  
 ta). {ta}\_o) ttas')))  
 and ns': non-speculative P (w-values P (w-values P (λ-. { }) (map snd ?obs-prefix)) (concat (map  
 (λ(t, ta). {ta}\_o) ttas')) (llist-of {ta'}\_o)  
 and ns'': non-speculative P (w-values P (w-values P (w-values P (λ-. { }) (map snd ?obs-prefix))  
 (concat (map (λ(t, ta). {ta}\_o) ttas')) {ta}\_o) (llist-of (concat (map (λ(t, ta). {ta}\_o) ttas'))  
**unfolding** ⟨list-of (ltake (enat ra-m) E') = ttas' @ (t', ta') # ttas''⟩  
**by**(simp-all add: lappend-llist-of-llist-of[symmetric] lmap-lappend-distrib non-speculative-lappend  
 del: lappend-llist-of-llist-of)  
 from ⟨mthr.if.RedT ?start-state ttas' s''⟩ wfx-start ns  
 have ts-ok'': ts-ok (init-fin-lift wfx) (thr s'') (shr s'')  
**using** vs-conf-start **by**(rule if-RedT-non-speculative-invar)  
 with ts''t' have wfx t': wfx t' (snd x-wa) (shr s'') **by**(cases x-wa)(auto dest: ts-okD)  
  
 {  
 have action-obs E ?wa =  
 snd (lnth (lconcat (lmap (λ(t, ta). llist-of (map (Pair t) {ta}\_o) E')) (length (concat (map (λ(t,  
 y). {y}\_o) ttas')) + length obs))  
**unfolding** E E' **by**(simp add: action-obs-def lnth-lappend2)  
 also from enat-length ⟨enat (ra - ?n) < llength E''⟩  
 have ... = lnth (lconcat (lmap (λ(t, ta). llist-of {ta}\_o) E')) (length (concat (map (λ(t, y). {y}\_o)  
 ttas')) + length obs)  
**unfolding** E'  
**by**(subst lnth-lmap[symmetric, **where** f=snd])(erule (1) less-trans, simp add: lmap-lconcat  
 llist.map-comp split-def o-def)  
 also from len-less  
 have enat (length (concat (map (λ(t, ta). {ta}\_o) ttas')) + length obs < llength (lconcat (ltake  
 (enat ra-m) (lmap (λ(t, ta). llist-of {ta}\_o) E')))  
**unfolding** ⟨list-of (ltake (enat ra-m) E') = ttas' @ (t', ta') # ttas''⟩[symmetric]  
**by**(simp add: ltake-lmap[symmetric] del: ltake-lmap)  
 note lnth-lconcat-ltake[OF this, symmetric]  
 also note ltake-lmap

```

also have ltake (enat ra-m) E' = llist-of (list-of (ltake (enat ra-m) E')) by(simp)
also note ⟨list-of (ltake (enat ra-m) E') = ttas' @ (t', ta') # ttas''⟩
also note lmap-llist-of also have (λ(t, ta). llist-of ⟦ta⟧o) = llist-of ∘ (λ(t, ta). ⟦ta⟧o)
  by(simp add: o-def split-def)
also note map-map[symmetric] also note lconcat-llist-of
also note lnth-llist-of
also have concat (map (λ(t, ta). ⟦ta⟧o) (ttas' @ (t', ta') # ttas'')) ! (length (concat (map (λ(t,
ta). ⟦ta⟧o) ttas'))) + length obs = NormalAction (NewHeapElem ad CTn)
  by(simp add: nth-append obs)
finally have action-obs E ?wa = NormalAction (NewHeapElem ad CTn) .
}
note wa-obs = this

from ⟨mthr.if.RedT ?start-state ttas' s''⟩ wfx-start ns vs-conf-start
have vs'': vs-conf P (shr s'') (w-values P (w-values P (λ-. {})) (map snd ?obs-prefix)) (concat (map
(λ(t, ta). ⟦ta⟧o) ttas')))
  by(rule if-RedT-non-speculative-invar)
from if-redT-non-speculative-vs-conf[OF ⟨mthr.if.redT s'' (t', ta') s'''⟩ ts-ok'' - vs'', of length
⟦ta'⟧o] ns'
have vs''': vs-conf P (shr s''') (w-values P (w-values P (w-values P (λ-. {})) (map snd ?obs-prefix))
(concat (map (λ(t, ta). ⟦ta⟧o) ttas'))) ⟦ta'⟧o
  by simp

from ⟨mthr.if.redT s'' (t', ta') s'''⟩ ts-ok'' ns' vs''
have ts-ok (init-fin-lift wfx) (thr s''') (shr s''')
  by(rule if-redT-non-speculative-invar)
with ⟨mthr.if.RedT s''' ttas'' σ'⟩
have hext: shr s''' ⊑ shr σ' using ns'' vs'''
  by(rule init-fin-RedT-hext-incr)

from red-wa have typeof-addr (shr s''') ad = ⊥ CTn]
using wfx't' ⟨NormalAction (NewHeapElem ad CTn) ∈ set ⟦ta'⟧o⟩ by cases(auto dest: red-NewHeapElemD)
with hext have typeof-addr (shr σ') ad = ⊥ CTn] by(rule typeof-addr-hext-mono)
with type-adal have adal ∈ action-loc P E ?wa using wa-obs unfolding E adal
  by cases (auto simp add: action-obs-def lnth-lappend1 lift-start-obs-def)
moreover have is-new-action (action-obs E ?wa) using wa-obs by simp
ultimately have ?wa ∈ new-actions-for P E adal by(rule new-actionsI)
thus ?thesis using wa-ra by blast
qed
qed

lemma executions-sc-hb:
  assumes wfxsyscls P
  and ts-ok wfx (thr (start-state f P C M vs)) start-heap
  and known-addr start-tid (f (fst (method P C M)) M (fst (snd (method P C M))) (fst (snd (snd
(method P C M)))) (the (snd (snd (snd (method P C M)))))) vs) ⊆ allocated start-heap
  shows
    executions-sc-hb (ℰ-start f P C M vs status) P
    (is executions-sc-hb ?E P)
proof
  fix E a adal a'
  assume E ∈ ?E a ∈ new-actions-for P E adal a' ∈ new-actions-for P E adal
  thus a = a' by(rule ℰ-new-actions-for-unique)
next

```

```

fix E ra adal
assume E ∈ ?E ra ∈ read-actions E adal ∈ action-loc P E ra
  and non-speculative P (λ-. { }) (ltake (enat ra) (lmap snd E))
with assms show ∃ wa. wa ∈ new-actions-for P E adal ∧ wa < ra
  by(rule Ex-new-action-for)
qed

```

lemma *executions-aux*:

```

assumes wf: wf-syscls P
  and wfx-start: ts-ok wfx (thr (start-state f P C M vs)) start-heap (is ts-ok wfx (thr ?start-state) -)
  and ka: known-addr start-tid (f (fst (method P C M)) M (fst (snd (method P C M)))) (fst (snd
(snd (method P C M)))) (the (snd (snd (snd (method P C M)))))) vs ⊆ allocated start-heap
shows executions-aux (E-start f P C M vs status) P
  (is executions-aux ?E P)

```

proof

```

fix E a adal a'
assume E ∈ ?E a ∈ new-actions-for P E adal a' ∈ new-actions-for P E adal
thus a = a' by(rule E-new-actions-for-unique)

```

next

```

fix E ws r adal
assume E: E ∈ ?E
  and wf-exec: P ⊢ (E, ws) ✓
  and read: r ∈ read-actions E adal ∈ action-loc P E r
  and sc: ∧a. [a < r; a ∈ read-actions E] ⇒ P, E ⊢ a ∼mrw ws a

```

interpret *jmm*: *executions-sc-hb* ?E P

using wf wfx-start ka by(rule *executions-sc-hb*)

from E wf-exec sc

have *ta-seq-consist* P Map.empty (ltake (enat r) (lmap snd E))

unfolding *ltake-lmap* by(rule *jmm.ta-seq-consist-mrwI*) simp

hence non-speculative P (λ-. { }) (ltake (enat r) (lmap snd E))

by(rule *ta-seq-consist-into-non-speculative*) simp

with wf wfx-start ka E read

have ∃ i. i ∈ new-actions-for P E adal ∧ i < r

by(rule *Ex-new-action-for*)

thus ∃ i < r. i ∈ new-actions-for P E adal by blast

qed

lemma *drf*:

assumes *cut-and-update*:

*if.cut-and-update*

(init-fin-lift-state status (start-state f P C M vs))

(mrw-values P Map.empty (map snd (lift-start-obs start-tid start-heap-obs)))

(is *if.cut-and-update* ?start-state (mrw-values - - (map - ?start-heap-obs)))

and wf: wf-syscls P

and wfx-start: ts-ok wfx (thr (start-state f P C M vs)) start-heap

and ka: known-addr start-tid (f (fst (method P C M)) M (fst (snd (method P C M)))) (fst (snd
(snd (method P C M)))) (the (snd (snd (snd (method P C M)))))) vs ⊆ allocated start-heap

shows *drf* (E-start f P C M vs status) P (is *drf* ?E -)

proof –

interpret *jmm*: *executions-sc-hb* ?E P

using wf wfx-start ka by(rule *executions-sc-hb*)

```

let ?n = length ?start-heap-obs
let ?E' = lappend (llist-of ?start-heap-obs) ' mthr.if.ℰ ?start-state

show ?thesis
proof
  fix E ws r
  assume E: E ∈ ?E'
  and wf: P ⊢ (E, ws) ✓
  and mrw:  $\bigwedge a. \llbracket a < r; a \in \text{read-actions } E \rrbracket \implies P, E \vdash a \rightsquigarrow_{\text{mrw}} ws \ a$ 
  show  $\exists E' \in ?E'. \exists ws'. P \vdash (E', ws') \checkmark \wedge \text{ltake } (\text{enat } r) \ E = \text{ltake } (\text{enat } r) \ E' \wedge$ 
     $\text{sequentially-consistent } P \ (E', ws') \wedge$ 
     $\text{action-tid } E \ r = \text{action-tid } E' \ r \wedge \text{action-obs } E \ r \approx \text{action-obs } E' \ r \wedge$ 
     $(r \in \text{actions } E \longrightarrow r \in \text{actions } E')$ 
  proof(cases  $\exists r'. r' \in \text{read-actions } E \wedge r \leq r'$ )
    case False
    have sequentially-consistent P (E, ws)
    proof(rule sequentially-consistentI)
      fix a
      assume a ∈ read-actions E
      with False have a < r by auto
      thus P, E ⊢ a  $\rightsquigarrow_{\text{mrw}}$  ws a using ⟨a ∈ read-actions E⟩ by(rule mrw)
    qed
    moreover have action-obs E r ≈ action-obs E r by(rule sim-action-refl)
    ultimately show ?thesis using wf E by blast
  next
    case True
    let ?P =  $\lambda r'. r' \in \text{read-actions } E \wedge r \leq r'$ 
    let ?r = Least ?P
    from True obtain r' where r': ?P r' by blast
    hence r: ?P ?r by(rule LeastI)
    {
      fix a
      assume a < ?r a ∈ read-actions E
      have P, E ⊢ a  $\rightsquigarrow_{\text{mrw}}$  ws a
      proof(cases a < r)
        case True
        thus ?thesis using ⟨a ∈ read-actions E⟩ by(rule mrw)
      next
        case False
        with ⟨a ∈ read-actions E⟩ have ?P a by simp
        hence ?r ≤ a by(rule Least-le)
        with ⟨a < ?r⟩ have False by simp
        thus ?thesis ..
      qed }
    note mrw' = this

  from E obtain E'' where E: E = lappend (llist-of ?start-heap-obs) E''
  and E'': E'' ∈ mthr.if.ℰ ?start-state by auto

  from E'' obtain E' where E': E'' = lconcat (lmap (λ(t, ta). llist-of (map (Pair t)  $\llbracket ta \rrbracket_o$ )) E')
  and τRuns: mthr.if.mthr.Runs ?start-state E'
  by(rule mthr.if.ℰ.cases)

  have r-len: length ?start-heap-obs ≤ ?r

```

```

proof(rule ccontr)
  assume  $\neg ?thesis$ 
  hence  $?r < \text{length } ?start\text{-heap}\text{-obs}$  by simp
  moreover with  $r \ E$  obtain  $t \ ad \ al \ v$  where  $?start\text{-heap}\text{-obs} \ ! \ ?r = (t, \text{NormalAction } (\text{ReadMem } ad \ al \ v))$ 
  by(cases  $?start\text{-heap}\text{-obs} \ ! \ ?r$ )(fastforce elim!: read-actions.cases simp add: actions-def action-obs-def lnth-lappend1)
  ultimately have  $(t, \text{NormalAction } (\text{ReadMem } ad \ al \ v)) \in \text{set } ?start\text{-heap}\text{-obs}$  unfolding
  in-set-conv-nth by blast
  thus False by(auto simp add: start-heap-obs-not-Read)
qed
let  $?n = \text{length } ?start\text{-heap}\text{-obs}$ 
from  $r \ r\text{-len } E$  have  $r: ?r - ?n \in \text{read-actions } E''$ 
  by(fastforce elim!: read-actions.cases simp add: actions-lappend action-obs-def lnth-lappend2
  elim: actionsE intro: read-actions.intros)

from  $r$  have  $?r - ?n \in \text{actions } E''$  by(auto)
hence  $\text{enat } (?r - ?n) < \text{llength } E''$  by(rule actionsE)
with  $\tau\text{Runs}$  obtain  $r\text{-m } r\text{-n } t\text{-r } ta\text{-r}$ 
  where  $E\text{-r}: \text{lnth } E'' (?r - ?n) = (t\text{-r}, \llbracket ta\text{-r} \rrbracket_o \ ! \ r\text{-n})$ 
  and  $r\text{-n}: r\text{-n} < \text{length } \llbracket ta\text{-r} \rrbracket_o$  and  $r\text{-m}: \text{enat } r\text{-m} < \text{llength } E'$ 
  and  $r\text{-conv}: ?r - ?n = (\sum i < r\text{-m}. \text{length } \llbracket \text{snd } (\text{lnth } E' \ i) \rrbracket_o) + r\text{-n}$ 
  and  $E'\text{-r-m}: \text{lnth } E' \ r\text{-m} = (t\text{-r}, ta\text{-r})$ 
  unfolding  $E'$  by(rule mthr.if.actions- $\mathcal{E}E\text{-aux}$ )

let  $?E' = \text{ldropn } (\text{Suc } r\text{-m}) \ E'$ 
let  $?r\text{-m}\text{-}E' = \text{ltake } (\text{enat } r\text{-m}) \ E'$ 
have  $E'\text{-unfold}: E' = \text{lappend } (\text{ltake } (\text{enat } r\text{-m}) \ E') \ (LCons (\text{lnth } E' \ r\text{-m}) \ ?E')$ 
  unfolding  $\text{ldropn}\text{-}\text{Suc}\text{-conv}\text{-ldropn}[OF \ r\text{-m}]$  by simp
hence  $\text{mthr.if.mthr.Runs } ?start\text{-state } (\text{lappend } ?r\text{-m}\text{-}E' \ (LCons (\text{lnth } E' \ r\text{-m}) \ ?E'))$ 
  using  $\tau\text{Runs}$  by simp
then obtain  $\sigma'$  where  $\sigma\text{-}\sigma': \text{mthr.if.mthr.Trsys } ?start\text{-state } (\text{list-of } ?r\text{-m}\text{-}E') \ \sigma'$ 
  and  $\tau\text{Runs}': \text{mthr.if.mthr.Runs } \sigma' \ (LCons (\text{lnth } E' \ r\text{-m}) \ ?E')$ 
  by(rule mthr.if.mthr.Runs-lappendE) simp
from  $\tau\text{Runs}'$  obtain  $\sigma'''$  where  $\text{red-ra}: \text{mthr.if.redT } \sigma' \ (t\text{-r}, ta\text{-r}) \ \sigma'''$ 
  and  $\tau\text{Runs}'': \text{mthr.if.mthr.Runs } \sigma''' \ ?E'$ 
  unfolding  $E'\text{-r-m}$  by cases

let  $?vs = \text{mrw-values } P \ \text{Map.empty } (\text{map } \text{snd } ?start\text{-heap}\text{-obs})$ 
{ fix  $a$ 
  assume  $\text{enat } a < \text{enat } ?r$ 
  and  $a \in \text{read-actions } E$ 
  have  $a < r$ 
  proof(rule ccontr)
    assume  $\neg a < r$ 
    with  $\langle a \in \text{read-actions } E \rangle$  have  $?P \ a$  by simp
    hence  $?r \leq a$  by(rule Least-le)
    with  $\langle \text{enat } a < \text{enat } ?r \rangle$  show False by simp
  qed
  hence  $P, E \vdash a \rightsquigarrow_{\text{mrw}} \text{ws } a$  using  $\langle a \in \text{read-actions } E \rangle$  by(rule mrw) }
with  $\langle E \in ?\mathcal{E}' \rangle \text{ wf}$  have  $\text{ta-seq-consist } P \ \text{Map.empty } (\text{lmap } \text{snd } (\text{ltake } (\text{enat } ?r) \ E))$ 
  by(rule jmm.ta-seq-consist-mrwI)

hence  $\text{start-sc}: \text{ta-seq-consist } P \ \text{Map.empty } (\text{lmap } \text{snd } ?start\text{-heap}\text{-obs})$ 

```

**and**  $ta\text{-seq-consist } P \text{ ?vs } (lmap \text{ snd } (ltake \text{ (enat } (?r - ?n)) E''))$   
**using**  $\langle ?n \leq ?r \rangle$  **unfolding**  $E \text{ ltake-lappend lmap-lappend-distrib}$   
**by** ( $simp\text{-all add: ta-seq-consist-lappend o-def}$ )

**note**  $this(2)$  **also from**  $r\text{-m}$   
**have**  $r\text{-m-sum-len-eq: } (\sum i < r\text{-m. llength } (lnth \text{ (lmap } (\lambda(t, ta). llist\text{-of } (map \text{ (Pair } t) \llbracket ta \rrbracket_o)) E')$   
 $i)) = \text{enat } (\sum i < r\text{-m. length } \llbracket \text{snd } (lnth E' i) \rrbracket_o)$   
**by** ( $subst \text{ sum-comp-morphism[symmetric, where } h = \text{enat}])$  ( $auto \text{ simp add: zero-enat-def split-def}$   
 $less\text{-trans[where } y = \text{enat } r\text{-m] intro: sum.cong}$ )  
**hence**  $ltake \text{ (enat } (?r - ?n)) E'' =$   
 $lappend \text{ (lconcat (lmap } (\lambda(t, ta). llist\text{-of } (map \text{ (Pair } t) \llbracket ta \rrbracket_o)) ?r\text{-m-E'})}$   
 $(ltake \text{ (enat } r\text{-n) (ldrop \text{ (enat } (\sum i < r\text{-m. length } \llbracket \text{snd } (lnth E' i) \rrbracket_o)) E')) E')$   
**unfolding**  $ltake\text{-lmap[symmetric] lconcat-ltake r-conv plus-enat-simps(1)[symmetric] ltake-plus-conv-lappend}$   
**unfolding**  $E'$  **by**  $simp$   
**finally have**  $ta\text{-seq-consist } P \text{ ?vs } (lmap \text{ snd } (lconcat \text{ (lmap } (\lambda(t, ta). llist\text{-of } (map \text{ (Pair } t) \llbracket ta \rrbracket_o))$   
 $?r\text{-m-E'}))$   
**and**  $sc\text{-ta-r: } ta\text{-seq-consist } P \text{ (mrw-values } P \text{ ?vs } (map \text{ snd } (list\text{-of } (lconcat \text{ (lmap } (\lambda(t, ta). llist\text{-of } (map \text{ (Pair } t) \llbracket ta \rrbracket_o)) ?r\text{-m-E'}))))$   
 $(lmap \text{ snd } (ltake \text{ (enat } r\text{-n) (ldropn } (\sum i < r\text{-m. length } \llbracket \text{snd } (lnth E' i) \rrbracket_o) E')) E'))$   
**unfolding**  $lmap\text{-lappend-distrib by}(simp\text{-all add: ta-seq-consist-lappend split-def ldrop-enat})$   
**note**  $this(1)$  **also**  
**have**  $lmap \text{ snd } (lconcat \text{ (lmap } (\lambda(t, ta). llist\text{-of } (map \text{ (Pair } t) \llbracket ta \rrbracket_o)) (ltake \text{ (enat } r\text{-m) } E'))$   
 $= llist\text{-of } (concat \text{ (map } (\lambda(t, ta). \llbracket ta \rrbracket_o) (list\text{-of } ?r\text{-m-E'})))$   
**unfolding**  $lmap\text{-lconcat llist.map-comp o-def split-def lconcat-llist-of[symmetric] map-map}$   
 $lmap\text{-llist-of[symmetric]}$   
**by**  $simp$   
**finally have**  $ta\text{-seq-consist } P \text{ ?vs } (llist\text{-of } (concat \text{ (map } (\lambda(t, ta). \llbracket ta \rrbracket_o) (list\text{-of } ?r\text{-m-E'}))))$  .  
**from**  $if.\text{sequential-completion[OF cut-and-update ta-seq-consist-convert-RA } \sigma\text{-}\sigma' \text{[folded mthr.if.RedT-def]}$   
 $this \text{ red-ra}]$   
**obtain**  $ta' \text{ ttas'}$   
**where**  $mthr.\text{if.mthr.Runs } \sigma' \text{ (LCons } (t\text{-r, } ta') \text{ ttas'})$   
**and**  $sc: ta\text{-seq-consist } P \text{ (mrw-values } P \text{ Map.empty (map snd ?start-heap-obs))}$   
 $(lconcat \text{ (lmap } (\lambda(t, ta). llist\text{-of } \llbracket ta \rrbracket_o) (lappend \text{ (llist\text{-of } (list\text{-of } ?r\text{-m-E')) (LCons}$   
 $(t\text{-r, } ta') \text{ ttas'))}))$   
**and**  $eq\text{-ta: } eq\text{-upto-seq-inconsist } P \llbracket ta\text{-r} \rrbracket_o \llbracket ta' \rrbracket_o \text{ (mrw-values } P \text{ ?vs } (concat \text{ (map } (\lambda(t, ta). \llbracket ta \rrbracket_o) (list\text{-of } ?r\text{-m-E'))}))$   
**by**  $blast$

**let**  $?E\text{-sc}' = lappend \text{ (llist\text{-of } (list\text{-of } ?r\text{-m-E')) (LCons } (t\text{-r, } ta') \text{ ttas'})$   
**let**  $?E\text{-sc}'' = lconcat \text{ (lmap } (\lambda(t, ta). llist\text{-of } (map \text{ (Pair } t) \llbracket ta \rrbracket_o)) ?E\text{-sc}')$   
**let**  $?E\text{-sc} = lappend \text{ (llist\text{-of } ?start\text{-heap-obs}) } ?E\text{-sc}''$

**from**  $\sigma\text{-}\sigma' \langle mthr.\text{if.mthr.Runs } \sigma' \text{ (LCons } (t\text{-r, } ta') \text{ ttas'}) \rangle$   
**have**  $mthr.\text{if.mthr.Runs } ?start\text{-state } ?E\text{-sc}'$  **by** ( $rule \text{ mthr.if.mthr.Trsys-into-Runs}$ )  
**hence**  $?E\text{-sc}'' \in mthr.\text{if.}\mathcal{E} \text{ ?start-state}$  **by** ( $rule \text{ mthr.if.}\mathcal{E}.\text{intros}$ )  
**hence**  $?E\text{-sc} \in ?\mathcal{E}$  **by** ( $rule \text{ imageI}$ )  
**moreover from**  $\langle ?E\text{-sc}'' \in mthr.\text{if.}\mathcal{E} \text{ ?start-state} \rangle$   
**have**  $tsa\text{-ok: thread-start-actions-ok } ?E\text{-sc}$  **by** ( $rule \text{ thread-start-actions-ok-init-fin}$ )

**from**  $sc$  **have**  $ta\text{-seq-consist } P \text{ Map.empty (lmap snd } ?E\text{-sc})$   
**by** ( $simp \text{ add: lmap-lappend-distrib o-def lmap-lconcat llist.map-comp split-def ta-seq-consist-lappend}$   
 $start\text{-sc}$ )  
**from**  $ta\text{-seq-consist-imp-sequentially-consistent[OF tsa-ok jmm.}\mathcal{E}\text{-new-actions-for-fun[OF } \langle ?E\text{-sc} \in ?\mathcal{E} \rangle]$   $this]$

**obtain** *ws-sc* **where** *sequentially-consistent P* (*?E-sc*, *ws-sc*)  
**and**  $P \vdash (\text{?E-sc}, \text{ws-sc}) \checkmark$  **unfolding** *start-heap-obs-def[symmetric]* **by** *iprover*  
**moreover** {  
   **have** *enat-sum-r-m-eq*: *enat* ( $\sum i < r\text{-m. length } \llbracket \text{snd } (\text{lnth } E' i) \rrbracket_o = \text{llength } (\text{lconcat } (\text{lmap } (\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \llbracket ta \rrbracket_o)) \text{?r-m-E'}))$ )  
   **by**(*auto intro: sum.cong simp add: less-trans[OF - r-m] lnth-ltake llength-lconcat-lfinite-conv-sum sum-comp-morphism[symmetric, where h=enat] zero-enat-def[symmetric] split-beta*)  
   **also have**  $\dots \leq \text{llength } E''$  **unfolding** *E'*  
   **by**(*blast intro: lprefix-llength-le lprefix-lconcatI lmap-lprefix*)  
   **finally have** *r-m-E*: *ltake* (*enat* ( $\text{?n} + (\sum i < r\text{-m. length } \llbracket \text{snd } (\text{lnth } E' i) \rrbracket_o)$ )) *E* = *ltake* (*enat* ( $\text{?n} + (\sum i < r\text{-m. length } \llbracket \text{snd } (\text{lnth } E' i) \rrbracket_o)$ )) *?E-sc*  
   **by**(*simp add: ltake-lappend lappend-eq-lappend-conv lmap-lappend-distrib r-m-sum-len-eq ltake-lmap[symmetric] min-def zero-enat-def[symmetric] E E' lconcat-ltake ltake-all del: ltake-lmap*)  
  
   **have** *drop-r-m-E*: *ldropn* ( $\text{?n} + (\sum i < r\text{-m. length } \llbracket \text{snd } (\text{lnth } E' i) \rrbracket_o)$ ) *E* = *lappend* (*llist-of* (*map* (*Pair t-r*)  $\llbracket ta\text{-r} \rrbracket_o$ )) (*lconcat* (*lmap* ( $\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \llbracket ta \rrbracket_o)$ )) (*ldropn* (*Suc r-m*) *E'*)))  
   (*is* - = *?drop-r-m-E*) **using** *E'-r-m* **unfolding** *E E'*  
   **by**(*subst (2) E'-unfold*)(*simp add: ldropn-lappend2 lmap-lappend-distrib enat-sum-r-m-eq[symmetric]*)  
  
   **have** *drop-r-m-E-sc*: *ldropn* ( $\text{?n} + (\sum i < r\text{-m. length } \llbracket \text{snd } (\text{lnth } E' i) \rrbracket_o)$ ) *?E-sc* =  
     *lappend* (*llist-of* (*map* (*Pair t-r*)  $\llbracket ta \rrbracket_o$ )) (*lconcat* (*lmap* ( $\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \llbracket ta \rrbracket_o)$ )) *ttas'*))  
   **by**(*simp add: ldropn-lappend2 lmap-lappend-distrib enat-sum-r-m-eq[symmetric]*)  
  
   **let** *?vs-r-m* = *mrw-values P ?vs* (*map snd* (*list-of* (*lconcat* (*lmap* ( $\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t) \llbracket ta \rrbracket_o)$ )) *?r-m-E'*))))  
   **note** *sc-ta-r* **also**  
   **from** *drop-r-m-E* **have** *ldropn* ( $\sum i < r\text{-m. length } \llbracket \text{snd } (\text{lnth } E' i) \rrbracket_o$ ) *E''* = *?drop-r-m-E*  
   **unfolding** *E* **by**(*simp add: ldropn-lappend2*)  
   **also have** *lmap snd* (*ltake* (*enat r-n*)  $\dots$ ) = *llist-of* (*take r-n*  $\llbracket ta\text{-r} \rrbracket_o$ ) **using** *r-n*  
   **by**(*simp add: ltake-lappend lmap-lappend-distrib ltake-lmap[symmetric] take-map o-def zero-enat-def[symmetric] del: ltake-lmap*)  
   **finally have** *sc-ta-r*: *ta-seq-consist P ?vs-r-m* (*llist-of* (*take r-n*  $\llbracket ta\text{-r} \rrbracket_o$ )) .  
   **note** *eq-ta*  
   **also have**  $\llbracket ta\text{-r} \rrbracket_o = \text{take } r\text{-n } \llbracket ta\text{-r} \rrbracket_o @ \text{drop } r\text{-n } \llbracket ta\text{-r} \rrbracket_o$  **by** *simp*  
   **finally have** *eq-upto-seq-inconsist P* (*take r-n*  $\llbracket ta\text{-r} \rrbracket_o @ \text{drop } r\text{-n } \llbracket ta\text{-r} \rrbracket_o$ )  $\llbracket ta' \rrbracket_o$  *?vs-r-m*  
   **by**(*simp add: list-of-lconcat split-def o-def map-concat*)  
   **from** *eq-upto-seq-inconsist-appendD[OF this sc-ta-r]*  
   **have** *r-n'*: *r-n*  $\leq \text{length } \llbracket ta' \rrbracket_o$   
   **and** *take-r-n-eq*: *take r-n*  $\llbracket ta' \rrbracket_o = \text{take } r\text{-n } \llbracket ta\text{-r} \rrbracket_o$   
   **and** *eq-r-n*: *eq-upto-seq-inconsist P* (*drop r-n*  $\llbracket ta\text{-r} \rrbracket_o$ ) (*drop r-n*  $\llbracket ta' \rrbracket_o$ ) (*mrw-values P ?vs-r-m* (*take r-n*  $\llbracket ta\text{-r} \rrbracket_o$ ))  
   **using** *r-n* **by**(*simp-all add: min-def*)  
   **from** *r-conv* ( $\text{?n} \leq \text{?r}$ ) **have** *r-conv'*:  $\text{?r} = (\text{?n} + (\sum i < r\text{-m. length } \llbracket \text{snd } (\text{lnth } E' i) \rrbracket_o)) + r\text{-n}$   
**by** *simp*  
   **from** *r-n' r-n take-r-n-eq r-m-E drop-r-m-E drop-r-m-E-sc*  
   **have** *take-r'-eq*: *ltake* (*enat ?r*) *E* = *ltake* (*enat ?r*) *?E-sc* **unfolding** *r-conv'*  
   **apply**(*subst (1 2) plus-enat-simps(1)[symmetric]*)  
   **apply**(*subst (1 2) ltake-plus-conv-lappend*)  
   **apply**(*simp add: lappend-eq-lappend-conv ltake-lappend1 ldrop-enat take-map*)  
   **done**  
   **hence** *take-r-eq*: *ltake* (*enat r*) *E* = *ltake* (*enat r*) *?E-sc*  
   **by**(*rule ltake-eq-ltake-antimono*)(*simp add: <?P ?r>*)

```

from eq-r-n Cons-nth-drop-Suc[OF r-n, symmetric]
have drop r-n  $\llbracket ta' \rrbracket_o \neq []$  by(auto simp add: eq-upto-seq-inconsist-simps)
hence r-n': r-n < length  $\llbracket ta' \rrbracket_o$  by simp
hence eq-r-n:  $\llbracket ta-r \rrbracket_o ! r-n \approx \llbracket ta' \rrbracket_o ! r-n$ 
  using eq-r-n Cons-nth-drop-Suc[OF r-n, symmetric] Cons-nth-drop-Suc[OF r-n', symmetric]
by(simp add: eq-upto-seq-inconsist-simps split: action.split-asm obs-event.split-asm if-split-asm)
obtain tid-eq: action-tid E r = action-tid ?E-sc r
  and obs-eq: action-obs E r  $\approx$  action-obs ?E-sc r
proof(cases r < ?r)
  case True
  { from True have action-tid E r = action-tid (ltake (enat ?r) E) r
    by(simp add: action-tid-def lnth-ltake)
    also note take-r'-eq
    also have action-tid (ltake (enat ?r) ?E-sc) r = action-tid ?E-sc r
      using True by(simp add: action-tid-def lnth-ltake)
    finally have action-tid E r = action-tid ?E-sc r . }
  moreover
  { from True have action-obs E r = action-obs (ltake (enat ?r) E) r
    by(simp add: action-obs-def lnth-ltake)
    also note take-r'-eq
    also have action-obs (ltake (enat ?r) ?E-sc) r = action-obs ?E-sc r
      using True by(simp add: action-obs-def lnth-ltake)
    finally have action-obs E r  $\approx$  action-obs ?E-sc r by simp }
  ultimately show thesis by(rule that)
next
  case False
  with  $\langle ?P ?r \rangle$  have r-eq: r = ?r by simp
  hence lnth E r = (t-r,  $\llbracket ta-r \rrbracket_o ! r-n$ ) using E-r r-conv' E by(simp add: lnth-lappend2)
  moreover have lnth ?E-sc r = (t-r,  $\llbracket ta' \rrbracket_o ! r-n$ ) using  $\langle ?n \leq ?r \rangle$  r-n'
  by(subst r-eq)(simp add: r-conv lnth-lappend2 lmap-lappend-distrib enat-sum-r-m-eq[symmetric]
    lnth-lappend1 del: length-lift-start-obs)
  ultimately have action-tid E r = action-tid ?E-sc r action-obs E r  $\approx$  action-obs ?E-sc r
    using eq-r-n by(simp-all add: action-tid-def action-obs-def)
  thus thesis by(rule that)
qed

  have enat r < enat ?n + llength (lconcat (lmap ( $\lambda(t, ta).$  llist-of (map (Pair t)  $\llbracket ta \rrbracket_o$ )) (lappend
    ?r-m-E' (LCons (t-r, ta') LNil))))
    using  $\langle ?P ?r \rangle$  r-n' unfolding lmap-lappend-distrib
    by(simp add: enat-sum-r-m-eq[symmetric] r-conv')
  also have llength (lconcat (lmap ( $\lambda(t, ta).$  llist-of (map (Pair t)  $\llbracket ta \rrbracket_o$ )) (lappend ?r-m-E'
    (LCons (t-r, ta') LNil))))  $\leq$  llength ?E-sc''
    by(rule lprefix-llength-le[OF lprefix-lconcatI])(simp add: lmap-lprefix)
  finally have r  $\in$  actions ?E-sc by(simp add: actions-def add-left-mono)
  note this tid-eq obs-eq take-r-eq }
  ultimately show ?thesis by blast
qed
qed(rule  $\mathcal{E}$ -new-actions-for-unique)
qed

```

**lemma** sc-legal:

**assumes** hb-completion:

if.hb-completion (init-fin-lift-state status (start-state f P C M vs)) (lift-start-obs start-tid start-heap-obs)





```

from start-heap-obs-not-Read
have ws: is-write-seen P (llist-of (lift-start-obs start-tid start-heap-obs)) ws
by(unfold in-set-conv-nth)(rule is-write-seenI, auto simp add: action-obs-def actions-def lift-start-obs-def
lnth-LCons elim!: read-actions.cases split: nat.split-asm)

with hb tsa
have  $\exists ws'. P \vdash (?E, ws') \checkmark \wedge$ 
 $(\forall n. n \in \text{read-actions } ?E \longrightarrow \text{length } ?\text{start-heap-obs} \leq n \longrightarrow P, ?E \vdash ws' n \leq_{hb} n) \wedge$ 
 $(\forall n < \text{length } ?\text{start-heap-obs}. ws' n = ws n)$ 
by(rule ta-hb-consistent-Read-hb)(rule jmm.E-new-actions-for-fun[OF E'])
then obtain ws' where wf-exec':  $P \vdash (?E, ws') \checkmark$ 
and read-hb:  $\bigwedge n. \llbracket n \in \text{read-actions } ?E; \text{length } ?\text{start-heap-obs} \leq n \rrbracket \Longrightarrow P, ?E \vdash ws' n \leq_{hb} n$ 
and same:  $\bigwedge n. n < \text{length } ?\text{start-heap-obs} \Longrightarrow ws' n = ws n$  by blast

from True have ?same ?E unfolding E by(simp add: ltake-lappend1)
moreover {
  fix a
  assume a:  $a \in \text{read-actions } ?E$ 
  have if  $a < r$  then  $ws' a = ws a$  else  $P, ?E \vdash ws' a \leq_{hb} a$ 
  proof(cases a < length ?start-heap-obs)
    case True
    with a have False using start-heap-obs-not-Read
    by cases(auto simp add: action-obs-def actions-def lnth-lappend1 lift-start-obs-def lnth-LCons
in-set-conv-nth split: nat.split-asm)
    thus ?thesis ..
  next
  case False
  with read-hb[of a] True a show ?thesis by auto
qed }
hence ?read ?E ws' by blast
moreover from True E have ?tid ?E by(simp add: action-tid-def lnth-lappend1)
moreover from True E have ?obs ?E by(simp add: action-obs-def lnth-lappend1)
moreover from True have ?actions ?E by(simp add: actions-def enat-less-enat-plusI)
ultimately show ?thesis using E' wf-exec' by blast
next
case False
hence r:  $\text{length } ?\text{start-heap-obs} \leq r$  by simp

show ?thesis
proof(cases enat r < llength E)
  case False
  then obtain ?same E ?read E ws ?tid E ?obs E ?actions E
  by(cases llength E)(fastforce elim!: read-actions.cases simp add: actions-def split: if-split-asm)+
  with wf-exec  $\langle E \in ?\mathcal{E} \rangle$  show ?thesis by blast
next
case True
note  $r' = \text{this}$ 

let  $?r = r - \text{length } ?\text{start-heap-obs}$ 
from E r r' have enat  $?r < \text{llength } E''$  by(cases llength E'')(auto)
with  $\tau\text{Runs}$  obtain r-m r-n t-r ta-r
  where E-r:  $\text{lnth } E'' ?r = (t-r, \llbracket ta-r \rrbracket_o ! r-n)$ 
  and r-n:  $r-n < \text{length } \llbracket ta-r \rrbracket_o$  and r-m: enat  $r-m < \text{llength } E'$ 
  and r-conv:  $?r = (\sum i < r-m. \text{length } \llbracket \text{snd } (\text{lnth } E' i) \rrbracket_o) + r-n$ 

```

**and**  $E'-r-m$ :  $\text{lnth } E' \ r-m = (t-r, ta-r)$   
**unfolding**  $E'$  **by**  $(\text{rule } mthr.if.actions-\mathcal{E}E-aux)$

**let**  $?E' = \text{ldropn } (Suc \ r-m) \ E'$   
**let**  $?r-m-E' = \text{ltake } (enat \ r-m) \ E'$   
**have**  $E'-\text{unfold}$ :  $E' = \text{lappend } (\text{ltake } (enat \ r-m) \ E') \ (LCons \ (\text{lnth } E' \ r-m) \ ?E')$   
**unfolding**  $\text{ldropn-Suc-conv-ldropn}[OF \ r-m]$  **by**  $\text{simp}$   
**hence**  $mthr.if.mthr.Runs \ ?start-state \ (\text{lappend } ?r-m-E' \ (LCons \ (\text{lnth } E' \ r-m) \ ?E'))$   
**using**  $\tauRuns$  **by**  $\text{simp}$   
**then obtain**  $\sigma'$  **where**  $\sigma-\sigma'$ :  $mthr.if.mthr.Trsys \ ?start-state \ (\text{list-of } ?r-m-E') \ \sigma'$   
**and**  $\tauRuns'$ :  $mthr.if.mthr.Runs \ \sigma' \ (LCons \ (\text{lnth } E' \ r-m) \ ?E')$   
**by**  $(\text{rule } mthr.if.mthr.Runs-lappendE) \ \text{simp}$   
**from**  $\tauRuns'$  **obtain**  $\sigma'''$  **where**  $\text{red-ra}$ :  $mthr.if.redT \ \sigma' \ (t-r, ta-r) \ \sigma'''$   
**and**  $\tauRuns''$ :  $mthr.if.mthr.Runs \ \sigma''' \ ?E'$   
**unfolding**  $E'-r-m$  **by**  $\text{cases}$

**let**  $?vs = \text{mrw-values } P \ \text{Map.empty} \ (\text{map } \text{snd} \ ?start\text{-heap-obs})$   
**from**  $\langle E \in ?\mathcal{E} \rangle \ \text{wf-exec}$  **have**  $ta\text{-seq-consist } P \ \text{Map.empty} \ (\text{lmap } \text{snd} \ (\text{ltake } (enat \ r) \ E))$   
**by**  $(\text{rule } jmm.ta\text{-seq-consist-mrwI})(\text{simp add: mrw})$   
**hence**  $ns$ :  $\text{non-speculative } P \ (\lambda-. \ \{\}) \ (\text{lmap } \text{snd} \ (\text{ltake } (enat \ r) \ E))$   
**by**  $(\text{rule } ta\text{-seq-consist-into-non-speculative}) \ \text{simp}$   
**also note**  $E$  **also note**  $\text{ltake-lappend2}$  **also note**  $E'$   
**also note**  $E'-\text{unfold}$  **also note**  $\text{lmap-lappend-distrib}$  **also note**  $\text{lmap-lappend-distrib}$   
**also note**  $\text{lconcat-lappend}$  **also note**  $\text{lmap.map}(2)$  **also note**  $E'-r-m$  **also note**  $\text{prod.simps}(2)$   
**also note**  $\text{ltake-lappend2}$  **also note**  $\text{lconcat-LCons}$  **also note**  $\text{ltake-lappend1}$   
**also note**  $\text{non-speculative-lappend}$  **also note**  $\text{lmap-lappend-distrib}$  **also note**  $\text{non-speculative-lappend}$   
**also have**  $\text{lconcat } (\text{lmap } (\lambda(t, ta). \ \text{lmap.map}(2) \ (\text{map } (Pair \ t) \ \{\!\!|ta|\!\!|_o\})) \ (\text{ltake } (enat \ r-m) \ E')) =$   
 $\text{lmap.map}(2) \ (\text{concat } (\text{map } (\lambda(t, ta). \ \text{map } (Pair \ t) \ \{\!\!|ta|\!\!|_o\}) \ (\text{list-of } (\text{ltake } (enat \ r-m) \ E'))))$   
**by**  $(\text{simp add: lconcat-lmap.map}[symmetric] \ \text{lmap-lmap.map}[symmetric] \ \text{lmap.map-comp} \ o\text{-def split-def})$   
 $\text{del: lmap-lmap.map}$   
**ultimately**  
**have**  $\text{non-speculative } P \ (\lambda-. \ \{\}) \ (\text{lmap } \text{snd} \ (\text{lmap.map}(2) \ ?start\text{-heap-obs}))$   
**and**  $\text{non-speculative } P \ (w\text{-values } P \ (\lambda-. \ \{\}) \ (\text{map } \text{snd} \ ?start\text{-heap-obs}))$   
 $(\text{lmap } \text{snd} \ (\text{lconcat } (\text{lmap } (\lambda(t, ta). \ \text{lmap.map}(2) \ (\text{map } (Pair \ t) \ \{\!\!|ta|\!\!|_o\})) \ (\text{ltake } (enat \ r-m) \ E'))))$   
**and**  $ns'$ :  $\text{non-speculative } P \ (w\text{-values } P \ (w\text{-values } P \ (\lambda-. \ \{\}) \ (\text{map } \text{snd} \ ?start\text{-heap-obs})) \ (\text{map } \text{snd} \ (\text{concat } (\text{map } (\lambda(t, ta). \ \text{map } (Pair \ t) \ \{\!\!|ta|\!\!|_o\}) \ (\text{list-of } (\text{ltake } (enat \ r-m) \ E'))))$   
 $(\text{lmap } \text{snd} \ (\text{ltake } (enat \ r-m) \ (\text{lmap.map}(2) \ (\text{map } (Pair \ t-r) \ \{\!\!|ta-r|\!\!|_o\}))))$   
**using**  $r \ r\text{-conv} \ r-m \ r-n$   
**by**  $(\text{simp-all add: length-concat } o\text{-def split-def } \text{sum-list-sum-nth } \text{length-list-of-conv-the-enat less-min-eq1 } \text{atLeast0LessThan } \text{lnth-ltake split: if-split-asm } \text{cong: sum.cong-simp})$   
**hence**  $ns$ :  $\text{non-speculative } P \ (w\text{-values } P \ (\lambda-. \ \{\}) \ (\text{map } \text{snd} \ ?start\text{-heap-obs}))$   
 $(\text{lmap.map}(2) \ (\text{concat } (\text{map } (\lambda(t, ta). \ \{\!\!|ta|\!\!|_o\}) \ (\text{list-of } (\text{ltake } (enat \ r-m) \ E'))))$   
**unfolding**  $\text{lconcat-lmap.map}[symmetric] \ \text{lmap-lconcat} \ \text{lmap-lmap.map}[symmetric] \ \text{lmap.map-comp}$   
 $o\text{-def split-def}$   
**by**  $(\text{simp})$

**from**  $ns'$   
**have**  $ns'$ :  $\text{non-speculative } P \ (w\text{-values } P \ (w\text{-values } P \ (\lambda-. \ \{\}) \ (\text{map } \text{snd} \ ?start\text{-heap-obs})) \ (\text{concat } (\text{map } (\lambda(t, ta). \ \{\!\!|ta|\!\!|_o\}) \ (\text{list-of } (\text{ltake } (enat \ r-m) \ E')))) \ (\text{lmap.map}(2) \ (\text{take } r-n \ \{\!\!|ta-r|\!\!|_o\}))$   
**unfolding**  $\text{map-concat} \ \text{map-map}$  **by**  $(\text{simp add: take-map}[symmetric] \ o\text{-def split-def})$

**let**  $?hb = \lambda ta'-r :: ('addr, 'thread-id, status \times 'x, 'heap, 'addr, ('addr, 'thread-id) \text{obs-event action}) \ \text{thread-action}.$   
 $ta\text{-hb-consistent } P \ (?start\text{-heap-obs} \ @ \ \text{concat } (\text{map } (\lambda(t, ta). \ \text{map } (Pair \ t) \ \{\!\!|ta|\!\!|_o\}) \ (\text{list-of } (\text{ltake } (enat \ r-m) \ E'))))$

(*enat* *r-m*) *E'*))) @ *map* (*Pair* *t-r*) (*take* *r-n*  $\llbracket ta-r \rrbracket_o$ )) (*l*list-of (*map* (*Pair* *t-r*) (*drop* *r-n*  $\llbracket ta'-r \rrbracket_o$ ))))  
**let** ?*sim* =  $\lambda ta'-r. (if \exists ad \text{ al } v. \llbracket ta-r \rrbracket_o ! r-n = \text{NormalAction} (\text{ReadMem } ad \text{ al } v) \text{ then}$   
*sim-action* *else* (=)) ( $\llbracket ta-r \rrbracket_o ! r-n$ ) ( $\llbracket ta'-r \rrbracket_o ! r-n$ )

**from** *red-ra* **obtain** *ta'-r*  $\sigma''''$   
**where** *red-ra'*: *mt*hr.*if*.*redT*  $\sigma'$  (*t-r*, *ta'-r*)  $\sigma''''$   
**and** *eq*: *take* *r-n*  $\llbracket ta'-r \rrbracket_o = \text{take } r-n \llbracket ta-r \rrbracket_o$   
**and** *hb*: ?*hb* *ta'-r*  
**and** *r-n'*: *r-n* < *length*  $\llbracket ta'-r \rrbracket_o$   
**and** *sim*: ?*sim* *ta'-r*  
**proof**(*cases*)  
**case** (*redT-normal* *x* *x'* *m'*)  
**note** *tst* =  $\langle \text{thr } \sigma' \text{ } t-r = \lfloor (x, \text{no-wait-locks}) \rfloor \rangle$   
**and** *red* =  $\langle t-r \vdash (x, \text{shr } \sigma') -ta-r \rightarrow i (x', m') \rangle$   
**and** *aok* =  $\langle \text{mt} \text{hr.} \text{if.} \text{actions-ok } \sigma' \text{ } t-r \text{ } ta-r \rangle$   
**and**  $\sigma''' = \langle \text{redT-upd } \sigma' \text{ } t-r \text{ } ta-r \text{ } x' \text{ } m' \text{ } \sigma''' \rangle$   
**from** *if.hb-completionD*[*OF* *hb-completion*  $\sigma-\sigma'$ [*folded* *mt*hr.*if*.*RedT-def*] *ns* *tst* *red* *aok* *ns'*]  
*r-n*  
**obtain** *ta'-r* *x''* *m''*  
**where** *red'*: *t-r*  $\vdash (x, \text{shr } \sigma') -ta'-r \rightarrow i (x'', m'')$   
**and** *aok'*: *mt*hr.*if*.*actions-ok*  $\sigma' \text{ } t-r \text{ } ta'-r$   
**and** *eq'*: *take* *r-n*  $\llbracket ta'-r \rrbracket_o = \text{take } r-n \llbracket ta-r \rrbracket_o$   
**and** *hb*: ?*hb* *ta'-r*  
**and** *r-n'*: *r-n* < *length*  $\llbracket ta'-r \rrbracket_o$   
**and** *sim*: ?*sim* *ta'-r* **by** *blast*  
**from** *redT-updWs-total*[*of* *t-r* *wset*  $\sigma' \llbracket ta'-r \rrbracket_w$ ]  
**obtain**  $\sigma''''$  **where** *redT-upd*  $\sigma' \text{ } t-r \text{ } ta'-r \text{ } x'' \text{ } m'' \text{ } \sigma''''$  **by** *fastforce*  
**with** *red'* *tst* *aok'* **have** *mt*hr.*if*.*redT*  $\sigma' (t-r, ta'-r) \sigma''''$  ..  
**thus** *thesis* **using** *eq'* *hb* *r-n'* *sim* **by**(*rule* *that*)  
**next**  
**case** (*redT-acquire* *x* *n* *ln*)  
**hence** ?*hb* *ta-r* **using** *set-convert-RA-not-Read*[**where** *ln=ln*]  
**by**  $-(\text{rule } ta\text{-hb-consistent-not-ReadI, fastforce simp del: set-convert-RA-not-Read dest!:$   
*in-set-dropD*)  
**with** *red-ra* *r-n* **show** ?*thesis* **by**(*auto* *intro*: *that*)  
**qed**  
**from** *hb*  
**have** *non-speculative* *P* (*w-values* *P* ( $\lambda-. \{\}$ ) (*map* *snd* (?*start-heap-obs* @ *concat* (*map* ( $\lambda(t, ta). \text{map} (\text{Pair } t) \llbracket ta \rrbracket_o$ ) (*list-of* (*l*take (*enat* *r-m*) *E'*))) @ *map* (*Pair* *t-r*) (*take* *r-n*  $\llbracket ta-r \rrbracket_o$ )))) (*l*map *snd* (*l*list-of (*map* (*Pair* *t-r*) (*drop* *r-n*  $\llbracket ta'-r \rrbracket_o$ ))))))  
**by**(*rule* *ta-hb-consistent-into-non-speculative*)  
**with** *ns'* *eq*[*symmetric*] **have** *non-speculative* *P* (*w-values* *P* ( $\lambda-. \{\}$ ) (*map* *snd* (?*start-heap-obs* @ *concat* (*map* ( $\lambda(t, ta). \text{map} (\text{Pair } t) \llbracket ta \rrbracket_o$ ) (*list-of* (*l*take (*enat* *r-m*) *E'*)))))) (*l*list-of (*map* *snd* (*map* (*Pair* *t-r*)  $\llbracket ta'-r \rrbracket_o$ ))))))  
**by**(*subst* *append-take-drop-id*[**where** *xs*= $\llbracket ta'-r \rrbracket_o$  **and** *n*=*r-n*, *symmetric*])(*simp* *add*: *o-def* *map-concat* *split-def* *lappend-l*list-of *l*list-of [*symmetric*] *non-speculative-lappend* *del*: *append-take-drop-id* *lappend-l*list-of *l*list-of)  
**with** *ns* **have** *ns''*: *non-speculative* *P* (*w-values* *P* ( $\lambda-. \{\}$ ) (*map* *snd* ?*start-heap-obs*)) (*l*list-of (*concat* (*map* ( $\lambda(t, ta). \llbracket ta \rrbracket_o$ ) (*list-of* (*l*take (*enat* *r-m*) *E'*) @  $\llbracket (t-r, ta'-r) \rrbracket$ ))))))  
**unfolding** *lconcat-l*list-of [*symmetric*] *map-append* *lappend-l*list-of *l*list-of [*symmetric*] *lmap-l*list-of [*symmetric*]  
*l*list.*map-comp*  
**by**(*simp* *add*: *o-def* *split-def* *non-speculative-lappend* *list-of-lconcat* *map-concat*)  
**from**  $\sigma-\sigma'$  *red-ra'* **have** *mt*hr.*if*.*RedT* ?*start-state* (*list-of* ?*r-m-E'* @  $\llbracket (t-r, ta'-r) \rrbracket$ )  $\sigma''''$   
**unfolding** *mt*hr.*if*.*RedT-def* ..

**with** *hb-completion*  
**have** *hb-completion'*: *if.hb-completion*  $\sigma''''$  (*?start-heap-obs* @ *concat* (*map* ( $\lambda(t, ta).$  *map* (*Pair*  $t$ )  $\{ta\}_o$ ) (*list-of* (*ltake* (*enat* *r-m*)  $E'$ ) @  $[(t-r, ta'-r)]$ )))  
**using** *ns''* **by** (*rule if.hb-completion-shift*)  
**from** *if.hb-completion-Runs*[*OF hb-completion'* *ta-hb-consistent-convert-RA*]  
**obtain** *ttas'* **where** *Runs'*: *mthr.if.mthr.Runs*  $\sigma''''$  *ttas'*  
**and** *hb'*: *ta-hb-consistent* *P* (*?start-heap-obs* @ *concat* (*map* ( $\lambda(t, ta).$  *map* (*Pair*  $t$ )  $\{ta\}_o$ ) (*list-of* (*ltake* (*enat* *r-m*)  $E'$ ) @  $[(t-r, ta'-r)]$ ))) (*lconcat* (*lmap* ( $\lambda(t, ta).$  *llist-of* (*map* (*Pair*  $t$ )  $\{ta\}_o$ ) *ttas'*)))  
**by** *blast*  
  
**let**  $?E = \text{lappend} (\text{lmap} (\lambda(t, ta).$  *llist-of* (*?start-heap-obs*) (*lconcat* (*lmap* ( $\lambda(t, ta).$  *llist-of* (*map* (*Pair*  $t$ )  $\{ta\}_o$ ) (*lappend* (*ltake* (*enat* *r-m*)  $E'$ ) (*LCons* (*t-r*, *ta'-r*) *ttas'*))))))  
  
**have**  $\mathcal{E}$ : *lconcat* (*lmap* ( $\lambda(t, ta).$  *llist-of* (*map* (*Pair*  $t$ )  $\{ta\}_o$ ) (*lappend* (*ltake* (*enat* *r-m*)  $E'$ ) (*LCons* (*t-r*, *ta'-r*) *ttas'*))))  $\in$  *mthr.if.* $\mathcal{E}$  *?start-state*  
**by** (*subst* (*4*) *llist-of-list-of*[*symmetric*])(*simp*, *blast* *intro*: *mthr.if.* $\mathcal{E}$ .*intros* *mthr.if.mthr.Trsys-into-Runs*  $\sigma$ - $\sigma'$  *mthr.if.mthr.Runs.Step* *red-ra'* *Runs'*)  
**hence**  $\mathcal{E}'$ :  $?E \in ?\mathcal{E}$  **by** *blast*  
  
**from**  $\mathcal{E}$  **have** *tsa*: *thread-start-actions-ok*  $?E$  **by** (*rule thread-start-actions-ok-init-fin*)  
**also let**  $?E' = \text{lappend} (\text{lmap} (\lambda(t, ta).$  *llist-of* (*lift-start-obs* *start-tid* *start-heap-obs* @ *concat* (*map* ( $\lambda(t, ta).$  *map* (*Pair*  $t$ )  $\{ta\}_o$ ) (*list-of* (*ltake* (*enat* *r-m*)  $E'$ ))) @ *map* (*Pair* *t-r*) (*take* *r-n*  $\{ta-r\}_o$ ))) (*lappend* (*llist-of* (*map* (*Pair* *t-r*) (*drop* *r-n*  $\{ta'-r\}_o$ ))) (*lconcat* (*lmap* ( $\lambda(t, ta).$  *llist-of* (*map* (*Pair*  $t$ )  $\{ta\}_o$ ) *ttas'*))))  
**have**  $?E = ?E'$   
**using** *eq[symmetric]*  
**by** (*simp* *add*: *lmap-lappend-distrib* *lappend-assoc* *lappend-llist-of-llist-of*[*symmetric*] *lconcat-llist-of*[*symmetric*] *lmap-llist-of*[*symmetric*] *llist.map-comp* *o-def* *split-def* *del*: *lmap-llist-of*)(*simp* *add*: *lappend-assoc*[*symmetric*] *lmap-lappend-distrib*[*symmetric*] *map-append*[*symmetric*] *lappend-llist-of-llist-of* *del*: *map-append*)  
**finally have** *tsa'*: *thread-start-actions-ok*  $?E'$ .  
  
**from** *hb* *hb'* *eq[symmetric]*  
**have** *HB*: *ta-hb-consistent* *P* (*?start-heap-obs* @ *concat* (*map* ( $\lambda(t, ta).$  *map* (*Pair*  $t$ )  $\{ta\}_o$ ) (*list-of* (*ltake* (*enat* *r-m*)  $E'$ ))) @ *map* (*Pair* *t-r*) (*take* *r-n*  $\{ta-r\}_o$ )) (*lappend* (*llist-of* (*map* (*Pair* *t-r*) (*drop* *r-n*  $\{ta'-r\}_o$ ))) (*lconcat* (*lmap* ( $\lambda(t, ta).$  *llist-of* (*map* (*Pair*  $t$ )  $\{ta\}_o$ ) *ttas'*))))  
**by**  $-(\text{rule } ta-hb-consistent-lappendI, \text{simp-all } add: \text{take-map}[symmetric] \text{ drop-map}[symmetric])$   
  
**define** *EE* **where** *EE* = *llist-of* (*?start-heap-obs* @ *concat* (*map* ( $\lambda(t, ta).$  *map* (*Pair*  $t$ )  $\{ta\}_o$ ) (*list-of* (*ltake* (*enat* *r-m*)  $E'$ ))) @ *map* (*Pair* *t-r*) (*take* *r-n*  $\{ta-r\}_o$ ))  
  
**from** *r* *r-conv* **have** *r-conv'*:  $r = (\sum i < r-m. \text{length } \{snd (\text{lnth } E' i)\}_o) + r-n + \text{length } ?start-heap-obs$  **by** *auto*  
**hence** *len-EE*: *llength* *EE* = *enat* *r* **using** *r-m* *r-n*  
**by** (*auto* *simp* *add*: *EE-def* *length-concat* *sum-list-sum-nth* *atLeast0LessThan* *lnth-ltake* *less-min-eq1* *split-def* *min-def* *length-list-of-conv-the-enat* *cong*: *sum.cong-simp*)  
  
**from** *r-conv* *r-m*  
**have** *r-conv3*: *llength* (*lconcat* (*lmap* ( $\lambda x.$  *llist-of* (*map* (*Pair* (*fst*  $x$ ))  $\{snd x\}_o$ ) (*ltake* (*enat* *r-m*)  $E'$ ))) = *enat* (*r* - *Suc* (*length* *start-heap-obs*) - *r-n*)  
**apply** (*simp* *add*: *llength-lconcat-lfinite-conv-sum* *lnth-ltake* *cong*: *sum.cong-simp* *conj-cong*)  
**apply** (*auto* *simp* *add*: *sum-comp-morphism*[**where** *h=enat*, *symmetric*] *zero-enat-def* *less-trans*[**where** *y=enat* *r-m*] *intro*: *sum.cong*)

```

done

have is-ws: is-write-seen P EE ws
proof(rule is-write-seenI)
  fix a ad al v
  assume a: a ∈ read-actions EE
  and a-obs: action-obs EE a = NormalAction (ReadMem ad al v)
  from a have a-r: a < r by cases(simp add: len-EE actions-def)

  from r E'-r-m r-m r-n r-conv3
  have eq: ltake (enat r) EE = ltake (enat r) E
  unfolding E E' EE-def
  apply(subst (2) E'-unfold)
  apply(simp add: ltake-lappend2 lappend-llist-of-llist-of[symmetric] lappend-eq-lappend-conv
lmap-lappend-distrib lconcat-llist-of[symmetric] o-def split-def lmap-llist-of[symmetric] del: lappend-llist-of-llist-of
lmap-llist-of)
  apply(subst ltake-lappend1)
  defer
  apply(simp add: ltake-lmap[symmetric] take-map[symmetric] ltake-llist-of[symmetric] del:
ltake-lmap ltake-llist-of)
  apply(auto simp add: min-def)
  done
hence sim: ltake (enat r) EE [≈] ltake (enat r) E by(rule eq-into-sim-actions)

from a sim have a': a ∈ read-actions E
  by(rule read-actions-change-prefix)(simp add: a-r)
from action-obs-change-prefix-eq[OF eq, of a] a-r a-obs
have a-obs': action-obs E a = NormalAction (ReadMem ad al v) by simp

have a-mrw: P,E ⊢ a ~mrw ws a using a-r a' by(rule mrw)
with ⟨E ∈ ?E⟩ wf-exec have ws-a-a: ws a < a
  by(rule jmm.mrw-before)(auto intro: a-r less-trans mrw)
hence [simp]: ws a < r using a-r by simp

from wf-exec have ws: is-write-seen P E ws by(rule wf-exec-is-write-seenD)
from is-write-seenD[OF this a' a-obs']
have ws a ∈ write-actions E
  and (ad, al) ∈ action-loc P E (ws a)
  and value-written P E (ws a) (ad, al) = v
  and ¬ P,E ⊢ a ≤hb ws a
  and is-volatile P al ⇒ ¬ P,E ⊢ a ≤so ws a
  and between: ∧a'. [ a' ∈ write-actions E; (ad, al) ∈ action-loc P E a';
P,E ⊢ ws a ≤hb a' ∧ P,E ⊢ a' ≤hb a ∨ is-volatile P al ∧ P,E ⊢ ws a ≤so a' ∧
P,E ⊢ a' ≤so a ]
  ⇒ a' = ws a by simp-all

from ⟨ws a ∈ write-actions E⟩ sim[symmetric]
show ws a ∈ write-actions EE by(rule write-actions-change-prefix) simp

from action-loc-change-prefix[OF sim, of ws a P] ⟨(ad, al) ∈ action-loc P E (ws a)⟩
show (ad, al) ∈ action-loc P EE (ws a) by(simp)

from value-written-change-prefix[OF eq, of ws a P] ⟨value-written P E (ws a) (ad, al) = v⟩
show value-written P EE (ws a) (ad, al) = v by simp

```

**from**  $wf\text{-}exec$  **have**  $t\text{-}a\text{-}E$ :  $thread\text{-}start\text{-}actions\text{-}ok\ E$   
**by**( $rule\ wf\text{-}exec\text{-}thread\text{-}start\text{-}actions\text{-}okD$ )

**from**  $\langle \neg P, E \vdash a \leq_{hb} ws\ a \rangle$  **show**  $\neg P, EE \vdash a \leq_{hb} ws\ a$   
**proof**( $rule\ contrapos\text{-}nn$ )  
**assume**  $P, EE \vdash a \leq_{hb} ws\ a$   
**thus**  $P, E \vdash a \leq_{hb} ws\ a$  **using**  $t\text{-}a\text{-}E\ sim$   
**by**( $rule\ happens\text{-}before\text{-}change\text{-}prefix$ )( $simp\text{-}all\ add$ :  $a\text{-}r$ )  
**qed**

{ **assume**  $is\text{-}volatile\ P\ al$   
**hence**  $\neg P, E \vdash a \leq_{so} ws\ a$  **by**  $fact$   
**thus**  $\neg P, EE \vdash a \leq_{so} ws\ a$   
**by**( $rule\ contrapos\text{-}nn$ )( $rule\ sync\text{-}order\text{-}change\text{-}prefix[OF - sim]$ ,  $simp\text{-}all\ add$ :  $a\text{-}r$ ) }

**fix**  $a'$   
**assume**  $a' \in write\text{-}actions\ EE\ (ad, al) \in action\text{-}loc\ P\ EE\ a'$   
**moreover**  
**hence**  $[simp]$ :  $a' < r$  **by**  $cases(simp\ add$ :  $actions\text{-}def\ len\text{-}EE$ )  
**ultimately** **have**  $a'$ :  $a' \in write\text{-}actions\ E\ (ad, al) \in action\text{-}loc\ P\ E\ a'$   
**using**  $sim\ action\text{-}loc\ change\text{-}prefix[OF\ sim, of\ a'\ P]$   
**by**( $auto\ intro$ :  $write\text{-}actions\ change\text{-}prefix$ )  
{ **assume**  $P, EE \vdash ws\ a \leq_{hb} a'\ P, EE \vdash a' \leq_{hb} a$   
**hence**  $P, E \vdash ws\ a \leq_{hb} a'\ P, E \vdash a' \leq_{hb} a$   
**using**  $t\text{-}a\text{-}E\ sim\ a\text{-}r$  **by**( $auto\ elim!$ :  $happens\text{-}before\text{-}change\text{-}prefix$ )  
**with**  $between[OF\ a']$  **show**  $a' = ws\ a$  **by**  $simp$  }  
{ **assume**  $is\text{-}volatile\ P\ al\ P, EE \vdash ws\ a \leq_{so} a'\ P, EE \vdash a' \leq_{so} a$   
**with**  $sim\ a\text{-}r\ between[OF\ a']$  **show**  $a' = ws\ a$   
**by**( $fastforce\ elim$ :  $sync\text{-}order\text{-}change\text{-}prefix\ intro!$ :  $disjI2\ del$ :  $disjCI$ ) }  
**qed**

**with**  $HB\ t\text{-}a'$   
**have**  $\exists ws'. P \vdash (?E', ws') \checkmark \wedge$   
 $(\forall n. n \in read\text{-}actions\ ?E' \longrightarrow length\ (?start\text{-}heap\text{-}obs\ @\ concat\ (map\ (\lambda(t, ta). map\ (Pair\ t)\ \{ta\}_o)\ (list\text{-}of\ (ltake\ (enat\ r\text{-}m)\ E'))))\ @\ map\ (Pair\ t\text{-}r)\ (take\ r\text{-}n\ \{ta\text{-}r\}_o)) \leq n \longrightarrow P, ?E' \vdash ws'\ n \leq_{hb} n) \wedge$   
 $(\forall n < length\ (lift\text{-}start\text{-}obs\ start\text{-}tid\ start\text{-}heap\text{-}obs\ @\ concat\ (map\ (\lambda(t, ta). map\ (Pair\ t)\ \{ta\}_o)\ (list\text{-}of\ (ltake\ (enat\ r\text{-}m)\ E'))))\ @\ map\ (Pair\ t\text{-}r)\ (take\ r\text{-}n\ \{ta\text{-}r\}_o)). ws'\ n = ws\ n)$   
**unfolding**  $EE\text{-}def$   
**by**( $rule\ ta\text{-}hb\text{-}consistent\text{-}Read\text{-}hb$ )( $rule\ jmm.\mathcal{E}\text{-}new\text{-}actions\text{-}for\text{-}fun[OF\ \mathcal{E}'[unfolding\ \langle ?E = ?E' \rangle]]$ )  
**also** **have**  $r\text{-}conv''$ :  $length\ (?start\text{-}heap\text{-}obs\ @\ concat\ (map\ (\lambda(t, ta). map\ (Pair\ t)\ \{ta\}_o)\ (list\text{-}of\ (ltake\ (enat\ r\text{-}m)\ E'))))\ @\ map\ (Pair\ t\text{-}r)\ (take\ r\text{-}n\ \{ta\text{-}r\}_o)) = r$   
**using**  $r\text{-}n\ r\text{-}m$  **unfolding**  $r\text{-}conv'$   
**by**( $auto\ simp\ add$ :  $length\text{-}concat\ sum\text{-}list\text{-}sum\text{-}nth\ atLeast0LessThan\ lnth\text{-}ltake\ split\text{-}def\ o\text{-}def\ less\text{-}min\text{-}eq1\ min\text{-}def\ length\text{-}list\text{-}of\ conv\text{-}the\ enat\ cong$ :  $sum.\text{cong}\text{-}simp$ )  
**finally** **obtain**  $ws'$  **where**  $wf\text{-}exec'$ :  $P \vdash (?E', ws') \checkmark$   
**and**  $read\text{-}hb$ :  $\bigwedge n. \llbracket n \in read\text{-}actions\ ?E'; r \leq n \rrbracket \Longrightarrow P, ?E' \vdash ws'\ n \leq_{hb} n$   
**and**  $read\text{-}same$ :  $\bigwedge n. n < r \Longrightarrow ws'\ n = ws\ n$  **by**  $blast$   
**have**  $?same\ ?E'$   
**apply**( $subst\ ltake\ lappend1$ ,  $simp\ add$ :  $r\text{-}conv''[symmetric]$   $length\text{-}list\text{-}of\ conv\text{-}the\ enat$ )  
**unfolding**  $E\ E'\ lappend\ llist\text{-}of\ llist\text{-}of[symmetric]$

```

apply(subst (1 2) ltake-lappend2, simp add: r[simplified])
apply(subst lappend-eq-lappend-conv, simp)
apply safe
apply(subst E'-unfold)
unfolding lmap-lappend-distrib
apply(subst lconcat-lappend, simp)
apply(subst lconcat-llist-of[symmetric])
apply(subst (3) lmap-llist-of[symmetric])
apply(subst (3) lmap-llist-of[symmetric])
apply(subst llist.map-comp)
apply(simp only: split-def o-def)
apply(subst llist-of-list-of, simp)
apply(subst (1 2) ltake-lappend2, simp add: r-conv3)
apply(subst lappend-eq-lappend-conv, simp)
apply safe
unfolding llist.map(2) lconcat-LCons E'-r-m snd-conv fst-conv take-map
apply(subst ltake-lappend1)
defer
apply(subst append-take-drop-id[where xs=⟦ta-r⟧o and n=r-n, symmetric])
unfolding map-append lappend-llist-of-llist-of[symmetric]
apply(subst ltake-lappend1)
using r-n
apply(simp add: min-def r-conv3)
apply(rule refl)
apply(simp add: r-conv3)
using r-n by arith

moreover {
  fix a
  assume a ∈ read-actions ?E'
  with read-hb[of a] read-same[of a]
  have if a < r then ws' a = ws a else P, ?E' ⊢ ws' a ≤hb a by simp }
hence ?read ?E' ws' by blast
moreover from r-m r-n r-n'
have E'-r: lnth ?E' r = (t-r, ⟦ta'-r⟧o ! r-n) unfolding r-conv'
  by(auto simp add: lnth-lappend nth-append length-concat sum-list-sum-nth atLeast0LessThan
split-beta lnth-ltake less-min-eq1 length-list-of-conv-the-enat cong: sum.cong-simp)
from E-r r have E-r: lnth E r = (t-r, ⟦ta-r⟧o ! r-n)
  unfolding E by(simp add: lnth-lappend)
  have r ∈ read-actions E ⟷ (∃ ad al v. ⟦ta-r⟧o ! r-n = NormalAction (ReadMem ad al v))
using True
by(auto elim!: read-actions.cases simp add: action-obs-def E-r actions-def intro!: read-actions.intros)
with sim E'-r E-r have ?tid ?E' ?obs ?E'
  by(auto simp add: action-tid-def action-obs-def)
moreover have ?actions ?E' using r-n r-m r-n' unfolding r-conv'
  by(cases llength ?E')(auto simp add: actions-def less-min-eq2 length-concat sum-list-sum-nth
atLeast0LessThan split-beta lnth-ltake less-min-eq1 length-list-of-conv-the-enat enat-plus-eq-enat-conv
cong: sum.cong-simp)
  ultimately show ?thesis using wf-exec' E'
  unfolding ⟨?E = ?E'⟩ by blast
qed
qed
qed
qed

```



end

**lemma** *w-value-mrw-value-conf*:

**assumes** *set-option* (*vs'* *adal*)  $\subseteq$  *vs* *adal*  $\times$  *UNIV*

**shows** *set-option* (*mrw-value* *P* *vs'* *ob* *adal*)  $\subseteq$  *w-value* *P* *vs* *ob* *adal*  $\times$  *UNIV*

**using** *assms* **by**(*cases* *adal*)(*cases* *ob* *rule*: *w-value-cases*, *auto*)

**lemma** *w-values-mrw-values-conf*:

**assumes** *set-option* (*vs'* *adal*)  $\subseteq$  *vs* *adal*  $\times$  *UNIV*

**shows** *set-option* (*mrw-values* *P* *vs'* *obs* *adal*)  $\subseteq$  *w-values* *P* *vs* *obs* *adal*  $\times$  *UNIV*

**using** *assms*

**by**(*induct* *obs* *arbitrary*: *vs'* *vs*)(*auto* *del*: *subsetI* *intro*: *w-value-mrw-value-conf*)

**lemma** *w-value-mrw-value-dom-eq-preserve*:

**assumes** *dom* *vs'* = {*adal*. *vs* *adal*  $\neq$  {}}

**shows** *dom* (*mrw-value* *P* *vs'* *ob*) = {*adal*. *w-value* *P* *vs* *ob* *adal*  $\neq$  {}}

**using** *assms*

**apply**(*cases* *ob* *rule*: *w-value-cases*)

**apply**(*simp-all* *add*: *dom-def* *split-beta* *del*: *not-None-eq*)

**apply**(*blast* *elim*: *equalityE* *dest*: *subsetD*) +

**done**

**lemma** *w-values-mrw-values-dom-eq-preserve*:

**assumes** *dom* *vs'* = {*adal*. *vs* *adal*  $\neq$  {}}

**shows** *dom* (*mrw-values* *P* *vs'* *obs*) = {*adal*. *w-values* *P* *vs* *obs* *adal*  $\neq$  {}}

**using** *assms*

**by**(*induct* *obs* *arbitrary*: *vs* *vs'*)(*auto* *del*: *equalityI* *intro*: *w-value-mrw-value-dom-eq-preserve*)

**context** *jmm-multithreaded* **begin**

**definition** *non-speculative-read* ::

*nat*  $\Rightarrow$  (*'l*, *'thread-id*, *'x*, *'m*, *'w*) *state*  $\Rightarrow$  (*'addr*  $\times$  *addr-loc*  $\Rightarrow$  *'addr val set*)  $\Rightarrow$  *bool*

**where**

*non-speculative-read* *n* *s* *vs*  $\longleftrightarrow$

( $\forall$  *ttas* *s'* *t* *x* *ta* *x'* *m'* *i* *ad* *al* *v* *v'*.

*s*  $\rightarrow$  *ttas*  $\rightarrow$  *s'*  $\rightarrow$  *non-speculative* *P* *vs* (*l*list-of (*concat* (*map* ( $\lambda$ (*t*, *ta*).  $\llbracket ta \rrbracket_o$ ) *ttas*)))  $\rightarrow$

*thr* *s'* *t* =  $\lfloor (x, \text{no-wait-locks}) \rfloor \rightarrow t \vdash (x, \text{shr } s') -ta \rightarrow (x', m') \rightarrow \text{actions-ok } s' t ta \rightarrow$

*i* < *length*  $\llbracket ta \rrbracket_o \rightarrow$

*non-speculative* *P* (*w-values* *P* *vs* (*concat* (*map* ( $\lambda$ (*t*, *ta*).  $\llbracket ta \rrbracket_o$ ) *ttas*))) (*l*list-of (*take* *i*  $\llbracket ta \rrbracket_o$ ))

$\rightarrow$

$\llbracket ta \rrbracket_o ! i = \text{NormalAction } (\text{ReadMem } ad \ al \ v) \rightarrow$

*v'*  $\in$  *w-values* *P* *vs* (*concat* (*map* ( $\lambda$ (*t*, *ta*).  $\llbracket ta \rrbracket_o$ ) *ttas*) @ *take* *i*  $\llbracket ta \rrbracket_o$ ) (*ad*, *al*)  $\rightarrow$

( $\exists$  *ta'* *x''* *m''*. *t*  $\vdash (x, \text{shr } s') -ta' \rightarrow (x'', m'') \wedge \text{actions-ok } s' t ta' \wedge$

*i* < *length*  $\llbracket ta' \rrbracket_o \wedge \text{take } i \llbracket ta' \rrbracket_o = \text{take } i \llbracket ta \rrbracket_o \wedge \llbracket ta' \rrbracket_o ! i = \text{NormalAction}$

(*ReadMem* *ad* *al* *v'*)  $\wedge$

*length*  $\llbracket ta' \rrbracket_o \leq \max n$  (*length*  $\llbracket ta \rrbracket_o$ ))

**lemma** *non-speculative-readI* [*intro?*]:

( $\bigwedge$  *ttas* *s'* *t* *x* *ta* *x'* *m'* *i* *ad* *al* *v* *v'*.

$\llbracket s \rightarrow$  *ttas*  $\rightarrow$  *s'*; *non-speculative* *P* *vs* (*l*list-of (*concat* (*map* ( $\lambda$ (*t*, *ta*).  $\llbracket ta \rrbracket_o$ ) *ttas*)));

*thr* *s'* *t* =  $\lfloor (x, \text{no-wait-locks}) \rfloor$ ; *t*  $\vdash (x, \text{shr } s') -ta \rightarrow (x', m')$ ; *actions-ok* *s'* *t* *ta*;

*i* < *length*  $\llbracket ta \rrbracket_o$ ; *non-speculative* *P* (*w-values* *P* *vs* (*concat* (*map* ( $\lambda$ (*t*, *ta*).  $\llbracket ta \rrbracket_o$ ) *ttas*))) (*l*list-of

(*take* *i*  $\llbracket ta \rrbracket_o$ ));

$\llbracket ta \rrbracket_o ! i = \text{NormalAction } (\text{ReadMem } ad \ al \ v);$   
 $v' \in w\text{-values } P \text{ vs } (\text{concat } (\text{map } (\lambda(t, ta). \llbracket ta \rrbracket_o) \ ttas) @ \text{take } i \ \llbracket ta \rrbracket_o) (ad, al) \rrbracket$   
 $\implies \exists ta' x'' m''. t \vdash (x, \text{shr } s') -ta' \rightarrow (x'', m'') \wedge \text{actions-ok } s' \ t \ ta' \wedge$   
 $i < \text{length } \llbracket ta' \rrbracket_o \wedge \text{take } i \ \llbracket ta' \rrbracket_o = \text{take } i \ \llbracket ta \rrbracket_o \wedge \llbracket ta' \rrbracket_o ! i = \text{NormalAction}$   
 $(\text{ReadMem } ad \ al \ v') \wedge$   
 $\text{length } \llbracket ta' \rrbracket_o \leq \max n \ (\text{length } \llbracket ta \rrbracket_o)$   
 $\implies \text{non-speculative-read } n \ s \text{ vs}$   
**unfolding** *non-speculative-read-def* **by** *blast*

**lemma** *non-speculative-readD*:

$\llbracket \text{non-speculative-read } n \ s \text{ vs}; s \rightarrow^* ttas \rightarrow^* s'; \text{non-speculative } P \text{ vs } (\text{l-list-of } (\text{concat } (\text{map } (\lambda(t, ta). \llbracket ta \rrbracket_o) \ ttas)))$   
 $\text{thr } s' \ t = \lfloor (x, \text{no-wait-locks}) \rfloor; t \vdash (x, \text{shr } s') -ta \rightarrow (x', m'); \text{actions-ok } s' \ t \ ta;$   
 $i < \text{length } \llbracket ta \rrbracket_o; \text{non-speculative } P \ (w\text{-values } P \text{ vs } (\text{concat } (\text{map } (\lambda(t, ta). \llbracket ta \rrbracket_o) \ ttas))) \ (\text{l-list-of}$   
 $(\text{take } i \ \llbracket ta \rrbracket_o));$   
 $\llbracket ta \rrbracket_o ! i = \text{NormalAction } (\text{ReadMem } ad \ al \ v);$   
 $v' \in w\text{-values } P \text{ vs } (\text{concat } (\text{map } (\lambda(t, ta). \llbracket ta \rrbracket_o) \ ttas) @ \text{take } i \ \llbracket ta \rrbracket_o) (ad, al) \rrbracket$   
 $\implies \exists ta' x'' m''. t \vdash (x, \text{shr } s') -ta' \rightarrow (x'', m'') \wedge \text{actions-ok } s' \ t \ ta' \wedge$   
 $i < \text{length } \llbracket ta' \rrbracket_o \wedge \text{take } i \ \llbracket ta' \rrbracket_o = \text{take } i \ \llbracket ta \rrbracket_o \wedge \llbracket ta' \rrbracket_o ! i = \text{NormalAction}$   
 $(\text{ReadMem } ad \ al \ v') \wedge$   
 $\text{length } \llbracket ta' \rrbracket_o \leq \max n \ (\text{length } \llbracket ta \rrbracket_o)$   
**unfolding** *non-speculative-read-def* **by** *blast*

**end**

### 8.11.2 *non-speculative generalises cut-and-update and ta-hb-consistent*

**context** *known-addr-typing* **begin**

**lemma** *read-non-speculative-new-actions-for*:

**fixes** *status f C M params E*  
**defines**  $E \equiv \text{lift-start-obs } \text{start-tid } \text{start-heap-obs}$   
**and**  $vs \equiv w\text{-values } P \ (\lambda-. \ \{\}) \ (\text{map } \text{snd } E)$   
**and**  $s \equiv \text{init-fin-lift-state } \text{status } (\text{start-state } f \ P \ C \ M \ \text{params})$   
**assumes** *wf: wf-syscls P*  
**and** *RedT: mthr.if.RedT s ttas s'*  
**and** *redT: mthr.if.redT s' (t, ta') s''*  
**and** *read: NormalAction (ReadMem ad al v) ∈ set ⌊ta'⌋<sub>o</sub>*  
**and** *ns: non-speculative P (λ-. ⌊⌋) (l-list-of (map snd E @ concat (map (λ(t, ta). ⌊ta⌋<sub>o</sub>) ttas)))*  
**and** *ka: known-addr start-tid (f (fst (method P C M)) M (fst (snd (method P C M))) (fst (snd (snd (method P C M))))) (the (snd (snd (snd (method P C M))))) params) ⊆ allocated start-heap*  
**and** *wt: ts-ok (init-fin-lift wf x) (thr s) (shr s)*  
**and** *type-adal: P, shr s' ⊢ ad@al : T*  
**shows**  $\exists w. w \in \text{new-actions-for } P \ (\text{l-list-of } (E @ \text{concat } (\text{map } (\lambda(t, ta). \text{map } (\text{Pair } t) \ \llbracket ta \rrbracket_o) \ ttas)))$   
 $(ad, al)$   
 $(\text{is } \exists w. \ ?\text{new-w } w)$   
**using** *RedT redT read ns[unfolded E-def] ka* **unfolding** *s-def*  
**proof**(*cases rule: read-ex-NewHeapElem*)  
**case** (*start CTn*)  
**then obtain** *n* **where**  $n: \text{start-heap-obs} ! n = \text{NewHeapElem } ad \ CTn$   
**and** *len: n < length start-heap-obs*  
**unfolding** *in-set-conv-nth* **by** *blast*  
**from** *ns* **have** *non-speculative P (w-values P (λ-. ⌊⌋) (map snd E)) (l-list-of (concat (map (λ(t, ta). ⌊ta⌋<sub>o</sub>) ttas)))*

```

unfolding lappend-llist-of-llist-of[symmetric]
by(simp add: non-speculative-lappend del: lappend-llist-of-llist-of)
with RedT wt have hext: start-heap  $\sqsubseteq$  shr s'
unfolding s-def E-def using start-state-vs-conf[OF wf]
by(auto dest!: init-fin-RedT-hext-incr simp add: start-state-def split-beta init-fin-lift-state-conv-simps)

from start have typeof-addr start-heap ad =  $\lfloor CTh \rfloor$ 
by(auto dest: NewHeapElem-start-heap-obsD[OF wf])
with hext have typeof-addr (shr s') ad =  $\lfloor CTh \rfloor$  by(rule typeof-addr-hext-mono)
with type-adal have (ad, al)  $\in$  action-loc-aux P (NormalAction (NewHeapElem ad CTh)) using n
len
by cases (auto simp add: action-obs-def lnth-lappend1 lift-start-obs-def)
with n len have ?new-w (Suc n)
by(simp add: new-actions-for-def actions-def E-def action-obs-def lift-start-obs-def nth-append)
thus ?thesis ..
next
case (Red ttas' s'' t' ta' s''' ttas'' CTh)
note ttas =  $\langle ttas = ttas' @ (t', ta') \# ttas'' \rangle$ 

from  $\langle NormalAction (NewHeapElem ad CTh) \in set \llbracket ta' \rrbracket_o \rangle$ 
obtain obs obs' where obs:  $\llbracket ta' \rrbracket_o = obs @ NormalAction (NewHeapElem ad CTh) \# obs'$ 
by(auto dest: split-list)

let ?n = length (lift-start-obs start-tid start-heap-obs)
let ?wa = ?n + length (concat (map ( $\lambda(t, ta). \llbracket ta \rrbracket_o$ ) ttas')) + length obs

have ?wa = ?n + length (concat (map ( $\lambda(t, ta). map (Pair t) \llbracket ta \rrbracket_o$ ) ttas')) + length obs
by(simp add: length-concat o-def split-def)
also have ... < length (E @ concat (map ( $\lambda(t, ta). map (Pair t) \llbracket ta \rrbracket_o$ ) ttas))
using obs ttas by(simp add: E-def)
also
from ttas obs
have (E @ concat (map ( $\lambda(t, ta). map (Pair t) \llbracket ta \rrbracket_o$ ) ttas)) ! ?wa = (t', NormalAction (NewHeapElem
ad CTh))
by(auto simp add: E-def lift-start-obs-def nth-append o-def split-def length-concat)
moreover
from  $\langle mthr.if.redT s'' (t', ta') s''' \rangle \langle NormalAction (NewHeapElem ad CTh) \in set \llbracket ta' \rrbracket_o \rangle$ 
obtain x-wa x-wa' where ts''t': thr s'' t' =  $\lfloor (x-wa, no-wait-locks) \rfloor$ 
and red-wa: mthr.init-fin t' (x-wa, shr s'') ta' (x-wa', shr s''')
by(cases) fastforce+

from start-state-vs-conf[OF wf]
have vs: vs-conf P (shr s) vs unfolding vs-def E-def s-def
by(simp add: init-fin-lift-state-conv-simps start-state-def split-def)

from ns
have ns: non-speculative P vs (llist-of (concat (map ( $\lambda(t, ta). \llbracket ta \rrbracket_o$ ) ttas'))))
and ns': non-speculative P (w-values P vs (concat (map ( $\lambda(t, ta). \llbracket ta \rrbracket_o$ ) ttas')))) (llist-of  $\llbracket ta' \rrbracket_o$ )
and ns'': non-speculative P (w-values P (w-values P vs (concat (map ( $\lambda(t, ta). \llbracket ta \rrbracket_o$ ) ttas'))))
 $\llbracket ta' \rrbracket_o$  (llist-of (concat (map ( $\lambda(t, ta). \llbracket ta \rrbracket_o$ ) ttas'))))
unfolding ttas vs-def
by(simp-all add: lappend-llist-of-llist-of[symmetric] non-speculative-lappend del: lappend-llist-of-llist-of)
from  $\langle mthr.if.RedT (init-fin-lift-state status (start-state f P C M params)) ttas' s'' \rangle$  wt ns
have ts-ok'': ts-ok (init-fin-lift wfx) (thr s'') (shr s'') using vs unfolding vs-def s-def

```

```

  by(rule if-RedT-non-speculative-invar)
  with  $ts''t'$  have  $wfxt'$ :  $wfx\ t'$  (snd  $x-wa$ ) (shr  $s''$ ) by(cases  $x-wa$ )(auto dest:  $ts-okD$ )

  from  $\langle mthr.if.RedT\ (init-fin-lift-state\ status\ (start-state\ f\ P\ C\ M\ params))\ ttas'\ s''\rangle\ wt\ ns$ 
  have  $vs''$ :  $vs-conf\ P\ (shr\ s'')\ (w-values\ P\ (w-values\ P\ (\lambda-. \{\})\ (map\ snd\ E))\ (concat\ (map\ (\lambda(t, ta). \llbracket ta \rrbracket_o)\ ttas'))$ 
  unfolding  $s-def\ E-def\ vs-def$ 
  by(rule if-RedT-non-speculative-invar)(simp add: start-state-def split-beta init-fin-lift-state-conv-simps
  start-state-vs-conf[OF  $wf$ ])
  from if-redT-non-speculative-vs-conf[OF  $\langle mthr.if.redT\ s''\ (t', ta')\ s''' \rangle\ ts-ok'' - vs''$ , of length  $\llbracket ta' \rrbracket_o$ ]
   $ns'$ 
  have  $vs'''$ :  $vs-conf\ P\ (shr\ s''')\ (w-values\ P\ (w-values\ P\ vs\ (concat\ (map\ (\lambda(t, ta). \llbracket ta \rrbracket_o)\ ttas'))$ 
   $\llbracket ta' \rrbracket_o$ )
  by(simp add:  $vs-def$ )

  from  $\langle mthr.if.redT\ s''\ (t', ta')\ s''' \rangle\ ts-ok''\ ns'\ vs''$ 
  have  $ts-ok\ (init-fin-lift\ wfx)\ (thr\ s''')\ (shr\ s''')$ 
  unfolding  $vs-def$  by(rule if-redT-non-speculative-invar)
  with  $\langle mthr.if.RedT\ s''' \ ttas''\ s' \rangle$ 
  have  $hext$ :  $shr\ s''' \trianglelefteq shr\ s'$  using  $ns''\ vs'''$ 
  by(rule init-fin-RedT-hext-incr)

  from  $red-wa$  have  $typeof-addr\ (shr\ s''')\ ad = \lfloor CTn \rfloor$ 
  using  $wfxt'\ \langle NormalAction\ (NewHeapElem\ ad\ CTn) \in set\ \llbracket ta' \rrbracket_o \rangle$  by cases(auto dest:  $red-NewHeapElemD$ )
  with  $hext$  have  $typeof-addr\ (shr\ s')\ ad = \lfloor CTn \rfloor$  by(rule typeof-addr-hext-mono)
  with  $type-adal$  have  $(ad, al) \in action-loc-aux\ P\ (NormalAction\ (NewHeapElem\ ad\ CTn))$  by cases
  auto
  ultimately have  $?new-w\ ?wa$ 
  by(simp add: new-actions-for-def actions-def action-obs-def)
  thus  $?thesis\ ..$ 
qed

```

lemma non-speculative-read-into-cut-and-update:

```

  fixes  $status\ f\ C\ M\ params\ E$ 
  defines  $E \equiv lift-start-obs\ start-tid\ start-heap-obs$ 
  and  $vs \equiv w-values\ P\ (\lambda-. \{\})\ (map\ snd\ E)$ 
  and  $s \equiv init-fin-lift-state\ status\ (start-state\ f\ P\ C\ M\ params)$ 
  and  $vs' \equiv mrw-values\ P\ Map.empty\ (map\ snd\ E)$ 
  assumes  $wf$ :  $wf-syscls\ P$ 
  and  $nsr$ :  $if.non-speculative-read\ n\ s\ vs$ 
  and  $wt$ :  $ts-ok\ (init-fin-lift\ wfx)\ (thr\ s)\ (shr\ s)$ 
  and  $ka$ :  $known-addr\ start-tid\ (f\ (fst\ (method\ P\ C\ M))\ M\ (fst\ (snd\ (method\ P\ C\ M))))\ (fst\ (snd\ (snd\ (method\ P\ C\ M))))\ (the\ (snd\ (snd\ (snd\ (method\ P\ C\ M))))\ params) \subseteq allocated\ start-heap$ 
  shows  $if.cut-and-update\ s\ vs'$ 
  proof(rule if.cut-and-updateI)
    fix  $ttas\ s'\ t\ x\ ta\ x'\ m'$ 
    assume  $Red$ :  $mthr.if.RedT\ s\ ttas\ s'$ 
    and  $sc$ :  $ta-seq-consist\ P\ vs'\ (llist-of\ (concat\ (map\ (\lambda(t, ta). \llbracket ta \rrbracket_o)\ ttas)))$ 
    and  $tst$ :  $thr\ s'\ t = \lfloor (x, no-wait-locks) \rfloor$ 
    and  $red$ :  $t \vdash (x, shr\ s') -ta \rightarrow_i (x', m')$ 
    and  $aok$ :  $mthr.if.actions-ok\ s'\ t\ ta$ 
    let  $?vs = w-values\ P\ vs\ (concat\ (map\ (\lambda(t, ta). \llbracket ta \rrbracket_o)\ ttas))$ 
    let  $?vs' = mrw-values\ P\ vs'\ (concat\ (map\ (\lambda(t, ta). \llbracket ta \rrbracket_o)\ ttas))$ 

```

```

from start-state-vs-conf[OF wf]
have vs: vs-conf P (shr s) vs unfolding vs-def E-def s-def
  by(simp add: init-fin-lift-state-conv-simps start-state-def split-def)

from sc have ns: non-speculative P vs (llist-of (concat (map (λ(t, ta). ⌊ta⌋o) ttas)))
  by(rule ta-seq-consist-into-non-speculative)(auto simp add: vs'-def vs-def del: subsetI intro: w-values-mrw-values-conf)

from ns have ns': non-speculative P (λ-. { }) (llist-of (map snd (lift-start-obs start-tid start-heap-obs)
@ concat (map (λ(t, ta). ⌊ta⌋o) ttas)))
  unfolding lappend-llist-of-llist-of[symmetric] vs-def
  by(simp add: non-speculative-lappend E-def non-speculative-start-heap-obs del: lappend-llist-of-llist-of)

have vs-vs'':  $\bigwedge adal. set-option (?vs' adal) \subseteq ?vs\ adal \times UNIV$ 
  by(rule w-values-mrw-values-conf)(auto simp add: vs'-def vs-def del: subsetI intro: w-values-mrw-values-conf)
from Red wt ns vs
have wt': ts-ok (init-fin-lift wfx) (thr s') (shr s')
  by(rule if-RedT-non-speculative-invar)
hence wt: init-fin-lift wfx t x (shr s') using tst by(rule ts-okD)

{ fix i
  have  $\exists ta' x'' m''. t \vdash (x, shr\ s') -ta' \rightarrow i (x'', m'') \wedge mthr.if.actions-ok\ s'\ t\ ta' \wedge$ 
     $length\ \llbracket ta' \rrbracket_o \leq \max n\ (length\ \llbracket ta \rrbracket_o) \wedge$ 
     $ta-seq-consist\ P\ ?vs' (llist-of\ (take\ i\ \llbracket ta' \rrbracket_o)) \wedge$ 
     $eq-upto-seq-inconsist\ P\ (take\ i\ \llbracket ta \rrbracket_o)\ (take\ i\ \llbracket ta' \rrbracket_o)\ ?vs' \wedge$ 
     $(ta-seq-consist\ P\ ?vs' (llist-of\ (take\ i\ \llbracket ta \rrbracket_o)) \longrightarrow ta' = ta)$ 

  proof(induct i)
    case 0
    show ?case using red aok
    by(auto simp del: split-paired-Ex simp add: eq-upto-seq-inconsist-simps)
  next
    case (Suc i)
    then obtain ta' x'' m''
    where red':  $t \vdash (x, shr\ s') -ta' \rightarrow i (x'', m'')$ 
    and aok': mthr.if.actions-ok s' t ta'
    and len:  $length\ \llbracket ta' \rrbracket_o \leq \max n\ (length\ \llbracket ta \rrbracket_o)$ 
    and sc': ta-seq-consist P ?vs' (llist-of (take i ⌊ta'⌋o))
    and eusi: eq-upto-seq-inconsist P (take i ⌊ta⌋o) (take i ⌊ta'⌋o) ?vs'
    and ta'-ta: ta-seq-consist P ?vs' (llist-of (take i ⌊ta⌋o))  $\implies$  ta' = ta
    by blast
    let ?vs'' = mrw-values P ?vs' (take i ⌊ta'⌋o)
    show ?case
    proof(cases i < length ⌊ta'⌋o  $\wedge \neg ta-seq-consist\ P\ ?vs' (llist-of\ (take\ (Suc\ i)\ \llbracket ta' \rrbracket_o)) \wedge \neg$ 
ta-seq-consist P ?vs' (llist-of (take (Suc i) ⌊ta⌋o)))
      case True
      hence i:  $i < length\ \llbracket ta' \rrbracket_o$  and  $\neg ta-seq-consist\ P\ ?vs'' (LCons\ (\llbracket ta' \rrbracket_o\ !\ i)\ LNil)$  using sc'
      by(auto simp add: take-Suc-conv-app-nth lappend-llist-of-llist-of[symmetric] ta-seq-consist-lappend
simp del: lappend-llist-of-llist-of)
      then obtain ad al v where ta'-i:  $\llbracket ta' \rrbracket_o\ !\ i = NormalAction\ (ReadMem\ ad\ al\ v)$ 
      by(auto split: action.split-asm obs-event.split-asm)
      from ta'-i True have read: NormalAction (ReadMem ad al v) ∈ set ⌊ta'⌋o by(auto simp add:
in-set-conv-nth)
      with red' have ad ∈ known-addr-if t x by(rule if-red-read-knows-addr)
      hence ad ∈ if.known-addr-state s' using tst by(rule if.known-addr-stateI)
      moreover from init-fin-red-read-typeable[OF red' wtt read]

```

```

obtain  $T$  where  $\text{type-adal}: P, \text{shr } s' \vdash \text{ad@al} : T \dots$ 

from  $\text{redT-updWs-total}[\text{of } t \text{ wset } s' \{ta'\}_w] \text{ red}' \text{ tst aok}'$ 
obtain  $s''$  where  $\text{redT}': \text{mthr.if.redT } s' (t, ta') s''$  by  $(\text{auto dest!}: \text{mthr.if.redT.redT-normal})$ 
with  $\text{wf Red}$ 
  have  $\exists w. w \in \text{new-actions-for } P (\text{l-list-of } (E @ \text{concat } (\text{map } (\lambda(t, ta). \text{map } (\text{Pair } t) \{ta\}_o) \text{ttas}))) (ad, al)$ 
  (is  $\exists w. ?\text{new-w } w)$ 
using  $\text{read ns' ka wt type-adal unfolding } s\text{-def } E\text{-def}$  by  $(\text{rule read-non-speculative-new-actions-for})$ 
then obtain  $w$  where  $w: ?\text{new-w } w \dots$ 
have  $(ad, al) \in \text{dom } ?vs'$ 
proof  $(\text{cases } w < \text{length } E)$ 
  case  $\text{True}$ 
    with  $w$  have  $(ad, al) \in \text{dom } vs'$  unfolding  $vs'\text{-def new-actions-for-def}$ 
      by  $(\text{clarsimp})(\text{erule mrw-values-new-actionD}[\text{rotated } 1], \text{auto simp del: split-paired-Ex simp}$ 
 $\text{add: set-conv-nth action-obs-def nth-append intro!}: \text{exI}[\text{where } x=w])$ 
      also have  $\text{dom } vs' \subseteq \text{dom } ?vs'$  by  $(\text{rule mrw-values-dom-mono})$ 
      finally show  $?thesis \dots$ 
  next
    case  $\text{False}$ 
    with  $w$  show  $?thesis$  unfolding  $\text{new-actions-for-def}$ 
      apply  $(\text{clarsimp})$ 
      apply  $(\text{erule mrw-values-new-actionD}[\text{rotated } 1])$ 
      apply  $(\text{simp-all add: set-conv-nth action-obs-def nth-append actions-def})$ 
      apply  $(\text{rule exI}[\text{where } x=w - \text{length } E])$ 
      apply  $(\text{subst nth-map}[\text{where } f=\text{snd, symmetric}])$ 
      apply  $(\text{simp-all add: length-concat o-def split-def map-concat})$ 
      done
    qed
  hence  $(ad, al) \in \text{dom } (\text{mrw-values } P ?vs' (\text{take } i \{ta'\}_o))$ 
    by  $(\text{rule subsetD}[\text{OF mrw-values-dom-mono}])$ 
  then obtain  $v' b$  where  $v': \text{mrw-values } P ?vs' (\text{take } i \{ta'\}_o) (ad, al) = \lfloor (v', b) \rfloor$  by  $\text{auto}$ 
moreover from  $vs\text{-vs''}[\text{of } (ad, al)]$ 
  have  $\text{set-option } (\text{mrw-values } P ?vs' (\text{take } i \{ta'\}_o) (ad, al)) \subseteq w\text{-values } P ?vs (\text{take } i \{ta'\}_o)$ 
 $(ad, al) \times \text{UNIV}$ 
    by  $(\text{rule w-values-mrw-values-conf})$ 
  ultimately have  $v' \in w\text{-values } P ?vs (\text{take } i \{ta'\}_o) (ad, al)$  by  $\text{simp}$ 
moreover from  $sc'$ 
  have  $\text{non-speculative } P (w\text{-values } P vs (\text{concat } (\text{map } (\lambda(t, ta). \{ta\}_o) \text{ttas}))) (\text{l-list-of } (\text{take } i \{ta'\}_o))$ 
    by  $(\text{blast intro: ta-seq-consist-into-non-speculative vs-vs'' del: subsetI})$ 
  ultimately obtain  $ta'' x'' m''$ 
    where  $\text{red}'': t \vdash (x, \text{shr } s') -ta'' \rightarrow i (x'', m'')$ 
    and  $\text{aok}'': \text{mthr.if.actions-ok } s' t ta''$ 
    and  $i': i < \text{length } \{ta''\}_o$ 
    and  $\text{eq: take } i \{ta''\}_o = \text{take } i \{ta'\}_o$ 
    and  $ta''\text{-}i: \{ta''\}_o ! i = \text{NormalAction } (\text{ReadMem } ad \text{ al } v')$ 
    and  $\text{len': length } \{ta''\}_o \leq \max n (\text{length } \{ta'\}_o)$ 
    using  $\text{if.non-speculative-readD}[\text{OF nsr Red ns tst red' aok' } i - ta'\text{-}i, \text{of } v']$  by  $\text{auto}$ 
  from  $\text{len' len}$  have  $\text{length } \{ta''\}_o \leq \max n (\text{length } \{ta'\}_o)$  by  $\text{simp}$ 
moreover have  $\text{ta-seq-consist } P ?vs' (\text{l-list-of } (\text{take } (\text{Suc } i) \{ta''\}_o))$ 
    using  $\text{eq } sc' i' ta''\text{-}i v'$ 
    by  $(\text{simp add: take-Suc-conv-app-nth lappend-l-list-of-l-list-of}[\text{symmetric}] \text{ta-seq-consist-lappend}$ 
 $\text{del: lappend-l-list-of-l-list-of})$ 

```

```

moreover
have eusi': eq-upto-seq-inconsist P (take (Suc i)  $\llbracket ta \rrbracket_o$ ) (take (Suc i)  $\llbracket ta' \rrbracket_o$ ) ?vs'
proof(cases i < length  $\llbracket ta \rrbracket_o$ )
  case True
    with i' i len eq eusi ta'-i ta''-i v' show ?thesis
  by(auto simp add: take-Suc-conv-app-nth ta'-ta eq-upto-seq-inconsist-simps intro: eq-upto-seq-inconsist-appendI)
next
  case False
    with i ta'-ta have  $\neg$  ta-seq-consist P ?vs' (llist-of (take i  $\llbracket ta \rrbracket_o$ )) by auto
    then show ?thesis using False i' eq eusi
    by(simp add: take-Suc-conv-app-nth eq-upto-seq-inconsist-append2)
qed
moreover {
  assume ta-seq-consist P ?vs' (llist-of (take (Suc i)  $\llbracket ta \rrbracket_o$ ))
  with True have ta'' = ta by simp }
ultimately show ?thesis using red'' aok'' True by blast
next
case False
hence ta-seq-consist P ?vs' (llist-of (take (Suc i)  $\llbracket ta \rrbracket_o$ ))  $\vee$ 
  length  $\llbracket ta' \rrbracket_o \leq i \vee$ 
  ta-seq-consist P ?vs' (llist-of (take (Suc i)  $\llbracket ta' \rrbracket_o$ ))
  (is ?case1  $\vee$  ?case2  $\vee$  ?case3) by auto
thus ?thesis
proof(elim disjCE)
  assume ?case1
  moreover
    hence eq-upto-seq-inconsist P (take (Suc i)  $\llbracket ta \rrbracket_o$ ) (take (Suc i)  $\llbracket ta \rrbracket_o$ ) ?vs'
    by(rule ta-seq-consist-imp-eq-upto-seq-inconsist-refl)
    ultimately show ?thesis using red aok by fastforce
  next
    assume ?case2 and  $\neg$  ?case1
    have eq-upto-seq-inconsist P (take (Suc i)  $\llbracket ta \rrbracket_o$ ) (take (Suc i)  $\llbracket ta' \rrbracket_o$ ) ?vs'
    proof(cases i < length  $\llbracket ta \rrbracket_o$ )
      case True
        from  $\langle ?case2 \rangle \langle \neg ?case1 \rangle$  have  $\neg$  ta-seq-consist P ?vs' (llist-of (take i  $\llbracket ta \rrbracket_o$ )) by(auto
simp add: ta'-ta)
        hence eq-upto-seq-inconsist P (take i  $\llbracket ta \rrbracket_o$  @ [ $\llbracket ta \rrbracket_o ! i$ ]) (take i  $\llbracket ta' \rrbracket_o$  @ []) ?vs'
        by(blast intro: eq-upto-seq-inconsist-appendI[OF eusi])
        thus ?thesis using True  $\langle ?case2 \rangle$  by(simp add: take-Suc-conv-app-nth)
      next
        case False with eusi  $\langle ?case2 \rangle$  show ?thesis by simp
    qed
    with red' aok' len sc' eusi  $\langle ?case2 \rangle \langle \neg ?case1 \rangle$  show ?thesis
    by (fastforce simp add: take-all simp del: split-paired-Ex)
  next
    assume ?case3 and  $\neg$  ?case1 and  $\neg$  ?case2
    with len eusi ta'-ta
    have eq-upto-seq-inconsist P (take (Suc i)  $\llbracket ta \rrbracket_o$ ) (take (Suc i)  $\llbracket ta' \rrbracket_o$ ) ?vs'
    by(cases i < length  $\llbracket ta \rrbracket_o$ )(auto simp add: take-Suc-conv-app-nth lappend-llist-of-llist-of[symmetric]
ta-seq-consist-lappend intro: eq-upto-seq-inconsist-appendI eq-upto-seq-inconsist-append2 cong: action.case-cong
obs-event.case-cong)
    with red' aok'  $\langle ?case3 \rangle$  len  $\langle \neg ?case1 \rangle$  show ?thesis by blast
  qed
qed

```

**qed** }  
**from** *this*[*of* *max n* (*length*  $\llbracket ta \rrbracket_o$ )]  
**show**  $\exists ta' x'' m''. t \vdash (x, shr\ s') -ta' \rightarrow_i (x'', m'') \wedge mthr.if.actions-ok\ s'\ t\ ta' \wedge ta-seq-consist\ P$   
 $?vs' (l\!l\!i\!s\!t\text{-of}\ \llbracket ta' \rrbracket_o) \wedge eq\text{-upto}\text{-seq}\text{-inconsistent}\ P\ \llbracket ta \rrbracket_o\ \llbracket ta' \rrbracket_o\ ?vs'$   
**by**(*auto simp del: split-paired-Ex cong: conj-cong*)  
**qed**

**lemma** *non-speculative-read-into-hb-completion:*

**fixes** *status f C M params E*  
**defines**  $E \equiv lift\text{-start}\text{-obs}\ start\text{-tid}\ start\text{-heap}\text{-obs}$   
**and**  $vs \equiv w\text{-values}\ P\ (\lambda\cdot. \{\})\ (map\ snd\ E)$   
**and**  $s \equiv init\text{-fin}\text{-lift}\text{-state}\ status\ (start\text{-state}\ f\ P\ C\ M\ params)$   
**assumes** *wf: wf-syscls P*  
**and** *nsr: if.non-speculative-read n s vs*  
**and** *wt: ts-ok (init-fin-lift wfx) (thr s) (shr s)*  
**and** *ka: known-addr start-tid (f (fst (method P C M)) M (fst (snd (method P C M))) (fst (snd (snd (method P C M)))) (the (snd (snd (snd (method P C M)))) params)  $\subseteq$  allocated start-heap*  
**shows** *if.hb-completion s E*

**proof**

**fix** *ttas s' t x ta x' m' i*  
**assume** *Red: mthr.if.RedT s ttas s'*  
**and** *ns: non-speculative P (w-values P ( $\lambda\cdot. \{\})$ ) (map snd E)) (l\!l\!i\!s\!t\text{-of}\ (concat\ (map\ (\lambda(t, ta). \llbracket ta \rrbracket\_o) ttas)))*  
**and** *tst: thr s' t =  $\lfloor (x, no\text{-wait}\text{-locks}) \rfloor$*   
**and** *red:  $t \vdash (x, shr\ s') -ta \rightarrow_i (x', m')$*   
**and** *aok: mthr.if.actions-ok s' t ta*  
**and** *nsi: non-speculative P (w-values P (w-values P ( $\lambda\cdot. \{\})$ ) (map snd E)) (concat (map (\lambda(t, ta). \llbracket ta \rrbracket\_o) ttas))) (l\!l\!i\!s\!t\text{-of}\ (take\ i\ \llbracket ta \rrbracket\_o))*

**let**  $?E = E @ concat\ (map\ (\lambda(t, ta). map\ (Pair\ t)\ \llbracket ta \rrbracket_o)\ ttas) @ map\ (Pair\ t)\ (take\ i\ \llbracket ta \rrbracket_o)$

**let**  $?vs = w\text{-values}\ P\ vs\ (concat\ (map\ (\lambda(t, ta). \llbracket ta \rrbracket_o)\ ttas))$

**from** *ns have ns': non-speculative P ( $\lambda\cdot. \{\})$  (l\!l\!i\!s\!t\text{-of}\ (map\ snd\ (lift\text{-start}\text{-obs}\ start\text{-tid}\ start\text{-heap}\text{-obs)) @ concat\ (map\ (\lambda(t, ta). \llbracket ta \rrbracket\_o) ttas)))*  
**unfolding** *lappend-l\!l\!i\!s\!t\text{-of}\ l\!l\!i\!s\!t\text{-of}[symmetric]*  
**by**(*simp add: non-speculative-lappend E-def non-speculative-start-heap-obs del: lappend-l\!l\!i\!s\!t\text{-of}\ l\!l\!i\!s\!t\text{-of}*)

**from** *start-state-vs-conf[OF wf]*

**have** *vs: vs-conf P (shr s) vs* **unfolding** *vs-def E-def s-def*

**by**(*simp add: init-fin-lift-state-conv-simps start-state-def split-def*)

**from** *Red wt ns vs*

**have** *wt': ts-ok (init-fin-lift wfx) (thr s') (shr s')*

**unfolding** *vs-def* **by**(*rule if-RedT-non-speculative-invar*)

**hence** *wtt: init-fin-lift wfx t x (shr s')* **using** *tst* **by**(*rule ts-okD*)

**{ fix** *j*

**have**  $\exists ta' x'' m''. t \vdash (x, shr\ s') -ta' \rightarrow_i (x'', m'') \wedge mthr.if.actions-ok\ s'\ t\ ta' \wedge length\ \llbracket ta' \rrbracket_o \leq max\ n\ (length\ \llbracket ta \rrbracket_o) \wedge$

$take\ i\ \llbracket ta' \rrbracket_o = take\ i\ \llbracket ta \rrbracket_o \wedge$

$ta\text{-hb-consistent}\ P\ ?E\ (l\!l\!i\!s\!t\text{-of}\ (map\ (Pair\ t)\ (take\ j\ (drop\ i\ \llbracket ta' \rrbracket_o)))) \wedge$

$(i < length\ \llbracket ta \rrbracket_o \longrightarrow i < length\ \llbracket ta' \rrbracket_o) \wedge$

$(if\ \exists ad\ al\ v. \llbracket ta \rrbracket_o ! i = NormalAction\ (ReadMem\ ad\ al\ v)\ then\ sim\text{-action}\ else$

$(=))\ (\llbracket ta \rrbracket_o ! i)\ (\llbracket ta' \rrbracket_o ! i)$



```

proof(induct j)
  case 0 from red aok show ?case by(fastforce simp del: split-paired-Ex)
next
  case (Suc j)
  then obtain ta' x'' m''
    where red':  $t \vdash (x, \text{shr } s') \rightarrow_i (x'', m'')$ 
    and aok':  $\text{mthr.if.actions-ok } s' t \text{ ta}'$ 
    and len:  $\text{length } \llbracket \text{ta}' \rrbracket_o \leq \max n (\text{length } \llbracket \text{ta} \rrbracket_o)$ 
    and eq:  $\text{take } i \llbracket \text{ta}' \rrbracket_o = \text{take } i \llbracket \text{ta} \rrbracket_o$ 
    and hb:  $\text{ta-hb-consistent } P \text{ ?E } (\text{llist-of } (\text{map } (\text{Pair } t) (\text{take } j (\text{drop } i \llbracket \text{ta}' \rrbracket_o))))$ 
    and len-i:  $i < \text{length } \llbracket \text{ta} \rrbracket_o \longrightarrow i < \text{length } \llbracket \text{ta}' \rrbracket_o$ 
    and sim-i:  $(\text{if } \exists \text{ad al } v. \llbracket \text{ta} \rrbracket_o ! i = \text{NormalAction } (\text{ReadMem ad al } v) \text{ then sim-action else$ 
(=)) ( $\llbracket \text{ta} \rrbracket_o ! i$ ) ( $\llbracket \text{ta}' \rrbracket_o ! i$ )
    by blast
  show ?case
  proof(cases i + j < length \llbracket \text{ta}' \rrbracket_o)
    case False
    with red' aok' len eq hb len-i sim-i show ?thesis by(fastforce simp del: split-paired-Ex)
  next
    case True
    note j = this
    show ?thesis
    proof(cases \exists ad al v. \llbracket \text{ta}' \rrbracket_o ! (i + j) = NormalAction (ReadMem ad al v))
      case True
      then obtain ad al v where ta'-j:  $\llbracket \text{ta}' \rrbracket_o ! (i + j) = \text{NormalAction } (\text{ReadMem ad al } v)$  by
blast
      hence read:  $\text{NormalAction } (\text{ReadMem ad al } v) \in \text{set } \llbracket \text{ta}' \rrbracket_o$  using j by(auto simp add:
in-set-conv-nth)
      with red' have ad  $\in \text{known-addrs-if } t \text{ x}$  by(rule if-red-read-knows-addr)
      hence ad  $\in \text{if.known-addrs-state } s' \text{ using } \text{tst}$  by(rule if.known-addrs-stateI)
      from init-fin-red-read-typeable[OF red' wtt read] obtain T
      where type-adal:  $P, \text{shr } s' \vdash \text{ad@al} : T ..$ 

      from redT-updWs-total[of t wset s'  $\llbracket \text{ta}' \rrbracket_w$ ] red' tst aok'
      obtain s'' where redT':  $\text{mthr.if.redT } s' (t, \text{ta}') s''$  by(auto dest!: mthr.if.redT.redT-normal)
      with wf Red
      have  $\exists w. w \in \text{new-actions-for } P (\text{llist-of } (E @ \text{concat } (\text{map } (\lambda(t, \text{ta}). \text{map } (\text{Pair } t) \llbracket \text{ta} \rrbracket_o) \text{ttas})))$ 
      (ad, al)
      (is  $\exists w. \text{?new-w } w$ )
      using read ns' ka wt type-adal unfolding s-def E-def
      by(rule read-non-speculative-new-actions-for)
      then obtain w where w:  $\text{?new-w } w ..$ 

      define E'' where  $E'' = \text{?E } @ \text{map } (\text{Pair } t) (\text{take } (\text{Suc } j) (\text{drop } i \llbracket \text{ta}' \rrbracket_o))$ 

      from Red redT' have  $\text{mthr.if.RedT } s (\text{ttas } @ [(t, \text{ta}')]) s''$  unfolding mthr.if.RedT-def ..
      hence tsa:  $\text{thread-start-actions-ok } (\text{llist-of } (\text{lift-start-obs start-tid start-heap-obs } @ \text{concat } (\text{map } (\lambda(t, \text{ta}). \text{map } (\text{Pair } t) \llbracket \text{ta} \rrbracket_o) (\text{ttas } @ [(t, \text{ta}')]))))$ 
      unfolding s-def by(rule thread-start-actions-ok-init-fin-RedT)
      hence thread-start-actions-ok (llist-of E'') unfolding E-def[symmetric] E''-def
      by(rule thread-start-actions-ok-prefix)(rule lprefix-llist-ofI, simp, metis append-take-drop-id
eq map-append)
      moreover from w have  $w \in \text{actions } (\text{llist-of } E'')$ 
      unfolding E''-def by(auto simp add: new-actions-for-def actions-def)

```

**moreover have**  $\text{length } ?E + j \in \text{actions } (\text{llist-of } E'')$  **using**  $j$  **by**(*auto simp add: E''-def actions-def*)  
**moreover from**  $w$  **have**  $\text{is-new-action } (\text{action-obs } (\text{llist-of } E'') w)$   
**by**(*auto simp add: new-actions-for-def action-obs-def actions-def nth-append E''-def*)  
**moreover have**  $\neg \text{is-new-action } (\text{action-obs } (\text{llist-of } E'') (\text{length } ?E + j))$   
**using**  $j$   $\text{ta}'\text{-}j$  **by**(*auto simp add: action-obs-def nth-append min-def E''-def*)(*subst (asm) nth-map, simp-all*)  
**ultimately have**  $\text{hb-w: } P, \text{llist-of } E'' \vdash w \leq_{\text{hb}} \text{length } ?E + j$   
**by**(*rule happens-before-new-not-new*)  
  
**define** *writes* **where**  
 $\text{writes} = \{w. P, \text{llist-of } E'' \vdash w \leq_{\text{hb}} \text{length } ?E + j \wedge w \in \text{write-actions } (\text{llist-of } E'') \wedge$   
 $(\text{ad}, \text{al}) \in \text{action-loc } P (\text{llist-of } E'') w\}$   
  
**define**  $w'$  **where**  $w' = \text{Max-torder } (\text{action-order } (\text{llist-of } E'')) \text{ writes}$   
  
**have** *writes-actions*:  $\text{writes} \subseteq \text{actions } (\text{llist-of } E'')$  **unfolding** *writes-def actions-def*  
**by**(*auto dest!: happens-before-into-action-order elim!: action-orderE simp add: actions-def*)  
**also have** *finite*  $\dots$  **by**(*simp add: actions-def*)  
**finally** (*finite-subset*) **have** *finite writes* .  
**moreover from**  $\text{hb-w } w$  **have**  $w\text{-writes: } w \in \text{writes}$   
**by**(*auto 4 3 simp add: writes-def new-actions-for-def action-obs-def actions-def nth-append E''-def intro!: write-actions.intros elim!: is-new-action.cases*)  
**hence**  $\text{writes} \neq \{\}$  **by** *auto*  
  
**with** *torder-action-order*  $\langle \text{finite writes} \rangle$   
**have**  $w'\text{-writes: } w' \in \text{writes}$  **using** *writes-actions* **unfolding**  $w'\text{-def}$  **by**(*rule Max-torder-in-set*)  
**moreover**  
 $\{ \text{fix } w''$   
 $\text{assume } w'' \in \text{writes}$   
 $\text{with } \text{torder-action-order } \langle \text{finite writes} \rangle$   
**have**  $\text{llist-of } E'' \vdash w'' \leq_a w'$  **using** *writes-actions* **unfolding**  $w'\text{-def}$  **by**(*rule Max-torder-above*)  
 $\}$   
  
**note**  $w'\text{-maximal} = \text{this}$   
  
**define**  $v'$  **where**  $v' = \text{value-written } P (\text{llist-of } E'') w' (\text{ad}, \text{al})$   
  
**from** *nsi ta-hb-consistent-into-non-speculative*[*OF hb*]  
**have**  $\text{nst': non-speculative } P (w\text{-values } P \text{ vs } (\text{concat } (\text{map } (\lambda(t, \text{ta}). \{\text{ta}\}_o) \text{ttas}))) (\text{llist-of}$   
 $(\text{take } (i + j) \{\text{ta}'\}_o))$   
**unfolding** *take-add lappend-llist-of-llist-of*[*symmetric*] *non-speculative-lappend vs-def eq*  
**by**(*simp add: non-speculative-lappend o-def map-concat split-def del: lappend-llist-of-llist-of*)  
  
**from**  $w'\text{-writes}$  **have**  $\text{adal-w': } (\text{ad}, \text{al}) \in \text{action-loc } P (\text{llist-of } E'') w'$  **by**(*simp add: writes-def*)  
**from**  $w'\text{-writes}$  **have**  $w' \in \text{write-actions } (\text{llist-of } E'')$   
**unfolding** *writes-def* **by** *blast*  
**then obtain**  $\text{is-write-action } (\text{action-obs } (\text{llist-of } E'') w')$   
**and**  $w'\text{-actions: } w' \in \text{actions } (\text{llist-of } E'')$  **by** *cases*  
**hence**  $v' \in w\text{-values } P (\lambda\cdot. \{\}) (\text{map } \text{snd } E'') (\text{ad}, \text{al})$   
**proof** *cases*  
**case** (*NewHeapElem ad' CTn*)  
**hence** *NormalAction* (*NewHeapElem ad' CTn*)  $\in \text{set } (\text{map } \text{snd } E'')$   
**using**  $w'\text{-actions}$  **unfolding** *in-set-conv-nth*  
**by**(*auto simp add: actions-def action-obs-def cong: conj-cong*)

```

moreover have  $ad' = ad$ 
  and  $(ad, al) \in \text{action-loc-aux } P \text{ (NormalAction (NewHeapElem ad CTn))}$ 
  using  $adal-w' \text{ NewHeapElem by auto}$ 
ultimately show  $?thesis \text{ using NewHeapElem unfolding } v'\text{-def}$ 
  by( $\text{simp add: value-written.simps w-values-new-actionD}$ )
next
  case ( $\text{WriteMem ad' al' v'}$ )
  hence  $\text{NormalAction (WriteMem ad' al' v')} \in \text{set (map snd E')}$ 
    using  $w'\text{-actions unfolding in-set-conv-nth}$ 
    by( $\text{auto simp add: actions-def action-obs-def cong: conj-cong}$ )
  moreover have  $ad' = ad \text{ al' = al}$  using  $adal-w' \text{ WriteMem by auto}$ 
  ultimately show  $?thesis \text{ using WriteMem unfolding } v'\text{-def}$ 
    by( $\text{simp add: value-written.simps w-values-WriteMemD}$ )
qed
hence  $v' \in w\text{-values } P \text{ vs (concat (map } (\lambda(t, ta). \llbracket ta \rrbracket_o \rrbracket \text{ ttas}) @ \text{take } (i + j) \llbracket ta' \rrbracket_o \rrbracket (ad, al)$ 
  using  $j \text{ ta'-j eq unfolding } E''\text{-def vs-def}$ 
  by( $\text{simp add: o-def split-def map-concat take-add take-Suc-conv-app-nth}$ )
from  $\text{if.non-speculative-readD[OF nsr Red ns[folded vs-def] tst red' aok' j nsi' ta'-j this]}$ 
obtain  $ta'' x'' m''$ 
  where  $\text{red''}: t \vdash (x, \text{shr } s') -ta'' \rightarrow i (x'', m'')$ 
  and  $\text{aok''}: \text{mthr.if.actions-ok } s' t ta''$ 
  and  $j': i + j < \text{length } \llbracket ta'' \rrbracket_o$ 
  and  $\text{eq'}: \text{take } (i + j) \llbracket ta'' \rrbracket_o = \text{take } (i + j) \llbracket ta' \rrbracket_o$ 
  and  $\text{ta''-j}: \llbracket ta'' \rrbracket_o ! (i + j) = \text{NormalAction (ReadMem ad al v')}$ 
  and  $\text{len'}: \text{length } \llbracket ta'' \rrbracket_o \leq \max n (\text{length } \llbracket ta' \rrbracket_o)$  by  $\text{blast}$ 

define  $EE$  where  $EE = ?E @ \text{map (Pair } t \text{) (take } j \text{ (drop } i \llbracket ta'' \rrbracket_o \rrbracket))}$ 
  define  $E'$  where  $E' = ?E @ \text{map (Pair } t \text{) (take } j \text{ (drop } i \llbracket ta'' \rrbracket_o \rrbracket)) @ [(t, \text{NormalAction (ReadMem ad al v')})]$ 

from  $\text{len' len have length } \llbracket ta'' \rrbracket_o \leq \max n (\text{length } \llbracket ta' \rrbracket_o)$  by  $\text{simp}$ 
moreover from  $\text{eq' eq j j' have take } i \llbracket ta'' \rrbracket_o = \text{take } i \llbracket ta' \rrbracket_o$ 
  by( $\text{auto simp add: take-add min-def}$ )
moreover {
  note  $hb$ 
  also have  $\text{eq''}: \text{take } j \text{ (drop } i \llbracket ta' \rrbracket_o \rrbracket) = \text{take } j \text{ (drop } i \llbracket ta'' \rrbracket_o \rrbracket)$ 
    using  $\text{eq' j j' by (simp add: take-add min-def)}$ 
  also have  $\text{ta-hb-consistent } P (?E @ \text{list-of (llist-of (map (Pair } t \text{) (take } j \text{ (drop } i \llbracket ta'' \rrbracket_o \rrbracket))))}$ 
     $(\text{llist-of } [(t, \llbracket ta'' \rrbracket_o ! (i + j))])$ 
    unfolding  $\text{llist-of.simps ta-hb-consistent-LCons ta-hb-consistent-LNil ta''-j prod.simps}$ 
     $\text{action.simps obs-event.simps list-of-llist-of append-assoc } E'\text{-def[symmetric, unfolded append-assoc]}$ 
    unfolding  $EE\text{-def[symmetric, unfolded append-assoc]}$ 
    proof( $\text{intro conjI TrueI exI[where } x=w \text{] strip}$ )
      have  $\text{llist-of } E'' [\approx] \text{llist-of } E' \text{ using } j \text{ len eq'' ta'-j unfolding } E''\text{-def } E'\text{-def}$ 
      by( $\text{auto simp add: sim-actions-def list-all2-append List.list-all2-refl split-beta take-Suc-conv-app-nth}$ 
         $\text{take-map[symmetric]})$ 
      moreover have  $\text{length } E'' = \text{length } E' \text{ using } j \text{ j' by (simp add: } E''\text{-def } E'\text{-def)}$ 
      ultimately have  $\text{sim: ltake (enat (length } E')) (\text{llist-of } E'') [\approx] \text{ltake (enat (length } E'))}$ 
         $(\text{llist-of } E') \text{ by simp}$ 

from  $w'\text{-actions } \langle \text{length } E'' = \text{length } E' \rangle$ 
have  $w'\text{-len: } w' < \text{length } E' \text{ by (simp add: actions-def)}$ 

from  $\langle w' \in \text{write-actions } (\text{llist-of } E'') \rangle \text{ sim}$ 

```

```

show  $w' \in \text{write-actions } (\text{llist-of } E')$  by (rule write-actions-change-prefix)(simp add:  $w'\text{-len}$ )
from  $\text{adal-}w'$  action-loc-change-prefix[OF sim, of  $w' P$ ]
show  $(ad, al) \in \text{action-loc } P (\text{llist-of } E') w'$  by (simp add:  $w'\text{-len}$ )

from  $ta'-j j$  have  $\text{length } ?E + j \in \text{read-actions } (\text{llist-of } E'')$ 
by (auto intro!: read-actions.intros simp add: action-obs-def actions-def  $E''\text{-def min-def}$ 
nth-append)(auto)
hence  $w' \neq \text{length } ?E + j$  using  $\langle w' \in \text{write-actions } (\text{llist-of } E'') \rangle$ 
by (auto dest: read-actions-not-write-actions)
with  $w'\text{-len}$  have  $w' < \text{length } ?E + j$  by (simp add:  $E'\text{-def}$ )
from  $j j' \text{ len' eq''}$ 
have  $\text{ltake } (\text{enat } (\text{length } ?E + j)) (\text{llist-of } E'') = \text{ltake } (\text{enat } (\text{length } ?E + j)) (\text{llist-of } E')$ 
by (auto simp add:  $E''\text{-def } E'\text{-def min-def take-Suc-conv-app-nth}$ )
from  $\text{value-written-change-prefix}$ [OF this, of  $w' P$ ]  $\langle w' < \text{length } ?E + j \rangle$ 
show  $\text{value-written } P (\text{llist-of } E') w' (ad, al) = v'$  unfolding  $v'\text{-def}$  by simp

from  $\langle \text{thread-start-actions-ok } (\text{llist-of } E'') \rangle \langle \text{llist-of } E'' [\approx] \text{llist-of } E' \rangle$ 
have  $\text{tsa}'': \text{thread-start-actions-ok } (\text{llist-of } E')$ 
by (rule thread-start-actions-ok-change)

from  $w'\text{-writes } j j' \text{ len len'}$  have  $P, \text{llist-of } E'' \vdash w' \leq_{hb} \text{length } EE$ 
by (auto simp add:  $EE\text{-def writes-def min-def ac-simps}$ )
thus  $P, \text{llist-of } E' \vdash w' \leq_{hb} \text{length } EE$  using  $\text{tsa}'' \text{ sim}$ 
by (rule happens-before-change-prefix)(simp add:  $w'\text{-len}$ , simp add:  $EE\text{-def } E'\text{-def}$ )

fix  $w''$ 
assume  $w'': w'' \in \text{write-actions } (\text{llist-of } E')$ 
and  $\text{adal-}w'': (ad, al) \in \text{action-loc } P (\text{llist-of } E') w''$ 

from  $w''$  have  $w''\text{-len}: w'' < \text{length } E'$  by (cases)(simp add: actions-def)

from  $w'' \text{ sim}[\text{symmetric}]$  have  $w'': w'' \in \text{write-actions } (\text{llist-of } E'')$ 
by (rule write-actions-change-prefix)(simp add:  $w''\text{-len}$ )
from  $\text{adal-}w'' \text{ action-loc-change-prefix}$ [OF  $\text{sim}[\text{symmetric}]$ , of  $w'' P$ ]  $w''\text{-len}$ 
have  $\text{adal-}w'': (ad, al) \in \text{action-loc } P (\text{llist-of } E'') w''$  by simp
{
  presume  $w'-w'': \text{llist-of } E' \vdash w' \leq_a w''$ 
  and  $w''\text{-hb}: P, \text{llist-of } E' \vdash w'' \leq_{hb} \text{length } EE$ 
from  $w''\text{-hb} \langle \text{thread-start-actions-ok } (\text{llist-of } E'') \rangle \text{ sim}[\text{symmetric}]$ 
have  $P, \text{llist-of } E'' \vdash w'' \leq_{hb} \text{length } EE$ 
by (rule happens-before-change-prefix)(simp add:  $w''\text{-len}$ , simp add:  $E'\text{-def } EE\text{-def}$ )
with  $w'' \text{ adal-}w'' j j' \text{ len len'}$  have  $w'' \in \text{writes}$ 
by (auto simp add: writes-def  $EE\text{-def min-def ac-simps split: if-split-asm}$ )
hence  $\text{llist-of } E'' \vdash w'' \leq_a w'$  by (rule  $w'\text{-maximal}$ )
hence  $\text{llist-of } E' \vdash w'' \leq_a w'$  using  $\text{sim}$ 
by (rule action-order-change-prefix)(simp-all add:  $w'\text{-len } w''\text{-len}$ )
thus  $w'' = w' w'' = w'$  using  $w'-w''$  by (rule antisymPD[OF antisym-action-order])+
}
{
assume  $P, \text{llist-of } E' \vdash w' \leq_{hb} w'' \wedge P, \text{llist-of } E' \vdash w'' \leq_{hb} \text{length } EE$ 
thus  $\text{llist-of } E' \vdash w' \leq_a w'' P, \text{llist-of } E' \vdash w'' \leq_{hb} \text{length } EE$ 
by (blast dest: happens-before-into-action-order)+
}
assume  $\text{is-volatile } P al \wedge P, \text{llist-of } E' \vdash w' \leq_{so} w'' \wedge P, \text{llist-of } E' \vdash w'' \leq_{so} \text{length } EE$ 
then obtain  $\text{vol}: \text{is-volatile } P al$ 

```

and so:  $P, \text{llist-of } E' \vdash w' \leq_{so} w''$   
 and so':  $P, \text{llist-of } E' \vdash w'' \leq_{so} \text{length } EE$  by blast  
 from so show  $\text{llist-of } E' \vdash w' \leq_a w''$  by (blast elim: sync-orderE)

show  $P, \text{llist-of } E' \vdash w'' \leq_{hb} \text{length } EE$   
 proof (cases is-new-action (action-obs (llist-of E') w''))  
 case True  
 with  $\langle w'' \in \text{write-actions (llist-of } E') \rangle \text{ ta''-j}$  show ?thesis  
 by (cases (rule happens-before-new-not-new[OF tsa'], auto simp add: actions-def EE-def E'-def action-obs-def min-def nth-append))  
 next  
 case False  
 with  $\langle w'' \in \text{write-actions (llist-of } E') \rangle \langle (ad, al) \in \text{action-loc } P (\text{llist-of } E') w'' \rangle$   
 obtain  $v''$  where  $\text{action-obs (llist-of } E') w'' = \text{NormalAction (WriteMem ad al } v'')$   
 by (cases (auto elim: is-write-action.cases))  
 with  $\text{ta''-j } w'' \text{ j' len len'}$   
 have  $P \vdash (\text{action-tid (llist-of } E') w'', \text{action-obs (llist-of } E') w'') \leadsto_{sw} (\text{action-tid (llist-of } E') (\text{length } EE), \text{action-obs (llist-of } E') (\text{length } EE))$   
 by (auto simp add: E'-def EE-def action-obs-def min-def nth-append Volatile)  
 with so' have  $P, \text{llist-of } E' \vdash w'' \leq_{sw} \text{length } EE$  by (rule sync-withI)  
 thus ?thesis unfolding po-sw-def [abs-def] by (blast intro: tranclp.r-into-trancl)  
 qed }  
 qed  
 ultimately have  $\text{ta-hb-consistent } P \text{ ?E (lappend (llist-of (map (Pair t) (take j (drop i \{ta''\}_o)))) (llist-of [(t, \{ta''\}_o ! (i + j))]))}$   
 by (rule ta-hb-consistent-lappendI) simp  
 hence  $\text{ta-hb-consistent } P \text{ ?E (llist-of (map (Pair t) (take (Suc j) (drop i \{ta''\}_o))))}$   
 using j' unfolding lappend-llist-of-llist-of by (simp add: take-Suc-conv-app-nth) }  
 moreover from len-i have  $i < \text{length } \{ta\}_o \longrightarrow i < \text{length } \{ta''\}_o$  using eq' j' by auto  
 moreover from sim-i eq' ta''-j ta'-j  
 have  $(\text{if } \exists ad \text{ al } v. \{ta\}_o ! i = \text{NormalAction (ReadMem ad al } v) \text{ then sim-action else } (=))$   
 $(\{ta\}_o ! i) (\{ta''\}_o ! i)$   
 by (cases j = 0) (auto split: if-split-asm, (metis add-strict-left-mono add-0-right nth-take)+)  
 ultimately show ?thesis using red'' aok'' by blast  
 next  
 case False  
 hence  $\text{ta-hb-consistent } P \text{ (?E @ list-of (llist-of (map (Pair t) (take j (drop i \{ta'\}_o))))}$   
 $(\text{llist-of [(t, \{ta'\}_o ! (i + j))])$   
 by (simp add: ta-hb-consistent-LCons split: action.split obs-event.split)  
 with hb  
 have  $\text{ta-hb-consistent } P \text{ ?E (lappend (llist-of (map (Pair t) (take j (drop i \{ta'\}_o)))) (llist-of [(t, \{ta'\}_o ! (i + j))]))}$   
 by (rule ta-hb-consistent-lappendI) simp  
 hence  $\text{ta-hb-consistent } P \text{ ?E (llist-of (map (Pair t) (take (Suc j) (drop i \{ta'\}_o))))}$   
 using j unfolding lappend-llist-of-llist-of by (simp add: take-Suc-conv-app-nth)  
 with red' aok' len eq len-i sim-i show ?thesis by blast  
 qed  
 qed  
 qed }  
 from this[of max n (length \{ta\}\_o)]  
 show  $\exists ta' x'' m''. t \vdash (x, \text{shr } s') -ta' \rightarrow_i (x'', m'') \wedge \text{mthr.if.actions-ok } s' t ta' \wedge$   
 $\text{take } i \{ta'\}_o = \text{take } i \{ta\}_o \wedge$   
 $\text{ta-hb-consistent } P \text{ ?E (llist-of (map (Pair t) (drop i \{ta'\}_o)))} \wedge$   
 $(i < \text{length } \{ta\}_o \longrightarrow i < \text{length } \{ta'\}_o) \wedge$

```

      (if  $\exists ad\ al\ v. \{ta\}_o ! i = NormalAction\ (ReadMem\ ad\ al\ v)$  then sim-action else
    (=)) ( $\{ta\}_o ! i$ ) ( $\{ta'\}_o ! i$ )
    by(simp del: split-paired-Ex cong: conj-cong split del: if-split) blast
qed

end

end

```

## 8.12 Type-safety proof for the Java memory model

```

theory JMM-Typesafe
imports
  JMM-Framework
begin

```

Create a dynamic list *heap-independent* of theorems for replacing heap-dependent constants by heap-independent ones.

```

ML <
structure Heap-Independent-Rules = Named-Thms
(
  val name = @{binding heap-independent}
  val description = Simplification rules for heap-independent constants
)
>
setup <Heap-Independent-Rules.setup>

locale heap-base' =
  h: heap-base
    addr2thread-id thread-id2addr
    spurious-wakeups
    empty-heap allocate  $\lambda-. \text{typeof-addr heap-read heap-write}$ 
  for addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
  and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
  and spurious-wakeups :: bool
  and empty-heap :: 'heap
  and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
  and typeof-addr :: 'addr  $\rightarrow$  htype
  and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
  and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
begin

definition typeof-h :: 'addr val  $\Rightarrow$  ty option
where typeof-h = h.typeof-h undefined
lemma typeof-h-conv-typeof-h [heap-independent, iff]: h.typeof-h h = typeof-h
by(rule ext)(case-tac x, simp-all add: typeof-h-def)
lemmas typeof-h-simps [simp] = h.typeof-h.simps [unfolded heap-independent]

definition cname-of :: 'addr  $\Rightarrow$  cname
where cname-of = h.cname-of undefined
lemma cname-of-conv-cname-of [heap-independent, iff]: h.cname-of h = cname-of
by(simp add: cname-of-def h.cname-of-def[abs-def])

```

**definition** *addr-loc-type* :: 'm prog  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  ty  $\Rightarrow$  bool  
**where** *addr-loc-type* P = h.addr-loc-type P undefined  
**notation** *addr-loc-type* (-  $\vdash$  -@- : - [50, 50, 50, 50] 51)  
**lemma** *addr-loc-type-conv-addr-loc-type* [heap-independent, iff]:  
 h.addr-loc-type P h = addr-loc-type P  
**by**(simp add: addr-loc-type-def h.addr-loc-type-def)  
**lemmas** *addr-loc-type-cases* [cases pred: addr-loc-type] =  
 h.addr-loc-type.cases[unfolded heap-independent]  
**lemmas** *addr-loc-type-intros* = h.addr-loc-type.intros[unfolded heap-independent]

**definition** *typeof-addr-loc* :: 'm prog  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  ty  
**where** *typeof-addr-loc* P = h.typeof-addr-loc P undefined  
**lemma** *typeof-addr-loc-conv-typeof-addr-loc* [heap-independent, iff]:  
 h.typeof-addr-loc P h = typeof-addr-loc P  
**by**(simp add: typeof-addr-loc-def h.typeof-addr-loc-def[abs-def])

**definition** *conf* :: 'a prog  $\Rightarrow$  'addr val  $\Rightarrow$  ty  $\Rightarrow$  bool  
**where** *conf* P  $\equiv$  h.conf P undefined  
**notation** *conf* (-  $\vdash$  - : $\leq$  - [51,51,51] 50)  
**lemma** *conf-conv-conf* [heap-independent, iff]: h.conf P h = conf P  
**by**(simp add: conf-def heap-base.conf-def[abs-def])  
**lemmas** *defval-conf* [simp] = h.defval-conf[unfolded heap-independent]

**definition** *lconf* :: 'm prog  $\Rightarrow$  (vname  $\rightarrow$  'addr val)  $\Rightarrow$  (vname  $\rightarrow$  ty)  $\Rightarrow$  bool  
**where** *lconf* P = h.lconf P undefined  
**notation** *lconf* (-  $\vdash$  - '(: $\leq$ )' - [51,51,51] 50)  
**lemma** *lconf-conv-lconf* [heap-independent, iff]: h.lconf P h = lconf P  
**by**(simp add: lconf-def h.lconf-def[abs-def])

**definition** *confs* :: 'm prog  $\Rightarrow$  'addr val list  $\Rightarrow$  ty list  $\Rightarrow$  bool  
**where** *confs* P = h.confs P undefined  
**notation** *confs* (-  $\vdash$  - [: $\leq$ ] - [51,51,51] 50)  
**lemma** *confs-conv-confs* [heap-independent, iff]: h.confs P h = confs P  
**by**(simp add: confs-def)

**definition** *tconf* :: 'm prog  $\Rightarrow$  'thread-id  $\Rightarrow$  bool  
**where** *tconf* P = h.tconf P undefined  
**notation** *tconf* (-  $\vdash$  -  $\sqrt{t}$  [51,51] 50)  
**lemma** *tconf-conv-tconf* [heap-independent, iff]: h.tconf P h = tconf P  
**by**(simp add: tconf-def h.tconf-def[abs-def])

**definition** *vs-conf* :: 'm prog  $\Rightarrow$  ('addr  $\times$  addr-loc  $\Rightarrow$  'addr val set)  $\Rightarrow$  bool  
**where** *vs-conf* P = h.vs-conf P undefined  
**lemma** *vs-conf-conv-vs-conf* [heap-independent, iff]: h.vs-conf P h = vs-conf P  
**by**(simp add: vs-conf-def h.vs-conf-def[abs-def])

**lemmas** *vs-confI* = h.vs-confI[unfolded heap-independent]  
**lemmas** *vs-confD* = h.vs-confD[unfolded heap-independent]

use non-speculativity to express that only type-correct values are read

**primrec** *vs-type-all* :: 'm prog  $\Rightarrow$  'addr  $\times$  addr-loc  $\Rightarrow$  'addr val set  
**where** *vs-type-all* P (ad, al) = {v.  $\exists T. P \vdash ad@al : T \wedge P \vdash v : \leq T$ }

**lemma** *vs-conf-vs-type-all* [simp]: vs-conf P (vs-type-all P)

**by**(rule *h.vs-confI*[*unfolded heap-independent*])(*simp*)

**lemma** *w-addr-vs-type-all*: *w-addr* (*vs-type-all* *P*)  $\subseteq$  *dom typeof-addr*  
**by**(auto *simp* *addr*: *w-addr-def* *h.conf-def*[*unfolded heap-independent*])

**lemma** *w-addr-vs-type-all-in-vs-type-all*:

$(\bigcup ad \in w\text{-addr} \ (vs\text{-type-all} \ P). \ \{(ad, al) | al. \ \exists T. \ P \vdash ad@al : T\}) \subseteq \{adal. \ vs\text{-type-all} \ P \ adal \neq \{\}\}$

**by**(auto *simp* *addr*: *w-addr-def* *vs-type-all-def* *intro*: *defval-conf*)

**declare** *vs-type-all.simps* [*simp del*]

**lemmas** *vs-conf-insert-iff* = *h.vs-conf-insert-iff*[*unfolded heap-independent*]

**end**

**locale** *heap'* =

*h*: *heap*

*addr2thread-id thread-id2addr*

*spurious-wakeups*

*empty-heap* *allocate*  $\lambda\cdot$ . *typeof-addr* *heap-read* *heap-write*

*P*

**for** *addr2thread-id* :: ('*addr* :: *addr*)  $\Rightarrow$  '*thread-id*

**and** *thread-id2addr* :: '*thread-id*  $\Rightarrow$  '*addr*

**and** *spurious-wakeups* :: *bool*

**and** *empty-heap* :: '*heap*

**and** *allocate* :: '*heap*  $\Rightarrow$  *h**type*  $\Rightarrow$  ('*heap*  $\times$  '*addr*) *set*

**and** *typeof-addr* :: '*addr*  $\rightarrow$  *h**type*

**and** *heap-read* :: '*heap*  $\Rightarrow$  '*addr*  $\Rightarrow$  *addr-loc*  $\Rightarrow$  '*addr val*  $\Rightarrow$  *bool*

**and** *heap-write* :: '*heap*  $\Rightarrow$  '*addr*  $\Rightarrow$  *addr-loc*  $\Rightarrow$  '*addr val*  $\Rightarrow$  '*heap*  $\Rightarrow$  *bool*

**and** *P* :: '*m* *prog*

**sublocale** *heap'* < *heap-base'*.

**context** *heap'* **begin**

**lemma** *vs-conf-w-value-WriteMemD*:

$\llbracket vs\text{-conf} \ P \ (w\text{-value} \ P \ vs \ ob); \ ob = \text{NormalAction} \ (\text{WriteMem} \ ad \ al \ v) \rrbracket$

$\implies \exists T. \ P \vdash ad@al : T \wedge P \vdash v : \leq T$

**by**(auto *elim*: *vs-confD*)

**lemma** *vs-conf-w-values-WriteMemD*:

$\llbracket vs\text{-conf} \ P \ (w\text{-values} \ P \ vs \ obs); \ \text{NormalAction} \ (\text{WriteMem} \ ad \ al \ v) \in \text{set} \ obs \rrbracket$

$\implies \exists T. \ P \vdash ad@al : T \wedge P \vdash v : \leq T$

**apply**(*induct obs arbitrary*: *vs*)

**apply**(auto 4 3 *elim*: *vs-confD* *intro*: *w-values-mono*[*THEN subsetD*])

**done**

**lemma** *w-values-vs-type-all-start-heap-obs*:

**assumes** *wf*: *wf-syscls* *P*

**shows** *w-values* *P* (*vs-type-all* *P*) (*map* *snd* (*lift-start-obs* *h.start-tid* *h.start-heap-obs*)) = *vs-type-all* *P*

(**is** ?*lhs* = ?*rhs*)



```

proof(rule antisym, rule le-funI, rule subsetI)
  fix adal v
  assume v: v ∈ ?lhs adal
  obtain ad al where adal: adal = (ad, al) by(cases adal)
  show v ∈ ?rhs adal
  proof(rule ccontr)
    assume v': ¬ ?thesis
    from in-w-valuesD[OF v[unfolded adal] this[unfolded adal]]
    obtain obs' wa obs''
      where eq: map snd (lift-start-obs h.start-tid h.start-heap-obs) = obs' @ wa # obs''
      and write: is-write-action wa
      and loc: (ad, al) ∈ action-loc-aux P wa
      and vwa: value-written-aux P wa al = v
    by blast+
  from write show False
  proof cases
    case (WriteMem ad' al' v')
      with vwa loc eq have WriteMem ad al v ∈ set h.start-heap-obs
        by(auto simp add: map-eq-append-conv Cons-eq-append-conv lift-start-obs-def)
      from h.start-heap-write-typeable[OF this] v' adal
      show ?thesis by(auto simp add: vs-type-all-def)
    next
      case (NewHeapElem ad' hT)
      with vwa loc eq have NewHeapElem ad hT ∈ set h.start-heap-obs
        by(auto simp add: map-eq-append-conv Cons-eq-append-conv lift-start-obs-def)
      hence typeof-addr ad = [hT]
        by(rule h.NewHeapElem-start-heap-obsD[OF wf])
      with v' adal loc vwa NewHeapElem show ?thesis
        by(auto simp add: vs-type-all-def intro: addr-loc-type-intros h.addr-loc-default-conf[unfolded
heap-independent])
      qed
    qed
  qed(rule w-values-greater)

end

```

```

lemma lprefix-lappend2I: lprefix xs ys  $\implies$  lprefix xs (lappend ys zs)
by(auto simp add: lappend-assoc lprefix-conv-lappend)

```

```

locale known-addr-typing' =
  h: known-addr-typing
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate λ-. typeof-addr heap-read heap-write
  allocated known-addr
  final r wfx
  P
  for addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
  and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
  and spurious-wakeups :: bool
  and empty-heap :: 'heap
  and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
  and typeof-addr :: 'addr  $\rightarrow$  htype

```

```

and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
and allocated :: 'heap  $\Rightarrow$  'addr set
and known-addr :: 'thread-id  $\Rightarrow$  'x  $\Rightarrow$  'addr set
and final :: 'x  $\Rightarrow$  bool
and r :: ('addr, 'thread-id, 'x, 'heap, 'addr, ('addr, 'thread-id) obs-event) semantics (-  $\vdash$  -  $\dashrightarrow$  -
[50,0,0,50] 80)
and wfx :: 'thread-id  $\Rightarrow$  'x  $\Rightarrow$  'heap  $\Rightarrow$  bool
and P :: 'md prog
+
assumes NewHeapElem-typed: — Should this be moved to known_addr_typing?
[[ t  $\vdash$  (x, h)  $\dashrightarrow$  (x', h'); NewHeapElem ad CTn  $\in$  set {ta}_o; typeof-addr ad  $\neq$  None ]]
 $\Rightarrow$  typeof-addr ad = [CTn]

```

**sublocale** known-addr-typing' < heap' **by** unfold-locales

**context** known-addr-typing' **begin**

**lemma** known-addr-typeable-in-vs-type-all:

*h.if.known-addr-state s  $\subseteq$  dom typeof-addr*

$\Rightarrow (\bigcup a \in h.if.known-addr-state s. \{(a, al) | al. \exists T. P \vdash a@al : T\}) \subseteq \{adal. vs-type-all P adal \neq \{\}\}$

**by**(auto 4 4 dest: subsetD simp add: vs-type-all.simps intro: defval-conf)

**lemma** if-NewHeapElem-typed:

$\llbracket t \vdash xh \dashrightarrow i x'h'; NormalAction (NewHeapElem ad CTn) \in set \{ta\}_o; typeof-addr ad \neq None \rrbracket$

$\Rightarrow typeof-addr ad = [CTn]$

**by**(cases rule: h.mthr.init-fin.cases)(auto dest: NewHeapElem-typed)

**lemma** if-redT-NewHeapElem-typed:

$\llbracket h.mthr.if.redT s (t, ta) s'; NormalAction (NewHeapElem ad CTn) \in set \{ta\}_o; typeof-addr ad \neq None \rrbracket$

$\Rightarrow typeof-addr ad = [CTn]$

**by**(cases rule: h.mthr.if.redT.cases)(auto dest: if-NewHeapElem-typed)

**lemma** non-speculative-written-value-typeable:

**assumes** wfx-start: ts-ok wfx (thr (h.start-state f P C M vs)) h.start-heap

**and** wfp: wf-syscls P

**and** E: E  $\in$  h. $\mathcal{E}$ -start f P C M vs status

**and** write: w  $\in$  write-actions E

**and** adal: (ad, al)  $\in$  action-loc P E w

**and** ns: non-speculative P (vs-type-all P) (lmap snd (ltake (enat w) E))

**shows**  $\exists T. P \vdash ad@al : T \wedge P \vdash value-written P E w (ad, al) : \leq T$

**proof** —

**let** ?start-state = init-fin-lift-state status (h.start-state f P C M vs)

**and** ?start-obs = lift-start-obs h.start-tid h.start-heap-obs

**and** ?v = value-written P E w (ad, al)

**from** write **have** iwa: is-write-action (action-obs E w) **by** cases

**from** E **obtain** E' **where** E': E = lappend (llist-of ?start-obs) E'

**and**  $\mathcal{E}$ : E'  $\in$  h.mthr.if. $\mathcal{E}$  ?start-state **by** blast

**from**  $\mathcal{E}$  **obtain** E'' **where** E'': E' = lconcat (lmap ( $\lambda(t, ta). llist-of (map (Pair t) \{ta\}_o)$ )) E''

**and** Runs: h.mthr.if.mthr.Runs ?start-state E''

```

by-(rule h.mthr.if.ℰ.cases[OF ℰ])

have wfx': ts-ok (init-fin-lift wfx) (thr ?start-state) (shr ?start-state)
  using wfx-start by(simp add: h.shr-start-state)

from ns E'
have ns: non-speculative P (vs-type-all P) (lmap snd (ldropn (length (lift-start-obs h.start-tid h.start-heap-obs))
  (ltake (enat w) E)))
  by(subst (asm) lappend-ltake-ldrop[where n=enat (length (lift-start-obs h.start-tid h.start-heap-obs)),
    symmetric])(simp add: non-speculative-lappend min-def ltake-lappend1 w-values-vs-type-all-start-heap-obs[OF
    wfP] ldrop-enat split: if-split-asm)

show ?thesis
proof(cases w < length ?start-obs)
  case True
  hence in-start: action-obs E w ∈ set (map snd ?start-obs)
  unfolding in-set-conv-nth E' by(simp add: lnth-lappend action-obs-def map-nth exI[where x=w])

  from iwa show ?thesis
  proof(cases)
    case (WriteMem ad' al' v')
    with adal have ad' = ad al' = al ?v = v' by(simp-all add: value-written.simps)
    with WriteMem in-start have WriteMem ad al ?v ∈ set h.start-heap-obs by auto
    thus ?thesis by(rule h.start-heap-write-typeable[unfolded heap-independent])
  next
    case (NewHeapElem ad' CTn)
    with adal have [simp]: ad' = ad by auto
    with NewHeapElem in-start have NewHeapElem ad CTn ∈ set h.start-heap-obs by auto
    with wfP have typeof-addr ad = [CTn] by(rule h.NewHeapElem-start-heap-obsD)
    with adal NewHeapElem show ?thesis
    by(cases al)(auto simp add: value-written.simps intro: addr-loc-type-intros h.addr-loc-default-conf[unfolded
    heap-independent])
  qed
  next
    case False
    define w' where w' = w - length ?start-obs
    with write False have w'-len: enat w' < llength E'
    by(cases llength E')(auto simp add: actions-def E' elim: write-actions.cases)
    with Runs obtain m-w n-w t-w ta-w
    where E'-w: lnth E' w' = (t-w, {ta-w}_o ! n-w)
    and n-w: n-w < length {ta-w}_o
    and m-w: enat m-w < llength E''
    and w-sum: w' = (∑ i<m-w. length {snd (lnth E'' i)}_o) + n-w
    and E''-m-w: lnth E'' m-w = (t-w, ta-w)
    unfolding E'' by(rule h.mthr.if.actions-ℰ E-aux)

    from E'-w have obs-w: action-obs E w = {ta-w}_o ! n-w
    using False E' w'-def by(simp add: action-obs-def lnth-lappend)

    let ?E'' = ldropn (Suc m-w) E''
    let ?m-E'' = ltake (enat m-w) E''
    have E'-unfold: E'' = lappend ?m-E'' (LCons (lnth E'' m-w) ?E'')
    unfolding ldropn-Suc-conv-ldropn[OF m-w] by simp
    hence h.mthr.if.mthr.Runs ?start-state (lappend ?m-E'' (LCons (lnth E'' m-w) ?E''))

```

```

    using Runs by simp
  then obtain  $\sigma'$  where  $\sigma\text{-}\sigma'$ :  $h.mthr.if.mthr.Trsys \text{ ?start-state } (list\text{-of } ?m\text{-}E'') \sigma'$ 
    and  $Runs'$ :  $h.mthr.if.mthr.Runs \sigma' (LCons (lnth E'' m\text{-}w) ?E'')$ 
    by(rule  $h.mthr.if.mthr.Runs\text{-}lappendE$ ) simp
  from  $Runs'$  obtain  $\sigma'''$  where  $red\text{-}w$ :  $h.mthr.if.redT \sigma' (t\text{-}w, ta\text{-}w) \sigma'''$ 
    and  $Runs''$ :  $h.mthr.if.mthr.Runs \sigma''' ?E''$ 
    unfolding  $E''\text{-}m\text{-}w$  by cases

  let  $?EE'' = lmap\ snd\ (lappend\ (lconcat\ (lmap\ (\lambda(t, ta). llist\text{-of}\ (map\ (Pair\ t)\ \{\!\{ta\}\!\}_o))\ ?m\text{-}E''))$ 
    ( $llist\text{-of}\ (map\ (Pair\ t\text{-}w)\ (take\ (n\text{-}w + 1)\ \{\!\{ta\text{-}w\}\!\}_o))$ ))
  have  $len\text{-}EE''$ :  $llength\ ?EE'' = enat\ (w' + 1)$  using  $n\text{-}w\ m\text{-}w$ 
    apply(simp add:  $w\text{-}sum$ )
    apply(subst  $llength\text{-}lconcat\text{-}lfinite\text{-}conv\text{-}sum$ )
    apply(simp-all add:  $split\text{-}beta\ plus\text{-}enat\text{-}simps(1)[symmetric]$   $add\text{-}Suc\text{-}right[symmetric]$   $del: plus\text{-}enat\text{-}simps(1)$ 
       $add\text{-}Suc\text{-}right$ )
    apply(subst  $sum\text{-}comp\text{-}morphism[symmetric, \text{ where } h=enat]$ )
    apply(simp-all add:  $zero\text{-}enat\text{-}def\ min\text{-}def\ le\text{-}Suc\text{-}eq$ )
    apply(rule  $sum.cong$ )
    apply(auto simp add:  $lnth\text{-}ltake\ less\text{-}trans[\text{ where } y=enat\ m\text{-}w]$ )
  done
  have prefix:  $lprefix\ ?EE'' (lmap\ snd\ E')$  unfolding  $E''$ 
    by(subst (2)  $E'\text{-}unfold$ )(rule  $lmap\text{-}lprefix, clarsimp\ simp\ add: lmap\text{-}lappend\text{-}distrib\ E''\text{-}m\text{-}w$ 
       $lprefix\text{-}lappend2I[OF\ lprefix\text{-}llist\text{-}ofI[OF\ exI[\text{ where } x=map\ (Pair\ t\text{-}w)\ (drop\ (n\text{-}w + 1)\ \{\!\{ta\text{-}w\}\!\}_o)]]$ 
       $map\text{-}append[symmetric]$ )

  from  $iwa\ False$  have  $iwa'$ :  $is\text{-}write\text{-}action\ (action\text{-}obs\ E'\ w')$  by(simp add:  $E'\ action\text{-}obs\text{-}def$ 
     $lnth\text{-}lappend\ w'\text{-}def$ )
  from  $ns\ False$ 
  have  $non\text{-}speculative\ P\ (vs\text{-}type\text{-}all\ P)\ (lmap\ snd\ (ltake\ (enat\ w')\ E'))$ 
    by(simp add:  $E'\ ltake\text{-}lappend\ lmap\text{-}lappend\text{-}distrib\ non\text{-}speculative\text{-}lappend\ ldropsn\text{-}lappend2$ 
       $w'\text{-}def$ )
  with  $iwa'$ 
  have  $non\text{-}speculative\ P\ (vs\text{-}type\text{-}all\ P)\ (lappend\ (lmap\ snd\ (ltake\ (enat\ w')\ E'))\ (LCons\ (action\text{-}obs\ E'\ w')\ LNil))$ 
    by cases(simp-all add:  $non\text{-}speculative\text{-}lappend$ )
  also have  $lappend\ (lmap\ snd\ (ltake\ (enat\ w')\ E'))\ (LCons\ (action\text{-}obs\ E'\ w')\ LNil) = lmap\ snd$ 
    ( $ltake\ (enat\ (w' + 1))\ E'$ )
    using  $w'\text{-}len$  by(simp add:  $ltake\text{-}Suc\text{-}conv\text{-}snoc\text{-}lnth\ lmap\text{-}lappend\text{-}distrib\ action\text{-}obs\text{-}def$ )
  also {
    have  $lprefix\ (lmap\ snd\ (ltake\ (enat\ (w' + 1))\ E'))\ (lmap\ snd\ E')$  by(rule  $lmap\text{-}lprefix$ ) simp
    with prefix have  $lprefix\ ?EE'' (lmap\ snd\ (ltake\ (enat\ (w' + 1))\ E')) \vee$ 
       $lprefix\ (lmap\ snd\ (ltake\ (enat\ (w' + 1))\ E'))\ ?EE''$ 
      by(rule  $lprefix\text{-}down\text{-}linear$ )
    moreover have  $llength\ (lmap\ snd\ (ltake\ (enat\ (w' + 1))\ E')) = enat\ (w' + 1)$ 
      using  $w'\text{-}len$  by(cases  $llength\ E'$ ) simp-all
    ultimately have  $lmap\ snd\ (ltake\ (enat\ (w' + 1))\ E') = ?EE''$ 
      using  $len\text{-}EE''$  by(auto dest:  $lprefix\text{-}llength\text{-}eq\text{-}imp\text{-}eq$ ) }
  finally
  have  $ns1$ :  $non\text{-}speculative\ P\ (vs\text{-}type\text{-}all\ P)\ (llist\text{-of}\ (concat\ (map\ (\lambda(t, ta). \{\!\{ta\}\!\}_o)\ (list\text{-of}\ ?m\text{-}E''))))$ 
    and  $ns2$ :  $non\text{-}speculative\ P\ (w\text{-}values\ P\ (vs\text{-}type\text{-}all\ P)\ (map\ snd\ (list\text{-of}\ (lconcat\ (lmap\ (\lambda(t, ta). llist\text{-of}\ (map\ (Pair\ t)\ \{\!\{ta\}\!\}_o))\ ?m\text{-}E'')))))$ 
    ( $llist\text{-of}\ (take\ (Suc\ n\text{-}w)\ \{\!\{ta\text{-}w\}\!\}_o)$ )
    by(simp-all add:  $lmap\text{-}lappend\text{-}distrib\ non\text{-}speculative\text{-}lappend\ split\text{-}beta\ lconcat\text{-}llist\text{-}of[symmetric]$ 
       $lmap\text{-}lconcat\ llist.map\text{-}comp\ o\text{-}def\ split\text{-}def\ list\text{-of}\text{-}lmap[symmetric]$   $del: list\text{-of}\text{-}lmap$ )

```

```

have vs-conf P (vs-type-all P) by simp
with  $\sigma$ - $\sigma'$  wfx' ns1
have wfx': ts-ok (init-fin-lift wfx) (thr  $\sigma'$ ) (shr  $\sigma'$ )
and vs-conf: vs-conf P (w-values P (vs-type-all P) (concat (map ( $\lambda(t, ta). \llbracket ta \rrbracket_o$ ) (list-of ?m-E''))))
  by(rule h.if-RedT-non-speculative-invar[unfolded h.mthr.if.RedT-def heap-independent])+

have concat (map ( $\lambda(t, ta). \llbracket ta \rrbracket_o$ ) (list-of ?m-E'')) = map snd (list-of (lconcat (lmap ( $\lambda(t, ta). \llist-of$ 
  (map (Pair t)  $\llbracket ta \rrbracket_o$ )) ?m-E''))))
  by(simp add: split-def lmap-lconcat llist.map-comp o-def list-of-lconcat map-concat)
with vs-conf have vs-conf P (w-values P (vs-type-all P) ...) by simp
with red-w wfx' ns2
have vs-conf': vs-conf P (w-values P (w-values P (vs-type-all P) (map snd (list-of (lconcat (lmap
  ( $\lambda(t, ta). \llist-of$  (map (Pair t)  $\llbracket ta \rrbracket_o$ )) ?m-E''))))) (take (Suc n-w)  $\llbracket ta-w \rrbracket_o$ ))
  (is vs-conf - ?vs')
  by(rule h.if-redT-non-speculative-vs-conf[unfolded heap-independent])

from len-EE'' have enat w' < llength ?EE'' by simp
from w'-len have lnth ?EE'' w' = action-obs E' w'
  using lprefix-lnthD[OF prefix  $\langle enat w' < llength ?EE'' \rangle$ ] by(simp add: action-obs-def)
hence ...  $\in$  lset ?EE'' using  $\langle enat w' < llength ?EE'' \rangle$  unfolding lset-conv-lnth by(auto intro!:
exI)
  also have ...  $\subseteq$  set (map snd (list-of (lconcat (lmap ( $\lambda(t, ta). \llist-of$  (map (Pair t)  $\llbracket ta \rrbracket_o$ ))
  ?m-E'')))) @ take (Suc n-w)  $\llbracket ta-w \rrbracket_o$ )
  by(auto 4 4 intro: rev-image-eqI rev-bexI simp add: split-beta lset-lconcat-lfinite dest: lset-lappend[THEN
subsetD])
  also have action-obs E' w' = action-obs E w
    using False by(simp add: E' w'-def lnth-lappend action-obs-def)
  also note obs-w-in-set = calculation and calculation = nothing

from iwa have ?v  $\in$  w-values P (vs-type-all P) (map snd (list-of (lconcat (lmap ( $\lambda(t, ta). \llist-of$ 
  (map (Pair t)  $\llbracket ta \rrbracket_o$ )) ?m-E'')))) @ take (Suc n-w)  $\llbracket ta-w \rrbracket_o$ ) (ad, al)
proof(cases)
  case (WriteMem ad' al' v')
  with adal have ad' = ad al' = al ?v = v' by(simp-all add: value-written.simps)
  with obs-w-in-set WriteMem show ?thesis
    by -(rule w-values-WriteMemD, simp)
next
  case (NewHeapElem ad' CTn)
  with adal have [simp]: ad' = ad and v: ?v = addr-loc-default P CTn al
    by(auto simp add: value-written.simps)
  with obs-w-in-set NewHeapElem adal show ?thesis
    by(unfold v)(rule w-values-new-actionD, simp-all)
qed
hence ?v  $\in$  ?vs' (ad, al) by simp
with vs-conf' show  $\exists T. P \vdash ad@al : T \wedge P \vdash ?v : \leq T$ 
  by(rule h.vs-confD[unfolded heap-independent])
qed
qed

lemma hb-read-value-typeable:
  assumes wfx-start: ts-ok wfx (thr (h.start-state f P C M vs)) h.start-heap
    (is ts-ok wfx (thr ?start-state) -)
  and wfp: wf-syscls P
  and E: E  $\in$  h.E-start f P C M vs status

```

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and wf:  $P \vdash (E, ws) \checkmark$ 
and races:  $\bigwedge a \text{ ad } al \ v. \llbracket \text{enat } a < \text{llength } E; \text{action-obs } E \ a = \text{NormalAction } (\text{ReadMem } ad \ al \ v); \neg$ 
 $P, E \vdash ws \ a \leq_{hb} a \rrbracket$ 
 $\implies \exists T. P \vdash ad@al : T \wedge P \vdash v : \leq T$ 
and r:  $\text{enat } a < \text{llength } E$ 
and read:  $\text{action-obs } E \ a = \text{NormalAction } (\text{ReadMem } ad \ al \ v)$ 
shows  $\exists T. P \vdash ad@al : T \wedge P \vdash v : \leq T$ 
using r read
proof(induction a arbitrary: ad al v rule: less-induct)
  case (less a)
    note  $r = \langle \text{enat } a < \text{llength } E \rangle$ 
    and read =  $\langle \text{action-obs } E \ a = \text{NormalAction } (\text{ReadMem } ad \ al \ v) \rangle$ 
    show ?case
    proof(cases P, E  $\vdash$  ws a  $\leq_{hb}$  a)
      case False with r read show ?thesis by(rule races)
    next
      case True
      note hb = this
      hence ao:  $E \vdash ws \ a \leq a$  by(rule happens-before-into-action-order)

from wf have ws: is-write-seen P E ws by(rule wf-exec-is-write-seenD)
from r have  $a \in \text{actions } E$  by(simp add: actions-def)
hence  $a \in \text{read-actions } E$  using read ..
from is-write-seenD[OF ws this read]
have write:  $ws \ a \in \text{write-actions } E$ 
  and adal-w:  $(ad, al) \in \text{action-loc } P \ E \ (ws \ a)$ 
  and written:  $\text{value-written } P \ E \ (ws \ a) \ (ad, al) = v$  by simp-all
from write have iwa: is-write-action (action-obs E (ws a)) by cases

let ?start-state = init-fin-lift-state status (h.start-state f P C M vs)
and ?start-obs = lift-start-obs h.start-tid h.start-heap-obs

show ?thesis
proof(cases ws a < a)
  case True
  let ?EE'' =  $\text{lmap snd } (\text{ltake } (\text{enat } (ws \ a)) \ E)$ 

  have non-speculative P (vs-type-all P) ?EE''
  proof(rule non-speculative-nthI)
    fix i ad' al' v'
    assume i:  $\text{enat } i < \text{llength } ?EE''$ 
    and nth-i:  $\text{lnth } ?EE'' \ i = \text{NormalAction } (\text{ReadMem } ad' \ al' \ v')$ 

    from i have  $i < ws \ a$  by simp
    hence i':  $i < a$  using True by(simp)
    moreover
    with r have  $\text{enat } i < \text{llength } E$  by(metis enat-ord-code(2) order-less-trans)
    moreover
    with nth-i i  $\langle i < ws \ a \rangle$ 
    have  $\text{action-obs } E \ i = \text{NormalAction } (\text{ReadMem } ad' \ al' \ v')$ 
    by(simp add: action-obs-def lnth-ltake ac-simps)
    ultimately have  $\exists T. P \vdash ad'@al' : T \wedge P \vdash v' : \leq T$  by(rule less.IH)
    hence  $v' \in \text{vs-type-all } P \ (ad', al')$  by(simp add: vs-type-all.simps)
    thus  $v' \in \text{w-values } P \ (\text{vs-type-all } P) \ (\text{list-of } (\text{ltake } (\text{enat } i) \ ?EE'')) \ (ad', al')$ 

```

```

    by(rule w-values-mono[THEN subsetD])
qed
with wfx-start wfP E write adal-w
show ?thesis unfolding written[symmetric] by(rule non-speculative-written-value-typeable)
next
case False

from E obtain E' where E': E = lappend (llist-of ?start-obs) E'
  and  $\mathcal{E}$ :  $E' \in h.mthr.if.\mathcal{E} \text{ ?start-state}$  by blast
from  $\mathcal{E}$  obtain E'' where E'':  $E' = lconcat (lmap (\lambda(t, ta). llist-of (map (Pair t) \{ta\}_o)) E'')$ 
  and Runs:  $h.mthr.if.mthr.Runs \text{ ?start-state } E''$ 
  by-(rule h.mthr.if. $\mathcal{E}$ .cases[OF  $\mathcal{E}$ ])

have wfx': ts-ok (init-fin-lift wfx) (thr ?start-state) (shr ?start-state)
  using wfx-start by(simp add: h.shr-start-state)

have a-start:  $\neg a < \text{length ?start-obs}$ 
proof
  assume  $a < \text{length ?start-obs}$ 
  with read have NormalAction (ReadMem ad al v)  $\in \text{snd 'set ?start-obs}$ 
    unfolding set-map[symmetric] in-set-conv-nth
    by(auto simp add: E' lnth-lappend action-obs-def)
  hence ReadMem ad al v  $\in \text{set } h.\text{start-heap-obs}$  by auto
  thus False by(simp add: h.start-heap-obs-not-Read)
qed
hence ws-a-not-le:  $\neg ws \ a < \text{length ?start-obs}$  using False by simp

define w where  $w = ws \ a - \text{length ?start-obs}$ 
from write ws-a-not-le w-def
have enat w  $< \text{llength } (lconcat (lmap (\lambda(t, ta). llist-of (map (Pair t) \{ta\}_o)) E''))$ 
  by(cases llength (lconcat (lmap (\lambda(t, ta). llist-of (map (Pair t) \{ta\}_o)) E''))(auto simp add:
actions-def E' E'' elim: write-actions.cases))
with Runs obtain m-w n-w t-w ta-w
  where E'-w:  $lnth \ E' \ w = (t-w, \{ta-w\}_o ! n-w)$ 
  and n-w:  $n-w < \text{length } \{ta-w\}_o$ 
  and m-w:  $\text{enat } m-w < \text{llength } E''$ 
  and w-sum:  $w = (\sum i < m-w. \text{length } \{snd (lnth \ E'' \ i)\}_o) + n-w$ 
  and E''-m-w:  $lnth \ E'' \ m-w = (t-w, ta-w)$ 
  unfolding E'' by(rule h.mthr.if.actions- $\mathcal{E}$  E-aux)

from E'-w have obs-w:  $\text{action-obs } E \ (ws \ a) = \{ta-w\}_o ! n-w$ 
  using ws-a-not-le E' w-def by(simp add: action-obs-def lnth-lappend)

let ?E'' = ldrown (Suc m-w) E''
let ?m-E'' = ltake (enat m-w) E''
have E'-unfold:  $E'' = lappend \ ?m-E'' \ (LCons (lnth \ E'' \ m-w) \ ?E'')$ 
  unfolding ldrown-Suc-conv-ldrown[OF m-w] by simp
hence h.mthr.if.mthr.Runs ?start-state (lappend ?m-E'' (LCons (lnth E'' m-w) ?E''))
  using Runs by simp
then obtain  $\sigma'$  where  $\sigma-\sigma'$ :  $h.mthr.if.mthr.Trsys \ ?start-state \ (\text{list-of } ?m-E'') \ \sigma'$ 
  and Runs':  $h.mthr.if.mthr.Runs \ \sigma' \ (LCons (lnth \ E'' \ m-w) \ ?E'')$ 
  by(rule h.mthr.if.mthr.Runs-lappendE) simp
from Runs' obtain  $\sigma'''$  where red-w:  $h.mthr.if.redT \ \sigma' \ (t-w, ta-w) \ \sigma'''$ 
  and Runs'':  $h.mthr.if.mthr.Runs \ \sigma''' \ ?E''$ 

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unfolding  $E''$ - $m$ - $w$  by cases

from write  $\langle a \in \text{read-actions } E \rangle$  have  $ws\ a \neq a$  by(auto dest: read-actions-not-write-actions)
with False have  $ws\ a > a$  by simp
with  $ao$  have new: is-new-action (action-obs E (ws a))
  by(simp add: action-order-def split: if-split-asm)
then obtain  $CTn$  where  $obs\ w': \text{action-obs } E\ (ws\ a) = \text{NormalAction } (\text{NewHeapElem } ad\ CTn)$ 
  using adal-w by cases auto

define  $a'$  where  $a' = a - \text{length } ?start\text{-obs}$ 
with False  $w\text{-def}$ 
have  $enat\ a' < llength\ (lconcat\ (lmap\ (\lambda(t, ta).\ llist\text{-of}\ (map\ (Pair\ t)\ \{\!\{ta\}\!\}_o))\ E''))$ 
  by(simp add: le-less-trans[OF -  $\langle enat\ w < llength\ (lconcat\ (lmap\ (\lambda(t, ta).\ llist\text{-of}\ (map\ (Pair\ t)\ \{\!\{ta\}\!\}_o))\ E'')) \rangle$ ])
with Runs obtain  $m\text{-}a\ n\text{-}a\ t\text{-}a\ ta\text{-}a$ 
  where  $E'\text{-}a: lnth\ E'\ a' = (t\text{-}a, \{\!\{ta\}\!\}_o ! n\text{-}a)$ 
  and  $n\text{-}a: n\text{-}a < \text{length } \{\!\{ta\}\!\}_o$ 
  and  $m\text{-}a: enat\ m\text{-}a < llength\ E''$ 
  and  $a\text{-sum}: a' = (\sum i < m\text{-}a.\ \text{length } \{\!\{snd\ (lnth\ E''\ i)\}\!\}_o) + n\text{-}a$ 
  and  $E''\text{-}m\text{-}a: lnth\ E''\ m\text{-}a = (t\text{-}a, ta\text{-}a)$ 
  unfolding  $E''$  by(rule h.mthr.if.actions- $\mathcal{E}E\text{-aux}$ )

from  $a\text{-start } E'\text{-}a$  read have  $obs\text{-}a: \{\!\{ta\}\!\}_o ! n\text{-}a = \text{NormalAction } (\text{ReadMem } ad\ al\ v)$ 
  using  $E'\ w\text{-def}$  by(simp add: action-obs-def lnth-lappend a'\text{-def})

let  $?E'' = ldropn\ (Suc\ m\text{-}a)\ E''$ 
let  $?m\text{-}E'' = ltake\ (enat\ m\text{-}a)\ E''$ 
have  $E'\text{-unfold}: E'' = lappend\ ?m\text{-}E''\ (LCons\ (lnth\ E''\ m\text{-}a)\ ?E'')$ 
  unfolding ldropn-Suc-conv-ldropn[OF m-a] by simp
hence  $h.mthr.if.mthr.Runs\ ?start\text{-state}\ (lappend\ ?m\text{-}E''\ (LCons\ (lnth\ E''\ m\text{-}a)\ ?E''))$ 
  using Runs by simp
then obtain  $\sigma''$  where  $\sigma\text{-}\sigma'': h.mthr.if.mthr.Trsys\ ?start\text{-state}\ (list\text{-of}\ ?m\text{-}E'')\ \sigma''$ 
  and  $Runs'': h.mthr.if.mthr.Runs\ \sigma''\ (LCons\ (lnth\ E''\ m\text{-}a)\ ?E'')$ 
  by(rule h.mthr.if.mthr.Runs-lappendE) simp
from  $Runs''$  obtain  $\sigma'''$  where  $red\text{-}a: h.mthr.if.redT\ \sigma''\ (t\text{-}a, ta\text{-}a)\ \sigma'''$ 
  and  $Runs''': h.mthr.if.mthr.Runs\ \sigma'''\ ?E''$ 
  unfolding  $E''\text{-}m\text{-}a$  by cases

let  $?EE'' = llist\text{-of}\ (concat\ (map\ (\lambda(t, ta).\ \{\!\{ta\}\!\}_o)\ (list\text{-of}\ ?m\text{-}E''))$ 
from  $m\text{-}a$  have  $enat\ m\text{-}a \leq llength\ E''$  by simp
hence  $len\text{-}EE'': llength\ ?EE'' = enat\ (a' - n\text{-}a)$ 
  by(simp add: a-sum length-concat sum-list-sum-nth atLeast0LessThan length-list-of-conv-the-enat min-def split-beta lnth-ltake)
have prefix: lprefix ?EE'' (lmap snd E') unfolding  $E''$ 
  by(subst (2) E'\text{-unfold})(simp add: lmap-lappend-distrib lmap-lconcat llist.map-comp o-def split-def lconcat-llist-of[symmetric] lmap-llist-of[symmetric] lprefix-lappend2I del: lmap-llist-of)

have  $ns: \text{non-speculative } P\ (vs\text{-type-all } P)\ ?EE''$ 
proof(rule non-speculative-nthI)
  fix  $i\ ad'\ al'\ v'$ 
  assume  $i: enat\ i < llength\ ?EE''$ 
  and  $lnth\text{-}i: lnth\ ?EE''\ i = \text{NormalAction } (\text{ReadMem } ad'\ al'\ v')$ 
  and  $\text{non-speculative } P\ (vs\text{-type-all } P)\ (ltake\ (enat\ i)\ ?EE'')$ 

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let ?i = i + length ?start-obs

from i len-EE'' have i < a' by simp
hence i': ?i < a by (simp add: a'-def)
moreover
hence enat ?i < llength E using ⟨enat a < llength E⟩ by (simp add: less-trans[where y=enat
a])
moreover have enat i < llength E' using i
  by -(rule less-le-trans[OF - lprefix-llength-le[OF prefix], simplified], simp)
from lprefix-lnthD[OF prefix i] lnth-i
have lnth (lmap snd E') i = NormalAction (ReadMem ad' al' v') by simp
hence action-obs E ?i = NormalAction (ReadMem ad' al' v') using ⟨enat i < llength E'⟩
  by (simp add: E' action-obs-def lnth-lappend E'')
ultimately have ∃ T. P ⊢ ad'@al' : T ∧ P ⊢ v' :≤ T by (rule less.IH)
hence v' ∈ vs-type-all P (ad', al') by (simp add: vs-type-all.simps)
thus v' ∈ w-values P (vs-type-all P) (list-of (ltake (enat i) ?EE'')) (ad', al')
  by (rule w-values-mono[THEN subsetD])
qed

have vs-conf P (vs-type-all P) by simp
with σ-σ'' wfx' ns
have wfx'': ts-ok (init-fin-lift wfx) (thr σ'') (shr σ'')
and vs'': vs-conf P (w-values P (vs-type-all P) (concat (map (λ(t, ta). ⌊ta⌋o) (list-of ?m-E''))))
  by (rule h.if-RedT-non-speculative-invar[unfolded heap-independent h.mthr.if.RedT-def])+

note red-w moreover
from n-w obs-w obs-w' have NormalAction (NewHeapElem ad CTh) ∈ set ⌊ta-w⌋o
  unfolding in-set-conv-nth by auto
moreover
have ta-a-read: NormalAction (ReadMem ad al v) ∈ set ⌊ta-a⌋o
  using n-a obs-a unfolding in-set-conv-nth by blast
from red-a have ∃ T. P ⊢ ad@al : T
proof (cases)
case (redT-normal x x' h')
from wfx'' ⟨thr σ'' t-a = ⌊(x, no-wait-locks)⌋⟩
have init-fin-lift wfx t-a x (shr σ'') by (rule ts-okD)
with ⟨t-a ⊢ (x, shr σ'') -ta-a→i (x', h')⟩
show ?thesis using ta-a-read
  by (rule h.init-fin-red-read-typeable[unfolded heap-independent])
next
case redT-acquire thus ?thesis using n-a obs-a ta-a-read by auto
qed
hence typeof-addr ad ≠ None by (auto elim: addr-loc-type-cases)
ultimately have typeof-addr ad = ⌊CTh⌋ by (rule if-redT-NewHeapElem-typed)
with written adal-w obs-w' show ?thesis
by (cases al) (auto simp add: value-written.simps intro: addr-loc-type-intros h.addr-loc-default-conf[unfolded
heap-independent])
qed
qed
qed

theorem
assumes wfx-start: ts-ok wfx (thr (h.start-state f P C M vs)) h.start-heap
and wfp: wf-syscls P

```

**and justified:**  $P \vdash (E, ws)$  weakly-justified-by  $J$   
**and  $J$ :**  $\text{range } (\text{justifying-exec} \circ J) \subseteq h.\mathcal{E}\text{-start } f P C M \text{ vs status}$   
**shows read-value-typeable-justifying:**  
 $\llbracket 0 < n; \text{enat } a < \text{llength } (\text{justifying-exec } (J n));$   
 $\text{action-obs } (\text{justifying-exec } (J n)) a = \text{NormalAction } (\text{ReadMem } ad \ al \ v) \rrbracket$   
 $\implies \exists T. P \vdash ad@al : T \wedge P \vdash v : \leq T$   
**and read-value-typeable-justified:**  
 $\llbracket E \in h.\mathcal{E}\text{-start } f P C M \text{ vs status}; P \vdash (E, ws) \checkmark;$   
 $\text{enat } a < \text{llength } E; \text{action-obs } E a = \text{NormalAction } (\text{ReadMem } ad \ al \ v) \rrbracket$   
 $\implies \exists T. P \vdash ad@al : T \wedge P \vdash v : \leq T$

**proof –**

**let**  $?E = \lambda n. \text{justifying-exec } (J n)$   
**and**  $? \varphi = \lambda n. \text{action-translation } (J n)$   
**and**  $?C = \lambda n. \text{committed } (J n)$   
**and**  $?ws = \lambda n. \text{justifying-ws } (J n)$   
**let**  $? \mathcal{E} = h.\mathcal{E}\text{-start } f P C M \text{ vs status}$   
**and**  $?start\text{-obs} = \text{lift-start-obs } h.\text{start-tid } h.\text{start-heap-obs}$   
**{ fix**  $a \ n$   
**assume**  $\text{enat } a < \text{llength } (\text{justifying-exec } (J n))$   
**and**  $\text{action-obs } (\text{justifying-exec } (J n)) a = \text{NormalAction } (\text{ReadMem } ad \ al \ v)$   
**and**  $n > 0$   
**thus**  $\exists T. P \vdash ad@al : T \wedge P \vdash v : \leq T$   
**proof**(*induction  $n$  arbitrary:  $a \ ad \ al \ v$* )  
**case**  $0$  **thus**  $?case$  **by** *simp*  
**next**  
**case**  $(\text{Suc } n')$   
**define**  $n$  **where**  $n = \text{Suc } n'$   
**with**  $\text{Suc}$  **have**  $n: 0 < n$  **and**  $a: \text{enat } a < \text{llength } (?E \ n)$   
**and**  $a\text{-obs}: \text{action-obs } (?E \ n) a = \text{NormalAction } (\text{ReadMem } ad \ al \ v)$   
**by** *simp-all*  
**have**  $wf\text{-}n: P \vdash (?E \ n, ?ws \ n) \checkmark$   
**using** *justified by(simp add: justification-well-formed-def)*  
**from**  $J$  **have**  $E: ?E \ n \in ? \mathcal{E}$   
**and**  $E': ?E \ n' \in ? \mathcal{E}$  **by** *auto*  
**from**  $a \ a\text{-obs}$   $wf\text{-}start \ wfP \ E \ wf\text{-}n$  **show**  $?case$   
**proof**(*rule hb-read-value-typeable[rotated -2]*)  
**fix**  $a' \ ad' \ al' \ v'$   
**assume**  $a': \text{enat } a' < \text{llength } (?E \ n)$   
**and**  $a'\text{-obs}: \text{action-obs } (?E \ n) a' = \text{NormalAction } (\text{ReadMem } ad' \ al' \ v')$   
**and**  $n\text{hb}: \neg P, ?E \ n \vdash ?ws \ n \ a' \leq_{hb} a'$   
**from**  $a'$  **have**  $a' \in \text{actions } (?E \ n)$  **by**(*simp add: actions-def*)  
**hence**  $\text{read-}a': a' \in \text{read-actions } (?E \ n)$  **using**  $a'\text{-obs} \dots$   
**with** *justified nhb* **have**  $\text{committed}': ? \varphi \ n \ a' \in ? \varphi \ n' \text{ ' } ?C \ n'$   
**unfolding** *is-weakly-justified-by.simps n-def uncommitted-reads-see-hb-def* **by** *blast*  
  
**from** *justified* **have**  $wfa\text{-}n: wf\text{-action-translation } E \ (J \ n)$   
**and**  $wfa\text{-}n': wf\text{-action-translation } E \ (J \ n')$  **by**(*simp-all add: wf-action-translations-def*)  
**hence**  $\text{inj-}n: \text{inj-on } (? \varphi \ n) (\text{actions } (?E \ n))$   
**and**  $\text{inj-}n': \text{inj-on } (? \varphi \ n') (\text{actions } (?E \ n'))$   
**by**(*blast dest: wf-action-translation-on-inj-onD*)  
**from** *justified* **have**  $C\text{-}n: ?C \ n \subseteq \text{actions } (?E \ n)$   
**and**  $C\text{-}n': ?C \ n' \subseteq \text{actions } (?E \ n')$   
**and**  $wf\text{-}n': P \vdash (?E \ n', ?ws \ n') \checkmark$   
**by**(*simp-all add: committed-subset-actions-def justification-well-formed-def*)

**from** *justified* **have**  $? \varphi \ n' \vdash ?C \ n' \subseteq ? \varphi \ n \vdash ?C \ n$   
**unfolding** *n-def* **by**(*simp add: is-commit-sequence-def*)  
**with** *n-def committed'* **have**  $? \varphi \ n \ a' \in ? \varphi \ n \vdash ?C \ n$  **by** *auto*  
**with** *inj-n C-n* **have** *committed: a' ∈ ?C n*  
**using**  $\langle a' \in \text{actions } (?E \ n) \rangle$  **by**(*auto dest: inj-onD*)  
**with** *justified read-a'* **have** *ws-committed: ws (?φ n a') ∈ ?φ n ⊢ ?C n*  
**by**(*rule weakly-justified-write-seen-hb-read-committed*)

**from** *wf-n* **have** *ws-n: is-write-seen P (?E n) (?ws n)* **by**(*rule wf-exec-is-write-seenD*)  
**from** *is-write-seenD[OF this read-a' a'-obs]*  
**have** *ws-write: ?ws n a' ∈ write-actions (?E n)*  
**and** *adal: (ad', al') ∈ action-loc P (?E n) (?ws n a')*  
**and** *written: value-written P (?E n) (?ws n a') (ad', al') = v'* **by** *simp-all*

**define** *a'' where a'' = inv-into (actions (?E n')) (?φ n') (?φ n a')*  
**from** *C-n' n committed'* **have**  $? \varphi \ n \ a' \in ? \varphi \ n' \vdash \text{actions } (?E \ n')$  **by** *auto*  
**hence** *a'': ?φ n' a'' = ?φ n a'*  
**and** *a''-action: a'' ∈ actions (?E n')* **using** *inj-n' committed' n*  
**by**(*simp-all add: a''-def f-inv-into-f inv-into-into*)  
**hence** *committed'': a'' ∈ ?C n'* **using** *committed' n inj-n' C-n'* **by**(*fastforce dest: inj-onD*)

**from** *committed committed'' wfa-n wfa-n' a''* **have** *action-obs (?E n') a'' ≈ action-obs (?E n)*  
**by**(*auto dest!: wf-action-translation-on-actionD intro: sim-action-trans sim-action-sym*)  
**with** *a'-obs committed'' C-n'* **have** *read-a'': a'' ∈ read-actions (?E n')*  
**by**(*auto intro: read-actions.intros*)

**then obtain** *ad'' al'' v''*  
**where** *a''-obs: action-obs (?E n') a'' = NormalAction (ReadMem ad'' al'' v'')* **by** *cases*

**from** *committed''* **have** *n' > 0* **using** *justified*  
**by**(*cases n')(simp-all add: is-commit-sequence-def*)  
**then obtain** *n'' where n'': n' = Suc n''* **by**(*cases n') simp-all*

**from** *justified* **have** *wfa-n'': wf-action-translation E (J n'')* **by**(*simp add: wf-action-translations-def*)  
**hence** *inj-n'': inj-on (?φ n'') (actions (?E n''))* **by**(*blast dest: wf-action-translation-on-inj-onD*) +  
**from** *justified* **have** *C-n'': ?C n'' ⊆ actions (?E n'')* **by**(*simp add: committed-subset-actions-def*)

**from** *justified committed' committed'' n-def read-a' read-a'' n*  
**have**  $? \varphi \ n \ (?ws \ n \ (\text{inv-into } (\text{actions } (?E \ n)) \ (? \varphi \ n) \ (? \varphi \ n' \ a''))) = ws \ (? \varphi \ n' \ a'')$   
**by**(*simp add: write-seen-committed-def*)  
**hence**  $? \varphi \ n \ (?ws \ n \ a') = ws \ (? \varphi \ n \ a')$  **using** *inj-n*  $\langle a' \in \text{actions } (?E \ n) \rangle$  **by**(*simp add: a''*)

**from** *ws-committed* **obtain** *w where w: ws (?φ n a') = ?φ n w*  
**and** *committed-w: w ∈ ?C n* **by** *blast*  
**from** *committed-w C-n* **have** *w ∈ actions (?E n)* **by** *blast*  
**hence** *w-def: w = ?ws n a'* **using**  $\langle ? \varphi \ n \ (?ws \ n \ a') = ws \ (? \varphi \ n \ a') \rangle$  *inj-n ws-write*  
**unfolding** *w* **by**(*auto dest: inj-onD*)  
**have** *committed-ws: ?ws n a' ∈ ?C n* **using** *committed-w* **by**(*simp add: w-def*)

**with** *wfa-n* **have** *sim-ws: action-obs (?E n) (?ws n a') ≈ action-obs E (?φ n (?ws n a'))*  
**by**(*blast dest: wf-action-translation-on-actionD*)

**from** *wfa-n committed-ws* **have** *sim-ws: action-obs* ( $?E\ n$ ) ( $?ws\ n\ a'$ )  $\approx$  *action-obs*  $E$  ( $? \varphi\ n$  ( $?ws\ n\ a'$ ))  
**by** (*blast dest: wf-action-translation-on-actionD*)  
**with** *adal* **have** *adal-E: (ad', al')  $\in$  action-loc  $P\ E$*  ( $? \varphi\ n$  ( $?ws\ n\ a'$ ))  
**by** (*simp add: action-loc-aux-sim-action*)  
  
**have**  $\exists w \in \text{write-actions } (?E\ n'). (ad', al') \in \text{action-loc } P\ (?E\ n')\ w \wedge \text{value-written } P\ (?E\ n')\ w\ (ad', al') = v'$   
**proof** (*cases*  $? \varphi\ n'\ a'' \in ? \varphi\ n'' \text{ ' } ?C\ n''$ )  
**case** *True*  
**then obtain**  $a'''$  **where**  $a''': ? \varphi\ n''\ a''' = ? \varphi\ n'\ a''$   
**and** *committed'''*:  $a''' \in ?C\ n''$  **by** *auto*  
**from** *committed''' C-n''* **have** *a'''-action:  $a''' \in \text{actions } (?E\ n'')$*  **by** *auto*  
  
**from** *committed'' committed''' wfa-n' wfa-n'' a'''* **have** *action-obs* ( $?E\ n''$ )  $a''' \approx$  *action-obs* ( $?E\ n'$ )  $a''$   
**by** (*auto dest!: wf-action-translation-on-actionD intro: sim-action-trans sim-action-sym*)  
**with** *read-a'' committed''' C-n''* **have** *read-a'''*:  $a''' \in \text{read-actions } (?E\ n'')$   
**by** (*cases(auto intro: read-actions.intros)*)  
  
**hence**  $? \varphi\ n'\ (?ws\ n'\ (\text{inv-into } (\text{actions } (?E\ n'))\ (? \varphi\ n')\ (? \varphi\ n''\ a''')) = \text{ws } (? \varphi\ n''\ a''')$   
**using** *justified committed'''*  
**unfolding** *is-weakly-justified-by.simps n'' Let-def write-seen-committed-def* **by** *blast*  
**also have** *inv-into* ( $\text{actions } (?E\ n')\ (? \varphi\ n')\ (? \varphi\ n''\ a''')$ )  $= a''$   
**using**  $a''' \text{ inj-}n'\ a''\text{-action}$  **by** (*simp*)  
**also note**  $a'''$  **also note**  $a''$   
**finally have**  $\varphi\text{-}n'$ :  $? \varphi\ n'\ (?ws\ n'\ a'') = \text{ws } (? \varphi\ n\ a')$  .  
**then have**  $\text{ws } (? \varphi\ n\ a') = ? \varphi\ n'\ (?ws\ n'\ a'')$  ..  
**with**  $\langle ? \varphi\ n\ (?ws\ n\ a') = \text{ws } (? \varphi\ n\ a') \rangle [\text{symmetric}]$   
**have** *eq-ws*:  $? \varphi\ n'\ (?ws\ n'\ a'') = ? \varphi\ n\ (?ws\ n\ a')$  **by** *simp*  
  
**from** *wf-n'[THEN wf-exec-is-write-seenD, THEN is-write-seenD, OF read-a'' a''-obs]*  
**have** *ws-write'*:  $?ws\ n'\ a'' \in \text{write-actions } (?E\ n')$  **by** *simp*  
  
**from** *justified read-a'' committed''*  
**have**  $\text{ws } (? \varphi\ n'\ a'') \in ? \varphi\ n' \text{ ' } ?C\ n'$  **by** (*rule weakly-justified-write-seen-hb-read-committed*)  
**then obtain**  $w'$  **where**  $w': \text{ws } (? \varphi\ n'\ a'') = ? \varphi\ n'\ w'$   
**and** *committed-w'*:  $w' \in ?C\ n'$  **by** *blast*  
**from** *committed-w' C-n'* **have**  $w' \in \text{actions } (?E\ n')$  **by** *blast*  
**hence**  $w'\text{-def: } w' = ?ws\ n'\ a''$  **using**  $\varphi\text{-}n'\ \text{inj-}n'\ \text{ws-write'}$   
**unfolding**  $w'\ a'' [\text{symmetric}]$  **by** (*auto dest: inj-onD*)  
**with** *committed-w'* **have** *committed-ws''*:  $?ws\ n'\ a'' \in \text{committed } (J\ n')$  **by** *simp*  
**with** *committed-ws wfa-n wfa-n' eq-ws*  
**have** *action-obs* ( $?E\ n')\ (?ws\ n'\ a'') \approx$  *action-obs* ( $?E\ n')\ (?ws\ n\ a')$   
**by** (*auto dest!: wf-action-translation-on-actionD intro: sim-action-trans sim-action-sym*)  
**hence** *adal-eq*: *action-loc*  $P\ (?E\ n')\ (?ws\ n'\ a'') =$  *action-loc*  $P\ (?E\ n')\ (?ws\ n\ a')$   
**by** (*simp add: action-loc-aux-sim-action*)  
**with** *adal* **have** *adal'*:  $(ad', al') \in \text{action-loc } P\ (?E\ n')\ (?ws\ n'\ a'')$  **by** (*simp add: action-loc-aux-sim-action*)  
  
**from** *committed-ws''* **have**  $?ws\ n'\ a'' \in \text{actions } (?E\ n')$  **using** *C-n'* **by** *blast*  
**with** *ws-write*  $\langle \text{action-obs } (?E\ n')\ (?ws\ n'\ a'') \approx \text{action-obs } (?E\ n')\ (?ws\ n\ a') \rangle$   
**have** *ws-write''*:  $?ws\ n'\ a'' \in \text{write-actions } (?E\ n')$   
**by** (*cases(auto intro: write-actions.intros simp add: sim-action-is-write-action-eq)*)

**from** *wfa-n' committed-ws''*  
**have** *sim-ws': action-obs* ( $?E\ n'$ ) ( $?ws\ n'\ a''$ )  $\approx$  *action-obs* *E* ( $? \varphi\ n'\ (?ws\ n'\ a'')$ )  
**by**(*blast dest: wf-action-translation-on-actionD*)  
**with** *adal' have adal'-E:* ( $ad', al'$ )  $\in$  *action-loc* *P E* ( $? \varphi\ n'\ (?ws\ n'\ a'')$ )  
**by**(*simp add: action-loc-aux-sim-action*)

**from** *justified committed-ws ws-write adal-E*  
**have** *value-written* *P* ( $?E\ n$ ) ( $?ws\ n\ a'$ ) ( $ad', al'$ ) = *value-written* *P E* ( $? \varphi\ n\ (?ws\ n\ a')$ ) ( $ad',$   
*al'*)  
**unfolding** *is-weakly-justified-by.simps Let-def value-written-committed-def* **by** *blast*  
**also note** *eq-ws[symmetric]*  
**also from** *justified committed-ws'' ws-write'' adal'-E*  
**have** *value-written* *P E* ( $? \varphi\ n'\ (?ws\ n'\ a'')$ ) ( $ad', al'$ ) = *value-written* *P* ( $?E\ n'$ ) ( $?ws\ n'\ a''$ )  
*(ad', al')*  
**unfolding** *is-weakly-justified-by.simps Let-def value-written-committed-def* **by**(*blast dest:*  
*sym*)  
**finally show** *?thesis using* *written ws-write'' adal'* **by** *auto*  
**next**  
**case** *False*  
**with** *justified read-a'' committed''*  
**have** *ws* ( $? \varphi\ n'\ a''$ )  $\in$   $? \varphi\ n'' \text{ ' } ?C\ n''$   
**unfolding** *is-weakly-justified-by.simps Let-def n'' committed-reads-see-committed-writes-weak-def*  
**by** *blast*  
**with** *a'' obtain w where w:*  $? \varphi\ n''\ w = ws\ (? \varphi\ n\ a')$   
**and** *committed-w:*  $w \in ?C\ n''$  **by** *auto*  
**from** *justified have*  $? \varphi\ n'' \text{ ' } ?C\ n'' \subseteq ? \varphi\ n' \text{ ' } ?C\ n'$  **by**(*simp add: is-commit-sequence-def n''*)  
**with** *committed-w w[symmetric]* **have** *ws* ( $? \varphi\ n\ a'$ )  $\in$   $? \varphi\ n' \text{ ' } ?C\ n'$  **by**(*auto*)  
**then obtain** *w' where w':*  $ws\ (? \varphi\ n\ a') = ? \varphi\ n'\ w'$  **and** *committed-w':*  $w' \in ?C\ n'$  **by** *blast*  
**from** *wfa-n' committed-w' have* *action-obs* ( $?E\ n'$ )  $w' \approx$  *action-obs* *E* ( $? \varphi\ n'\ w'$ )  
**by**(*blast dest: wf-action-translation-on-actionD*)  
**from** *this[folded w', folded*  $\langle ? \varphi\ n\ (?ws\ n\ a') = ws\ (? \varphi\ n\ a') \rangle$  *sim-ws[symmetric]*  
**have** *sim-w': action-obs* ( $?E\ n'$ )  $w' \approx$  *action-obs* ( $?E\ n$ ) ( $?ws\ n\ a'$ ) **by**(*rule sim-action-trans*)  
**with** *ws-write committed-w' C-n' have* *write-w':*  $w' \in$  *write-actions* ( $?E\ n'$ )  
**by**(*cases*)(*auto intro!: write-actions.intros simp add: sim-action-is-write-action-eq*)  
**hence** *value-written* *P* ( $?E\ n'$ )  $w'$  ( $ad', al'$ ) = *value-written* *P E* ( $? \varphi\ n'\ w'$ ) ( $ad', al'$ )  
**using** *adal-E committed-w' justified*  
**unfolding**  $\langle ? \varphi\ n\ (?ws\ n\ a') = ws\ (? \varphi\ n\ a') \rangle$  *w' is-weakly-justified-by.simps Let-def*  
*value-written-committed-def* **by** *blast*  
**also note** *w'[symmetric]*  
**also note**  $\langle ? \varphi\ n\ (?ws\ n\ a') = ws\ (? \varphi\ n\ a') \rangle$  *[symmetric]*  
**also have** *value-written* *P E* ( $? \varphi\ n\ (?ws\ n\ a')$ ) ( $ad', al'$ ) = *value-written* *P* ( $?E\ n$ ) ( $?ws\ n\ a'$ )  
*(ad', al')*  
**using** *justified committed-ws ws-write adal-E*  
**unfolding** *is-weakly-justified-by.simps Let-def value-written-committed-def* **by**(*blast dest:*  
*sym*)  
**also have** ( $ad', al'$ )  $\in$  *action-loc* *P* ( $?E\ n'$ )  $w'$  **using** *sim-w' adal* **by**(*simp add: ac-*  
*tion-loc-aux-sim-action*)  
**ultimately show** *?thesis using* *written write-w'* **by** *auto*  
**qed**  
**then obtain** *w where w:*  $w \in$  *write-actions* ( $?E\ n'$ )  
**and** *adal:* ( $ad', al'$ )  $\in$  *action-loc* *P* ( $?E\ n'$ )  $w$   
**and** *written:* *value-written* *P* ( $?E\ n'$ )  $w$  ( $ad', al'$ ) =  $v'$  **by** *blast*  
**from** *w have w-len:*  $enat\ w < llength\ (?E\ n')$   
**by**(*cases*)(*simp add: actions-def*)

```

let ?EE'' = lmap snd (ltake (enat w) (?E n'))
have non-speculative P (vs-type-all P) ?EE''
proof(rule non-speculative-nthI)
  fix i ad al v
  assume i: enat i < llength ?EE''
    and i-nth: lnth ?EE'' i = NormalAction (ReadMem ad al v)
    and ns: non-speculative P (vs-type-all P) (ltake (enat i) ?EE'')

  from i w-len have i < w by(simp add: min-def not-le split: if-split-asm)
  with w-len have enat i < llength (?E n') by(simp add: less-trans[where y=enat w])
  moreover
  from i-nth i < i < w w-len
  have action-obs (?E n') i = NormalAction (ReadMem ad al v)
    by(simp add: action-obs-def ac-simps less-trans[where y=enat w] lnth-ltake)
  moreover from n'' have 0 < n' by simp
  ultimately have  $\exists T. P \vdash ad@al : T \wedge P \vdash v : \leq T$  by(rule Suc.IH)
  hence  $v \in vs\text{-type-all } P (ad, al)$  by(simp add: vs-type-all.simps)
  thus  $v \in w\text{-values } P (vs\text{-type-all } P) (list\text{-of } (ltake (enat i) ?EE'')) (ad, al)$ 
    by(rule w-values-mono[THEN subsetD])
qed
with wfx-start wfP E' w adal
show  $\exists T. P \vdash ad'@al' : T \wedge P \vdash v' : \leq T$ 
  unfolding written[symmetric] by(rule non-speculative-written-value-typeable)
qed
qed
}
note justifying = this

assume a: enat a < llength E
  and read: action-obs E a = NormalAction (ReadMem ad al v)
  and E:  $E \in h.\mathcal{E}\text{-start } f P C M \text{ vs status}$ 
  and wf:  $P \vdash (E, ws) \checkmark$ 
from a have action:  $a \in actions E$  by(auto simp add: actions-def action-obs-def)
with justified obtain n a' where a':  $a = ?\varphi n a'$ 
  and committed':  $a' \in ?C n$  by(auto simp add: is-commit-sequence-def)
from justified have C-n:  $?C n \subseteq actions (?E n)$ 
  and C-Sn:  $?C (Suc n) \subseteq actions (?E (Suc n))$ 
  and wf-tr: wf-action-translation E (J n)
  and wf-tr': wf-action-translation E (J (Suc n))
  by(auto simp add: committed-subset-actions-def wf-action-translations-def)
from C-n committed' have action':  $a' \in actions (?E n)$  by blast
from wf-tr committed' a'
have action-tid E a = action-tid (?E n) a' action-obs E a  $\approx$  action-obs (?E n) a'
  by(auto simp add: wf-action-translation-on-def intro: sim-action-sym)
with read obtain v'
  where action-obs (?E n) a' = NormalAction (ReadMem ad al v')
  by(clarsimp simp add: action-obs-def)
with action' have read':  $a' \in read\text{-actions } (?E n) ..$ 

from justified have  $?\varphi n \text{ ' } ?C n \subseteq ?\varphi (Suc n) \text{ ' } ?C (Suc n)$ 
  by(simp add: is-commit-sequence-def)
with committed' a' have  $a \in \dots$  by auto
then obtain a'' where a'':  $a = ?\varphi (Suc n) a''$ 

```

**and**  $\text{committed}''$ :  $a'' \in ?C \text{ (Suc } n)$  **by** *auto*  
**from**  $\text{committed}'' \text{ C-Sn}$  **have**  $\text{action}''$ :  $a'' \in \text{actions } (?E \text{ (Suc } n))$  **by** *blast*  
**with**  $\text{wf-tr}'$  **have**  $a'' = \text{inv-into } (\text{actions } (?E \text{ (Suc } n))) \text{ } (? \varphi \text{ (Suc } n)) \text{ } a$   
**by**(*simp add: a'' wf-action-translation-on-def*)  
**with**  $\text{justified read}' \text{ committed}' a'$  **have**  $\text{ws-a}$ :  $\text{ws } a = ? \varphi \text{ (Suc } n) \text{ } (? \text{ws } \text{ (Suc } n) \text{ } a'')$   
**by**(*simp add: write-seen-committed-def*)  
**from**  $\text{wf-tr}' \text{ committed}'' a''$   
**have**  $\text{action-tid } E \text{ } a = \text{action-tid } (?E \text{ (Suc } n)) \text{ } a''$   
**and**  $\text{action-obs } E \text{ } a \approx \text{action-obs } (?E \text{ (Suc } n)) \text{ } a''$   
**by**(*auto simp add: wf-action-translation-on-def intro: sim-action-sym*)  
**with**  $\text{read}$  **obtain**  $v''$   
**where**  $a\text{-obs}''$ :  $\text{action-obs } (?E \text{ (Suc } n)) \text{ } a'' = \text{NormalAction } (\text{ReadMem } ad \text{ } al \text{ } v'')$   
**by**(*clarsimp simp add: action-obs-def*)  
**with**  $\text{action}''$  **have**  $\text{read}''$ :  $a'' \in \text{read-actions } (?E \text{ (Suc } n))$   
**by**(*auto intro: read-actions.intros simp add: action-obs-def*)  
**have**  $a \in \text{read-actions } E \text{ } \text{action-obs } E \text{ } a = \text{NormalAction } (\text{ReadMem } ad \text{ } al \text{ } v)$   
**using**  $\text{action read}$  **by**(*auto intro: read-actions.intros simp add: action-obs-def read*)  
**from**  $\text{is-write-seenD}[OF \text{ wf-exec-is-write-seenD}[OF \text{ wf}] \text{ this}]$   
**have**  $v\text{-eq}$ :  $v = \text{value-written } P \text{ } E \text{ } (\text{ws } a) \text{ } (ad, al)$   
**and**  $adal$ :  $(ad, al) \in \text{action-loc } P \text{ } E \text{ } (\text{ws } a)$  **by** *simp-all*  
**from**  $\text{justified}$  **have**  $P \vdash (?E \text{ (Suc } n), ? \text{ws } \text{ (Suc } n)) \checkmark$  **by**(*simp add: justification-well-formed-def*)  
**from**  $\text{is-write-seenD}[OF \text{ wf-exec-is-write-seenD}[OF \text{ this}] \text{ read}'' a\text{-obs}'']$   
**have**  $\text{write}''$ :  $? \text{ws } \text{ (Suc } n) \text{ } a'' \in \text{write-actions } (?E \text{ (Suc } n))$   
**and**  $\text{written}''$ :  $\text{value-written } P \text{ } (?E \text{ (Suc } n)) \text{ } (? \text{ws } \text{ (Suc } n) \text{ } a'') \text{ } (ad, al) = v''$   
**by** *simp-all*  
**from**  $\text{justified read}'' \text{ committed}''$   
**have**  $\text{ws } (? \varphi \text{ (Suc } n) \text{ } a'') \in ? \varphi \text{ (Suc } n) \text{ } ' ?C \text{ (Suc } n)$   
**by**(*rule weakly-justified-write-seen-hb-read-committed*)  
**then** **obtain**  $w$  **where**  $w$ :  $\text{ws } (? \varphi \text{ (Suc } n) \text{ } a'') = ? \varphi \text{ (Suc } n) \text{ } w$   
**and**  $\text{committed-w}$ :  $w \in ?C \text{ (Suc } n)$  **by** *blast*  
**with**  $\text{C-Sn}$  **have**  $w \in \text{actions } (?E \text{ (Suc } n))$  **by** *blast*  
**moreover** **have**  $\text{ws } (? \varphi \text{ (Suc } n) \text{ } a'') = ? \varphi \text{ (Suc } n) \text{ } (? \text{ws } \text{ (Suc } n) \text{ } a'')$   
**using**  $\text{ws-a } a''$  **by** *simp*  
**ultimately** **have**  $w\text{-def}$ :  $w = ? \text{ws } \text{ (Suc } n) \text{ } a''$   
**using**  $\text{wf-action-translation-on-inj-onD}[OF \text{ wf-tr}'] \text{ write}''$   
**unfolding**  $w$  **by**(*auto dest: inj-onD*)  
**with**  $\text{committed-w}$  **have**  $? \text{ws } \text{ (Suc } n) \text{ } a'' \in ?C \text{ (Suc } n)$  **by** *simp*  
**hence**  $\text{value-written } P \text{ } E \text{ } (\text{ws } a) \text{ } (ad, al) = \text{value-written } P \text{ } (?E \text{ (Suc } n)) \text{ } (? \text{ws } \text{ (Suc } n) \text{ } a'') \text{ } (ad, al)$   
**using**  $adal \text{ justified write}''$  **by**(*simp add: value-written-committed-def ws-a*)  
**with**  $v\text{-eq written}''$  **have**  $v = v''$  **by** *simp*  
**from**  $\text{read}''$  **have**  $\text{enat } a'' < \text{llength } (?E \text{ (Suc } n))$  **by**(*cases*)(*simp add: actions-def*)  
**thus**  $\exists T. P \vdash ad@al : T \wedge P \vdash v \leq T$   
**by**(*rule justifying*)(*simp-all add: a-obs'' v = v''*)  
**qed**

**corollary** *weakly-legal-read-value-typeable*:

**assumes**  $\text{wfx-start}$ :  $\text{ts-ok wfx } (\text{thr } (h.\text{start-state } f \text{ } P \text{ } C \text{ } M \text{ } \text{ws})) \text{ } h.\text{start-heap}$   
**and**  $\text{wfp}$ :  $\text{wfsyscls } P$

```

and legal: weakly-legal-execution  $P$  ( $h.\mathcal{E}\text{-start } f P C M \text{ vs status}$ ) ( $E, ws$ )
and  $a$ :  $\text{enat } a < \text{llength } E$ 
and read:  $\text{action-obs } E a = \text{NormalAction } (\text{ReadMem } ad \ al \ v)$ 
shows  $\exists T. P \vdash ad@al : T \wedge P \vdash v : \leq T$ 
proof –
  from legal obtain  $J$ 
    where  $P \vdash (E, ws)$  weakly-justified-by  $J$ 
    and  $\text{range } (\text{justifying-exec} \circ J) \subseteq h.\mathcal{E}\text{-start } f P C M \text{ vs status}$ 
    and  $E \in h.\mathcal{E}\text{-start } f P C M \text{ vs status}$ 
    and  $P \vdash (E, ws) \checkmark$  by(rule legal-executionE)
    with  $wfx\text{-start } wfP$  show ?thesis using a read by(rule read-value-typeable-justified)
qed

corollary legal-read-value-typeable:
   $\llbracket ts\text{-ok } wfx \ (thr \ (h.\text{start-state } f P C M \text{ vs})) \ h.\text{start-heap}; wf\text{-syscls } P;$ 
   $\text{legal-execution } P \ (h.\mathcal{E}\text{-start } f P C M \text{ vs status}) \ (E, ws);$ 
   $\text{enat } a < \text{llength } E; \text{action-obs } E a = \text{NormalAction } (\text{ReadMem } ad \ al \ v) \rrbracket$ 
   $\implies \exists T. P \vdash ad@al : T \wedge P \vdash v : \leq T$ 
by(erule (1) weakly-legal-read-value-typeable)(rule legal-imp-weakly-legal-execution)

end

end

```

### 8.13 JMM Instantiation with Jinja – common parts

```

theory JMM-Common
imports
  JMM-Framework
  JMM-Typesafe
  ../Common/BinOp
  ../Common/ExternalCallWF
begin

context heap begin

lemma heap-copy-loc-not-New: assumes  $\text{heap-copy-loc } a \ a' \ al \ h \ ob \ h'$ 
  shows  $\text{NewHeapElem } a'' \ x \in \text{set } ob \implies \text{False}$ 
using assms
by(auto elim: heap-copy-loc.cases)

lemma heap-copies-not-New:
  assumes  $\text{heap-copies } a \ a' \ als \ h \ obs \ h'$ 
  and  $\text{NewHeapElem } a'' \ x \in \text{set } obs$ 
  shows  $\text{False}$ 
using assms
by induct(auto dest: heap-copy-loc-not-New)

lemma heap-clone-New-same-addr-same:
  assumes  $\text{heap-clone } P \ h \ a \ h' \ [(obs, a')]$ 
  and  $obs ! i = \text{NewHeapElem } a'' \ x \ i < \text{length } obs$ 
  and  $obs ! j = \text{NewHeapElem } a'' \ x' \ j < \text{length } obs$ 
  shows  $i = j$ 

```



using *assms*

apply *cases*

apply(*fastforce simp add: nth-Cons' gr0-conv-Suc in-set-conv-nth split: if-split-asm dest: heap-copies-not-New*)+  
done

**lemma** *red-external-New-same-addr-same:*

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle;$   
 $\{ta\}_o ! i = NewHeapElem\ a'\ x; i < length\ \{ta\}_o;$   
 $\{ta\}_o ! j = NewHeapElem\ a'\ x'; j < length\ \{ta\}_o \rrbracket$   
 $\implies i = j$

by(*auto elim!: red-external.cases simp add: nth-Cons' split: if-split-asm dest: heap-clone-New-same-addr-same*)

**lemma** *red-external-aggr-New-same-addr-same:*

$\llbracket (ta, va, h') \in red\_external\_aggr\ P\ t\ a\ M\ vs\ h;$   
 $\{ta\}_o ! i = NewHeapElem\ a'\ x; i < length\ \{ta\}_o;$   
 $\{ta\}_o ! j = NewHeapElem\ a'\ x'; j < length\ \{ta\}_o \rrbracket$   
 $\implies i = j$

by(*auto simp add: external-WT-defs.simps red-external-aggr-def nth-Cons' split: if-split-asm if-split-asm dest: heap-clone-New-same-addr-same*)

**lemma** *heap-copy-loc-read-typeable:*

assumes *heap-copy-loc a a' al h obs h'*  
 and *ReadMem ad al' v ∈ set obs*  
 and  $P, h \vdash a @ al : T$   
 shows  $ad = a \wedge al' = al$

using *assms* by *cases auto*

**lemma** *heap-copies-read-typeable:*

assumes *heap-copies a a' als h obs h'*  
 and *ReadMem ad al' v ∈ set obs*  
 and *list-all2 (λal T. P, h ⊢ a @ al : T) als Ts*  
 shows  $ad = a \wedge al' \in set\ als$

using *assms*

**proof**(*induct arbitrary: Ts*)

case *Nil* thus ?case by *simp*

next

case (*Cons al h ob h' als obs h''*)

from  $\langle list\_all2\ (\lambda al\ T.\ P, h \vdash a @ al : T)\ (al \# als)\ Ts \rangle$

obtain  $T\ Ts'$  where  $Ts\ [simp]:\ Ts = T \# Ts'$

and  $P, h \vdash a @ al : T$

and  $list\_all2\ (\lambda al\ T.\ P, h \vdash a @ al : T)\ als\ Ts'$

by(*auto simp add: list-all2-Cons1*)

from  $\langle ReadMem\ ad\ al'\ v \in set\ (ob\ @\ obs) \rangle$

show ?case **unfolding** *set-append Un-iff*

**proof**

assume *ReadMem ad al' v ∈ set ob*

with  $\langle heap\_copy\_loc\ a\ a'\ al\ h\ ob\ h' \rangle$

have  $ad = a \wedge al' = al$  using  $\langle P, h \vdash a @ al : T \rangle$

by(*rule heap-copy-loc-read-typeable*)

thus ?thesis by *simp*

next

assume *ReadMem ad al' v ∈ set obs*

moreover from  $\langle heap\_copy\_loc\ a\ a'\ al\ h\ ob\ h' \rangle$

have  $h \sqsubseteq h'$  by(*rule hext-heap-copy-loc*)

```

    from ⟨list-all2 (λal T. P, h ⊢ a@al : T) als Ts'⟩
    have list-all2 (λal T. P, h' ⊢ a@al : T) als Ts'
      by(rule List.list-all2-mono)(rule addr-loc-type-hext-mono[OF - ⟨h ≤ h'⟩])
    ultimately have ad = a ∧ al' ∈ set als by(rule Cons)
    thus ?thesis by simp
  qed
qed

lemma heap-clone-read-typeable:
  assumes clone: heap-clone P h a h' [(obs, a')]
  and read: ReadMem ad al v ∈ set obs
  shows ad = a ∧ (∃ T'. P, h ⊢ ad@al : T')
using clone
proof cases
  case (ObjClone C H' FDTs obs')
  let ?als = map (λ((F, D), Tm). CField D F) FDTs
  let ?Ts = map (λ(FD, T). fst (the (map-of FDTs FD))) FDTs
  note ⟨heap-copies a a' ?als H' obs' h'⟩
  moreover
  from ⟨obs = NewHeapElem a' (Class-type C) # obs'⟩ read
  have ReadMem ad al v ∈ set obs' by simp
  moreover
  from ⟨(H', a') ∈ allocate h (Class-type C)⟩ have h ≤ H' by(rule hext-allocate)
  hence typeof-addr H' a = [Class-type C] using ⟨typeof-addr h a = [Class-type C]⟩
    by(rule typeof-addr-hext-mono)
  hence type: list-all2 (λal T. P, H' ⊢ a@al : T) ?als ?Ts
    using ⟨P ⊢ C has-fields FDTs⟩
    unfolding list-all2-map1 list-all2-map2 list-all2-same
    by(fastforce intro: addr-loc-type.intros simp add: has-field-def dest: weak-map-of-SomeI)
  ultimately have ad = a ∧ al ∈ set ?als by(rule heap-copies-read-typeable)
  hence [simp]: ad = a and al ∈ set ?als by simp-all
  then obtain F D T where [simp]: al = CField D F and ((F, D), T) ∈ set FDTs by auto
  with type ⟨h ≤ H'⟩ ⟨typeof-addr h a = [Class-type C]⟩ show ?thesis
    unfolding list-all2-map1 list-all2-map2 list-all2-same
    by(fastforce elim!: ballE[where x=((F, D), T)] addr-loc-type.cases dest: typeof-addr-hext-mono
      intro: addr-loc-type.intros)
next
  case (ArrClone T n H' FDTs obs')
  let ?als = map (λ((F, D), Tfm). CField D F) FDTs @ map ACell [0..

```

hence  $al \in \text{set } (\text{map } (\lambda((F, D), \text{Tfm}). \text{CField } D \ F) \ \text{FDTs}) \vee al \in \text{set } (\text{map } \text{ACell } [0..<n])$  **by** *simp*  
 thus *?thesis*  
**proof**  
 assume  $al \in \text{set } (\text{map } (\lambda((F, D), \text{Tfm}). \text{CField } D \ F) \ \text{FDTs})$   
 then obtain  $F \ D \ \text{Tfm}$  **where**  $[simp]: al = \text{CField } D \ F$  **and**  $((F, D), \text{Tfm}) \in \text{set } \text{FDTs}$  **by** *auto*  
 with  $\text{type } \text{type}' \langle h \sqsubseteq H \rangle \langle \text{typeof-addr } h \ a = \lfloor \text{Array-type } T \ n \rfloor \rangle$  **show** *?thesis*  
 by(*fastforce* *elim!*:  $\text{ballE}[\text{where } x=((F, D), \text{Tfm})] \ \text{addr-loc-type.cases} \ \text{intro: addr-loc-type.intros}$   
*simp* *add: list-all2-append list-all2-map1 list-all2-map2 list-all2-same*)  
**next**  
 assume  $al \in \text{set } (\text{map } \text{ACell } [0..<n])$   
 then obtain  $n'$  **where**  $[simp]: al = \text{ACell } n'$  **and**  $n' < n$  **by** *auto*  
 with  $\text{type } \text{type}' \langle h \sqsubseteq H \rangle \langle \text{typeof-addr } h \ a = \lfloor \text{Array-type } T \ n \rfloor \rangle$   
**show** *?thesis* **by**(*fastforce* *dest: list-all2-nthD[where p=n]* *elim: addr-loc-type.cases* *intro: addr-loc-type.intros*)  
**qed**  
**qed**

**lemma** *red-external-read-mem-typeable*:  
 assumes  $\text{red}: P, t \vdash \langle a \cdot M(vs), h \rangle \rightarrow \text{ext} \langle va, h' \rangle$   
 and  $\text{read}: \text{ReadMem } ad \ al \ v \in \text{set } \{ta\}_o$   
 shows  $\exists T'. P, h \vdash ad@al : T'$   
**using** *red read*  
**by** *cases(fastforce dest: heap-clone-read-typeable intro: addr-loc-type.intros)+*  
**end**

**context** *heap-conf* **begin**

**lemma** *heap-clone-typeof-addrD*:  
 assumes  $\text{heap-clone } P \ h \ a \ h' \ [(obs, a')]$   
 and  $hconf \ h$   
 shows  $\text{NewHeapElem } a'' \ x \in \text{set } obs \implies a'' = a' \wedge \text{typeof-addr } h' \ a' = \text{Some } x$   
**using** *assms*  
**by**(*fastforce* *elim!*: *heap-clone.cases* *dest: allocate-SomeD hext-heap-copies heap-copies-not-New typeof-addr-is-type*  
*elim: hext-objD hext-arrD*)

**lemma** *red-external-New-typeof-addrD*:  
 $\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle \rightarrow \text{ext} \langle va, h' \rangle; \text{NewHeapElem } a' \ x \in \text{set } \{ta\}_o; hconf \ h \rrbracket$   
 $\implies \text{typeof-addr } h' \ a' = \text{Some } x$   
**by**(*erule red-external.cases*)(*auto* *dest: heap-clone-typeof-addrD*)

**lemma** *red-external-aggr-New-typeof-addrD*:  
 $\llbracket (ta, va, h') \in \text{red-external-aggr } P \ t \ a \ M \ vs \ h; \text{NewHeapElem } a' \ x \in \text{set } \{ta\}_o;$   
 $\text{is-native } P \ (\text{the } (\text{typeof-addr } h \ a)) \ M; hconf \ h \rrbracket$   
 $\implies \text{typeof-addr } h' \ a' = \text{Some } x$   
**apply**(*auto* *simp* *add: is-native.simps external-WT-defs.simps red-external-aggr-def split: if-split-asm*)  
**apply**(*blast* *dest: heap-clone-typeof-addrD*)  
**done**

**end**

**context** *heap-conf* **begin**

**lemma** *heap-copy-loc-non-speculative-typeable*:  
 assumes  $\text{copy}: \text{heap-copy-loc } ad \ ad' \ al \ h \ obs \ h'$

**and** *sc*: *non-speculative P vs (llist-of (map NormalAction obs))*  
**and** *vs*: *vs-conf P h vs*  
**and** *hconf*: *hconf h*  
**and** *wt*:  $P, h \vdash ad@al : T$ ,  $P, h \vdash ad'@al : T$   
**shows** *heap-base.heap-copy-loc (heap-read-typed P) heap-write ad ad' al h obs h'*  
**proof** –  
**from** *copy obtain v where* *obs*: *obs = [ReadMem ad al v, WriteMem ad' al v]*  
**and** *read*: *heap-read h ad al v* **and** *write*: *heap-write h ad' al v h'* **by** *cases*  
**from** *obs sc have*  $v \in vs (ad, al)$  **by** *auto*  
**with** *vs wt have*  $v: P, h \vdash v : \leq T$  **by** (*blast dest: vs-confD addr-loc-type-fun*) +  
**with** *read wt have* *heap-read-typed P h ad al v*  
**by** (*auto intro: heap-read-typedI dest: addr-loc-type-fun*)  
**thus** *?thesis using write unfolding obs by (rule heap-base.heap-copy-loc.intros)*  
**qed**

**lemma** *heap-copy-loc-non-speculative-vs-conf*:  
**assumes** *copy: heap-copy-loc ad ad' al h obs h'*  
**and** *sc*: *non-speculative P vs (llist-of (take n (map NormalAction obs)))*  
**and** *vs*: *vs-conf P h vs*  
**and** *hconf*: *hconf h*  
**and** *wt*:  $P, h \vdash ad@al : T$ ,  $P, h \vdash ad'@al : T$   
**shows** *vs-conf P h' (w-values P vs (take n (map NormalAction obs)))*  
**proof** –  
**from** *copy obtain v where* *obs*: *obs = [ReadMem ad al v, WriteMem ad' al v]*  
**and** *read*: *heap-read h ad al v* **and** *write*: *heap-write h ad' al v h'* **by** *cases*  
**from** *write have hext: h  $\triangleleft$  h'* **by** (*rule hext-heap-write*)  
**with** *vs have vs': vs-conf P h' vs* **by** (*rule vs-conf-hext*)  
**show** *?thesis*  
**proof** (*cases n > 0*)  
**case** *True*  
**with** *obs sc have*  $v \in vs (ad, al)$  **by** (*auto simp add: take-Cons'*)  
**with** *vs wt have*  $v: P, h \vdash v : \leq T$  **by** (*blast dest: vs-confD addr-loc-type-fun*) +  
**with** *hext wt have*  $P, h' \vdash ad'@al : T$ ,  $P, h' \vdash v : \leq T$   
**by** (*blast intro: addr-loc-type-hext-mono conf-hext*) +  
**thus** *?thesis using vs' obs*  
**by** (*auto simp add: take-Cons' intro!: vs-confI split: if-split-asm dest: vs-confD*)  
**next**  
**case** *False* **thus** *?thesis using vs' by simp*  
**qed**  
**qed**

**lemma** *heap-copies-non-speculative-typeable*:  
**assumes** *heap-copies ad ad' als h obs h'*  
**and** *non-speculative P vs (llist-of (map NormalAction obs))*  
**and** *vs-conf P h vs*  
**and** *hconf h*  
**and** *list-all2* ( $\lambda al T. P, h \vdash ad@al : T$ ) *als Ts list-all2* ( $\lambda al T. P, h \vdash ad'@al : T$ ) *als Ts*  
**shows** *heap-base.heap-copies (heap-read-typed P) heap-write ad ad' als h obs h'*  
**using** *assms*  
**proof** (*induct arbitrary: Ts vs*)  
**case** *Nil* **show** *?case by (auto intro: heap-base.heap-copies.intros)*  
**next**  
**case** (*Cons al h ob h' als obs h''*)  
**note**  $sc = \langle non-speculative P vs (llist-of (map NormalAction (ob @ obs))) \rangle$

**and**  $vs = \langle vs\text{-conf } P \ h \ vs \rangle$   
**and**  $hconf = \langle hconf \ h \rangle$   
**and**  $wt = \langle list\text{-all2 } (\lambda al \ T. \ P, h \vdash ad@al : T) \ (al \ \# \ als) \ Ts \rangle \langle list\text{-all2 } (\lambda al \ T. \ P, h \vdash ad'@al : T) \ (al \ \# \ als) \ Ts \rangle$

**have**  $sc1$ : *non-speculative*  $P \ vs \ (l\text{list-of } (map \ NormalAction \ ob))$   
**and**  $sc2$ : *non-speculative*  $P \ (w\text{-values } P \ vs \ (map \ NormalAction \ ob)) \ (l\text{list-of } (map \ NormalAction \ obs))$

**using**  $sc$  **by** (*simp-all add: non-speculative-lappend lappend-llist-of-llist-of[symmetric] del: lappend-llist-of-llist-of*)

**from**  $wt$  **obtain**  $T \ Ts'$  **where**  $Ts: Ts = T \ \# \ Ts'$   
**and**  $wt1$ :  $P, h \vdash ad@al : T \ P, h \vdash ad'@al : T$   
**and**  $wt2$ :  $list\text{-all2 } (\lambda al \ T. \ P, h \vdash ad@al : T) \ als \ Ts' \ list\text{-all2 } (\lambda al \ T. \ P, h \vdash ad'@al : T) \ als \ Ts'$   
**by** (*auto simp add: list-all2-Cons1*)  
**from**  $\langle heap\text{-copy-loc } ad \ ad' \ al \ h \ ob \ h' \rangle \ sc1 \ vs \ hconf \ wt1$   
**have**  $copy$ :  $heap\text{-base.heap-copy-loc } (heap\text{-read-typed } P) \ heap\text{-write } ad \ ad' \ al \ h \ ob \ h'$   
**by** (*rule heap-copy-loc-non-speculative-typeable*) +  
**from**  $heap\text{-copy-loc-non-speculative-vs-conf}[OF \ \langle heap\text{-copy-loc } ad \ ad' \ al \ h \ ob \ h' \rangle - vs \ hconf \ wt1, \ of \ length \ ob] \ sc1$   
**have**  $vs'$ :  $vs\text{-conf } P \ h' \ (w\text{-values } P \ vs \ (map \ NormalAction \ ob))$  **by** *simp*

**from**  $\langle heap\text{-copy-loc } ad \ ad' \ al \ h \ ob \ h' \rangle$   
**have**  $h \sqsubseteq h'$  **by** (*rule hext-heap-copy-loc*)  
**with**  $wt2$  **have**  $wt2'$ :  $list\text{-all2 } (\lambda al \ T. \ P, h' \vdash ad@al : T) \ als \ Ts' \ list\text{-all2 } (\lambda al \ T. \ P, h' \vdash ad'@al : T) \ als \ Ts'$   
**by**  $-(erule \ List.list\text{-all2-mono}[OF \ - \ addr\text{-loc-type-hext-mono}, \ assumption+]) +$

**from**  $copy \ hconf \ wt1$  **have**  $hconf'$ :  $hconf \ h'$   
**by** (*rule heap-conf-read.hconf-heap-copy-loc-mono[OF heap-conf-read-heap-read-typed]*)

**from**  $sc2 \ vs' \ hconf' \ wt2'$  **have**  $heap\text{-base.heap-copies } (heap\text{-read-typed } P) \ heap\text{-write } ad \ ad' \ als \ h' \ obs \ h''$  **by** (*rule Cons*)

**with**  $copy$  **show**  $?case$  **by** (*rule heap-base.heap-copies.Cons*)  
**qed**

**lemma** *heap-copies-non-speculative-vs-conf*:

**assumes**  $heap\text{-copies } ad \ ad' \ als \ h \ obs \ h'$   
**and** *non-speculative*  $P \ vs \ (l\text{list-of } (take \ n \ (map \ NormalAction \ obs)))$   
**and**  $vs\text{-conf } P \ h \ vs$   
**and**  $hconf \ h$   
**and**  $list\text{-all2 } (\lambda al \ T. \ P, h \vdash ad@al : T) \ als \ Ts \ list\text{-all2 } (\lambda al \ T. \ P, h \vdash ad'@al : T) \ als \ Ts$   
**shows**  $vs\text{-conf } P \ h' \ (w\text{-values } P \ vs \ (take \ n \ (map \ NormalAction \ obs)))$   
**using** *assms*  
**proof** (*induction arbitrary: Ts vs n*)  
**case** *Nil* **thus**  $?case$  **by** *simp*  
**next**  
**case** (*Cons al h ob h' als obs h'*)  
**note**  $sc = \langle non\text{-speculative } P \ vs \ (l\text{list-of } (take \ n \ (map \ NormalAction \ (ob \ @ \ obs)))) \rangle$   
**and**  $hcl = \langle heap\text{-copy-loc } ad \ ad' \ al \ h \ ob \ h' \rangle$   
**and**  $vs = \langle vs\text{-conf } P \ h \ vs \rangle$   
**and**  $hconf = \langle hconf \ h \rangle$   
**and**  $wt = \langle list\text{-all2 } (\lambda al \ T. \ P, h \vdash ad@al : T) \ (al \ \# \ als) \ Ts \rangle \langle list\text{-all2 } (\lambda al \ T. \ P, h \vdash ad'@al : T) \ (al \ \# \ als) \ Ts \rangle$   
**let**  $?vs' = w\text{-values } P \ vs \ (take \ n \ (map \ NormalAction \ ob))$

```

from sc have sc1: non-speculative P vs (llist-of (take n (map NormalAction ob)))
  and sc2: non-speculative P ?vs' (llist-of (take (n - length ob) (map NormalAction obs)))
by(simp-all add: lappend-llist-of-llist-of[symmetric] non-speculative-lappend del: lappend-llist-of-llist-of)

from wt obtain T Ts' where Ts: Ts = T # Ts'
  and wt1: P, h ⊢ ad@al : T, h ⊢ ad'@al : T
  and wt2: list-all2 (λal T. P, h ⊢ ad@al : T) als Ts' list-all2 (λal T. P, h ⊢ ad'@al : T) als Ts'
  by(auto simp add: list-all2-Cons1)

from hcl sc1 vs hconf wt1 have vs': vs-conf P h' ?vs' by(rule heap-copy-loc-non-speculative-vs-conf)

show ?case
proof(cases n < length ob)
  case True
    from ⟨heap-copies ad ad' als h' obs h'⟩ have h' ⊑ h'' by(rule hext-heap-copies)
    with vs' have vs-conf P h'' ?vs' by(rule vs-conf-hext)
    thus ?thesis using True by simp
  next
    case False
    note sc2 vs'
    moreover from False sc1 have sc1': non-speculative P vs (llist-of (map NormalAction ob)) by
simp
    with hcl have heap-base.heap-copy-loc (heap-read-typed P) heap-write ad ad' al h ob h'
      using vs hconf wt1 by(rule heap-copy-loc-non-speculative-typeable)
    hence hconf h' using hconf wt1
      by(rule heap-conf-read.hconf-heap-copy-loc-mono[OF heap-conf-read-heap-read-typed])
    moreover
    from hcl have h ⊑ h' by(rule hext-heap-copy-loc)
    with wt2 have wt2': list-all2 (λal T. P, h' ⊢ ad@al : T) als Ts' list-all2 (λal T. P, h' ⊢ ad'@al :
T) als Ts'
      by -(erule List.list-all2-mono[OF - addr-loc-type-hext-mono], assumption+)+
    ultimately have vs-conf P h'' (w-values P ?vs' (take (n - length ob) (map NormalAction obs)))
      by(rule Cons.IH)
    with False show ?thesis by simp
  qed
qed

lemma heap-clone-non-speculative-typeable-Some:
  assumes clone: heap-clone P h ad h' [(obs, ad')]
  and sc: non-speculative P vs (llist-of (map NormalAction obs))
  and vs: vs-conf P h vs
  and hconf: hconf h
  shows heap-base.heap-clone allocate typeof-addr (heap-read-typed P) heap-write P h ad h' [(obs, ad')]
using clone
proof(cases)
  case (ObjClone C h'' FDTs obs')
    note FDTs = ⟨P ⊢ C has-fields FDTs⟩
    and obs = ⟨obs = NewHeapElem ad' (Class-type C) # obs'⟩
    let ?als = map (λ((F, D), Tfm). CField D F) FDTs
    let ?Ts = map (λ(FD, T). fst (the (map-of FDTs FD))) FDTs
    let ?vs = w-value P vs (NormalAction (NewHeapElem ad' (Class-type C) :: ('addr, 'thread-id
obs-event))
    from ⟨(h'', ad') ∈ allocate h (Class-type C)⟩ have hext: h ⊑ h'' by(rule hext-heap-ops)

```

hence  $\text{type: typeof-addr } h'' \text{ ad} = \lfloor \text{Class-type } C \rfloor$  **using**  $\langle \text{typeof-addr } h \text{ ad} = \lfloor \text{Class-type } C \rfloor \rangle$   
**by**(rule typeof-addr-hext-mono)

**note**  $\langle \text{heap-copies ad ad' ?als } h'' \text{ obs' } h' \rangle$   
**moreover from**  $sc$  **have**  $\text{non-speculative } P \text{ ?vs } (\text{l-list-of } (\text{map NormalAction obs'}))$   
**by**(simp add: obs)  
**moreover from**  $\langle P \vdash C \text{ has-fields FDTs} \rangle$   
**have**  $\text{is-class } P \text{ } C$  **by**(rule has-fields-is-class)  
**hence**  $\text{is-htype } P \text{ } (C)$  **by** simp  
**with**  $vs \langle (h'', \text{ad}') \in \text{allocate } h \text{ } (C) \rangle$   
**have**  $vs\text{-conf } P \text{ } h'' \text{ ?vs}$  **by**(rule vs-conf-allocate)  
**moreover from**  $\langle (h'', \text{ad}') \in \text{allocate } h \text{ } (C) \rangle$   $h\text{conf } \langle \text{is-htype } P \text{ } (C) \rangle$   
**have**  $h\text{conf } h''$  **by**(rule hconf-allocate-mono)  
**moreover from**  $\text{type FDTs}$  **have**  $\text{list-all2 } (\lambda \text{al } T. P, h'' \vdash \text{ad}@al : T) \text{ ?als ?Ts}$   
**unfolding**  $\text{list-all2-map1 list-all2-map2 list-all2-same}$   
**by**(fastforce intro: addr-loc-type.intros simp add: has-field-def dest: weak-map-of-SomeI)  
**moreover from**  $\langle (h'', \text{ad}') \in \text{allocate } h \text{ } (C) \rangle$   $\langle \text{is-htype } P \text{ } (C) \rangle$   
**have**  $\text{typeof-addr } h'' \text{ ad}' = \lfloor \text{Class-type } C \rfloor$  **by**(auto dest: allocate-SomeD)  
**with FDTs** **have**  $\text{list-all2 } (\lambda \text{al } T. P, h'' \vdash \text{ad}'@al : T) \text{ ?als ?Ts}$   
**unfolding**  $\text{list-all2-map1 list-all2-map2 list-all2-same}$   
**by**(fastforce intro: addr-loc-type.intros simp add: has-field-def dest: weak-map-of-SomeI)  
**ultimately**  
**have**  $\text{copy: heap-base.heap-copies } (\text{heap-read-typed } P) \text{ heap-write ad ad' } (\text{map } (\lambda((F, D), \text{Tfm}).$   
 $\text{CField } D \text{ } F) \text{ FDTs}) \text{ } h'' \text{ obs' } h'$   
**by**(rule heap-copies-non-speculative-typeable)+  
**from**  $\langle \text{typeof-addr } h \text{ ad} = \lfloor \text{Class-type } C \rfloor \rangle$   $\langle (h'', \text{ad}') \in \text{allocate } h \text{ } (C) \rangle$   $\text{FDTs copy}$   
**show**  $\text{?thesis unfolding obs by(rule heap-base.heap-clone.intros)}$

**next**  
**case**  $(\text{ArrClone } T \text{ } n \text{ } h'' \text{ FDTs obs'})$   
**note**  $\text{obs} = \langle \text{obs} = \text{NewHeapElem ad' } (\text{Array-type } T \text{ } n) \# \text{ obs'} \rangle$   
**and**  $\text{new} = \langle (h'', \text{ad}') \in \text{allocate } h \text{ } (\text{Array-type } T \text{ } n) \rangle$   
**and**  $\text{FDTs} = \langle P \vdash \text{Object has-fields FDTs} \rangle$   
**let**  $\text{?als} = \text{map } (\lambda((F, D), \text{Tfm}). \text{CField } D \text{ } F) \text{ FDTs @ map ACell } [0..<n]$   
**let**  $\text{?Ts} = \text{map } (\lambda(FD, T). \text{fst } (\text{the } (\text{map-of FDTs FD}))) \text{ FDTs @ replicate } n \text{ } T$   
**let**  $\text{?vs} = \text{w-value } P \text{ vs } (\text{NormalAction } (\text{NewHeapElem ad' } (\text{Array-type } T \text{ } n) :: ('addr, 'thread-id)$   
 $\text{obs-event}))$   
**from**  $\text{new}$  **have**  $\text{hext: } h \sqsubseteq h''$  **by**(rule hext-heap-ops)  
**hence**  $\text{type: typeof-addr } h'' \text{ ad} = \lfloor \text{Array-type } T \text{ } n \rfloor$  **using**  $\langle \text{typeof-addr } h \text{ ad} = \lfloor \text{Array-type } T \text{ } n \rfloor \rangle$   
**by**(rule typeof-addr-hext-mono)

**note**  $\langle \text{heap-copies ad ad' ?als } h'' \text{ obs' } h' \rangle$   
**moreover from**  $sc$  **have**  $\text{non-speculative } P \text{ ?vs } (\text{l-list-of } (\text{map NormalAction obs'}))$  **by**(simp add: obs)

**moreover from**  $\langle \text{typeof-addr } h \text{ ad} = \lfloor \text{Array-type } T \text{ } n \rfloor \rangle$   $\langle h\text{conf } h \rangle$  **have**  $\text{is-htype } P \text{ } (\text{Array-type } T \text{ } n)$   
**by**(auto dest: typeof-addr-is-type)  
**with**  $vs \text{ new}$  **have**  $vs\text{-conf } P \text{ } h'' \text{ ?vs}$  **by**(rule vs-conf-allocate)  
**moreover from**  $\text{new hconf } \langle \text{is-htype } P \text{ } (\text{Array-type } T \text{ } n) \rangle$  **have**  $h\text{conf } h''$  **by**(rule hconf-allocate-mono)  
**moreover**  
**from**  $\text{type FDTs}$  **have**  $\text{list-all2 } (\lambda \text{al } T. P, h'' \vdash \text{ad}@al : T) \text{ ?als ?Ts}$   
**by**(fastforce intro: list-all2-all-nthI addr-loc-type.intros simp add: has-field-def list-all2-append  
 $\text{list-all2-map1 list-all2-map2 list-all2-same dest: weak-map-of-SomeI}$ )  
**moreover from**  $\text{new } \langle \text{is-htype } P \text{ } (\text{Array-type } T \text{ } n) \rangle$   
**have**  $\text{typeof-addr } h'' \text{ ad}' = \lfloor \text{Array-type } T \text{ } n \rfloor$   
**by**(auto dest: allocate-SomeD)

hence  $\text{list-all2 } (\lambda al T. P, h'' \vdash ad'@al : T) \text{ ?als ?Ts using FDTs}$   
 by(fastforce intro: list-all2-all-nthI addr-loc-type.intros simp add: has-field-def list-all2-append  
 list-all2-map1 list-all2-map2 list-all2-same dest: weak-map-of-SomeI)  
 ultimately have copy: heap-base.heap-copies (heap-read-typed P) heap-write ad ad' (map ( $\lambda((F, D),$   
 $Tfm). CField D F$ ) FDTs @ map ACell  $[0..<n]$ )  $h'' \text{ obs}' h'$   
 by(rule heap-copies-non-speculative-typeable)+  
 from  $\langle \text{typeof-addr } h \text{ ad} = \lfloor \text{Array-type } T \ n \rfloor \rangle$  new FDTs copy show ?thesis  
 unfolding obs by(rule heap-base.heap-clone.ArrClone)  
 qed

lemma heap-clone-non-speculative-vs-conf-Some:

assumes clone: heap-clone P h ad h'  $\lfloor (obs, ad') \rfloor$   
 and sc: non-speculative P vs (llist-of (take n (map NormalAction obs)))  
 and vs: vs-conf P h vs  
 and hconf: hconf h  
 shows vs-conf P h' (w-values P vs (take n (map NormalAction obs)))  
 using clone  
 proof(cases)  
 case (ObjClone C h'' FDTs obs')  
 note FDTs =  $\langle P \vdash C \text{ has-fields FDTs} \rangle$   
 and obs =  $\langle obs = \text{NewHeapElem } ad' (\text{Class-type } C) \# obs' \rangle$   
 let ?als = map ( $\lambda((F, D), Tfm). CField D F$ ) FDTs  
 let ?Ts = map ( $\lambda(FD, T). \text{fst } (the (map-of FDTs FD))$ ) FDTs  
 let ?vs = w-value P vs (NormalAction (NewHeapElem ad' (Class-type C) :: ('addr, 'thread-id)  
 obs-event))  
 from  $\langle (h'', ad') \in \text{allocate } h (\text{Class-type } C) \rangle$  have hext:  $h \trianglelefteq h''$  by(rule hext-heap-ops)  
 hence type:  $\text{typeof-addr } h'' \text{ ad} = \lfloor \text{Class-type } C \rfloor$  using  $\langle \text{typeof-addr } h \text{ ad} = \lfloor \text{Class-type } C \rfloor \rangle$   
 by(rule typeof-addr-hext-mono)  
  
 note  $\langle \text{heap-copies } ad \text{ ad}' \text{ ?als } h'' \text{ obs}' h' \rangle$   
 moreover from sc have non-speculative P ?vs (llist-of (take (n - 1) (map NormalAction obs')))  
 by(simp add: obs take-Cons' split: if-split-asm)  
 moreover from  $\langle P \vdash C \text{ has-fields FDTs} \rangle$   
 have is-class P C by(rule has-fields-is-class)  
 hence is-htype P (Class-type C) by simp  
 with vs  $\langle (h'', ad') \in \text{allocate } h (\text{Class-type } C) \rangle$   
 have vs-conf P h'' ?vs by(rule vs-conf-allocate)  
 moreover from  $\langle (h'', ad') \in \text{allocate } h (\text{Class-type } C) \rangle$  hconf  $\langle \text{is-htype } P (\text{Class-type } C) \rangle$   
 have hconf h'' by(rule hconf-allocate-mono)  
 moreover from type FDTs have list-all2  $(\lambda al T. P, h'' \vdash ad'@al : T) \text{ ?als ?Ts}$   
 unfolding list-all2-map1 list-all2-map2 list-all2-same  
 by(fastforce intro: addr-loc-type.intros simp add: has-field-def dest: weak-map-of-SomeI)  
 moreover from  $\langle (h'', ad') \in \text{allocate } h (\text{Class-type } C) \rangle$   $\langle \text{is-htype } P (\text{Class-type } C) \rangle$   
 have  $\text{typeof-addr } h'' \text{ ad}' = \lfloor \text{Class-type } C \rfloor$  by(auto dest: allocate-SomeD)  
 with FDTs have list-all2  $(\lambda al T. P, h'' \vdash ad'@al : T) \text{ ?als ?Ts}$   
 unfolding list-all2-map1 list-all2-map2 list-all2-same  
 by(fastforce intro: addr-loc-type.intros simp add: has-field-def dest: weak-map-of-SomeI)  
 ultimately  
 have vs': vs-conf P h' (w-values P ?vs (take (n - 1) (map NormalAction obs')))  
 by(rule heap-copies-non-speculative-vs-conf)  
 show ?thesis  
 proof(cases n > 0)  
 case True  
 with obs vs' show ?thesis by(simp add: take-Cons')



```

next
  case False
  from ⟨heap-copies ad ad' ?als h'' obs' h'⟩ have  $h'' \sqsubseteq h'$  by(rule hext-heap-copies)
  with ⟨ $h \sqsubseteq h''$ ⟩ have  $h \sqsubseteq h'$  by(rule hext-trans)
  with vs have vs-conf P h' vs by(rule vs-conf-hext)
  thus ?thesis using False by simp
qed
next
case (ArrClone T N h'' FDTs obs')
note obs = ⟨obs = NewHeapElem ad' (Array-type T N) # obs'⟩
and new = ⟨(h'', ad') ∈ allocate h (Array-type T N)⟩
and FDTs = ⟨P ⊢ Object has-fields FDTs⟩
let ?als = map (λ((F, D), Tfm). CField D F) FDTs @ map ACell [0..h \sqsubseteq h'' by(rule hext-heap-ops)
hence type: typeof-addr h'' ad = [Array-type T N] using ⟨typeof-addr h ad = [Array-type T N]⟩
by(rule typeof-addr-hext-mono)

note ⟨heap-copies ad ad' ?als h'' obs' h'⟩
moreover from sc have non-speculative P ?vs (l1ist-of (take (n - 1) (map NormalAction obs')))
by(simp add: obs take-Cons' split: if-split-asm)
moreover from ⟨typeof-addr h ad = [Array-type T N]⟩ ⟨hconf h⟩ have is-htype P (Array-type T
N)
by(auto dest: typeof-addr-is-type)
with vs new have vs-conf P h'' ?vs by(rule vs-conf-allocate)
moreover from new hconf ⟨is-htype P (Array-type T N)⟩ have hconf h'' by(rule hconf-allocate-mono)
moreover
from type FDTs have list-all2 (λal T. P, h'' ⊢ ad@al : T) ?als ?Ts
by(fastforce intro: list-all2-all-nthI addr-loc-type.intros simp add: has-field-def list-all2-append
list-all2-map1 list-all2-map2 list-all2-same dest: weak-map-of-SomeI)
moreover from new ⟨is-htype P (Array-type T N)⟩
have typeof-addr h'' ad' = [Array-type T N]
by(auto dest: allocate-SomeD)
hence list-all2 (λal T. P, h'' ⊢ ad'@al : T) ?als ?Ts using FDTs
by(fastforce intro: list-all2-all-nthI addr-loc-type.intros simp add: has-field-def list-all2-append
list-all2-map1 list-all2-map2 list-all2-same dest: weak-map-of-SomeI)
ultimately have vs': vs-conf P h' (w-values P ?vs (take (n - 1) (map NormalAction obs')))
by(rule heap-copies-non-speculative-vs-conf)
show ?thesis
proof(cases n > 0)
case True
with obs vs' show ?thesis by(simp add: take-Cons')
next
case False
from ⟨heap-copies ad ad' ?als h'' obs' h'⟩ have  $h'' \sqsubseteq h'$  by(rule hext-heap-copies)
with ⟨ $h \sqsubseteq h''$ ⟩ have  $h \sqsubseteq h'$  by(rule hext-trans)
with vs have vs-conf P h' vs by(rule vs-conf-hext)
thus ?thesis using False by simp
qed
qed

```

lemma heap-clone-non-speculative-typeable-None:

**assumes** *heap-clone*  $P\ h\ ad\ h'\ None$   
**shows** *heap-base.heap-clone allocate typeof-addr (heap-read-typed  $P$ ) heap-write  $P\ h\ ad\ h'\ None$*   
**using** *assms*  
**by**(*cases*)(*blast intro: heap-base.heap-clone.intros*)+

**lemma** *red-external-non-speculative-typeable:*

**assumes** *red:*  $P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle$   
**and** *sc:* *non-speculative*  $P\ Vs$  (*l*list-of (*map* *NormalAction*  $\{ta\}_o$ ))  
**and** *vs:* *vs-conf*  $P\ h\ Vs$   
**and** *hconf:* *hconf*  $h$   
**shows** *heap-base.red-external addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate*  
*typeof-addr (heap-read-typed  $P$ ) heap-write  $P\ t\ h\ a\ M\ vs\ ta\ va\ h'$*   
**using** *assms*  
**by**(*cases*)(*auto intro: heap-base.red-external.intros heap-clone-non-speculative-typeable-None heap-clone-non-specul*  
*dest: hext-heap-clone elim: vs-conf-hext*)

**lemma** *red-external-non-speculative-vs-conf:*

**assumes** *red:*  $P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle$   
**and** *sc:* *non-speculative*  $P\ Vs$  (*l*list-of (*take*  $n$  (*map* *NormalAction*  $\{ta\}_o$ )))  
**and** *vs:* *vs-conf*  $P\ h\ Vs$   
**and** *hconf:* *hconf*  $h$   
**shows** *vs-conf*  $P\ h'$  (*w-values*  $P\ Vs$  (*take*  $n$  (*map* *NormalAction*  $\{ta\}_o$ )))  
**using** *assms*  
**by**(*cases*)(*auto intro: heap-base.red-external.intros heap-clone-non-speculative-vs-conf-Some dest: hext-heap-clone*  
*elim: vs-conf-hext simp add: take-Cons'*)

**lemma** *red-external-aggr-non-speculative-typeable:*

**assumes** *red:*  $(ta, va, h') \in red\text{-external-aggr}\ P\ t\ a\ M\ vs\ h$   
**and** *sc:* *non-speculative*  $P\ Vs$  (*l*list-of (*map* *NormalAction*  $\{ta\}_o$ ))  
**and** *vs:* *vs-conf*  $P\ h\ Vs$   
**and** *hconf:* *hconf*  $h$   
**and** *native:* *is-native*  $P$  (*the* (*typeof-addr*  $h\ a$ ))  $M$   
**shows**  $(ta, va, h') \in heap\text{-base.red-external-aggr}\ addr2thread\text{-id}\ thread\text{-id2addr}\ spurious\text{-wakeups}$   
*empty-heap allocate typeof-addr (heap-read-typed  $P$ ) heap-write  $P\ t\ a\ M\ vs\ h$*   
**using** *assms*  
**by**(*cases the (typeof-addr  $h\ a$ )*)(*auto 4 3 simp add: is-native.simps external-WT-defs.simps red-external-aggr-def*  
*heap-base.red-external-aggr-def split: if-split-asm split del: if-split del: disjCI intro: heap-clone-non-speculative-typeable*  
*heap-clone-non-speculative-typeable-Some dest: sees-method-decl-above*)

**lemma** *red-external-aggr-non-speculative-vs-conf:*

**assumes** *red:*  $(ta, va, h') \in red\text{-external-aggr}\ P\ t\ a\ M\ vs\ h$   
**and** *sc:* *non-speculative*  $P\ Vs$  (*l*list-of (*take*  $n$  (*map* *NormalAction*  $\{ta\}_o$ )))  
**and** *vs:* *vs-conf*  $P\ h\ Vs$   
**and** *hconf:* *hconf*  $h$   
**and** *native:* *is-native*  $P$  (*the* (*typeof-addr*  $h\ a$ ))  $M$   
**shows** *vs-conf*  $P\ h'$  (*w-values*  $P\ Vs$  (*take*  $n$  (*map* *NormalAction*  $\{ta\}_o$ )))  
**using** *assms*  
**by**(*cases the (typeof-addr  $h\ a$ )*)(*auto 4 3 simp add: is-native.simps external-WT-defs.simps red-external-aggr-def*  
*heap-base.red-external-aggr-def take-Cons' split: if-split-asm split del: if-split del: disjCI intro: heap-clone-non-specul*  
*dest: hext-heap-clone elim: vs-conf-hext dest: sees-method-decl-above*)

**end**

**declare** *split-paired-Ex* [*simp del*]

```

declare eq-upto-seq-inconsist-simps [simp]

context heap-progress begin

lemma heap-copy-loc-non-speculative-read:
  assumes hrt: heap-read-typeable hconf P
  and vs: vs-conf P h vs
  and type: P, h ⊢ a@al : T P, h ⊢ a'@al : T
  and hconf: hconf h
  and copy: heap-copy-loc a a' al h obs h'
  and i: i < length obs
  and read: obs ! i = ReadMem a'' al'' v
  and v: v' ∈ w-values P vs (map NormalAction (take i obs)) (a'', al'')
  shows ∃ obs' h''. heap-copy-loc a a' al h obs' h'' ∧ i < length obs' ∧ take i obs' = take i obs ∧
    obs' ! i = ReadMem a'' al'' v' ∧ length obs' ≤ length obs ∧
    non-speculative P vs (llist-of (map NormalAction obs'))

using copy
proof cases
  case (1 v'')
  with read i have [simp]: i = 0 v'' = v a'' = a al'' = al
  by (simp-all add: nth-Cons split: nat.split-asm)
  from v have v' ∈ vs (a, al) by simp
  with vs type have conf: P, h ⊢ v' :≤ T by (auto dest: addr-loc-type-fun vs-confD)
  let ?obs'' = [ReadMem a al v', WriteMem a' al v']
  from hrt type(1) conf hconf have heap-read h a al v' by (rule heap-read-typeableD)
  moreover from heap-write-total[OF hconf type(2) conf]
  obtain h'' where heap-write h a' al v' h'' ..
  ultimately have heap-copy-loc a a' al h ?obs'' h'' ..
  thus ?thesis using 1 ⟨v' ∈ vs (a, al)⟩ by (auto)
qed

lemma heap-copies-non-speculative-read:
  assumes hrt: heap-read-typeable hconf P
  and copies: heap-copies a a' als h obs h'
  and vs: vs-conf P h vs
  and type1: list-all2 (λal T. P, h ⊢ a@al : T) als Ts
  and type2: list-all2 (λal T. P, h ⊢ a'@al : T) als Ts
  and hconf: hconf h
  and i: i < length obs
  and read: obs ! i = ReadMem a'' al'' v
  and v: v' ∈ w-values P vs (map NormalAction (take i obs)) (a'', al'')
  and ns: non-speculative P vs (llist-of (map NormalAction (take i obs)))
  shows ∃ obs' h''. heap-copies a a' als h obs' h'' ∧ i < length obs' ∧ take i obs' = take i obs ∧
    obs' ! i = ReadMem a'' al'' v' ∧ length obs' ≤ length obs
  (is ?concl als h obs vs i)
using copies vs type1 type2 hconf i read v ns
proof (induction arbitrary: Ts vs i)
  case Nil thus ?case by simp
next
  case (Cons al h ob h' als obs h'' Ts vs)
  note copy = ⟨heap-copy-loc a a' al h ob h'⟩
  note vs = ⟨vs-conf P h vs⟩
  note type1 = ⟨list-all2 (λal T. P, h ⊢ a@al : T) (al # als) Ts⟩
  and type2 = ⟨list-all2 (λal T. P, h ⊢ a'@al : T) (al # als) Ts⟩

```

```

note  $hconf = \langle hconf\ h \rangle$ 
note  $i = \langle i < length\ (ob\ @\ obs) \rangle$ 
note  $read = \langle (ob\ @\ obs)\ !\ i = ReadMem\ a''\ al''\ v \rangle$ 
note  $v = \langle v' \in w\text{-values}\ P\ vs\ (map\ NormalAction\ (take\ i\ (ob\ @\ obs)))\ (a'',\ al'') \rangle$ 
note  $ns = \langle non\text{-speculative}\ P\ vs\ (l\text{list-of}\ (map\ NormalAction\ (take\ i\ (ob\ @\ obs)))) \rangle$ 

from  $type1$  obtain  $T\ Ts'$  where  $Ts: Ts = T \# Ts'$ 
  and  $type1': P, h \vdash a@al : T$ 
  and  $type1'': list\text{-all2}\ (\lambda al\ T.\ P, h \vdash a@al : T)\ als\ Ts'$ 
  by  $(auto\ simp\ add: list\text{-all2}\text{-}Cons1)$ 
from  $type2\ Ts$  have  $type2': P, h \vdash a'@al : T$ 
  and  $type2'': list\text{-all2}\ (\lambda al\ T.\ P, h \vdash a'@al : T)\ als\ Ts'$ 
  by  $simp\text{-all}$ 
show  $?case$ 
proof  $(cases\ i < length\ ob)$ 
  case  $True$ 
    with  $read\ v$ 
    have  $ob\ !\ i = ReadMem\ a''\ al''\ v$ 
      and  $v' \in w\text{-values}\ P\ vs\ (map\ NormalAction\ (take\ i\ ob))\ (a'',\ al'')$  by  $(simp\text{-all}\ add: nth\text{-append})$ 
    from  $heap\text{-copy}\text{-}loc\text{-}non\text{-speculative}\text{-}read[OF\ hrt\ vs\ type1'\ type2'\ hconf\ copy\ True\ this]$ 
    obtain  $ob'\ H''$  where  $copy': heap\text{-copy}\text{-}loc\ a\ a'\ al\ h\ ob'\ H''$ 
      and  $i': i < length\ ob'$  and  $take\ i\ ob' = take\ i\ ob$ 
      and  $ob'\ !\ i = ReadMem\ a''\ al''\ v'$ 
      and  $length\ ob' \leq length\ ob$ 
      and  $ns: non\text{-speculative}\ P\ vs\ (l\text{list-of}\ (map\ NormalAction\ ob'))$  by  $blast$ 
    moreover {
      from  $copy'$  have  $hext: h \sqsubseteq H''$  by  $(rule\ hext\text{-}heap\text{-}copy\text{-}loc)$ 
      have  $hconf\ H''$ 
      by  $(rule\ heap\text{-}conf\text{-}read.hconf\text{-}heap\text{-}copy\text{-}loc\text{-}mono[OF\ heap\text{-}conf\text{-}read\text{-}heap\text{-}read\text{-}typed])(rule\ heap\text{-}copy\text{-}loc\text{-}non\text{-}copy'\ ns\ vs\ hconf\ type1'\ type2',\ fact+)$ 
    }
    moreover
      from  $type1''$  have  $list\text{-all2}\ (\lambda al\ T.\ P, H'' \vdash a@al : T)\ als\ Ts'$ 
        by  $(rule\ List.list\text{-all2}\text{-}mono)(rule\ addr\text{-}loc\text{-}type\text{-}hext\text{-}mono[OF\ \text{-}\ hext])$ 
      moreover from  $type2''$  have  $list\text{-all2}\ (\lambda al\ T.\ P, H'' \vdash a'@al : T)\ als\ Ts'$ 
        by  $(rule\ List.list\text{-all2}\text{-}mono)(rule\ addr\text{-}loc\text{-}type\text{-}hext\text{-}mono[OF\ \text{-}\ hext])$ 
      moreover note  $calculation\ }$ 
    from  $heap\text{-copies}\text{-}progress[OF\ this]$ 
    obtain  $obs'\ h'''$  where  $*: heap\text{-copies}\ a\ a'\ als\ H''\ obs'\ h'''$  by  $blast$ 
    moreover note  $heap\text{-copies}\text{-}length[OF\ *]$ 
    moreover note  $heap\text{-copy}\text{-}loc\text{-}length[OF\ copy']$ 
    moreover note  $heap\text{-copies}\text{-}length[OF\ \langle heap\text{-copies}\ a\ a'\ als\ h'\ obs'\ h'' \rangle]$ 
    ultimately show  $?thesis$  using  $True$  by  $(auto\ intro!: heap\text{-copies}.Cons\ exI\ simp\ add: nth\text{-append})$ 
  next
    case  $False$ 
    let  $?vs' = w\text{-values}\ P\ vs\ (map\ NormalAction\ ob)$ 
    let  $?i' = i - length\ ob$ 

    from  $ns\ False$  obtain  $ns': non\text{-speculative}\ P\ vs\ (l\text{list-of}\ (map\ NormalAction\ ob))$ 
      and  $ns'': non\text{-speculative}\ P\ ?vs'\ (l\text{list-of}\ (map\ NormalAction\ (take\ ?i'\ obs)))$ 
      by  $(simp\ add: lappend\text{-}l\text{list-of}\text{-}l\text{list-of}[symmetric]\ non\text{-speculative}\text{-}lappend\ del: lappend\text{-}l\text{list-of}\text{-}l\text{list-of})$ 

    from  $heap\text{-copy}\text{-}loc\text{-}non\text{-speculative}\text{-}vs\text{-}conf[OF\ copy\ \text{-}\ vs\ hconf\ type1'\ type2',\ \text{where}\ n=length\ ob]$ 
     $ns'$ 
    have  $vs\text{-}conf\ P\ h'\ ?vs'$  by  $simp$ 

```

```

moreover
from copy have hext:  $h \sqsubseteq h'$  by(rule hext-heap-copy-loc)
from type1'' have list-all2 ( $\lambda al\ T.\ P, h' \vdash a@al : T$ ) als Ts'
  by(rule List.list-all2-mono)(rule addr-loc-type-hext-mono[OF - hext])
moreover from type2'' have list-all2 ( $\lambda al\ T.\ P, h' \vdash a'@al : T$ ) als Ts'
  by(rule List.list-all2-mono)(rule addr-loc-type-hext-mono[OF - hext])
moreover have hconf h'
by(rule heap-conf-read.hconf-heap-copy-loc-mono[OF heap-conf-read-heap-read-typed])(rule heap-copy-loc-non-speculative-t-copy ns' vs hconf type1' type2', fact+)
moreover from i False have  $?i' < \text{length } \text{obs}$  by simp
moreover from read False have  $\text{obs} ! ?i' = \text{ReadMem } a''\ al''\ v$  by(simp add: nth-append)
moreover from v False have  $v' \in w\text{-values } P\ ?vs'$  (map NormalAction (take ?i' obs)) (a'', al'')
by(simp)
  ultimately have  $?concl\ als\ h'\ \text{obs}\ ?vs'\ ?i'$  using ns'' by(rule Cons.IH)
  thus  $?thesis$  using False copy by safe(auto intro!: heap-copies.Cons exI simp add: nth-append)
qed
qed

```

**lemma** *heap-clone-non-speculative-read*:

```

assumes hrt: heap-read-typeable hconf P
and clone: heap-clone P h a h' [(obs, a')]
and vs: vs-conf P h vs
and hconf: hconf h
and i:  $i < \text{length } \text{obs}$ 
and read:  $\text{obs} ! i = \text{ReadMem } a''\ al''\ v$ 
and v:  $v' \in w\text{-values } P\ \text{vs}$  (map NormalAction (take i obs)) (a'', al'')
and ns: non-speculative P vs (llist-of (map NormalAction (take i obs)))
shows  $\exists \text{obs}'\ h''. \text{heap-clone } P\ h\ a\ h''\ [(obs', a')] \wedge i < \text{length } \text{obs}' \wedge \text{take } i\ \text{obs}' = \text{take } i\ \text{obs} \wedge$ 
   $\text{obs}' ! i = \text{ReadMem } a''\ al''\ v' \wedge \text{length } \text{obs}' \leq \text{length } \text{obs}$ 

```

**using** *clone*

**proof** *cases*

**case** (*ObjClone C h'' FDTs obs'*)

```

note  $\text{obs} = \langle \text{obs} = \text{NewHeapElem } a' (\text{Class-type } C) \# \text{obs}' \rangle$ 
note  $\text{FDTs} = \langle P \vdash C \text{ has-fields } \text{FDTs} \rangle$ 
let  $?als = \text{map } (\lambda (F, D), Tm). \text{CField } D\ F) \text{ FDTs}$ 
let  $?Ts = \text{map } (\lambda (FD, T). \text{fst } (\text{the } (\text{map-of } \text{FDTs } FD))) \text{ FDTs}$ 
let  $?vs = w\text{-value } P\ \text{vs} (\text{NormalAction } (\text{NewHeapElem } a' (\text{Class-type } C))) :: ('addr, 'thread-id)$ 
obs-event action
let  $?i = i - 1$ 
from i read obs have  $i-0: i > 0$  by(simp add: nth-Cons' split: if-split-asm)

```

```

from  $\langle P \vdash C \text{ has-fields } \text{FDTs} \rangle$  have is-class P C by(rule has-fields-is-class)
with  $\langle (h'', a') \in \text{allocate } h (\text{Class-type } C) \rangle$ 
have type-a': typeof-addr  $h''\ a' = \lfloor \text{Class-type } C \rfloor$  and hext:  $h \sqsubseteq h''$ 
  by(auto dest: allocate-SomeD hext-allocate)

```

```

note  $\langle \text{heap-copies } a\ a'\ ?als\ h''\ \text{obs}'\ h' \rangle$ 
moreover from  $\langle \text{typeof-addr } h\ a = \lfloor \text{Class-type } C \rfloor \rangle$  hconf have is-htype P (Class-type C)
  by(rule typeof-addr-is-type)
with  $\langle (h'', a') \in \text{allocate } h (\text{Class-type } C) \rangle$ 
have vs-conf P  $h''\ ?vs$  by(rule vs-conf-allocate)
moreover
from hext  $\langle \text{typeof-addr } h\ a = \lfloor \text{Class-type } C \rfloor \rangle$ 

```

```

have typeof-addr  $h'' a = \lfloor \text{Class-type } C \rfloor$  by(rule typeof-addr-hext-mono)
hence list-all2 ( $\lambda al T. P, h'' \vdash a @ al : T$ ) ?als ?Ts using FDTs
  unfolding list-all2-map1 list-all2-map2 list-all2-same
  by(fastforce intro: addr-loc-type.intros simp add: has-field-def dest: weak-map-of-SomeI)
moreover from FDTs type-a'
have list-all2 ( $\lambda al T. P, h'' \vdash a' @ al : T$ ) ?als ?Ts
  unfolding list-all2-map1 list-all2-map2 list-all2-same
  by(fastforce intro: addr-loc-type.intros simp add: has-field-def dest: weak-map-of-SomeI)
moreover from  $\langle (h'', a') \in \text{allocate } h \text{ (Class-type } C) \rangle$  hconf  $\langle \text{is-htype } P \text{ (Class-type } C) \rangle$ 
have hconf  $h''$  by(rule hconf-allocate-mono)
moreover from  $i$  read  $i-0$  obs have  $?i < \text{length } \text{obs}' \text{ obs}' ! ?i = \text{ReadMem } a'' al'' v$  by simp-all
moreover from  $v$   $i-0$  obs
have  $v' \in w\text{-values } P ?vs (\text{map NormalAction } (\text{take } ?i \text{ obs}')) (a'', al'')$  by(simp add: take-Cons')
moreover from  $ns$   $i-0$  obs
have non-speculative  $P ?vs (\text{lmap-of } (\text{map NormalAction } (\text{take } ?i \text{ obs}')))$  by(simp add: take-Cons')
ultimately have  $\exists \text{obs}'' h'''. \text{heap-copies } a \ a' ?als h'' \text{obs}'' h''' \wedge$ 
   $?i < \text{length } \text{obs}'' \wedge \text{take } ?i \text{obs}'' = \text{take } ?i \text{obs}' \wedge \text{obs}'' ! ?i = \text{ReadMem } a'' al''$ 
 $v' \wedge$ 
   $\text{length } \text{obs}'' \leq \text{length } \text{obs}'$ 
  by(rule heap-copies-non-speculative-read[OF hrt])
thus ?thesis using  $\langle \text{typeof-addr } h \ a = \lfloor \text{Class-type } C \rfloor \rangle \langle (h'', a') \in \text{allocate } h \text{ (Class-type } C) \rangle$  FDTs
obs  $i-0$ 
by(auto 4 4 intro: heap-clone.ObjClone simp add: take-Cons')
next
case (ArrClone  $T \ n \ h''$  FDTs obs')

note  $\text{obs} = \langle \text{obs} = \text{NewHeapElem } a' \text{ (Array-type } T \ n) \ \# \ \text{obs}' \rangle$ 
note  $\text{FDTs} = \langle P \vdash \text{Object has-fields FDTs} \rangle$ 
let ?als =  $\text{map } (\lambda((F, D), \text{Tfm}). \text{CField } D \ F) \ \text{FDTs} @ \text{map } \text{ACell } [0..<n]$ 
let ?Ts =  $\text{map } (\lambda(FD, T). \text{fst } (\text{the } (\text{map-of FDTs } FD))) \ \text{FDTs} @ \text{replicate } n \ T$ 
let ?vs =  $w\text{-value } P \ \text{vs } (\text{NormalAction } (\text{NewHeapElem } a' \text{ (Array-type } T \ n)) :: ('addr, 'thread-id)$ 
obs-event action)
let  $?i = i - 1$ 
from  $i$  read obs have  $i-0: i > 0$  by(simp add: nth-Cons' split: if-split-asm)

from  $\langle \text{typeof-addr } h \ a = \lfloor \text{Array-type } T \ n \rfloor \rangle$  hconf
have is-htype  $P \text{ (Array-type } T \ n)$  by(rule typeof-addr-is-type)
with  $\langle (h'', a') \in \text{allocate } h \text{ (Array-type } T \ n) \rangle$ 
have type-a': typeof-addr  $h'' a' = \lfloor \text{Array-type } T \ n \rfloor$ 
  and hext:  $h \sqsubseteq h''$ 
by(auto dest: allocate-SomeD hext-allocate)

note  $\langle \text{heap-copies } a \ a' ?als h'' \text{obs}' h' \rangle$ 
moreover from  $vs \langle (h'', a') \in \text{allocate } h \text{ (Array-type } T \ n) \rangle \langle \text{is-htype } P \text{ (Array-type } T \ n) \rangle$ 
have vs-conf  $P \ h'' ?vs$  by(rule vs-conf-allocate)
moreover from hext  $\langle \text{typeof-addr } h \ a = \lfloor \text{Array-type } T \ n \rfloor \rangle$ 
have type'a: typeof-addr  $h'' a = \lfloor \text{Array-type } T \ n \rfloor$ 
  by(auto intro: hext-arrD)
from type'a FDTs have list-all2 ( $\lambda al T. P, h'' \vdash a @ al : T$ ) ?als ?Ts
  by(fastforce intro: list-all2-all-nthI addr-loc-type.intros simp add: has-field-def list-all2-append
list-all2-map1 list-all2-map2 list-all2-same dest: weak-map-of-SomeI)
moreover from type-a' FDTs
have list-all2 ( $\lambda al T. P, h'' \vdash a' @ al : T$ ) ?als ?Ts
  by(fastforce intro: list-all2-all-nthI addr-loc-type.intros simp add: has-field-def list-all2-append

```

*list-all2-map1 list-all2-map2 list-all2-same dest: weak-map-of-SomeI*  
**moreover from**  $\langle (h'', a') \in \text{allocate } h \text{ (Array-type } T \text{ n)} \rangle \text{ hconf } \langle \text{is-htype } P \text{ (Array-type } T \text{ n)} \rangle$   
**have**  $\text{hconf } h'' \text{ by (rule hconf-allocate-mono)}$   
**moreover from**  $i \text{ read } i-0 \text{ obs have } ?i < \text{length obs'} \text{ obs'} ! ?i = \text{ReadMem } a'' \text{ al'' } v \text{ by simp-all}$   
**moreover from**  $v \text{ i-0 obs}$   
**have**  $v' \in w\text{-values } P \text{ ?vs (map NormalAction (take ?i obs')) (a'', al'')} \text{ by (simp add: take-Cons')}$   
**moreover from**  $ns \text{ i-0 obs}$   
**have**  $\text{non-speculative } P \text{ ?vs (llist-of (map NormalAction (take ?i obs')))} \text{ by (simp add: take-Cons')}$   
**ultimately have**  $\exists \text{obs'' } h'''. \text{heap-copies } a \text{ a' ?als } h'' \text{ obs'' } h''' \wedge$   
 $?i < \text{length obs''} \wedge \text{take ?i obs''} = \text{take ?i obs'} \wedge \text{obs''} ! ?i = \text{ReadMem } a'' \text{ al''}$   
 $v' \wedge$   
 $\text{length obs''} \leq \text{length obs'}$   
**by** (rule *heap-copies-non-speculative-read*[OF *hrt*])  
**thus**  $?thesis \text{ using } \langle \text{typeof-addr } h \text{ a} = \lfloor \text{Array-type } T \text{ n} \rfloor \rangle \langle (h'', a') \in \text{allocate } h \text{ (Array-type } T \text{ n)} \rangle$   
*FDTs obs i-0*  
**by** (auto 4 4 intro: *heap-clone.ArrClone simp add: take-Cons'*)  
**qed**

**lemma** *red-external-non-speculative-read:*  
**assumes** *hrt: heap-read-typeable hconf P*  
**and** *vs: vs-conf P (shr s) vs*  
**and** *red:  $P, t \vdash \langle a.M(vs'), \text{shr } s \rangle -ta \rightarrow \text{ext} \langle va, h' \rangle$*   
**and** *aok: final-thread.actions-ok final s t ta*  
**and** *hconf: hconf (shr s)*  
**and** *i:  $i < \text{length } \llbracket ta \rrbracket_o$*   
**and** *read:  $\llbracket ta \rrbracket_o ! i = \text{ReadMem } a'' \text{ al'' } v$*   
**and** *v:  $v' \in w\text{-values } P \text{ vs (map NormalAction (take i } \llbracket ta \rrbracket_o)) (a'', al'')$*   
**and** *ns: non-speculative P vs (llist-of (map NormalAction (take i } \llbracket ta \rrbracket\_o)))*  
**shows**  $\exists ta'' va'' h''. P, t \vdash \langle a.M(vs'), \text{shr } s \rangle -ta'' \rightarrow \text{ext} \langle va'', h'' \rangle \wedge \text{final-thread.actions-ok final s t}$   
 $ta'' \wedge$

$$i < \text{length } \llbracket ta'' \rrbracket_o \wedge \text{take } i \llbracket ta'' \rrbracket_o = \text{take } i \llbracket ta \rrbracket_o \wedge$$

$$\llbracket ta'' \rrbracket_o ! i = \text{ReadMem } a'' \text{ al'' } v' \wedge \text{length } \llbracket ta'' \rrbracket_o \leq \text{length } \llbracket ta \rrbracket_o$$

**using** *red i read*

**proof** *cases*

**case** [*simp*]: (*RedClone obs a'*)  
**from** *heap-clone-non-speculative-read*[OF *hrt*  $\langle \text{heap-clone } P \text{ (shr } s) \text{ a } h' \lfloor (obs, a') \rfloor \rangle \text{ vs hconf, of } i$   
 $a'' \text{ al'' } v \text{ v'} \rfloor i \text{ read } v \text{ ns}$   
**show**  $?thesis \text{ using aok}$   
**by** (*fastforce* intro: *red-external.RedClone simp add: final-thread.actions-ok-iff*)  
**qed** (auto *simp add: nth-Cons*)

**lemma** *red-external-aggr-non-speculative-read:*  
**assumes** *hrt: heap-read-typeable hconf P*  
**and** *vs: vs-conf P (shr s) vs*  
**and** *red:  $(ta, va, h') \in \text{red-external-aggr } P \text{ t a } M \text{ vs' (shr } s)$*   
**and** *native: is-native P (the (typeof-addr (shr s) a)) M*  
**and** *aok: final-thread.actions-ok final s t ta*  
**and** *hconf: hconf (shr s)*  
**and** *i:  $i < \text{length } \llbracket ta \rrbracket_o$*   
**and** *read:  $\llbracket ta \rrbracket_o ! i = \text{ReadMem } a'' \text{ al'' } v$*   
**and** *v:  $v' \in w\text{-values } P \text{ vs (map NormalAction (take i } \llbracket ta \rrbracket_o)) (a'', al'')$*   
**and** *ns: non-speculative P vs (llist-of (map NormalAction (take i } \llbracket ta \rrbracket\_o)))*  
**shows**  $\exists ta'' va'' h''. (ta'', va'', h'') \in \text{red-external-aggr } P \text{ t a } M \text{ vs' (shr } s) \wedge \text{final-thread.actions-ok}$   
 $\text{final s t } ta'' \wedge$

```

       $i < \text{length } \llbracket ta'' \rrbracket_o \wedge \text{take } i \llbracket ta'' \rrbracket_o = \text{take } i \llbracket ta \rrbracket_o \wedge$ 
       $\llbracket ta'' \rrbracket_o ! i = \text{ReadMem } a'' \text{ al'' } v' \wedge \text{length } \llbracket ta'' \rrbracket_o \leq \text{length } \llbracket ta \rrbracket_o$ 
using red native aok hconf i read v ns
apply(simp add: red-external-aggr-def final-thread.actions-ok-iff ex-disj-distrib conj-disj-distribR split
nth-Cons' del: if-split split: if-split-asm disj-split-asm)
apply(drule heap-clone-non-speculative-read[OF hrt - vs hconf, of - - - i a'' al'' v v'])
apply simp-all
apply(fastforce)
done

end

declare split-paired-Ex [simp]
declare eq-upto-seq-inconsist-simps [simp del]

context allocated-heap begin

lemma heap-copy-loc-allocated-same:
  assumes heap-copy-loc a a' al h obs h'
  shows allocated h' = allocated h
using assms
by cases(auto del: subsetI simp: heap-write-allocated-same)

lemma heap-copy-loc-allocated-mono:
  heap-copy-loc a a' al h obs h'  $\implies$  allocated h  $\subseteq$  allocated h'
by(simp add: heap-copy-loc-allocated-same)

lemma heap-copies-allocated-same:
  assumes heap-copies a a' al h obs h'
  shows allocated h' = allocated h
using assms
by(induct)(auto simp add: heap-copy-loc-allocated-same)

lemma heap-copies-allocated-mono:
  heap-copies a a' al h obs h'  $\implies$  allocated h  $\subseteq$  allocated h'
by(simp add: heap-copies-allocated-same)

lemma heap-clone-allocated-mono:
  assumes heap-clone P h a h' aobs
  shows allocated h  $\subseteq$  allocated h'
using assms
by cases(blast del: subsetI intro: heap-copies-allocated-mono allocate-allocated-mono intro: subset-trans)+

lemma red-external-allocated-mono:
  assumes P, t  $\vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow_{ext} \langle va, h' \rangle$ 
  shows allocated h  $\subseteq$  allocated h'
using assms
by(cases)(blast del: subsetI intro: heap-clone-allocated-mono heap-write-allocated-same)+

lemma red-external-aggr-allocated-mono:
   $\llbracket (ta, va, h') \in \text{red-external-aggr } P \text{ t a } M \text{ vs } h; \text{is-native } P \text{ (the (typeof-addr h a)) } M \rrbracket$ 
 $\implies \text{allocated } h \subseteq \text{allocated } h'$ 
by(cases the (typeof-addr h a))(auto simp add: is-native.simps external-WT-defs.simps red-external-aggr-def)

```



*split: if-split-asm dest: heap-clone-allocated-mono sees-method-decl-above)*

**lemma** *heap-clone-allocatedD*:

**assumes** *heap-clone*  $P\ h\ a\ h'\ [(obs, a')]$   
**and** *NewHeapElem*  $a''\ x \in set\ obs$   
**shows**  $a'' \in allocated\ h' \wedge a'' \notin allocated\ h$

**using** *assms*

**by** *cases(auto dest: allocate-allocatedD heap-copies-allocated-mono heap-copies-not-New)*

**lemma** *red-external-allocatedD*:

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle - ta \rightarrow ext\ \langle va, h' \rangle; NewHeapElem\ a'\ x \in set\ \{ta\}_o \rrbracket$   
 $\implies a' \in allocated\ h' \wedge a' \notin allocated\ h$

**by**(*erule red-external.cases*)(*auto dest: heap-clone-allocatedD*)

**lemma** *red-external-aggr-allocatedD*:

$\llbracket (ta, va, h') \in red-external-aggr\ P\ t\ a\ M\ vs\ h; NewHeapElem\ a'\ x \in set\ \{ta\}_o;$   
 $is-native\ P\ (the\ (typeof-addr\ h\ a))\ M \rrbracket$   
 $\implies a' \in allocated\ h' \wedge a' \notin allocated\ h$

**by**(*auto simp add: is-native.simps external-WT-defs.simps red-external-aggr-def split: if-split-asm dest: heap-clone-allocatedD sees-method-decl-above*)

**lemma** *heap-clone-NewHeapElemD*:

**assumes** *heap-clone*  $P\ h\ a\ h'\ [(obs, a')]$   
**and**  $ad \in allocated\ h'$   
**and**  $ad \notin allocated\ h$   
**shows**  $\exists\ CTn. NewHeapElem\ ad\ CTn \in set\ obs$

**using** *assms*

**by** *cases(auto dest!: allocate-allocatedD heap-copies-allocated-same)*

**lemma** *heap-clone-fail-allocated-same*:

**assumes** *heap-clone*  $P\ h\ a\ h'\ None$   
**shows**  $allocated\ h' = allocated\ h$

**using** *assms*

**by**(*cases*)(*auto*)

**lemma** *red-external-NewHeapElemD*:

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle - ta \rightarrow ext\ \langle va, h' \rangle; a' \in allocated\ h'; a' \notin allocated\ h \rrbracket$   
 $\implies \exists\ CTn. NewHeapElem\ a'\ CTn \in set\ \{ta\}_o$

**by**(*erule red-external.cases*)(*auto dest: heap-clone-NewHeapElemD heap-clone-fail-allocated-same*)

**lemma** *red-external-aggr-NewHeapElemD*:

$\llbracket (ta, va, h') \in red-external-aggr\ P\ t\ a\ M\ vs\ h; a' \in allocated\ h'; a' \notin allocated\ h;$   
 $is-native\ P\ (the\ (typeof-addr\ h\ a))\ M \rrbracket$   
 $\implies \exists\ CTn. NewHeapElem\ a'\ CTn \in set\ \{ta\}_o$

**by**(*cases the (typeof-addr h a)*)(*auto simp add: is-native.simps external-WT-defs.simps red-external-aggr-def split: if-split-asm dest: heap-clone-fail-allocated-same heap-clone-NewHeapElemD sees-method-decl-above*)

**end**

**context** *heap-base* **begin**

**lemma** *binop-known-addr*:

**assumes** *ok: start-heap-ok*

**shows**  $binop\ bop\ v1\ v2 = \llbracket Inl\ v \rrbracket \implies ka-Val\ v \subseteq ka-Val\ v1 \cup ka-Val\ v2 \cup set\ start-addr$

**and**  $\text{binop } bop \ v1 \ v2 = \llbracket \text{Inr } a \rrbracket \implies a \in \text{ka-Val } v1 \cup \text{ka-Val } v2 \cup \text{set start-addr}$   
**apply**(cases bop, auto split: if-split-asm)[1]  
**apply**(cases bop, auto split: if-split-asm simp add: addr-of-sys-xcpt-start-addr[OF ok])  
**done**

**lemma** *heap-copy-loc-known-addr-ReadMem*:

**assumes** *heap-copy-loc*  $a \ a' \ al \ h \ ob \ h'$

**and** *ReadMem*  $ad \ al' \ v \in \text{set } ob$

**shows**  $ad = a$

**using** *assms* **by** cases simp

**lemma** *heap-copies-known-addr-ReadMem*:

**assumes** *heap-copies*  $a \ a' \ als \ h \ obs \ h'$

**and** *ReadMem*  $ad \ al \ v \in \text{set } obs$

**shows**  $ad = a$

**using** *assms*

**by**(induct)(auto dest: heap-copy-loc-known-addr-ReadMem)

**lemma** *heap-clone-known-addr-ReadMem*:

**assumes** *heap-clone*  $P \ h \ a \ h' \ \llbracket (obs, a') \rrbracket$

**and** *ReadMem*  $ad \ al \ v \in \text{set } obs$

**shows**  $ad = a$

**using** *assms*

**by** cases(auto dest: heap-copies-known-addr-ReadMem)

**lemma** *red-external-known-addr-ReadMem*:

$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow \text{ext} \langle va, h' \rangle; \text{ReadMem } ad \ al \ v \in \text{set } \llbracket ta \rrbracket_o \rrbracket$

$\implies ad \in \{\text{thread-id2addr } t, a\} \cup (\bigcup (\text{ka-Val } ' \text{ set } vs)) \cup \text{set start-addr}$

**by**(erule red-external.cases)(simp-all add: heap-clone-known-addr-ReadMem)

**lemma** *red-external-aggr-known-addr-ReadMem*:

$\llbracket (ta, va, h') \in \text{red-external-aggr } P \ t \ a \ M \ vs \ h; \text{ReadMem } ad \ al \ v \in \text{set } \llbracket ta \rrbracket_o \rrbracket$

$\implies ad \in \{\text{thread-id2addr } t, a\} \cup (\bigcup (\text{ka-Val } ' \text{ set } vs)) \cup \text{set start-addr}$

**apply**(auto simp add: red-external-aggr-def split: if-split-asm dest: heap-clone-known-addr-ReadMem)  
**done**

**lemma** *heap-copy-loc-known-addr-WriteMem*:

**assumes** *heap-copy-loc*  $a \ a' \ al \ h \ ob \ h'$

**and**  $ob \ ! \ n = \text{WriteMem } ad \ al' \ (\text{Addr } a'') \ n < \text{length } ob$

**shows**  $a'' \in \text{new-obs-addr} (\text{take } n \ ob)$

**using** *assms*

**by** cases(auto simp add: nth-Cons new-obs-addr-def split: nat.split-asm)

**lemma** *heap-copies-known-addr-WriteMem*:

**assumes** *heap-copies*  $a \ a' \ als \ h \ obs \ h'$

**and**  $obs \ ! \ n = \text{WriteMem } ad \ al \ (\text{Addr } a'') \ n < \text{length } obs$

**shows**  $a'' \in \text{new-obs-addr} (\text{take } n \ obs)$

**using** *assms*

**by**(induct arbitrary: n)(auto simp add: nth-append new-obs-addr-def dest: heap-copy-loc-known-addr-WriteMem split: if-split-asm)

**lemma** *heap-clone-known-addr-WriteMem*:

**assumes** *heap-clone*  $P \ h \ a \ h' \ \llbracket (obs, a') \rrbracket$

**and**  $obs \ ! \ n = \text{WriteMem } ad \ al \ (\text{Addr } a'') \ n < \text{length } obs$

**shows**  $a'' \in \text{new-obs-addr} (\text{take } n \text{ obs})$   
**using** *assms*  
**by** *cases(auto simp add: nth-Cons new-obs-addr-def split: nat.split-asm dest: heap-copies-known-addr-WriteMem)*

**lemma** *red-external-known-addr-WriteMem*:  

$$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle; \llbracket ta \rrbracket_o ! n = \text{WriteMem } ad \text{ al } (\text{Addr } a'); n < \text{length } \llbracket ta \rrbracket_o \rrbracket$$

$$\implies a' \in \{\text{thread-id2addr } t, a\} \cup (\bigcup (ka\text{-Val } ' \text{ set } vs)) \cup \text{set start-addr} \cup \text{new-obs-addr} (\text{take } n \llbracket ta \rrbracket_o)$$
**by**(*erule red-external.cases*)(*auto dest: heap-clone-known-addr-WriteMem*)

**lemma** *red-external-aggr-known-addr-WriteMem*:  

$$\llbracket (ta, va, h') \in \text{red-external-aggr } P \text{ } t \text{ } a \text{ } M \text{ } vs \text{ } h;$$

$$\llbracket ta \rrbracket_o ! n = \text{WriteMem } ad \text{ al } (\text{Addr } a'); n < \text{length } \llbracket ta \rrbracket_o \rrbracket$$

$$\implies a' \in \{\text{thread-id2addr } t, a\} \cup (\bigcup (ka\text{-Val } ' \text{ set } vs)) \cup \text{set start-addr} \cup \text{new-obs-addr} (\text{take } n \llbracket ta \rrbracket_o)$$
**apply**(*auto simp add: red-external-aggr-def split: if-split-asm dest: heap-clone-known-addr-WriteMem*)  
**done**

**lemma** *red-external-known-addr-mono*:  
**assumes** *ok: start-heap-ok*  
**and** *red:  $P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle$*   
**shows**  $(\text{case } va \text{ of } \text{RetVal } v \Rightarrow ka\text{-Val } v \mid \text{RetExc } a \Rightarrow \{a\} \mid \text{RetStaySame} \Rightarrow \{\}) \subseteq \{\text{thread-id2addr } t, a\} \cup (\bigcup (ka\text{-Val } ' \text{ set } vs)) \cup \text{set start-addr} \cup \text{new-obs-addr } \llbracket ta \rrbracket_o$   
**using** *red*  
**by** *cases(auto simp add: addr-of-sys-xcpt-start-addr[OF ok] new-obs-addr-def heap-clone.simps)*

**lemma** *red-external-aggr-known-addr-mono*:  
**assumes** *ok: start-heap-ok*  
**and** *red:  $(ta, va, h') \in \text{red-external-aggr } P \text{ } t \text{ } a \text{ } M \text{ } vs \text{ } h \text{ is-native } P \text{ (the (typeof-addr } h \text{ } a)) M$*   
**shows**  $(\text{case } va \text{ of } \text{RetVal } v \Rightarrow ka\text{-Val } v \mid \text{RetExc } a \Rightarrow \{a\} \mid \text{RetStaySame} \Rightarrow \{\}) \subseteq \{\text{thread-id2addr } t, a\} \cup (\bigcup (ka\text{-Val } ' \text{ set } vs)) \cup \text{set start-addr} \cup \text{new-obs-addr } \llbracket ta \rrbracket_o$   
**using** *red*  
**apply**(*cases the (typeof-addr h a)*)  
**apply**(*auto simp add: red-external-aggr-def addr-of-sys-xcpt-start-addr[OF ok] new-obs-addr-def heap-clone.simps split: if-split-asm*)  
**apply**(*auto simp add: is-native.simps elim!: external-WT-defs.cases dest: sees-method-decl-above*)  
**done**

**lemma** *red-external-NewThread-idD*:  

$$\llbracket P, t \vdash \langle a \cdot M(vs), h \rangle -ta \rightarrow ext \langle va, h' \rangle; \text{NewThread } t' (C, M', a') h'' \in \text{set } \llbracket ta \rrbracket_t \rrbracket$$

$$\implies t' = \text{addr2thread-id } a \wedge a' = a$$
**by**(*erule red-external.cases*) *simp-all*

**lemma** *red-external-aggr-NewThread-idD*:  

$$\llbracket (ta, va, h') \in \text{red-external-aggr } P \text{ } t \text{ } a \text{ } M \text{ } vs \text{ } h;$$

$$\text{NewThread } t' (C, M', a') h'' \in \text{set } \llbracket ta \rrbracket_t \rrbracket$$

$$\implies t' = \text{addr2thread-id } a \wedge a' = a$$
**apply**(*auto simp add: red-external-aggr-def split: if-split-asm*)  
**done**

**end**

**locale** *heap'' =*  
*heap'*

```

    addr2thread-id thread-id2addr
    spurious-wakeups
    empty-heap allocate typeof-addr heap-read heap-write
    P
  for addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
  and thread-id2addr :: 'thread-id ⇒ 'addr
  and spurious-wakeups :: bool
  and empty-heap :: 'heap
  and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
  and typeof-addr :: 'addr → htype
  and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
  and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
  and P :: 'm prog
  +
  assumes allocate-typeof-addr-SomeD:  $\llbracket (h', a) \in \text{allocate } h \ hT; \text{typeof-addr } a \neq \text{None} \rrbracket \implies \text{typeof-addr } a = \lfloor hT \rfloor$ 
begin

lemma heap-copy-loc-New-type-match:
   $\llbracket h.\text{heap-copy-loc } a \ a' \text{ al } h \text{ obs } h'; \text{NewHeapElem } ad \ CTn \in \text{set } obs; \text{typeof-addr } ad \neq \text{None} \rrbracket$ 
   $\implies \text{typeof-addr } ad = \lfloor CTn \rfloor$ 
by(erule h.heap-copy-loc.cases) simp

lemma heap-copies-New-type-match:
   $\llbracket h.\text{heap-copies } a \ a' \text{ als } h \text{ obs } h'; \text{NewHeapElem } ad \ CTn \in \text{set } obs; \text{typeof-addr } ad \neq \text{None} \rrbracket$ 
   $\implies \text{typeof-addr } ad = \lfloor CTn \rfloor$ 
by(induct rule: h.heap-copies.induct)(auto dest: heap-copy-loc-New-type-match)

lemma heap-clone-New-type-match:
   $\llbracket h.\text{heap-clone } P \ h \ a \ h' \lfloor (obs, a') \rfloor; \text{NewHeapElem } ad \ CTn \in \text{set } obs; \text{typeof-addr } ad \neq \text{None} \rrbracket$ 
   $\implies \text{typeof-addr } ad = \lfloor CTn \rfloor$ 
by(erule h.heap-clone.cases)(auto dest: allocate-typeof-addr-SomeD heap-copies-New-type-match)

lemma red-external-New-type-match:
   $\llbracket h.\text{red-external } P \ t \ a \ M \text{ vs } h \ ta \ va \ h'; \text{NewHeapElem } ad \ CTn \in \text{set } \{\{ta\}_o; \text{typeof-addr } ad \neq \text{None}\} \rrbracket$ 
   $\implies \text{typeof-addr } ad = \lfloor CTn \rfloor$ 
by(erule h.red-external.cases)(auto dest: heap-clone-New-type-match)

lemma red-external-aggr-New-type-match:
   $\llbracket (ta, va, h') \in h.\text{red-external-aggr } P \ t \ a \ M \text{ vs } h; \text{NewHeapElem } ad \ CTn \in \text{set } \{\{ta\}_o; \text{typeof-addr } ad \neq \text{None}\} \rrbracket$ 
   $\implies \text{typeof-addr } ad = \lfloor CTn \rfloor$ 
by(auto simp add: h.red-external-aggr-def split: if-split-asm dest: heap-clone-New-type-match)

end

end
theory JMM-J
imports
  JMM-Framework
  ../J/Threaded
begin

sublocale J-heap-base < red-mthr:

```

```

heap-multithreaded-base
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  final-expr mred P convert-RA
for P
.

context J-heap-base begin

abbreviation J- $\mathcal{E}$  ::
  'addr J-prog  $\Rightarrow$  cname  $\Rightarrow$  mname  $\Rightarrow$  'addr val list  $\Rightarrow$  status
 $\Rightarrow$  ('thread-id  $\times$  ('addr, 'thread-id) obs-event action) llist set
where
  J- $\mathcal{E}$  P  $\equiv$  red-mthr. $\mathcal{E}$ -start P J-local-start P

end

end

```

## 8.14 JMM Instantiation for J

```

theory DRF-J
imports
  JMM-Common
  JMM-J
  ../J/ProgressThreaded
  SC-Legal
begin

primrec ka :: 'addr expr  $\Rightarrow$  'addr set
and kas :: 'addr expr list  $\Rightarrow$  'addr set
where
  ka (new C) = {}
| ka (newA T[e]) = ka e
| ka (Cast T e) = ka e
| ka (e instanceof T) = ka e
| ka (Val v) = ka-Val v
| ka (Var V) = {}
| ka (e1 «bop» e2) = ka e1  $\cup$  ka e2
| ka (V := e) = ka e
| ka (a[e]) = ka a  $\cup$  ka e
| ka (a[e] := e') = ka a  $\cup$  ka e  $\cup$  ka e'
| ka (a.length) = ka a
| ka (e.F{D}) = ka e
| ka (e.F{D} := e') = ka e  $\cup$  ka e'
| ka (e.compareAndSwap(D.F, e', e'')) = ka e  $\cup$  ka e'  $\cup$  ka e''
| ka (e.M(es)) = ka e  $\cup$  kas es
| ka {V:T=vo; e} = ka e  $\cup$  (case vo of None  $\Rightarrow$  {} | Some v  $\Rightarrow$  ka-Val v)
| ka (Synchronized x e e') = ka e  $\cup$  ka e'
| ka (InSynchronized x a e) = insert a (ka e)
| ka (e;; e') = ka e  $\cup$  ka e'
| ka (if (e) e1 else e2) = ka e  $\cup$  ka e1  $\cup$  ka e2

```

|  $ka \text{ (while } (b) \ e) = ka \ b \cup ka \ e$   
|  $ka \text{ (throw } e) = ka \ e$   
|  $ka \text{ (try } e \text{ catch } (C \ V) \ e') = ka \ e \cup ka \ e'$

|  $kas \ [] = \{\}$   
|  $kas \ (e \# \ es) = ka \ e \cup kas \ es$

**definition**  $ka\text{-locals} :: 'addr \ locals \Rightarrow 'addr \ set$   
**where**  $ka\text{-locals} \ xs = \{a. \text{Addr } a \in \text{ran } xs\}$

**lemma**  $ka\text{-Val-subset-ka-locals}$ :  
 $xs \ V = \lfloor v \rfloor \Longrightarrow ka\text{-Val } v \subseteq ka\text{-locals } xs$   
**by**( $\text{cases } v$ )( $\text{auto simp add: ka-locals-def ran-def}$ )

**lemma**  $ka\text{-locals-update-subset}$ :  
 $ka\text{-locals } (xs(V := None)) \subseteq ka\text{-locals } xs$   
 $ka\text{-locals } (xs(V \mapsto v)) \subseteq ka\text{-Val } v \cup ka\text{-locals } xs$   
**by**( $\text{auto simp add: ka-locals-def ran-def}$ )

**lemma**  $ka\text{-locals-empty}$  [ $\text{simp}$ ]:  $ka\text{-locals } Map.empty = \{\}$   
**by**( $\text{simp add: ka-locals-def}$ )

**lemma**  $kas\text{-append}$  [ $\text{simp}$ ]:  $kas \ (es @ \ es') = kas \ es \cup kas \ es'$   
**by**( $\text{induct } es$ )  $\text{auto}$

**lemma**  $kas\text{-map-Val}$  [ $\text{simp}$ ]:  $kas \ (\text{map } Val \ vs) = \bigcup (ka\text{-Val } ' \ set \ vs)$   
**by**( $\text{induct } vs$ )  $\text{auto}$

**lemma**  $ka\text{-blocks}$ :  
 $\llbracket \text{length } pns = \text{length } Ts; \text{length } vs = \text{length } Ts \rrbracket$   
 $\Longrightarrow ka \text{ (blocks } pns \ Ts \ vs \ body) = \bigcup (ka\text{-Val } ' \ set \ vs) \cup ka \ body$   
**by**( $\text{induct } pns \ Ts \ vs \ body \text{ rule: blocks.induct}$ )( $\text{auto}$ )

**lemma**  $WT\text{-ka}$ :  $P, E \vdash e :: T \Longrightarrow ka \ e = \{\}$   
**and**  $WTs\text{-kas}$ :  $P, E \vdash es [\cdot] Ts \Longrightarrow kas \ es = \{\}$   
**by**( $\text{induct rule: WT-WTs.inducts}$ )( $\text{auto simp add: typeof-ka}$ )

**context**  $J\text{-heap-base}$  **begin**

**primrec**  $J\text{-known-addr}$ s ::  $'thread\text{-id} \Rightarrow 'addr \ expr \times 'addr \ locals \Rightarrow 'addr \ set$   
**where**  $J\text{-known-addr}$ s  $t \ (e, xs) = \text{insert } (\text{thread-id2addr } t) \ (ka \ e \cup ka\text{-locals } xs \cup \text{set } \text{start-addr}$ s)

**lemma** **assumes**  $wf$ :  $wf\text{-J-prog } P$   
**and**  $ok$ :  $\text{start-heap-ok}$   
**shows**  $\text{red-known-addr}$ s- $\text{mono}$ :  
 $P, t \vdash \langle e, s \rangle \text{-ta} \rightarrow \langle e', s' \rangle \Longrightarrow J\text{-known-addr}$ s  $t \ (e', \text{lcl } s') \subseteq J\text{-known-addr}$ s  $t \ (e, \text{lcl } s) \cup \text{new-obs-addr}$ s  $\llbracket ta \rrbracket_o$   
**and**  $\text{reds-known-addr}$ s- $\text{mono}$ :  
 $P, t \vdash \langle es, s \rangle \text{[-ta-]} \rightarrow \langle es', s' \rangle \Longrightarrow kas \ es' \cup ka\text{-locals } (\text{lcl } s') \subseteq \text{insert } (\text{thread-id2addr } t) \ (kas \ es \cup ka\text{-locals } (\text{lcl } s)) \cup \text{new-obs-addr}$ s  $\llbracket ta \rrbracket_o \cup \text{set } \text{start-addr}$ s  
**proof**( $\text{induct rule: red-reds.inducts}$ )  
**case**  $\text{RedVar}$  **thus** ? $\text{case}$  **by**( $\text{auto dest: ka-Val-subset-ka-locals}$ )  
**next**  
**case**  $\text{RedLAss}$  **thus** ? $\text{case}$  **by**( $\text{auto simp add: ka-locals-def ran-def}$ )

```

next
  case RedBinOp thus ?case by(auto dest: binop-known-addr[OF ok])
next
  case RedBinOpFail thus ?case by(auto dest: binop-known-addr[OF ok])
next
  case RedCall thus ?case
    by(auto simp add: ka-blocks new-obs-addr-def wf-mdecl-def dest!: sees-wf-mdecl[OF wf] WT-ka)
next
  case (RedCallExternal s a T M Ts T D vs ta va h') thus ?case
    by(cases va)(auto dest!: red-external-known-addr-mono[OF ok])
next
  case (BlockRed e h l V vo ta e' h' l')
    thus ?case using ka-locals-update-subset[where xs = l and V=V] ka-locals-update-subset[where
xs = l' and V=V]
    apply(cases l V)
    apply(auto simp del: fun-upd-apply del: subsetI)
    apply(blast dest: ka-Val-subset-ka-locals)+
    done
qed(simp-all add: new-obs-addr-def addr-of-sys-xcpt-start-addr[OF ok] subset-Un1 subset-Un2 sub-
set-insert ka-Val-subset-new-obs-Addr-ReadMem ka-blocks del: fun-upd-apply, blast+)

lemma red-known-addr-ReadMem:
   $\llbracket P, t \vdash \langle e, s \rangle \rightarrow \langle e', s' \rangle; \text{ReadMem } ad \text{ al } v \in \text{set } \{ta\}_o \rrbracket \implies ad \in J\text{-known-addr } t (e, lcl \ s)$ 
  and reds-known-addr-ReadMem:
   $\llbracket P, t \vdash \langle es, s \rangle \rightarrow \langle es', s' \rangle; \text{ReadMem } ad \text{ al } v \in \text{set } \{ta\}_o \rrbracket$ 
   $\implies ad \in \text{insert } (\text{thread-id2addr } t) (kas \ es \cup ka\text{-locals } (lcl \ s)) \cup \text{set } start\text{-addr}$ 
proof(induct rule: red-reds.inducts)
  case RedCallExternal thus ?case by simp (blast dest: red-external-known-addr-ReadMem)
next
  case (BlockRed e h l V vo ta e' h' l')
    thus ?case using ka-locals-update-subset[where xs = l and V=V] ka-locals-update-subset[where
xs = l' and V=V]
    by(auto simp del: fun-upd-apply)
qed(simp-all, blast+)

lemma red-known-addr-WriteMem:
   $\llbracket P, t \vdash \langle e, s \rangle \rightarrow \langle e', s' \rangle; \{ta\}_o ! n = \text{WriteMem } ad \text{ al } (Addr \ a); n < \text{length } \{ta\}_o \rrbracket$ 
   $\implies a \in J\text{-known-addr } t (e, lcl \ s) \vee a \in \text{new-obs-addr } (take \ n \ \{ta\}_o)$ 
  and reds-known-addr-WriteMem:
   $\llbracket P, t \vdash \langle es, s \rangle \rightarrow \langle es', s' \rangle; \{ta\}_o ! n = \text{WriteMem } ad \text{ al } (Addr \ a); n < \text{length } \{ta\}_o \rrbracket$ 
   $\implies a \in \text{insert } (\text{thread-id2addr } t) (kas \ es \cup ka\text{-locals } (lcl \ s)) \cup \text{set } start\text{-addr} \cup \text{new-obs-addr } (take$ 
 $n \ \{ta\}_o)$ 
proof(induct rule: red-reds.inducts)
  case RedCASSucceed thus ?case by(auto simp add: nth-Cons split: nat.split-asm)
next
  case RedCallExternal thus ?case by simp (blast dest: red-external-known-addr-WriteMem)
next
  case (BlockRed e h l V vo ta e' h' l')
    thus ?case using ka-locals-update-subset[where xs = l and V=V] ka-locals-update-subset[where
xs = l' and V=V]
    by(auto simp del: fun-upd-apply)
qed(simp-all, blast+)

end

```

**context** *J-heap* **begin**

**lemma**

**assumes** *wf*: *wf-J-prog P*

**and** *ok*: *start-heap-ok*

**shows** *red-known-addr-new-thread*:

$\llbracket P, t \vdash \langle e, s \rangle \rightarrow \langle e', s' \rangle; \text{NewThread } t' \ x' \ h' \in \text{set } \llbracket ta \rrbracket_t \rrbracket$

$\implies J\text{-known-addr } t' \ x' \subseteq J\text{-known-addr } t \ (e, \text{lcl } s)$

**and** *reds-known-addr-new-thread*:

$\llbracket P, t \vdash \langle es, s \rangle \rightarrow \langle es', s' \rangle; \text{NewThread } t' \ x' \ h' \in \text{set } \llbracket ta \rrbracket_t \rrbracket$

$\implies J\text{-known-addr } t' \ x' \subseteq \text{insert } (\text{thread-id2addr } t) \ (\text{kas } es \cup \text{ka-locals } (\text{lcl } s) \cup \text{set start-addr})$

**proof**(*induct rule: red-reds.inducts*)

**case** *RedCallExternal* **thus** ?*case*

**apply** *clarsimp*

**apply**(*frule* (1) *red-external-new-thread-sub-thread*)

**apply**(*frule* (1) *red-external-NewThread-idD*)

**apply** *clarsimp*

**apply**(*drule* (1) *addr2thread-id-inverse*)

**apply** *simp*

**apply**(*drule* *sub-Thread-sees-run[OF wf]*)

**apply** *clarsimp*

**apply**(*auto* 4 4 *dest: sees-wf-mdecl[OF wf] WT-ka simp add: wf-mdecl-def*)

**done**

**next**

**case** (*BlockRed e h l V vo ta e' h' l'*)

**thus** ?*case* **using** *ka-locals-update-subset*[**where** *xs = l* **and** *V = V*] *ka-locals-update-subset*[**where** *xs = l'* **and** *V = V*]

**by**(*cases l V*)(*auto simp del: fun-upd-apply*)

**qed**(*simp-all, blast+*)

**lemma** *red-New-same-addr-same*:

$\llbracket \text{convert-extTA extTA}, P, t \vdash \langle e, s \rangle \rightarrow \langle e', s' \rangle;$

$\llbracket ta \rrbracket_o ! i = \text{NewHeapElem } a \ x; i < \text{length } \llbracket ta \rrbracket_o;$

$\llbracket ta \rrbracket_o ! j = \text{NewHeapElem } a \ x'; j < \text{length } \llbracket ta \rrbracket_o \rrbracket$

$\implies i = j$

**and** *reds-New-same-addr-same*:

$\llbracket \text{convert-extTA extTA}, P, t \vdash \langle es, s \rangle \rightarrow \langle es', s' \rangle;$

$\llbracket ta \rrbracket_o ! i = \text{NewHeapElem } a \ x; i < \text{length } \llbracket ta \rrbracket_o;$

$\llbracket ta \rrbracket_o ! j = \text{NewHeapElem } a \ x'; j < \text{length } \llbracket ta \rrbracket_o \rrbracket$

$\implies i = j$

**apply**(*induct rule: red-reds.inducts*)

**apply**(*auto dest: red-external-New-same-addr-same simp add: nth-Cons split: nat.split-asm*)

**done**

**end**

**locale** *J-allocated-heap* = *allocated-heap* +

**constrains** *addr2thread-id* :: (*'addr* :: *addr*)  $\Rightarrow$  *'thread-id*

**and** *thread-id2addr* :: *'thread-id*  $\Rightarrow$  *'addr*

**and** *spurious-wakeups* :: *bool*

**and** *empty-heap* :: *'heap*

**and** *allocate* :: *'heap*  $\Rightarrow$  *htype*  $\Rightarrow$  (*'heap*  $\times$  *'addr*) *set*



**and** *typeof-addr* :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype  
**and** *heap-read* :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool  
**and** *heap-write* :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool  
**and** *P* :: 'addr *J-prog*

**sublocale** *J-allocated-heap* < *J-heap*  
**by**(*unfold-locales*)

**context** *J-allocated-heap* **begin**

**lemma** *red-allocated-mono*:  $P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle \implies \text{allocated } (hp\ s) \subseteq \text{allocated } (hp\ s')$   
**and** *reds-allocated-mono*:  $P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle \implies \text{allocated } (hp\ s) \subseteq \text{allocated } (hp\ s')$   
**by**(*induct rule: red-reds.inducts*)(*auto dest: allocate-allocatedD heap-write-allocated-same red-external-allocated-mono del: subsetI*)

**lemma** *red-allocatedD*:

$\llbracket P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; \text{NewHeapElem } ad\ CTn \in \text{set } \{ta\}_o \rrbracket \implies ad \in \text{allocated } (hp\ s') \wedge ad \notin \text{allocated } (hp\ s)$

**and** *reds-allocatedD*:

$\llbracket P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; \text{NewHeapElem } ad\ CTn \in \text{set } \{ta\}_o \rrbracket \implies ad \in \text{allocated } (hp\ s') \wedge ad \notin \text{allocated } (hp\ s)$

**by**(*induct rule: red-reds.inducts*)(*auto dest: allocate-allocatedD heap-write-allocated-same red-external-allocatedD*)

**lemma** *red-allocated-NewHeapElemD*:

$\llbracket P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; ad \in \text{allocated } (hp\ s'); ad \notin \text{allocated } (hp\ s) \rrbracket \implies \exists CTn. \text{NewHeapElem } ad\ CTn \in \text{set } \{ta\}_o$

**and** *reds-allocated-NewHeapElemD*:

$\llbracket P, t \vdash \langle es, s \rangle [-ta \rightarrow] \langle es', s' \rangle; ad \in \text{allocated } (hp\ s'); ad \notin \text{allocated } (hp\ s) \rrbracket \implies \exists CTn. \text{NewHeapElem } ad\ CTn \in \text{set } \{ta\}_o$

**by**(*induct rule: red-reds.inducts*)(*auto dest: allocate-allocatedD heap-write-allocated-same red-external-NewHeapElemD*)

**lemma** *mred-allocated-multithreaded*:

$\text{allocated-multithreaded } addr2thread\text{-id } thread\text{-id2addr } empty\text{-heap } allocate\ \text{typeof-addr } heap\text{-write } allocated\ final\text{-expr } (mred\ P)\ P$

**proof**

**fix** *t x m ta x' m'*

**assume** *mred P t (x, m) ta (x', m')*

**thus**  $\text{allocated } m \subseteq \text{allocated } m'$

**by**(*auto dest: red-allocated-mono del: subsetI simp add: split-beta*)

**next**

**fix** *x t m ta x' m' ad CTn*

**assume** *mred P t (x, m) ta (x', m')*

**and**  $\text{NewHeapElem } ad\ CTn \in \text{set } \{ta\}_o$

**thus**  $ad \in \text{allocated } m' \wedge ad \notin \text{allocated } m$

**by**(*auto dest: red-allocatedD simp add: split-beta*)

**next**

**fix** *t x m ta x' m' ad*

**assume** *mred P t (x, m) ta (x', m')*

**and**  $ad \in \text{allocated } m' \wedge ad \notin \text{allocated } m$

**thus**  $\exists CTn. \text{NewHeapElem } ad\ CTn \in \text{set } \{ta\}_o$

**by**(*auto dest: red-allocated-NewHeapElemD simp add: split-beta*)

**next**

**fix** *t x m ta x' m' i a CTn j CTn'*

**assume** *mred P t (x, m) ta (x', m')*

```

    and  $\{ta\}_o ! i = \text{NewHeapElem } a \text{ } CTh \ i < \text{length } \{ta\}_o$ 
    and  $\{ta\}_o ! j = \text{NewHeapElem } a \text{ } CTh' \ j < \text{length } \{ta\}_o$ 
    thus  $i = j$  by (auto dest: red-New-same-addr-same simp add: split-beta)
qed

```

end

```

sublocale  $J\text{-allocated-heap} < \text{red-mthr: allocated-multithreaded}$ 
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write allocated
  final-expr mred  $P$ 
   $P$ 
by (rule mred-allocated-multithreaded)

```

**context**  $J\text{-allocated-heap}$  **begin**

**lemma**  $mred\text{-known-addr}$ :

**assumes**  $wf$ :  $wf\text{-}J\text{-prog } P$

**and**  $ok$ :  $start\text{-heap-ok}$

**shows**  $known\text{-addrs } addr2thread\text{-id } thread\text{-id2addr } empty\text{-heap } allocate\ type\ of\text{-}addr\ heap\text{-write } allocated\ J\text{-known-addr}\ final\text{-expr } (mred\ P) \ P$

**proof**

**fix**  $t\ x\ m\ ta\ x'\ m'$

**assume**  $mred\ P\ t\ (x, m)\ ta\ (x', m')$

**thus**  $J\text{-known-addr}\ t\ x' \subseteq J\text{-known-addr}\ t\ x \cup new\text{-obs-addr}\ \{ta\}_o$

**by** (auto del: subsetI simp add: split-beta dest: red-known-addr-mono[ $OF\ wf\ ok$ ])

**next**

**fix**  $t\ x\ m\ ta\ x'\ m'\ t'\ x'' m''$

**assume**  $mred\ P\ t\ (x, m)\ ta\ (x', m')$

**and**  $NewThread\ t'\ x'' m'' \in set\ \{ta\}_t$

**thus**  $J\text{-known-addr}\ t'\ x'' \subseteq J\text{-known-addr}\ t\ x$

**by** (auto del: subsetI simp add: split-beta dest: red-known-addr-new-thread[ $OF\ wf\ ok$ ])

**next**

**fix**  $t\ x\ m\ ta\ x'\ m'\ ad\ al\ v$

**assume**  $mred\ P\ t\ (x, m)\ ta\ (x', m')$

**and**  $ReadMem\ ad\ al\ v \in set\ \{ta\}_o$

**thus**  $ad \in J\text{-known-addr}\ t\ x$

**by** (auto simp add: split-beta dest: red-known-addr-ReadMem)

**next**

**fix**  $t\ x\ m\ ta\ x'\ m'\ n\ ad\ al\ ad'$

**assume**  $mred\ P\ t\ (x, m)\ ta\ (x', m')$

**and**  $\{ta\}_o ! n = WriteMem\ ad\ al\ (Addr\ ad')\ n < \text{length } \{ta\}_o$

**thus**  $ad' \in J\text{-known-addr}\ t\ x \vee ad' \in new\text{-obs-addr}\ (take\ n\ \{ta\}_o)$

**by** (auto simp add: split-beta dest: red-known-addr-WriteMem)

qed

end

**context**  $J\text{-heap}$  **begin**

**lemma**  $red\text{-read-typeable}$ :

$\llbracket convert\text{-ext}TA\ extTA, P, t \vdash \langle e, s \rangle -ta \rightarrow \langle e', s' \rangle; P, E, hp\ s \vdash e : T; ReadMem\ ad\ al\ v \in set\ \{ta\}_o \rrbracket$

```

⇒⇒ ∃ T'. P, hp s ⊢ ad@al : T'
and reds-read-typeable:
  [ convert-extTA extTA, P, t ⊢ ⟨es, s⟩ [-ta→] ⟨es', s'⟩; P, E, hp s ⊢ es [:] Ts; ReadMem ad al v ∈ set
  {ta}_o ]
  ⇒⇒ ∃ T'. P, hp s ⊢ ad@al : T'
proof(induct arbitrary: E T and E Ts rule: red-reds.inducts)
  case RedAAcc thus ?case
    by(fastforce intro: addr-loc-type.intros simp add: nat-less-iff word-sle-eq)
next
  case RedFAcc thus ?case
    by(fastforce intro: addr-loc-type.intros)
next
  case RedCASSucceed thus ?case
    by(fastforce intro: addr-loc-type.intros)
next
  case RedCASFail thus ?case
    by(fastforce intro: addr-loc-type.intros)
next
  case RedCallExternal thus ?case
    by(auto intro: red-external-read-mem-typeable)
qed auto

end

primrec new-types :: ('a, 'b, 'addr) exp ⇒ ty set
  and new-typess :: ('a, 'b, 'addr) exp list ⇒ ty set
where
  new-types (new C) = {Class C}
  new-types (newA T[e]) = insert (T[]) (new-types e)
  new-types (Cast T e) = new-types e
  new-types (e instanceof T) = new-types e
  new-types (Val v) = {}
  new-types (Var V) = {}
  new-types (e1 «bop» e2) = new-types e1 ∪ new-types e2
  new-types (V := e) = new-types e
  new-types (a[e]) = new-types a ∪ new-types e
  new-types (a[e] := e') = new-types a ∪ new-types e ∪ new-types e'
  new-types (a.length) = new-types a
  new-types (e.F{D}) = new-types e
  new-types (e.F{D} := e') = new-types e ∪ new-types e'
  new-types (e.compareAndSwap(D.F, e', e'')) = new-types e ∪ new-types e' ∪ new-types e''
  new-types (e.M(es)) = new-types e ∪ new-typess es
  new-types {V:T=vo; e} = new-types e
  new-types (Synchronized x e e') = new-types e ∪ new-types e'
  new-types (InSynchronized x a e) = new-types e
  new-types (e;; e') = new-types e ∪ new-types e'
  new-types (if (e) e1 else e2) = new-types e ∪ new-types e1 ∪ new-types e2
  new-types (while (b) e) = new-types b ∪ new-types e
  new-types (throw e) = new-types e
  new-types (try e catch(C V) e') = new-types e ∪ new-types e'

  new-typess [] = {}
  new-typess (e # es) = new-types e ∪ new-typess es

```

**lemma** *new-types-blocks*:

$\llbracket \text{length } pns = \text{length } Ts; \text{length } vs = \text{length } Ts \rrbracket \implies \text{new-types } (\text{blocks } pns \text{ vs } Ts \text{ } e) = \text{new-types } e$   
**apply**(*induct rule*: *blocks.induct*)  
**apply**(*simp-all*)  
**done**

**context** *J-heap-base* **begin**

**lemma** *WTrt-new-types-types*:  $P, E, h \vdash e : T \implies \text{new-types } e \subseteq \text{types } P$   
**and** *WTrts-new-types-types*:  $P, E, h \vdash es \text{ } [::] \text{ } Ts \implies \text{new-types } es \subseteq \text{types } P$   
**by**(*induct rule*: *WTrt-WTrts.inducts*) *simp-all*

**end**

**lemma** *WT-new-types-types*:  $P, E \vdash e :: T \implies \text{new-types } e \subseteq \text{types } P$   
**and** *WTs-new-types-types*:  $P, E \vdash es \text{ } [::] \text{ } Ts \implies \text{new-types } es \subseteq \text{types } P$   
**by**(*induct rule*: *WT-WTs.inducts*) *simp-all*

**context** *J-heap-conf* **begin**

**lemma** *red-New-typeof-addrD*:

$\llbracket \text{convert-extTA } extTA, P, t \vdash \langle e, s \rangle \text{ } -ta \rightarrow \langle e', s' \rangle; \text{new-types } e \subseteq \text{types } P; hconf \text{ } (hp \text{ } s); \text{NewHeapElem } a \text{ } x \in \text{set } \{ta\}_o \rrbracket$   
 $\implies \text{typeof-addr } (hp \text{ } s') \text{ } a = \text{Some } x$   
**and** *reds-New-typeof-addrD*:  
 $\llbracket \text{convert-extTA } extTA, P, t \vdash \langle es, s \rangle \text{ } [-ta \rightarrow] \langle es', s' \rangle; \text{new-types } es \subseteq \text{types } P; hconf \text{ } (hp \text{ } s); \text{NewHeapElem } a \text{ } x \in \text{set } \{ta\}_o \rrbracket$   
 $\implies \text{typeof-addr } (hp \text{ } s') \text{ } a = \text{Some } x$   
**apply**(*induct rule*: *red-reds.inducts*)  
**apply**(*auto dest*: *allocate-SomeD red-external-New-typeof-addrD*)  
**done**

**lemma** *J-conf-read-heap-read-typed*:

*J-conf-read addr2thread-id thread-id2addr empty-heap allocate typeof-addr (heap-read-typed P) heap-write hconf P*

**proof** –

**interpret** *conf*: *heap-conf-read*  
*addr2thread-id thread-id2addr*  
*spurious-wakeups*  
*empty-heap allocate typeof-addr heap-read-typed P heap-write hconf*  
*P*  
**by**(*rule heap-conf-read-heap-read-typed*)  
**show** *?thesis* **by**(*unfold-locales*)

**qed**

**lemma** *red-non-speculative-vs-conf*:

$\llbracket \text{convert-extTA } extTA, P, t \vdash \langle e, s \rangle \text{ } -ta \rightarrow \langle e', s' \rangle; P, E, hp \text{ } s \vdash e : T; \text{non-speculative } P \text{ vs } (\text{lList-of } (\text{take } n \text{ } (\text{map } \text{NormalAction } \{ta\}_o))); \text{vs-conf } P \text{ } (hp \text{ } s) \text{ vs}; hconf \text{ } (hp \text{ } s) \rrbracket$   
 $\implies \text{vs-conf } P \text{ } (hp \text{ } s') \text{ } (w\text{-values } P \text{ vs } (\text{take } n \text{ } (\text{map } \text{NormalAction } \{ta\}_o)))$   
**and** *reds-non-speculative-vs-conf*:  
 $\llbracket \text{convert-extTA } extTA, P, t \vdash \langle es, s \rangle \text{ } [-ta \rightarrow] \langle es', s' \rangle; P, E, hp \text{ } s \vdash es \text{ } [::] \text{ } Ts; \text{non-speculative } P \text{ vs } (\text{lList-of } (\text{take } n \text{ } (\text{map } \text{NormalAction } \{ta\}_o))); \text{vs-conf } P \text{ } (hp \text{ } s) \text{ vs}; hconf \text{ } (hp \text{ } s) \rrbracket$

$\Rightarrow$  *vs-conf*  $P$  (*hp*  $s'$ ) (*w-values*  $P$  *vs* (*take*  $n$  (*map* *NormalAction*  $\{ta\}_o$ )))  
**proof**(*induct arbitrary*:  $E$   $T$  **and**  $E$   $T$ s *rule*: *red-reds.inducts*)  
**case** (*RedAAss*  $h$   $a$   $U$   $n$   $i$   $w$   $V$   $h'$   $xs$ )  
**from**  $\langle \text{sint } i < \text{int } n \rangle \langle 0 \leq s \ i \rangle$  **have**  $\text{nat } (\text{sint } i) < n$   
**by** (*simp add*: *word-sle-eq nat-less-iff*)  
**with**  $\langle \text{typeof-addr } h \ a = \lfloor \text{Array-type } U \ n \rfloor \rangle$  **have**  $P, h \vdash a @ \text{ACell } (\text{nat } (\text{sint } i)) : U$   
**by**(*auto intro*: *addr-loc-type.intros*)  
**moreover from**  $\langle \text{heap-write } h \ a \ (\text{ACell } (\text{nat } (\text{sint } i))) \ w \ h' \rangle$  **have**  $h \sqsubseteq h'$  **by**(*rule hext-heap-write*)  
**ultimately have**  $P, h' \vdash a @ \text{ACell } (\text{nat } (\text{sint } i)) : U$  **by**(*rule addr-loc-type-hext-mono*)  
**moreover from**  $\langle \text{typeof}_h \ w = \lfloor V \rfloor \rangle \langle P \vdash V \leq U \rangle$  **have**  $P, h \vdash w : \leq U$  **by**(*simp add*: *conf-def*)  
**with**  $\langle h \sqsubseteq h' \rangle$  **have**  $P, h' \vdash w : \leq U$  **by**(*rule conf-hext*)  
**ultimately have**  $\exists T. P, h' \vdash a @ \text{ACell } (\text{nat } (\text{sint } i)) : T \wedge P, h' \vdash w : \leq T$  **by** *blast*  
**thus** *?case using RedAAss*  
**by**(*auto intro*!: *vs-confI split*: *if-split-asm dest*: *vs-confD simp add*: *take-Cons'*)(*blast dest*: *vs-confD*  
*hext-heap-write intro*: *addr-loc-type-hext-mono conf-hext*) +  
**next**  
**case** (*RedFAss*  $h$   $e$   $D$   $F$   $v$   $h'$   $xs$ )  
**hence**  $\exists T. P, h' \vdash e @ \text{CField } D \ F : T \wedge P, h' \vdash v : \leq T$   
**by**(*force dest*!: *hext-heap-write intro*!: *addr-loc-type.intros intro*: *typeof-addr-hext-mono type-of-hext-type-of*  
*simp add*: *conf-def*)  
**thus** *?case using RedFAss*  
**by**(*auto intro*!: *vs-confI simp add*: *take-Cons'* *split*: *if-split-asm dest*: *vs-confD*)(*blast dest*: *vs-confD*  
*hext-heap-write intro*: *addr-loc-type-hext-mono conf-hext*) +  
**next**  
**case** (*RedCASSucceed*  $h$   $a$   $D$   $F$   $v$   $v'$   $h'$   $l$ )  
**hence**  $\exists T. P, h' \vdash a @ \text{CField } D \ F : T \wedge P, h' \vdash v' : \leq T$   
**by**(*force dest*!: *hext-heap-write intro*!: *addr-loc-type.intros intro*: *typeof-addr-hext-mono type-of-hext-type-of*  
*simp add*: *conf-def take-Cons'*)  
**thus** *?case using RedCASSucceed*  
**by**(*auto simp add*: *take-Cons'* *split*: *if-split-asm dest*: *vs-confD intro*!: *vs-confI*)  
(*blast dest*: *vs-confD hext-heap-write intro*: *addr-loc-type-hext-mono conf-hext*) +  
**next**  
**case** *RedCallExternal* **thus** *?case by*(*auto intro*: *red-external-non-speculative-vs-conf*)  
**qed**(*auto dest*: *vs-conf-allocate hext-allocate intro*: *vs-conf-hext simp add*: *take-Cons'*)

**lemma** *red-non-speculative-typeable*:  
 $\llbracket \text{convert-extTA } \text{extTA}, P, t \vdash \langle e, s \rangle \rightarrow \langle e', s' \rangle; P, E, hp \ s \vdash e : T;$   
 $\text{non-speculative } P \text{ vs } (\text{llist-of } (\text{map } \text{NormalAction } \{ta\}_o)); \text{vs-conf } P \ (hp \ s) \ \text{vs}; h\text{conf } (hp \ s) \rrbracket$   
 $\Rightarrow J\text{-heap-base.red } \text{addr2thread-id } \text{thread-id2addr } \text{spurious-wakeups } \text{empty-heap } \text{allocate } \text{typeof-addr}$   
 $(\text{heap-read-typed } P) \ \text{heap-write } (\text{convert-extTA } \text{extTA}) \ P \ t \ e \ s \ ta \ e' \ s'$   
**and** *reds-non-speculative-typeable*:  
 $\llbracket \text{convert-extTA } \text{extTA}, P, t \vdash \langle es, s \rangle \rightarrow \langle es', s' \rangle; P, E, hp \ s \vdash es [:] \ Ts;$   
 $\text{non-speculative } P \text{ vs } (\text{llist-of } (\text{map } \text{NormalAction } \{ta\}_o)); \text{vs-conf } P \ (hp \ s) \ \text{vs}; h\text{conf } (hp \ s) \rrbracket$   
 $\Rightarrow J\text{-heap-base.reds } \text{addr2thread-id } \text{thread-id2addr } \text{spurious-wakeups } \text{empty-heap } \text{allocate } \text{typeof-addr}$   
 $(\text{heap-read-typed } P) \ \text{heap-write } (\text{convert-extTA } \text{extTA}) \ P \ t \ es \ s \ ta \ es' \ s'$   
**proof**(*induct arbitrary*:  $E$   $T$  **and**  $E$   $T$ s *rule*: *red-reds.inducts*)  
**case** *RedCall* **thus** *?case by*(*blast intro*: *J-heap-base.red-reds.RedCall*)  
**next**  
**case** *RedCallExternal* **thus** *?case*  
**by**(*auto intro*: *J-heap-base.red-reds.RedCallExternal red-external-non-speculative-typeable*)  
**qed**(*auto intro*: *J-heap-base.red-reds.intros intro*!: *heap-read-typedI dest*: *vs-confD addr-loc-type-fun*)

**end**

**sublocale**  $J\text{-heap-base} < \text{red-mthr}$ :

*if-multithreaded*  
*final-expr*  
*mred P*  
*convert-RA*  
**for**  $P$

**by**(*unfold-locales*)

**locale**  $J\text{-allocated-heap-conf} =$

*J-heap-conf*  
*addr2thread-id thread-id2addr*  
*spurious-wakeups*  
*empty-heap allocate typeof-addr heap-read heap-write hconf*  
 $P$

+

*J-allocated-heap*  
*addr2thread-id thread-id2addr*  
*spurious-wakeups*  
*empty-heap allocate typeof-addr heap-read heap-write*  
*allocated*  
 $P$

**for**  $\text{addr2thread-id} :: ('addr :: \text{addr}) \Rightarrow 'thread\text{-id}$   
**and**  $\text{thread-id2addr} :: 'thread\text{-id} \Rightarrow 'addr$   
**and**  $\text{spurious-wakeups} :: \text{bool}$   
**and**  $\text{empty-heap} :: 'heap$   
**and**  $\text{allocate} :: 'heap \Rightarrow htype \Rightarrow ('heap \times 'addr) \text{ set}$   
**and**  $\text{typeof-addr} :: 'heap \Rightarrow 'addr \rightarrow htype$   
**and**  $\text{heap-read} :: 'heap \Rightarrow 'addr \Rightarrow \text{addr-loc} \Rightarrow 'addr \text{ val} \Rightarrow \text{bool}$   
**and**  $\text{heap-write} :: 'heap \Rightarrow 'addr \Rightarrow \text{addr-loc} \Rightarrow 'addr \text{ val} \Rightarrow 'heap \Rightarrow \text{bool}$   
**and**  $\text{hconf} :: 'heap \Rightarrow \text{bool}$   
**and**  $\text{allocated} :: 'heap \Rightarrow 'addr \text{ set}$   
**and**  $P :: 'addr \text{ J-prog}$

**begin**

**lemma**  $\text{mred-known-addr-typing}$ :

**assumes**  $\text{wf} : \text{wf-J-prog } P$

**and**  $\text{ok} : \text{start-heap-ok}$

**shows**  $\text{known-addr-typing addr2thread-id thread-id2addr empty-heap allocate typeof-addr heap-write allocated J-known-addr final-expr (mred } P) (\lambda t \ x \ h. \exists ET. \text{sconf-type-ok } ET \ t \ x \ h) \ P$

**proof** –

**interpret**  $\text{known-addr}$

*addr2thread-id thread-id2addr*  
*spurious-wakeups*  
*empty-heap allocate typeof-addr heap-read heap-write*  
*allocated J-known-addr*  
*final-expr mred P P*  
**using**  $\text{wf ok by(rule mred-known-addr)}$

**show**  $?thesis$

**proof**

**fix**  $t \ x \ m \ ta \ x' \ m'$

**assume**  $\text{mred } P \ t \ (x, m) \ ta \ (x', m')$

**thus**  $m \sqsubseteq m'$  **by**(*auto dest: red-hext-incr simp add: split-beta*)

```

next
  fix  $t\ x\ m\ ta\ x'\ m'\ vs$ 
  assume  $red: mred\ P\ t\ (x, m)\ ta\ (x', m')$ 
  and  $ts-ok: \exists ET. sconf-type-ok\ ET\ t\ x\ m$ 
  and  $vs: vs-conf\ P\ m\ vs$ 
  and  $ns: non-speculative\ P\ vs\ (l\!ist-of\ (map\ NormalAction\ \{ta\}_o))$ 

  let  $?mred = J\text{-heap-base}.mred\ addr2thread-id\ thread-id2addr\ spurious-wakeups\ empty-heap\ allocate$ 
   $typeof-addr\ (heap-read-typed\ P)\ heap-write\ P$ 

  have  $lift: lifting-inv\ final-expr\ ?mred\ sconf-type-ok$ 
  by(intro  $J\text{-conf-read}.lifting-inv-sconf-subject-ok\ J\text{-conf-read-heap-read-typed}\ wf$ )
  moreover
  from  $ts-ok$  obtain  $ET$  where  $type: sconf-type-ok\ ET\ t\ x\ m ..$ 
  with  $red\ vs\ ns$  have  $red': ?mred\ t\ (x, m)\ ta\ (x', m')$ 
  by(auto simp add: split-beta sconf-type-ok-def sconf-def type-ok-def dest: red-non-speculative-typeable)
  ultimately have  $sconf-type-ok\ ET\ t\ x'\ m'$  using  $type$ 
  by(rule  $lifting-inv.invariant-red$ [where  $r=?mred$ ])
  thus  $\exists ET. sconf-type-ok\ ET\ t\ x'\ m' ..$ 
  { fix  $t''\ x''\ m''$ 
    assume  $New: NewThread\ t''\ x''\ m'' \in set\ \{ta\}_t$ 
    with  $red$  have  $m'' = snd\ (x', m')$  by(rule  $red-mthr.new-thread-memory$ )
    with  $lift\ red'$  type  $New$ 
    show  $\exists ET. sconf-type-ok\ ET\ t''\ x''\ m''$ 
    by-(rule  $lifting-inv.invariant-NewThread$ [where  $r=?mred$ ], simp-all) }
  { fix  $t''\ x''$ 
    assume  $\exists ET. sconf-type-ok\ ET\ t''\ x''\ m$ 
    with  $lifting-inv.invariant-other$ [where  $r=?mred$ , OF  $lift\ red'$  type]
    show  $\exists ET. sconf-type-ok\ ET\ t''\ x''\ m'$  by blast }
next
  fix  $t\ x\ m\ ta\ x'\ m'\ vs\ n$ 
  assume  $red: mred\ P\ t\ (x, m)\ ta\ (x', m')$ 
  and  $ts-ok: \exists ET. sconf-type-ok\ ET\ t\ x\ m$ 
  and  $vs: vs-conf\ P\ m\ vs$ 
  and  $ns: non-speculative\ P\ vs\ (l\!ist-of\ (take\ n\ (map\ NormalAction\ \{ta\}_o)))$ 
  thus  $vs-conf\ P\ m'\ (w-values\ P\ vs\ (take\ n\ (map\ NormalAction\ \{ta\}_o)))$ 
  by(cases  $x$ )(auto dest: red-non-speculative-vs-conf simp add: sconf-type-ok-def type-ok-def sconf-def)
next
  fix  $t\ x\ m\ ta\ x'\ m'\ ad\ al\ v$ 
  assume  $mred\ P\ t\ (x, m)\ ta\ (x', m')$ 
  and  $\exists ET. sconf-type-ok\ ET\ t\ x\ m$ 
  and  $ReadMem\ ad\ al\ v \in set\ \{ta\}_o$ 
  thus  $\exists T. P, m \vdash ad@al : T$ 
  by(fastforce simp add: sconf-type-ok-def type-ok-def sconf-def split-beta dest: red-read-typeable)
next
  fix  $t\ x\ m\ ta\ x'\ m'\ ad\ hT$ 
  assume  $mred\ P\ t\ (x, m)\ ta\ (x', m')$ 
  and  $\exists ET. sconf-type-ok\ ET\ t\ x\ m$ 
  and  $NewHeapElem\ ad\ hT \in set\ \{ta\}_o$ 
  thus  $typeof-addr\ m'\ ad = \lfloor hT \rfloor$ 
  by(auto dest: red-New-typeof-addrD[where  $x=hT$ ] dest!:  $WTrt\ new-types-types$  simp add: split-beta sconf-type-ok-def sconf-def type-ok-def)
qed
qed

```

**end**

**context** *J-allocated-heap-conf* **begin**

**lemma** *executions-sc*:

**assumes** *wf*: *wf-J-prog* *P*  
**and** *wf-start*: *wf-start-state* *P C M vs*  
**and** *vs2*:  $\bigcup (ka\text{-}Val \text{ ' } set \text{ } vs) \subseteq set \text{ } start\text{-}addrs$   
**shows** *executions-sc-hb* (*J-E* *P C M vs status*) *P*  
**(is** *executions-sc-hb* *?E* *P*)

**proof** –

**from** *wf-start* **obtain** *Ts T pns body D* **where** *ok*: *start-heap-ok*  
**and** *sees*:  $P \vdash C \text{ sees } M : Ts \rightarrow T = [(pns, body)] \text{ in } D$   
**and** *vs1*:  $P, start\text{-}heap \vdash vs [:\leq] Ts$  **by** *cases auto*

**interpret** *known-addrs-typing*  
*addr2thread-id thread-id2addr*  
*spurious-wakeups*  
*empty-heap allocate typeof-addr heap-read heap-write*  
*allocated J-known-addrs*  
*final-expr mred P  $\lambda t x h. \exists ET. sconf\text{-}type\text{-}ok ET t x h P$*   
**using** *wf ok* **by**(*rule mred-known-addrs-typing*)

**from** *wf-prog-wf-syscls*[*OF wf*] *J-start-state-sconf-type-ok*[*OF wf wf-start*]

**show** *?thesis*

**proof**(*rule executions-sc-hb*)

**from** *wf sees* **have** *wf-mdecl wf-J-mdecl P D (M, Ts, T, [(pns, body)])* **by**(*rule sees-wf-mdecl*)  
**then obtain** *T'* **where** *len1*: *length pns* = *length Ts* **and** *wt*:  $P, [this \mapsto Class D, pns \mapsto Ts] \vdash body$   
 $:: T'$   
**by**(*auto simp add: wf-mdecl-def*)  
**from** *vs1* **have** *len2*: *length vs* = *length Ts* **by**(*rule list-all2-lengthD*)  
**show** *J-known-addrs start-tid (( $\lambda(pns, body) vs. (blocks (this \# pns) (Class (fst (method P C M))$*   
 $\# fst (snd (method P C M))) (Null \# vs) body, Map.empty)) (the (snd (snd (snd (method P C M))))$   
 $vs) \subseteq allocated start\text{-}heap$   
**using** *sees vs2 len1 len2 WT-ka*[*OF wt*]  
**by**(*auto simp add: split-beta start-addrs-allocated ka-blocks intro: start-tid-start-addrs*[*OF wf-prog-wf-syscls*[*OF*  
 $wf] ok$ ])  
**qed**  
**qed**

**end**

**declare** *split-paired-Ex* [*simp del*]

**context** *J-progress* **begin**

**lemma** *ex-WTrt-simps*:

$P, E, h \vdash e : T \implies \exists E T. P, E, h \vdash e : T$

**by** *blast*

**abbreviation** (*input*) *J-non-speculative-read-bound*  $:: nat$

**where** *J-non-speculative-read-bound*  $\equiv 2$



**lemma assumes** *hrt*: heap-read-typeable *hconf* *P*

**and** *vs*: *vs-conf* *P* (*shr* *s*) *vs*

**and** *hconf*: *hconf* (*shr* *s*)

**shows** *red-non-speculative-read*:

$\llbracket P, t \vdash \langle e, (\text{shr } s, xs) \rangle \rightarrow \langle e', (h', xs') \rangle; \exists E \ T. \ P, E, \text{shr } s \vdash e : T; \\ \text{red-mthr.mthr.if.actions-ok } s \ t \ ta; \\ I < \text{length } \llbracket ta \rrbracket_o; \llbracket ta \rrbracket_o ! I = \text{ReadMem } a'' \ al'' \ v; v' \in w\text{-values } P \text{ vs } (\text{map NormalAction } (\text{take } I \ \llbracket ta \rrbracket_o)) \ (a'', al'')) \rrbracket \\ \text{non-speculative } P \text{ vs } (\text{lList-of } (\text{map NormalAction } (\text{take } I \ \llbracket ta \rrbracket_o))) \rrbracket \\ \implies \exists ta' \ e'' \ xs'' \ h''. \ P, t \vdash \langle e, (\text{shr } s, xs) \rangle \rightarrow \langle e'', (h'', xs'') \rangle \wedge \\ \text{red-mthr.mthr.if.actions-ok } s \ t \ ta' \wedge \\ I < \text{length } \llbracket ta' \rrbracket_o \wedge \text{take } I \ \llbracket ta' \rrbracket_o = \text{take } I \ \llbracket ta \rrbracket_o \wedge \\ \llbracket ta' \rrbracket_o ! I = \text{ReadMem } a'' \ al'' \ v' \wedge \text{length } \llbracket ta' \rrbracket_o \leq \max J\text{-non-speculative-read-bound } (\text{length } \llbracket ta \rrbracket_o) \\ \text{and } \text{reds-non-speculative-read}: \\ \llbracket P, t \vdash \langle es, (\text{shr } s, xs) \rangle \rightarrow \langle es', (h', xs') \rangle; \exists E \ Ts. \ P, E, \text{shr } s \vdash es \ [ : ] \ Ts; \\ \text{red-mthr.mthr.if.actions-ok } s \ t \ ta; \\ I < \text{length } \llbracket ta \rrbracket_o; \llbracket ta \rrbracket_o ! I = \text{ReadMem } a'' \ al'' \ v; v' \in w\text{-values } P \text{ vs } (\text{map NormalAction } (\text{take } I \ \llbracket ta \rrbracket_o)) \ (a'', al'')) \rrbracket \\ \text{non-speculative } P \text{ vs } (\text{lList-of } (\text{map NormalAction } (\text{take } I \ \llbracket ta \rrbracket_o))) \rrbracket \\ \implies \exists ta' \ es'' \ xs'' \ h''. \ P, t \vdash \langle es, (\text{shr } s, xs) \rangle \rightarrow \langle es'', (h'', xs'') \rangle \wedge \\ \text{red-mthr.mthr.if.actions-ok } s \ t \ ta' \wedge \\ I < \text{length } \llbracket ta' \rrbracket_o \wedge \text{take } I \ \llbracket ta' \rrbracket_o = \text{take } I \ \llbracket ta \rrbracket_o \wedge \\ \llbracket ta' \rrbracket_o ! I = \text{ReadMem } a'' \ al'' \ v' \wedge \text{length } \llbracket ta' \rrbracket_o \leq \max J\text{-non-speculative-read-bound } (\text{length } \llbracket ta \rrbracket_o) \\ \llbracket ta \rrbracket_o) \\ \text{proof}(\text{induct } e \ hxs \equiv (\text{shr } s, xs) \ ta \ e' \ hxs' \equiv (h', xs') \\ \text{and } es \ hxs \equiv (\text{shr } s, xs) \ ta \ es' \ hxs' \equiv (h', xs') \\ \text{arbitrary: } xs \ xs' \text{ and } xs \ xs' \text{ rule: red-reds.inducts}) \\ \text{case } (\text{RedAAcc } a \ U \ n \ i \ v \ e) \\ \text{hence } [simp]: I = 0 \ al'' = \text{ACell } (\text{nat } (\text{sint } i)) \ a'' = a \\ \text{and } v': v' \in vs \ (a, \text{ACell } (\text{nat } (\text{sint } i))) \text{ by } simp\text{-all} \\ \text{from } \text{RedAAcc} \text{ have } \text{adal}: P, \text{shr } s \vdash a @ \text{ACell } (\text{nat } (\text{sint } i)) : U \\ \text{by } (\text{auto intro: addr-loc-type.intros simp add: nat-less-iff word-sle-eq}) \\ \text{from } v' \text{ vs } \text{adal} \text{ have } P, \text{shr } s \vdash v' : \leq U \text{ by } (\text{auto dest!: vs-confD dest: addr-loc-type-fun}) \\ \text{with } hrt \text{ adal} \text{ have } \text{heap-read } (\text{shr } s) \ a \ (\text{ACell } (\text{nat } (\text{sint } i))) \ v' \text{ using } hconf \text{ by } (\text{rule heap-read-typeableD}) \\ \text{with } \langle \text{typeof-addr } (\text{shr } s) \ a = \lfloor \text{Array-type } U \ n \rfloor \rangle \langle 0 \leq \text{sint } i \rangle \langle \text{sint } i < \text{int } n \rangle \\ \langle \text{red-mthr.mthr.if.actions-ok } s \ t \ \llbracket \text{ReadMem } a \ (\text{ACell } (\text{nat } (\text{sint } i))) \ v \rrbracket \rangle \\ \text{show } ?case \text{ by } (\text{fastforce intro: red-reds.RedAAcc}) \\ \text{next} \\ \text{case } (\text{RedFAcc } a \ D \ F \ v) \\ \text{hence } [simp]: I = 0 \ al'' = \text{CField } D \ F \ a'' = a \\ \text{and } v': v' \in vs \ (a, \text{CField } D \ F) \text{ by } simp\text{-all} \\ \text{from } \text{RedFAcc} \text{ obtain } E \ T \text{ where } P, E, \text{shr } s \vdash \text{addr } a \cdot F \{D\} : T \text{ by } blast \\ \text{with } \text{RedFAcc} \text{ have } \text{adal}: P, \text{shr } s \vdash a @ \text{CField } D \ F : T \text{ by } (\text{auto 4 4 intro: addr-loc-type.intros}) \\ \text{from } v' \text{ vs } \text{adal} \text{ have } P, \text{shr } s \vdash v' : \leq T \text{ by } (\text{auto dest!: vs-confD dest: addr-loc-type-fun}) \\ \text{with } hrt \text{ adal} \text{ have } \text{heap-read } (\text{shr } s) \ a \ (\text{CField } D \ F) \ v' \text{ using } hconf \text{ by } (\text{rule heap-read-typeableD}) \\ \text{with } \langle \text{red-mthr.mthr.if.actions-ok } s \ t \ \llbracket \text{ReadMem } a \ (\text{CField } D \ F) \ v \rrbracket \rangle \\ \text{show } ?case \text{ by } (\text{fastforce intro: red-reds.RedFAcc}) \\ \text{next} \\ \text{case } (\text{RedCASSucceed } a \ D \ F \ v'' \ v''') \\ \text{hence } [simp]: I = 0 \ al'' = \text{CField } D \ F \ a'' = a \ v'' = v \\ \text{and } v': v' \in vs \ (a, \text{CField } D \ F) \text{ by } (\text{auto simp add: take-Cons' split: if-split-asm}) \\ \text{from } \text{RedCASSucceed.prem1} \text{ obtain } E \ T \text{ where} \\ P, E, \text{shr } s \vdash \text{addr } a \cdot \text{compareAndSwap}(D \cdot F, \text{Val } v'', \text{Val } v''') : T \text{ by } clarify$

```

then obtain  $T$  where  $adal: P, shr\ s \vdash a @ CField\ D\ F : T$ 
  and  $v'': P, shr\ s \vdash v'' \leq T$  and  $v''': P, shr\ s \vdash v''' \leq T$ 
  by(fastforce intro: addr-loc-type.intros simp add: conf-def)
from  $v'$  vs  $adal$  have  $P, shr\ s \vdash v' \leq T$  by(auto dest!: vs-confD dest: addr-loc-type-fun)
from  $hrt\ adal\ this\ hconf$  have  $read: heap-read\ (shr\ s)\ a\ (CField\ D\ F)\ v'$  by(rule heap-read-typeableD)
show  $?case$ 
proof(cases v' = v'')
  case True
    then show  $?thesis$  using RedCASSucceed
    by(fastforce intro: red-reds.RedCASSucceed)
  next
    case False
    then show  $?thesis$  using read RedCASSucceed
    by(fastforce intro: RedCASFail)
  qed
next
  case (RedCASFail a D F v'' v''' v'''')
  hence [simp]:  $I = 0\ al'' = CField\ D\ F\ a'' = a\ v'' = v$ 
    and  $v': v' \in vs\ (a, CField\ D\ F)$  by(auto simp add: take-Cons' split: if-split-asm)
  from RedCASFail.premis(1) obtain  $E\ T$  where
     $P, E, shr\ s \vdash addr\ a \cdot compareAndSwap(D \cdot F, Val\ v'', Val\ v''') : T$  by(iprover)
  then obtain  $T$  where  $adal: P, shr\ s \vdash a @ CField\ D\ F : T$ 
    and  $v''': P, shr\ s \vdash v''' \leq T$  and  $v''': P, shr\ s \vdash v'''' \leq T$ 
    by(fastforce intro: addr-loc-type.intros simp add: conf-def)
  from  $v'$  vs  $adal$  have  $P, shr\ s \vdash v' \leq T$  by(auto dest!: vs-confD dest: addr-loc-type-fun)
  from  $hrt\ adal\ this\ hconf$  have  $read: heap-read\ (shr\ s)\ a\ (CField\ D\ F)\ v'$  by(rule heap-read-typeableD)
  show  $?case$ 
  proof(cases v' = v''')
    case True
      from heap-write-total[OF hconf adal v'''] obtain  $h'$  where
         $heap-write\ (shr\ s)\ a\ (CField\ D\ F)\ v'''\ h' ..$ 
      with read RedCASFail True show  $?thesis$ 
      by(fastforce intro: RedCASSucceed)
    next
      case False
      with read RedCASFail show  $?thesis$  by(fastforce intro: red-reds.RedCASFail)
    qed
  next
    case (RedCallExternal a U M Ts Tr D ps ta' va h' ta e')
    from  $\langle P, t \vdash \langle a \cdot M(ps), hp\ (shr\ s, xs) \rangle - ta' \rightarrow ext\ \langle va, h' \rangle$ 
    have  $red: P, t \vdash \langle a \cdot M(ps), shr\ s \rangle - ta' \rightarrow ext\ \langle va, h' \rangle$  by simp
    from RedCallExternal have  $aok: red-mthr.mthr.if.actions-ok\ s\ t\ ta'$  by simp
    from RedCallExternal have  $I < length\ \llbracket ta' \rrbracket_o$ 
      and  $\llbracket ta' \rrbracket_o ! I = ReadMem\ a''\ al''\ v$ 
      and  $v' \in w-values\ P\ vs\ (map\ NormalAction\ (take\ I\ \llbracket ta' \rrbracket_o))\ (a'', al'')$ 
      and  $non-speculative\ P\ vs\ (llist-of\ (map\ NormalAction\ (take\ I\ \llbracket ta' \rrbracket_o)))$  by simp-all
    from red-external-non-speculative-read[OF hrt vs red aok hconf this]
       $\langle typeof-addr\ (hp\ (shr\ s, xs))\ a = \lfloor U \rfloor \rangle$ 
       $\langle P \vdash class-type-of\ U\ sees\ M: Ts \rightarrow Tr = Native\ in\ D \rangle$ 
       $\langle ta = extTA2J\ P\ ta' \rangle$ 
       $\langle I < length\ \llbracket ta' \rrbracket_o \rangle$ 
    show  $?case$  by(fastforce intro: red-reds.RedCallExternal)
  next
    case NewArrayRed thus  $?case$  by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.NewArrayRed)

```

```

next
  case CastRed thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.CastRed)
next
  case InstanceOfRed thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.InstanceOfRed)
next
  case BinOpRed1 thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.BinOpRed1)
next
  case BinOpRed2 thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.BinOpRed2)
next
  case LAssRed thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.LAssRed)
next
  case AAccRed1 thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.AAccRed1)
next
  case AAccRed2 thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.AAccRed2)
next
  case AAssRed1 thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.AAssRed1)
next
  case AAssRed2 thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.AAssRed2)
next
  case AAssRed3 thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.AAssRed3)+
next
  case ALengthRed thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.ALengthRed)
next
  case FAccRed thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.FAccRed)
next
  case FAssRed1 thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.FAssRed1)
next
  case FAssRed2 thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.FAssRed2)
next
  case CASRed1 thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.CASRed1)
next
  case CASRed2 thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.CASRed2)
next
  case CASRed3 thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.CASRed3)
next
  case CallObj thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.CallObj)

```

```

next
  case CallParams thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.CallParams)
next
  case BlockRed thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(fastforce intro: red-reds.BlockRed)+
next
  case SynchronizedRed1 thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.SynchronizedRed1)
next
  case SynchronizedRed2 thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.SynchronizedRed2)
next
  case SeqRed thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.SeqRed)
next
  case CondRed thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.CondRed)
next
  case ThrowRed thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.ThrowRed)
next
  case TryRed thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.TryRed)
next
  case ListRed1 thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.ListRed1)
next
  case ListRed2 thus ?case
    by(clarsimp simp add: split-paired-Ex ex-WTrt-simps)(blast intro: red-reds.ListRed2)
qed(simp-all)

end

```

```

sublocale J-allocated-heap-conf < if-known-addr-base
  J-known-addr
  final-expr mred P convert-RA
.

```

```

declare split-paired-Ex [simp]
declare eq-upto-seq-inconsist-simps [simp del]

```

```

locale J-allocated-progress =
  J-progress
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write hconf
  P
+
  J-allocated-heap-conf
  addr2thread-id thread-id2addr
  spurious-wakeups

```

```

empty-heap allocate typeof-addr heap-read heap-write hconf
allocated
P
for addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
and thread-id2addr :: 'thread-id ⇒ 'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
and typeof-addr :: 'heap ⇒ 'addr → htype
and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool
and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool
and hconf :: 'heap ⇒ bool
and allocated :: 'heap ⇒ 'addr set
and P :: 'addr J-prog
begin

lemma non-speculative-read:
  assumes wf: wf-J-prog P
  and hrt: heap-read-typeable hconf P
  and wf-start: wf-start-state P C M vs
  and ka:  $\bigcup (ka\text{-Val } \text{'set vs}) \subseteq \text{set start-addrs}$ 
  shows red-mthr.if.non-speculative-read J-non-speculative-read-bound
    (init-fin-lift-state status (J-start-state P C M vs))
    (w-values P (λ-. { }) (map snd (lift-start-obs start-tid start-heap-obs)))
  (is red-mthr.if.non-speculative-read - ?start-state ?start-vs)
proof(rule red-mthr.if.non-speculative-readI)
  fix ttas s' t x ta x' m' i ad al v v'
  assume τRed: red-mthr.mthr.if.RedT P ?start-state ttas s'
  and sc: non-speculative P ?start-vs (llist-of (concat (map (λ(t, ta). {ta}_o) ttas)))
  and ts't: thr s' t = [(x, no-wait-locks)]
  and red: red-mthr.init-fin P t (x, shr s') ta (x', m')
  and aok: red-mthr.mthr.if.actions-ok s' t ta
  and i: i < length {ta}_o
  and ns': non-speculative P (w-values P ?start-vs (concat (map (λ(t, ta). {ta}_o) ttas))) (llist-of
    (take i {ta}_o))
  and read: {ta}_o ! i = NormalAction (ReadMem ad al v)
  and v': v' ∈ w-values P ?start-vs (concat (map (λ(t, ta). {ta}_o) ttas) @ take i {ta}_o) (ad, al)

  from wf-start obtain Ts T pns body D where ok: start-heap-ok
  and sees: P ⊢ C sees M:Ts→T = [(pns, body)] in D
  and conf: P, start-heap ⊢ vs [≤] Ts by cases auto

  let ?conv = λttas. concat (map (λ(t, ta). {ta}_o) ttas)
  let ?vs' = w-values P ?start-vs (?conv ttas)
  let ?wt-ok = init-fin-lift-inv sconf-type-ok
  let ?ET-start = J-sconf-type-ET-start P C M
  let ?start-obs = map snd (lift-start-obs start-tid start-heap-obs)
  let ?start-state = init-fin-lift-state status (J-start-state P C M vs)

interpret known-addrs-typing
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  allocated J-known-addrs

```

```

    final-expr mred P  $\lambda t x h. \exists ET. \text{sconf-type-ok } ET t x h P$ 
    using wf ok by(rule mred-known-addr-typing)

    from wf sees have wf-mdecl wf-J-mdecl P D (M, Ts, T, [(pns, body)]) by(rule sees-wf-mdecl)
    then obtain T' where len1: length pns = length Ts and wt: P, [this  $\mapsto$  Class D, pns  $\mapsto$  Ts]  $\vdash$  body
    :: T'
    by(auto simp add: wf-mdecl-def)
    from conf have len2: length vs = length Ts by(rule list-all2-lengthD)

    from wf wf-start have ts-ok-start: ts-ok (init-fin-lift ( $\lambda t x h. \exists ET. \text{sconf-type-ok } ET t x h$ )) (thr
    ?start-state) (shr ?start-state)
    unfolding ts-ok-init-fin-lift-init-fin-lift-state shr-start-state by(rule J-start-state-sconf-type-ok)
    have sc': non-speculative P ?start-vs (lmap snd (lconcat (lmap ( $\lambda(t, ta). \text{llist-of } (\text{map } (\text{Pair } t)$ 
     $\llbracket ta \rrbracket_o$ )) (llist-of ttas))))
    using sc by(simp add: lmap-lconcat llist.map-comp o-def split-def lconcat-llist-of[symmetric])

    from start-state-vs-conf[OF wf-prog-wf-syscls[OF wf]]
    have vs-conf-start: vs-conf P (shr ?start-state) ?start-vs
    by(simp add: init-fin-lift-state-conv-simps start-state-def split-beta)
    with  $\tau_{\text{Red}}$  ts-ok-start sc
    have wt': ts-ok (init-fin-lift ( $\lambda t x h. \exists ET. \text{sconf-type-ok } ET t x h$ )) (thr s') (shr s')
    and vs': vs-conf P (shr s') ?vs' by(rule if-RedT-non-speculative-invar)+

    from red i read obtain e xs e' xs' ta'
    where x: x = (Running, e, xs) and x': x' = (Running, e', xs')
    and ta: ta = convert-TA-initial (convert-obs-initial ta')
    and red': P, t  $\vdash$   $\langle e, (\text{shr } s', xs) \rangle -ta' \rightarrow \langle e', (m', xs') \rangle$ 
    by cases fastforce+

    from ts't wt' x obtain E T where wte: P, E, shr s'  $\vdash$  e : T
    and hconf: hconf (shr s')
    by(auto dest!: ts-okD simp add: sconf-type-ok-def sconf-def type-ok-def)

    have aok': red-mthr.mthr.if.actions-ok s' t ta' using aok unfolding ta by simp

    from i read v' ta ns' have i < length  $\llbracket ta' \rrbracket_o$  and  $\llbracket ta' \rrbracket_o ! i = \text{ReadMem } ad \ al \ v$ 
    and v'  $\in$  w-values P ?vs' (map NormalAction (take i  $\llbracket ta' \rrbracket_o$ )) (ad, al)
    and non-speculative P ?vs' (llist-of (map NormalAction (take i  $\llbracket ta' \rrbracket_o$ )))
    by(simp-all add: take-map)

    from red-non-speculative-read[OF hrt vs' hconf red' - aok' this] wte
    obtain ta'' e'' xs'' h''
    where red'': P, t  $\vdash$   $\langle e, (\text{shr } s', xs) \rangle -ta'' \rightarrow \langle e'', (h'', xs'') \rangle$ 
    and aok'': red-mthr.mthr.if.actions-ok s' t ta''
    and i'': i < length  $\llbracket ta'' \rrbracket_o$ 
    and eq'': take i  $\llbracket ta'' \rrbracket_o = \text{take } i \llbracket ta' \rrbracket_o$ 
    and read'':  $\llbracket ta'' \rrbracket_o ! i = \text{ReadMem } ad \ al \ v'$ 
    and len'': length  $\llbracket ta'' \rrbracket_o \leq \max J\text{-non-speculative-read-bound } (\text{length } \llbracket ta' \rrbracket_o)$  by blast

    let ?x' = (Running, e'', xs'')
    let ?ta' = convert-TA-initial (convert-obs-initial ta'')
    from red'' have red-mthr.init-fin P t (x, shr s') ?ta' (?x', h'')
    unfolding x by -(rule red-mthr.init-fin.NormalAction, simp)
    moreover from aok'' have red-mthr.mthr.if.actions-ok s' t ?ta' by simp

```

moreover from  $i''$  have  $i < \text{length } \llbracket ?ta' \rrbracket_o$  **by** *simp*  
 moreover from  $eq''$  have  $\text{take } i \llbracket ?ta' \rrbracket_o = \text{take } i \llbracket ta \rrbracket_o$  **unfolding**  $ta$  **by** (*simp add: take-map*)  
 moreover from  $\text{read'' } i''$  have  $\llbracket ?ta' \rrbracket_o ! i = \text{NormalAction } (\text{ReadMem } ad \ al \ v')$  **by** (*simp add: nth-map*)  
 moreover from  $\text{len''}$  have  $\text{length } \llbracket ?ta' \rrbracket_o \leq \max J\text{-non-speculative-read-bound } (\text{length } \llbracket ta \rrbracket_o)$   
**unfolding**  $ta$  **by** *simp*  
 ultimately  
 show  $\exists ta' x'' m''. \text{red-mthr.init-fin } P \ t \ (x, \text{shr } s') \ ta' \ (x'', m'') \wedge$   
 $\text{red-mthr.mthr.if.actions-ok } s' \ t \ ta' \wedge$   
 $i < \text{length } \llbracket ta' \rrbracket_o \wedge \text{take } i \llbracket ta' \rrbracket_o = \text{take } i \llbracket ta \rrbracket_o \wedge$   
 $\llbracket ta' \rrbracket_o ! i = \text{NormalAction } (\text{ReadMem } ad \ al \ v') \wedge$   
 $\text{length } \llbracket ta' \rrbracket_o \leq \max J\text{-non-speculative-read-bound } (\text{length } \llbracket ta \rrbracket_o)$   
**by** *blast*  
**qed**

**lemma** *J-cut-and-update:*

assumes *wf*: *wf-J-prog*  $P$   
 and *hrt*: *heap-read-typeable hconf*  $P$   
 and *wf-start*: *wf-start-state*  $P \ C \ M \ vs$   
 and *ka*:  $\bigcup (ka\text{-Val } ' \text{ set } vs) \subseteq \text{set start-addr}$   
 shows  $\text{red-mthr.if.cut-and-update } (\text{init-fin-lift-state status } (J\text{-start-state } P \ C \ M \ vs))$   
 $(\text{mrw-values } P \ \text{Map.empty } (\text{map snd } (\text{lift-start-obs start-tid start-heap-obs})))$

**proof** –

from *wf-start* **obtain**  $Ts \ T \ pns \ \text{body } D$  **where** *ok*: *start-heap-ok*  
 and *sees*:  $P \vdash C \text{ sees } M: Ts \rightarrow T = \lfloor (pns, \text{body}) \rfloor$  **in**  $D$   
 and *conf*:  $P, \text{start-heap} \vdash vs \lfloor \leq \rfloor Ts$  **by** *cases auto*

**interpret** *known-addr*-typing

*addr2thread-id thread-id2addr*  
*spurious-wakeups*  
*empty-heap allocate typeof-addr heap-read heap-write*  
*allocated J-known-addr*  
*final-expr mred*  $P \ \lambda t \ x \ h. \exists ET. \text{sconf-type-ok } ET \ t \ x \ h \ P$   
**using** *wf ok* **by** (*rule mred-known-addr*-typing)

**let**  $?start\text{-vs} = w\text{-values } P \ (\lambda\cdot. \{\}) \ (\text{map snd } (\text{lift-start-obs start-tid start-heap-obs}))$   
**let**  $?wt\text{-ok} = \text{init-fin-lift-inv sconf-type-ok}$   
**let**  $?ET\text{-start} = J\text{-sconf-type-ET-start } P \ C \ M$   
**let**  $?start\text{-obs} = \text{map snd } (\text{lift-start-obs start-tid start-heap-obs})$   
**let**  $?start\text{-state} = \text{init-fin-lift-state status } (J\text{-start-state } P \ C \ M \ vs)$

**from** *wf sees* **have** *wf-mdecl wf-J-mdecl*  $P \ D \ (M, Ts, T, \lfloor (pns, \text{body}) \rfloor)$  **by** (*rule sees-wf-mdecl*)  
**then obtain**  $T'$  **where**  $\text{len1}: \text{length } pns = \text{length } Ts$  **and**  $wt: P, [\text{this} \mapsto \text{Class } D, pns \mapsto] Ts \vdash \text{body}$   
 $:: T'$

**by** (*auto simp add: wf-mdecl-def*)  
**from** *conf* **have**  $\text{len2}: \text{length } vs = \text{length } Ts$  **by** (*rule list-all2-lengthD*)

**note** *wf-prog-wf-syscls*[*OF wf*] *non-speculative-read*[*OF wf hrt wf-start ka*]

**moreover**

**from** *wf wf-start* **have** *ts-ok-start*: *ts-ok*  $(\text{init-fin-lift } (\lambda t \ x \ h. \exists ET. \text{sconf-type-ok } ET \ t \ x \ h)) \ (\text{thr } ?start\text{-state}) \ (\text{shr } ?start\text{-state})$

**unfolding** *ts-ok-init-fin-lift-init-fin-lift-state shr-start-state* **by** (*rule J-start-state-sconf-type-ok*)

**moreover**

**have** *ka*: *J-known-addr start-tid*  $((\lambda (pns, \text{body}) \ vs. (\text{blocks } (\text{this } \# \ pns) \ (\text{Class } (\text{fst } (\text{method } P \ C$

$M)) \# \text{fst} (\text{snd} (\text{method } P \ C \ M))) (\text{Null} \# \text{vs}) \text{body}, \text{Map.empty})) (\text{the} (\text{snd} (\text{snd} (\text{snd} (\text{method } P \ C \ M)))) \text{vs}) \subseteq \text{allocated start-heap}$   
**using** *sees* *ka* *len1* *len2* *WT-ka*[*OF wt*]  
**by**(*auto simp add: split-beta start-addr-allocated ka-blocks intro: start-tid-start-addr*[*OF wf-prog-wf-syscls*[*OF wf*] *ok*])  
**ultimately show** *?thesis* **by**(*rule non-speculative-read-into-cut-and-update*)  
**qed**

**lemma** *J-drf*:

**assumes** *wf*: *wf-J-prog* *P*  
**and** *hrt*: *heap-read-typeable hconf* *P*  
**and** *wf-start*: *wf-start-state* *P C M vs*  
**and** *ka*:  $\bigcup (ka\text{-Val} \text{ ' set } vs) \subseteq \text{set start-addr}$   
**shows** *drf* (*J- $\mathcal{E}$*  *P C M vs status*) *P*

**proof** –

**from** *wf-start* **obtain** *Ts T pns body D* **where** *ok*: *start-heap-ok*  
**and** *sees*:  $P \vdash C \text{ sees } M: Ts \rightarrow T = \lfloor (pns, \text{body}) \rfloor \text{ in } D$   
**and** *conf*:  $P, \text{start-heap} \vdash vs \[:\leq] Ts$  **by** *cases auto*

**from** *J-cut-and-update*[*OF assms*] *wf-prog-wf-syscls*[*OF wf*] *J-start-state-sconf-type-ok*[*OF wf wf-start*]  
**show** *?thesis*

**proof**(*rule known-addr-tying.drf*[*OF mred-known-addr-tying*[*OF wf ok*]])

**from** *wf* *sees* **have** *wf-mdecl wf-J-mdecl P D (M, Ts, T,  $\lfloor (pns, \text{body}) \rfloor$ )* **by**(*rule sees-wf-mdecl*)

**then obtain** *T'* **where** *len1*: *length pns* = *length Ts* **and** *wt*:  $P, [\text{this} \rightarrow \text{Class } D, pns \mapsto Ts] \vdash \text{body}$

**::** *T'*

**by**(*auto simp add: wf-mdecl-def*)

**from** *conf* **have** *len2*: *length vs* = *length Ts* **by**(*rule list-all2-lengthD*)

**show** *J-known-addr start-tid* ( $(\lambda (pns, \text{body}) \text{vs. } (\text{blocks} (\text{this} \# pns) (\text{Class} (\text{fst} (\text{method } P \ C \ M))) \# \text{fst} (\text{snd} (\text{method } P \ C \ M))) (\text{Null} \# \text{vs}) \text{body}, \text{Map.empty})) (\text{the} (\text{snd} (\text{snd} (\text{snd} (\text{method } P \ C \ M)))) \text{vs}) \subseteq \text{allocated start-heap}$

**using** *sees* *ka* *len1* *len2* *WT-ka*[*OF wt*]

**by**(*auto simp add: split-beta start-addr-allocated ka-blocks intro: start-tid-start-addr*[*OF wf-prog-wf-syscls*[*OF wf*] *ok*])

**qed**

**qed**

**lemma** *J-sc-legal*:

**assumes** *wf*: *wf-J-prog* *P*  
**and** *hrt*: *heap-read-typeable hconf* *P*  
**and** *wf-start*: *wf-start-state* *P C M vs*  
**and** *ka*:  $\bigcup (ka\text{-Val} \text{ ' set } vs) \subseteq \text{set start-addr}$   
**shows** *sc-legal* (*J- $\mathcal{E}$*  *P C M vs status*) *P*

**proof** –

**from** *wf-start* **obtain** *Ts T pns body D* **where** *ok*: *start-heap-ok*

**and** *sees*:  $P \vdash C \text{ sees } M: Ts \rightarrow T = \lfloor (pns, \text{body}) \rfloor \text{ in } D$

**and** *conf*:  $P, \text{start-heap} \vdash vs \[:\leq] Ts$  **by** *cases auto*

**interpret** *known-addr-tying*

*addr2thread-id thread-id2addr*

*spurious-wakeups*

*empty-heap allocate typeof-addr heap-read heap-write*

*allocated J-known-addr*

*final-expr mred P  $\lambda t \ x \ h. \exists ET. \text{sconf-type-ok } ET \ t \ x \ h \ P$*

**using** *wf ok* **by**(*rule mred-known-addr-tying*)



```

let ?start-vs = w-values P (λ-. {}) (map snd (lift-start-obs start-tid start-heap-obs))
let ?wt-ok = init-fin-lift-inv sconf-type-ok
let ?ET-start = J-sconf-type-ET-start P C M
let ?start-obs = map snd (lift-start-obs start-tid start-heap-obs)
let ?start-state = init-fin-lift-state status (J-start-state P C M vs)

from wf sees have wf-mdecl wf-J-mdecl P D (M, Ts, T, [(pns, body)]) by(rule sees-wf-mdecl)
then obtain T' where len1: length pns = length Ts and wt: P, [this ↦ Class D, pns [↦] Ts] ⊢ body
:: T'
  by(auto simp add: wf-mdecl-def)
from conf have len2: length vs = length Ts by(rule list-all2-lengthD)

note wf-prog-wf-syscls[OF wf] non-speculative-read[OF wf hrt wf-start ka]
moreover
  from wf wf-start have ts-ok-start: ts-ok (init-fin-lift (λt x h. ∃ ET. sconf-type-ok ET t x h)) (thr
?start-state) (shr ?start-state)
    unfolding ts-ok-init-fin-lift-init-fin-lift-state shr-start-state by(rule J-start-state-sconf-type-ok)
    moreover have ka-allocated: J-known-addr start-tid ((λ(pns, body) vs. (blocks (this # pns) (Class
(fst (method P C M)) # fst (snd (method P C M))) (Null # vs) body, Map.empty)) (the (snd (snd
(snd (method P C M)))))) vs) ⊆ allocated start-heap
    using sees ka len1 len2 WT-ka[OF wt]
    by(auto simp add: split-beta start-addr-allocated ka-blocks intro: start-tid-start-addr[OF wf-prog-wf-syscls[OF
wf] ok])
    ultimately have red-mthr.if.hb-completion ?start-state (lift-start-obs start-tid start-heap-obs)
      by(rule non-speculative-read-into-hb-completion)

thus ?thesis using wf-prog-wf-syscls[OF wf] J-start-state-sconf-type-ok[OF wf wf-start]
  by(rule sc-legal)(rule ka-allocated)
qed

lemma J-jmm-consistent:
  assumes wf: wf-J-prog P
  and hrt: heap-read-typeable hconf P
  and wf-start: wf-start-state P C M vs
  and ka: ⋃ (ka-Val ' set vs) ⊆ set start-addr
  shows jmm-consistent (J- $\mathcal{E}$  P C M vs status) P
  (is jmm-consistent ? $\mathcal{E}$  P)
proof –
  interpret drf ? $\mathcal{E}$  P using assms by(rule J-drf)
  interpret sc-legal ? $\mathcal{E}$  P using assms by(rule J-sc-legal)
  show ?thesis by unfold-locales
qed

lemma J-ex-sc-exec:
  assumes wf: wf-J-prog P
  and hrt: heap-read-typeable hconf P
  and wf-start: wf-start-state P C M vs
  and ka: ⋃ (ka-Val ' set vs) ⊆ set start-addr
  shows ∃ E ws. E ∈ J- $\mathcal{E}$  P C M vs status ∧ P ⊢ (E, ws) ✓ ∧ sequentially-consistent P (E, ws)
  (is ∃ E ws. - ∈ ? $\mathcal{E}$  ∧ -)
proof –
  interpret jmm: executions-sc-hb ? $\mathcal{E}$  P using assms by –(rule executions-sc)

  let ?start-state = init-fin-lift-state status (J-start-state P C M vs)

```

```

let ?start-mrw = mrw-values P Map.empty (map snd (lift-start-obs start-tid start-heap-obs))

from red-mthr.if.sequential-completion-Runs[OF red-mthr.if.cut-and-update-imp-sc-completion[OF
J-cut-and-update[OF assms]] ta-seq-consist-convert-RA]
obtain ttas where Red: red-mthr.mthr.if.mthr.Runs P ?start-state ttas
  and sc: ta-seq-consist P ?start-mrw (lconcat (lmap ( $\lambda(t, ta).$  llist-of  $\{\{ta\}_o\}$ ) ttas)) by blast
  let ?E = lappend (llist-of (lift-start-obs start-tid start-heap-obs)) (lconcat (lmap ( $\lambda(t, ta).$  llist-of
(map (Pair t)  $\{\{ta\}_o\}$ ) ttas))
from Red have ?E  $\in$  ?E by (blast intro: red-mthr.mthr.if.E.intros)
moreover from Red have tsa: thread-start-actions-ok ?E
  by (blast intro: red-mthr.thread-start-actions-ok-init-fin red-mthr.mthr.if.E.intros)
from sc have ta-seq-consist P Map.empty (lmap snd ?E)
  unfolding lmap-lappend-distrib lmap-lconcat llist.map-comp split-def o-def lmap-llist-of map-map
snd-conv
  by (simp add: ta-seq-consist-lappend ta-seq-consist-start-heap-obs)
from ta-seq-consist-imp-sequentially-consistent[OF tsa jmm.E-new-actions-for-fun[OF  $\langle ?E \in ?E \rangle$ 
this]]
obtain ws where sequentially-consistent P ( $?E, ws$ )  $P \vdash (?E, ws) \checkmark$  by iprover
ultimately show ?thesis by blast
qed

```

**theorem** *J-consistent*:

```

assumes wf: wf-J-prog P
and hrt: heap-read-typeable hconf P
and wf-start: wf-start-state P C M vs
and ka:  $\bigcup (ka\text{-Val} \text{ ' set vs}) \subseteq \text{set start-addrs}$ 
shows  $\exists E$  ws. legal-execution P (J-E P C M vs status) (E, ws)

```

**proof** –

```

let ?E = J-E P C M vs status
interpret sc-legal ?E P using assms by (rule J-sc-legal)
from J-ex-sc-exec[OF assms]
obtain E ws where  $E \in ?E$   $P \vdash (E, ws) \checkmark$  sequentially-consistent P (E, ws) by blast
hence legal-execution P ?E (E, ws) by (rule SC-is-legal)
thus ?thesis by blast

```

**qed**

**end**

**end**

**theory** JMM-JVM

**imports**

```

JMM-Framework
../JVM/JVMThreaded

```

**begin**

**sublocale** JVM-heap-base < execd-mthr:

```

heap-multithreaded-base
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write
  JVM-final mexecd P convert-RA
for P

```

.

**context** *JVM-heap-base* **begin**

**abbreviation** *JVMd- $\mathcal{E}$*  ::

*'addr jvm-prog*  $\Rightarrow$  *cname*  $\Rightarrow$  *mname*  $\Rightarrow$  *'addr val list*  $\Rightarrow$  *status*  
 $\Rightarrow$  (*'thread-id*  $\times$  (*'addr*, *'thread-id*) *obs-event action*) *llist set*

**where** *JVMd- $\mathcal{E}$*  *P*  $\equiv$  *execd-mthr. $\mathcal{E}$ -start P JVM-local-start P*

**end**

**end**

## 8.15 JMM Instantiation for bytecode

**theory** *DRF-JVM*

**imports**

*JMM-Common*

*JMM-JVM*

*../BV/BVProgressThreaded*

*SC-Legal*

**begin**

### 8.15.1 DRF guarantee for the JVM

**abbreviation** (*input*) *ka-xcp* :: *'addr option*  $\Rightarrow$  *'addr set*

**where** *ka-xcp*  $\equiv$  *set-option*

**primrec** *jvm-ka* :: *'addr jvm-thread-state*  $\Rightarrow$  *'addr set*

**where**

*jvm-ka* (*xcp*, *frs*) =  
*ka-xcp xcp*  $\cup$  ( $\bigcup$  (*stk*, *loc*, *C*, *M*, *pc*)  $\in$  *set frs*. ( $\bigcup$  *v*  $\in$  *set stk*. *ka-Val v*)  $\cup$  ( $\bigcup$  *v*  $\in$  *set loc*. *ka-Val v*))

**context** *heap* **begin**

**lemma** *red-external-aggr-read-mem-typeable*:

$\llbracket (ta, va, h') \in \text{red-external-aggr } P \ t \ a \ M \ vs \ h; \text{ReadMem } ad \ al \ v \in \text{set } \{ta\}_o \rrbracket$   
 $\implies \exists T'. P, h \vdash ad@al : T'$

**by**(*auto simp add: red-external-aggr-def split-beta split: if-split-asm dest: heap-clone-read-typeable*)

**end**

**context** *JVM-heap-base* **begin**

**definition** *jvm-known-addr*s :: *'thread-id*  $\Rightarrow$  *'addr jvm-thread-state*  $\Rightarrow$  *'addr set*

**where** *jvm-known-addr*s *t xcpfrs* = {*thread-id2addr t*}  $\cup$  *jvm-ka xcpfrs*  $\cup$  *set start-addr*s

**end**

**context** *JVM-heap* **begin**

**lemma** *exec-instr-known-addr*s:

**assumes** *ok*: *start-heap-ok*

**and** *exec*: (*ta*, *xcp'*, *h'*, *frs'*)  $\in$  *exec-instr i P t h stk loc C M pc frs*

**and** *check*: *check-instr i P h stk loc C M pc frs*

**shows**  $jvm-known-addr\ t\ (xcp',\ frs') \subseteq jvm-known-addr\ t\ (None,\ (stk,\ loc,\ C,\ M,\ pc)\ \# \ frs) \cup new-obs-addr\ \{\!|ta|\!\}_o$

**proof** –

**note**  $[simp] = jvm-known-addr-def\ new-obs-addr-def\ addr-of-sys-xcpt-start-addr[OF\ ok]\ subset-Un1\ subset-Un2\ subset-insert\ ka-Val-subset-new-obs-Addr-ReadMem\ SUP-subset-mono\ split-beta\ neq-Nil-conv\ tl-conv-drop\ set-drop-subset\ is-Ref-def$

**from** *exec check* **show** ?thesis

**proof**(cases i)

**case** *Load* **with** *exec check* **show** ?thesis **by** *auto*

**next**

**case** (*Store V*) **with** *exec check* **show** ?thesis

**using** *set-update-subset-insert[of loc V]*

**by**(*clarsimp simp del: set-update-subsetI*) *blast*

**next**

**case** (*Push v*)

**with** *check* **have**  $ka-Val\ v = \{\}$  **by**(cases v) *simp-all*

**with** *Push exec check* **show** ?thesis **by**(*simp*)

**next**

**case** (*CAS F D*)

**then** **show** ?thesis **using** *exec check*

**by**(*clarsimp split: if-split-asm*)(*fastforce dest!: in-set-dropD*) $+$

**next**

**case** (*Invoke M' n*)

**show** ?thesis

**proof**(cases  $stk\ !\ n = Null$ )

**case** *True* **with** *exec check Invoke* **show** ?thesis **by**(*simp*)

**next**

**case**  $[simp]:\ False$

**with** *check Invoke* **obtain** *a* **where**  $stkn: stk\ !\ n = Addr\ a\ n < length\ stk$  **by** *auto*

**hence**  $a: a \in (\bigcup v \in set\ stk. ka-Val\ v)$  **by**(*fastforce dest: nth-mem*)

**show** ?thesis

**proof**(cases  $snd\ (snd\ (snd\ (method\ P\ (class-type-of\ (the\ (typeof-addr\ h\ (the-Addr\ (stk\ !\ n))))))\ M')) = Native$ )

**case** *True*

**with** *exec check Invoke a stkn* **show** ?thesis

**apply** *clarsimp*

**apply**(*drule red-external-aggr-known-addr-mono[OF ok], simp*)

**apply**(*auto dest!: in-set-takeD dest: bspec subsetD split: extCallRet.split-asm simp add:*

*has-method-def is-native.simps*)

**done**

**next**

**case** *False*

**with** *exec check Invoke a stkn* **show** ?thesis

**by**(*auto simp add: set-replicate-conv-if dest!: in-set-takeD*)

**qed**

**qed**

**next**

**case** *Swap* **with** *exec check* **show** ?thesis

**by**(cases *stk*)(*simp, case-tac list, auto*)

**next**

**case** (*BinOpInstr bop*) **with** *exec check* **show** ?thesis

**using** *binop-known-addr[OF ok, of bop hd (drop (Suc 0) stk) hd stk]*

```

    apply(cases stk)
    apply(simp, case-tac list, simp)
    apply clarsimp
    apply(drule (2) binop-progress)
    apply(auto 6 2 split: sum.split-asm)
    done
next
  case MExit with exec check show ?thesis by(auto split: if-split-asm)
qed(clarsimp split: if-split-asm)+
qed

lemma exec-d-known-addr-mono:
  assumes ok: start-heap-ok
  and exec: mexecd P t (xcpfrs, h) ta (xcpfrs', h')
  shows jvm-known-addr t xcpfrs'  $\subseteq$  jvm-known-addr t xcpfrs  $\cup$  new-obs-addr  $\llbracket ta \rrbracket_o$ 
using exec
apply(cases xcpfrs)
apply(cases xcpfrs')
apply(simp add: split-beta)
apply(erule jvmd-NormalE)
apply(cases fst xcpfrs)
  apply(fastforce simp add: check-def split-beta del: subsetI dest!: exec-instr-known-addr[OF ok])
  apply(fastforce simp add: jvm-known-addr-def split-beta dest!: in-set-dropD)
done

lemma exec-instr-known-addr-ReadMem:
  assumes exec: (ta, xcp', h', frs')  $\in$  exec-instr i P t h stk loc C M pc frs
  and check: check-instr i P h stk loc C M pc frs
  and read: ReadMem ad al v  $\in$  set  $\llbracket ta \rrbracket_o$ 
  shows ad  $\in$  jvm-known-addr t (None, (stk, loc, C, M, pc) # frs)
using assms
proof(cases i)
  case ALoad thus ?thesis using assms
    by(cases stk)(case-tac [2] list, auto simp add: split-beta is-Ref-def jvm-known-addr-def split:
if-split-asm)
next
  case (Invoke M n)
  with check have stk ! n  $\neq$  Null  $\longrightarrow$  the-Addr (stk ! n)  $\in$  ka-Val (stk ! n) stk ! n  $\in$  set stk
  by(auto simp add: is-Ref-def)
  with assms Invoke show ?thesis
    by(auto simp add: split-beta is-Ref-def simp del: ka-Val.simps nth-mem split: if-split-asm dest!:
red-external-aggr-known-addr-ReadMem in-set-takeD del: is-AddrE)(auto simp add: jvm-known-addr-def
simp del: ka-Val.simps nth-mem del: is-AddrE)
next
  case Getfield thus ?thesis using assms
    by(auto simp add: jvm-known-addr-def neq-Nil-conv is-Ref-def split: if-split-asm)
next
  case CAS thus ?thesis using assms
    apply(cases stk; simp)
    subgoal for v stk
      apply(cases stk; simp)
      subgoal for v stk
        by(cases stk)(auto split: if-split-asm simp add: jvm-known-addr-def is-Ref-def)
    done

```

**done**  
**qed**(*auto simp add: split-beta is-Ref-def neq-Nil-conv split: if-split-asm*)

**lemma** *mexecd-known-addr-ReadMem:*

$\llbracket \text{mexecd } P \ t \ (xcpfrs, h) \ ta \ (xcpfrs', h') ; \text{ReadMem } ad \ al \ v \in \text{set } \{ta\}_o \rrbracket$   
 $\implies ad \in \text{jvm-known-addr } t \ xcpfrs$

**apply**(*cases xcpfrs*)  
**apply**(*cases xcpfrs'*)  
**apply** *simp*  
**apply**(*erule jvmd-NormalE*)  
**apply**(*cases fst xcpfrs*)  
**apply**(*auto simp add: check-def dest: exec-instr-known-addr-ReadMem*)  
**done**

**lemma** *exec-instr-known-addr-WriteMem:*

**assumes** *exec: (ta, xcp', h', frs') ∈ exec-instr i P t h stk loc C M pc frs*

**and** *check: check-instr i P h stk loc C M pc frs*

**and** *write:  $\{ta\}_o ! n = \text{WriteMem } ad \ al \ (\text{Addr } a) \ n < \text{length } \{ta\}_o$*

**shows**  $a \in \text{jvm-known-addr } t \ (\text{None}, (\text{stk}, \text{loc}, C, M, \text{pc}) \# \text{frs}) \vee a \in \text{new-obs-addr } (\text{take } n \ \{ta\}_o)$

**using** *assms*

**proof**(*cases i*)

**case** (*Invoke M n*)

**with** *check* **have**  $\text{stk} ! n \neq \text{Null} \longrightarrow \text{the-Addr } (\text{stk} ! n) \in \text{ka-Val } (\text{stk} ! n) \ \text{stk} ! n \in \text{set } \text{stk}$

**by**(*auto simp add: is-Ref-def*)

**thus** *?thesis* **using** *assms Invoke*

**by**(*auto simp add: is-Ref-def split-beta split: if-split-asm simp del: ka-Val.simps nth-mem dest!: red-external-aggr-known-addr-WriteMem in-set-takeD del: is-AddrE*)(*auto simp add: jvm-known-addr-def del: is-AddrE*)

**next**

**case** *AStore* **with** *assms* **show** *?thesis*

**by**(*cases stk*)(*auto simp add: jvm-known-addr-def split: if-split-asm*)

**next**

**case** *Putfield* **with** *assms* **show** *?thesis*

**by**(*cases stk*)(*auto simp add: jvm-known-addr-def split: if-split-asm*)

**next**

**case** *CAS* **with** *assms* **show** *?thesis*

**apply**(*cases stk; simp*)

**subgoal for** *v stk*

**apply**(*cases stk; simp*)

**subgoal for** *v stk*

**by**(*cases stk*)(*auto split: if-split-asm simp add: take-Cons' jvm-known-addr-def*)

**done**

**done**

**qed**(*auto simp add: split-beta split: if-split-asm*)

**lemma** *mexecd-known-addr-WriteMem:*

$\llbracket \text{mexecd } P \ t \ (xcpfrs, h) \ ta \ (xcpfrs', h') ; \{ta\}_o ! n = \text{WriteMem } ad \ al \ (\text{Addr } a) ; \ n < \text{length } \{ta\}_o \rrbracket$   
 $\implies a \in \text{jvm-known-addr } t \ xcpfrs \vee a \in \text{new-obs-addr } (\text{take } n \ \{ta\}_o)$

**apply**(*cases xcpfrs*)

**apply**(*cases xcpfrs'*)

**apply** *simp*

**apply**(*erule jvmd-NormalE*)

**apply**(*cases fst xcpfrs*)

**apply**(*auto simp add: check-def dest: exec-instr-known-addr-WriteMem*)

done

**lemma** *exec-instr-known-addr-new-thread*:

**assumes** *exec*:  $(ta, xcp', h', frs') \in \text{exec-instr } i \ P \ t \ h \ stk \ loc \ C \ M \ pc \ frs$

**and** *check*: *check-instr* *i P h stk loc C M pc frs*

**and** *new*: *NewThread*  $t' \ x' \ h'' \in \text{set } \{ta\}_t$

**shows** *jvm-known-addr*  $t' \ x' \subseteq \text{jvm-known-addr } t \ (None, (stk, loc, C, M, pc) \# frs)$

**using** *assms*

**proof**(*cases i*)

**case** (*Invoke M n*)

**with** *assms* **have**  $stk \ ! \ n \neq \text{Null} \longrightarrow \text{the-Addr } (stk \ ! \ n) \in \text{ka-Val } (stk \ ! \ n) \wedge \text{thread-id2addr} \ (\text{addr2thread-id } (\text{the-Addr } (stk \ ! \ n))) = \text{the-Addr } (stk \ ! \ n) \ stk \ ! \ n \in \text{set } stk$

**apply**(*auto simp add: is-Ref-def split: if-split-asm*)

**apply**(*frule red-external-aggr-NewThread-idD, simp, simp*)

**apply**(*drule red-external-aggr-new-thread-sub-thread*)

**apply**(*auto intro: addr2thread-id-inverse*)

**done**

**with** *assms Invoke* **show** *?thesis*

**apply**(*auto simp add: is-Ref-def split-beta split: if-split-asm simp del: nth-mem del: is-AddrE*)

**apply**(*drule red-external-aggr-NewThread-idD*)

**apply**(*auto simp add: extNTA2JVM-def jvm-known-addr-def split-beta simp del: nth-mem del: is-AddrE*)

**done**

**qed**(*auto simp add: split-beta split: if-split-asm*)

**lemma** *mexecd-known-addr-new-thread*:

$\llbracket \text{mexecd } P \ t \ (xcpfrs, h) \ ta \ (xcpfrs', h'); \text{NewThread } t' \ x' \ h'' \in \text{set } \{ta\}_t \rrbracket$

$\implies \text{jvm-known-addr } t' \ x' \subseteq \text{jvm-known-addr } t \ xcpfrs$

**apply**(*cases xcpfrs*)

**apply**(*cases xcpfrs'*)

**apply** *simp*

**apply**(*erule jvmd-NormalE*)

**apply**(*cases fst xcpfrs*)

**apply**(*auto 4 3 simp add: check-def dest: exec-instr-known-addr-new-thread*)

**done**

**lemma** *exec-instr-New-same-addr-same*:

$\llbracket (ta, xcp', h', frs') \in \text{exec-instr } ins \ P \ t \ h \ stk \ loc \ C \ M \ pc \ frs;$

$\{ta\}_o \ ! \ i = \text{NewHeapElem } a \ x; \ i < \text{length } \{ta\}_o;$

$\{ta\}_o \ ! \ j = \text{NewHeapElem } a \ x'; \ j < \text{length } \{ta\}_o \rrbracket$

$\implies i = j$

**apply**(*cases ins*)

**apply**(*auto simp add: nth-Cons' split: prod.split-asm if-split-asm*)

**apply**(*auto split: extCallRet.split-asm dest: red-external-aggr-New-same-addr-same*)

**done**

**lemma** *exec-New-same-addr-same*:

$\llbracket (ta, xcp', h', frs') \in \text{exec } P \ t \ (xcp, h, frs);$

$\{ta\}_o \ ! \ i = \text{NewHeapElem } a \ x; \ i < \text{length } \{ta\}_o;$

$\{ta\}_o \ ! \ j = \text{NewHeapElem } a \ x'; \ j < \text{length } \{ta\}_o \rrbracket$

$\implies i = j$

**apply**(*cases (P, t, xcp, h, frs) rule: exec.cases*)

**apply**(*auto dest: exec-instr-New-same-addr-same*)

**done**

**lemma** *exec-1-d-New-same-addr-same*:

$\llbracket P, t \vdash \text{Normal} (xcp, h, frs) \text{---} ta \text{---} jvmd \Rightarrow \text{Normal} (xcp', h', frs');$   
 $\llbracket \{ta\}_o ! i = \text{NewHeapElem } a \ x; i < \text{length } \{ta\}_o;$   
 $\llbracket \{ta\}_o ! j = \text{NewHeapElem } a \ x'; j < \text{length } \{ta\}_o \rrbracket$   
 $\Rightarrow i = j$

**by**(*erule jvmd-NormalE*)(*rule exec-New-same-addr-same*)

**end**

**locale** *JVM-allocated-heap* = *allocated-heap* +

**constrains** *addr2thread-id* :: ('addr :: addr)  $\Rightarrow$  'thread-id  
**and** *thread-id2addr* :: 'thread-id  $\Rightarrow$  'addr  
**and** *spurious-wakeups* :: bool  
**and** *empty-heap* :: 'heap  
**and** *allocate* :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set  
**and** *typeof-addr* :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype  
**and** *heap-read* :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool  
**and** *heap-write* :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool  
**and** *allocated* :: 'heap  $\Rightarrow$  'addr set  
**and** *P* :: 'addr jvm-prog

**sublocale** *JVM-allocated-heap* < *JVM-heap*

**by**(*unfold-locales*)

**context** *JVM-allocated-heap* **begin**

**lemma** *exec-instr-allocated-mono*:

$\llbracket (ta, xcp', h', frs') \in \text{exec-instr } i \ P \ t \ h \ stk \ loc \ C \ M \ pc \ frs; \text{check-instr } i \ P \ h \ stk \ loc \ C \ M \ pc \ frs \rrbracket$   
 $\Rightarrow \text{allocated } h \subseteq \text{allocated } h'$

**apply**(*cases i*)

**apply**(*auto 4 4 simp add: split-beta has-method-def is-native.simps split: if-split-asm sum.split-asm*  
*intro: allocate-allocated-mono dest: heap-write-allocated-same dest!: red-external-aggr-allocated-mono*  
*del: subsetI*)

**done**

**lemma** *mexecd-allocated-mono*:

$mexecd \ P \ t \ (xcpfrs, h) \ ta \ (xcpfrs', h') \Rightarrow \text{allocated } h \subseteq \text{allocated } h'$

**apply**(*cases xcpfrs*)

**apply**(*cases xcpfrs'*)

**apply**(*simp*)

**apply**(*erule jvmd-NormalE*)

**apply**(*cases fst xcpfrs*)

**apply**(*auto del: subsetI simp add: check-def dest: exec-instr-allocated-mono*)

**done**

**lemma** *exec-instr-allocatedD*:

$\llbracket (ta, xcp', h', frs') \in \text{exec-instr } i \ P \ t \ h \ stk \ loc \ C \ M \ pc \ frs;$   
 $\text{check-instr } i \ P \ h \ stk \ loc \ C \ M \ pc \ frs; \text{NewHeapElem } ad \ CTn \in \text{set } \{ta\}_o \rrbracket$   
 $\Rightarrow ad \in \text{allocated } h' \wedge ad \notin \text{allocated } h$

**apply**(*cases i*)

**apply**(*auto 4 4 split: if-split-asm prod.split-asm dest: allocate-allocatedD dest!: red-external-aggr-allocatedD*)



*simp add: has-method-def is-native.simps*)  
**done**

**lemma** *mexecd-allocatedD*:

$\llbracket \text{mexecd } P \ t \ (xcpfrs, h) \ ta \ (xcpfrs', h'); \text{NewHeapElem } ad \ CTn \in \text{set } \{ta\}_o \rrbracket$   
 $\implies ad \in \text{allocated } h' \wedge ad \notin \text{allocated } h$   
**apply**(*cases xcpfrs*)  
**apply**(*cases xcpfrs'*)  
**apply**(*simp*)  
**apply**(*erule jvmd-NormalE*)  
**apply**(*cases fst xcpfrs*)  
**apply**(*auto del: subsetI dest: exec-instr-allocatedD simp add: check-def*)  
**done**

**lemma** *exec-instr-NewHeapElemD*:

$\llbracket (ta, xcp', h', frs') \in \text{exec-instr } i \ P \ t \ h \ stk \ loc \ C \ M \ pc \ frs; \text{check-instr } i \ P \ h \ stk \ loc \ C \ M \ pc \ frs; \\ ad \in \text{allocated } h'; ad \notin \text{allocated } h \rrbracket$   
 $\implies \exists CTn. \text{NewHeapElem } ad \ CTn \in \text{set } \{ta\}_o$   
**apply**(*cases i*)  
**apply**(*auto 4 3 split: if-split-asm prod.split-asm sum.split-asm dest: allocate-allocatedD heap-write-allocated-same*  
*dest!: red-external-aggr-NewHeapElemD simp add: is-native.simps has-method-def*)  
**done**

**lemma** *mexecd-NewHeapElemD*:

$\llbracket \text{mexecd } P \ t \ (xcpfrs, h) \ ta \ (xcpfrs', h'); ad \in \text{allocated } h'; ad \notin \text{allocated } h \rrbracket$   
 $\implies \exists CTn. \text{NewHeapElem } ad \ CTn \in \text{set } \{ta\}_o$   
**apply**(*cases xcpfrs*)  
**apply**(*cases xcpfrs'*)  
**apply**(*simp*)  
**apply**(*erule jvmd-NormalE*)  
**apply**(*cases fst xcpfrs*)  
**apply**(*auto dest: exec-instr-NewHeapElemD simp add: check-def*)  
**done**

**lemma** *mexecd-allocated-multithreaded*:

*allocated-multithreaded addr2thread-id thread-id2addr empty-heap allocate typeof-addr heap-write allocated JVM-final (mexecd P) P*

**proof**

**fix** *t x m ta x' m'*  
**assume** *mexecd P t (x, m) ta (x', m')*  
**thus** *allocated m  $\subseteq$  allocated m'* **by**(*rule mexecd-allocated-mono*)  
**next**  
**fix** *x t m ta x' m' ad CTn*  
**assume** *mexecd P t (x, m) ta (x', m')*  
**and** *NewHeapElem ad CTn  $\in$  set {ta}\_o*  
**thus** *ad  $\in$  allocated m'  $\wedge$  ad  $\notin$  allocated m* **by**(*rule mexecd-allocatedD*)  
**next**  
**fix** *t x m ta x' m' ad*  
**assume** *mexecd P t (x, m) ta (x', m')*  
**and** *ad  $\in$  allocated m' ad  $\notin$  allocated m*  
**thus**  $\exists CTn. \text{NewHeapElem } ad \ CTn \in \text{set } \{ta\}_o$  **by**(*rule mexecd-NewHeapElemD*)  
**next**  
**fix** *t x m ta x' m' i a CTn j CTn'*  
**assume** *mexecd P t (x, m) ta (x', m')*

```

    and  $\{ta\}_o ! i = \text{NewHeapElem } a \ CTh \ i < \text{length } \{ta\}_o$ 
    and  $\{ta\}_o ! j = \text{NewHeapElem } a \ CTh' \ j < \text{length } \{ta\}_o$ 
    thus  $i = j$  by(auto dest: exec-1-d-New-same-addr-same simp add: split-beta)
  qed

```

**end**

```

sublocale JVM-allocated-heap < execd-mthr: allocated-multithreaded
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr heap-read heap-write allocated
  JVM-final mexecd P
  P
by(rule mexecd-allocated-multithreaded)

```

**context** JVM-allocated-heap **begin**

**lemma** mexecd-known-addr:

**assumes** *wf: wf-prog wfmd P*

**and** *ok: start-heap-ok*

**shows** *known-addr addr2thread-id thread-id2addr empty-heap allocate typeof-addr heap-write allocated jvm-known-addr JVM-final (mexecd P) P*

**proof**

**fix**  $t \ x \ m \ ta \ x' \ m'$

**assume** *mexecd P t (x, m) ta (x', m')*

**thus** *jvm-known-addr t x'  $\subseteq$  jvm-known-addr t x  $\cup$  new-obs-addr  $\{ta\}_o$*

**by**(rule *exec-d-known-addr-mono[OF ok]*)

**next**

**fix**  $t \ x \ m \ ta \ x' \ m' \ t' \ x'' \ m''$

**assume** *mexecd P t (x, m) ta (x', m')*

**and** *NewThread t' x'' m''  $\in$  set  $\{ta\}_t$*

**thus** *jvm-known-addr t' x''  $\subseteq$  jvm-known-addr t x* **by**(rule *mexecd-known-addr-new-thread*)

**next**

**fix**  $t \ x \ m \ ta \ x' \ m' \ ad \ al \ v$

**assume** *mexecd P t (x, m) ta (x', m')*

**and** *ReadMem ad al v  $\in$  set  $\{ta\}_o$*

**thus** *ad  $\in$  jvm-known-addr t x* **by**(rule *mexecd-known-addr-ReadMem*)

**next**

**fix**  $t \ x \ m \ ta \ x' \ m' \ n \ ad \ al \ ad'$

**assume** *mexecd P t (x, m) ta (x', m')*

**and**  $\{ta\}_o ! n = \text{WriteMem } ad \ al \ (\text{Addr } ad') \ n < \text{length } \{ta\}_o$

**thus** *ad'  $\in$  jvm-known-addr t x  $\vee$  ad'  $\in$  new-obs-addr (take n  $\{ta\}_o$ )*

**by**(rule *mexecd-known-addr-WriteMem*)

**qed**

**end**

**context** JVM-heap **begin**

**lemma** *exec-instr-read-typeable:*

**assumes** *exec: (ta, xcp', h', frs')  $\in$  exec-instr i P t h stk loc C M pc frs*

**and** *check: check-instr i P h stk loc C M pc frs*

**and** *read: ReadMem ad al v  $\in$  set  $\{ta\}_o$*

**shows**  $\exists T'. P, h \vdash ad@al : T'$

```

using exec check read
proof(cases i)
  case ALoad
    with assms show ?thesis
      by(fastforce simp add: split-beta is-Ref-def nat-less-iff word-sless-alt intro: addr-loc-type.intros split: if-split-asm)
next
  case (Getfield F D)
    with assms show ?thesis
      by(clarsimp simp add: split-beta is-Ref-def split: if-split-asm)(blast intro: addr-loc-type.intros dest: has-visible-field has-field-mono)
next
  case (Invoke M n)
    with exec check read obtain a vs ta' va T
      where (ta', va, h')  $\in$  red-external-aggr P t a M vs h
      and ReadMem ad al v  $\in$  set  $\{ta'\}_o$ 
      by(auto split: if-split-asm simp add: is-Ref-def)
      thus ?thesis by(rule red-external-aggr-read-mem-typeable)
next
  case (CAS F D)
    with assms show ?thesis
      by(clarsimp simp add: split-beta is-Ref-def conf-def split: if-split-asm)
      (force intro: addr-loc-type.intros dest: has-visible-field[THEN has-field-mono])
qed(auto simp add: split-beta is-Ref-def split: if-split-asm)

lemma exec-1-d-read-typeable:
   $\llbracket P, t \vdash \text{Normal } (xcp, h, frs) \text{ --ta--} jvmd \rightarrow \text{Normal } (xcp', h', frs'); \text{ReadMem ad al } v \in \text{set } \{ta'\}_o \rrbracket$ 
   $\implies \exists T'. P, h \vdash \text{ad}@al : T'$ 
apply(erule jvmd-NormalE)
apply(cases (P, t, xcp, h, frs) rule: exec.cases)
apply(auto intro: exec-instr-read-typeable simp add: check-def)
done

end

sublocale JVM-heap-base < execd-mthr:
  if-multithreaded
  JVM-final
  mexecd P
  convert-RA
  for P
by(unfold-locales)

context JVM-heap-conf begin

lemma JVM-conf-read-heap-read-typed:
  JVM-conf-read addr2thread-id thread-id2addr empty-heap allocate typeof-addr (heap-read-typed P)
  heap-write hconf P
proof –
  interpret conf: heap-conf-read
    addr2thread-id thread-id2addr
    spurious-wakeups
    empty-heap allocate typeof-addr heap-read-typed P heap-write hconf

```

```

  P
  by(rule heap-conf-read-heap-read-typed)
  show ?thesis by(unfold-locales)
qed

```

**lemma** *exec-instr-New-typeof-addrD*:

```

  [| (ta, xcp', h', frs') ∈ exec-instr i P t h stk loc C M pc frs;
    check-instr i P h stk loc C M pc frs; hconf h;
    NewHeapElem a x ∈ set {ta}_o |]
  ⇒ typeof-addr h' a = Some x

```

**apply**(cases i)

**apply**(auto dest: allocate-SomeD split: prod.split-asm if-split-asm)

**apply**(auto 4 4 split: extCallRet.split-asm dest!: red-external-aggr-New-typeof-addrD simp add: has-method-def is-native.simps)

**done**

**lemma** *exec-1-d-New-typeof-addrD*:

```

  [| P, t ⊢ Normal (xcp, h, frs) -ta-jvmd→ Normal (xcp', h', frs'); NewHeapElem a x ∈ set {ta}_o;
    hconf h |]
  ⇒ typeof-addr h' a = Some x

```

**apply**(erule jvmd-NormalE)

**apply**(cases xcp)

**apply**(auto dest: exec-instr-New-typeof-addrD simp add: check-def)

**done**

**lemma** *exec-instr-non-speculative-typeable*:

**assumes** *exec*: (ta, xcp', h', frs') ∈ *exec-instr* i P t h stk loc C M pc frs

**and** *check*: *check-instr* i P h stk loc C M pc frs

**and** *sc*: *non-speculative* P vs (llist-of (map NormalAction {ta}\_o))

**and** *vs-conf*: *vs-conf* P h vs

**and** *hconf*: *hconf* h

**shows** (ta, xcp', h', frs') ∈ *JVM-heap-base.exec-instr* addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr (heap-read-typed P) heap-write i P t h stk loc C M pc frs

**proof** –

**note** [simp] = *JVM-heap-base.exec-instr.simps*

**and** [split] = *if-split-asm prod.split-asm sum.split-asm*

**and** [split del] = *if-split*

**from** *assms* **show** ?thesis

**proof**(cases i)

**case** *ALoad* **with** *assms* **show** ?thesis

**by**(auto 4 3 intro!: heap-read-typedI dest: vs-confD addr-loc-type-fun)

**next**

**case** *Getfield* **with** *assms* **show** ?thesis

**by**(auto 4 3 intro!: heap-read-typedI dest: vs-confD addr-loc-type-fun)

**next**

**case** *CAS* **with** *assms* **show** ?thesis

**by**(auto 4 3 intro!: heap-read-typedI dest: vs-confD addr-loc-type-fun)

**next**

**case** *Invoke* **with** *assms* **show** ?thesis

**by**(fastforce dest: red-external-aggr-non-speculative-typeable simp add: has-method-def is-native.simps)

**qed**(auto)

**qed**

**lemma** *exec-instr-non-speculative-vs-conf*:

```

assumes exec: (ta, xcp', h', frs') ∈ exec-instr i P t h stk loc C M pc frs
and check: check-instr i P h stk loc C M pc frs
and sc: non-speculative P vs (llist-of (take n (map NormalAction {ta}o)))
and vs-conf: vs-conf P h vs
and hconf: hconf h
shows vs-conf P h' (w-values P vs (take n (map NormalAction {ta}o)))
proof –
  note [simp] = JVM-heap-base.exec-instr.simps take-Cons'
    and [split] = if-split-asm prod.split-asm sum.split-asm
    and [split del] = if-split
  from assms show ?thesis
  proof(cases i)
    case New with assms show ?thesis
      by(auto 4 4 dest: hext-allocate vs-conf-allocate intro: vs-conf-hext)
    next
      case NewArray with assms show ?thesis
        by(auto 4 4 dest: hext-allocate vs-conf-allocate intro: vs-conf-hext cong: if-cong)
    next
      case Invoke with assms show ?thesis
        by(fastforce dest: red-external-aggr-non-speculative-vs-conf simp add: has-method-def is-native.simps)
    next
      case AStore
      {
        assume hd (tl (tl stk)) ≠ Null
        and ¬ the-Intg (hd (tl stk)) < s 0
        and ¬ int (alen-of-htype (the (typeof-addr h (the-Addr (hd (tl (tl stk))))))) ≤ sint (the-Intg (hd (tl stk))))
        and P ⊢ the (typeofh (hd stk)) ≤ the-Array (ty-of-htype (the (typeof-addr h (the-Addr (hd (tl (tl stk)))))))
        moreover hence nat (sint (the-Intg (hd (tl stk)))) < alen-of-htype (the (typeof-addr h (the-Addr (hd (tl (tl stk)))))))
        by(auto simp add: not-le nat-less-iff word-sle-eq word-sless-eq not-less)
        with assms AStore have nat (sint (the-Intg (hd (tl stk)))) < alen-of-htype (the (typeof-addr h' (the-Addr (hd (tl (tl stk)))))))
        by(auto dest!: hext-arrD hext-heap-write)
        ultimately have ∃ T. P, h' ⊢ the-Addr (hd (tl (tl stk)))@ACell (nat (sint (the-Intg (hd (tl stk))))))
        : T ∧ P, h' ⊢ hd stk :≤ T
        using assms AStore
        by(auto 4 4 simp add: is-Ref-def conf-def dest!: hext-heap-write dest: hext-arrD intro!: addr-loc-type.intros
intro: typeof-addr-hext-mono type-of-hext-type-of) }
      thus ?thesis using assms AStore
      by(auto intro!: vs-confI)(blast intro: addr-loc-type-hext-mono conf-hext dest: hext-heap-write
vs-confD)+
    next
      case Putfield
      show ?thesis using assms Putfield
      by(auto intro!: vs-confI dest!: hext-heap-write)(blast intro: addr-loc-type.intros addr-loc-type-hext-mono
typeof-addr-hext-mono has-field-mono[OF has-visible-field] conf-hext dest: vs-confD)+
    next
      case CAS
      show ?thesis using assms CAS
      by(auto intro!: vs-confI dest!: hext-heap-write)(blast intro: addr-loc-type.intros addr-loc-type-hext-mono
typeof-addr-hext-mono has-field-mono[OF has-visible-field] conf-hext dest: vs-confD)+
    qed(auto)

```

qed

**lemma** *mexecd-non-speculative-typeable*:

$\llbracket P, t \vdash \text{Normal } (xcp, h, stk) - ta - jvmd \rightarrow \text{Normal } (xcp', h', frs'); \text{ non-speculative } P \text{ vs } (\text{llist-of } (\text{map } \text{NormalAction } \{ta\}_o));$   
 $\text{vs-conf } P \text{ h vs; hconf h } \rrbracket$

$\Rightarrow \text{JVM-heap-base.exec-1-d addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr (heap-read-typed } P) \text{ heap-write } P \text{ t (Normal } (xcp, h, stk)) \text{ ta (Normal } (xcp', h', frs'))$

**apply**(*erule jvmd-NormalE*)

**apply**(*cases xcp*)

**apply**(*auto intro!*: *JVM-heap-base.exec-1-d.intros simp add: JVM-heap-base.exec-d-def check-def JVM-heap-base.exec-d-def*  
*intro: exec-instr-non-speculative-typeable*)

**done**

**lemma** *mexecd-non-speculative-vs-conf*:

$\llbracket P, t \vdash \text{Normal } (xcp, h, stk) - ta - jvmd \rightarrow \text{Normal } (xcp', h', frs');$   
 $\text{non-speculative } P \text{ vs } (\text{llist-of } (\text{take } n \text{ (map NormalAction } \{ta\}_o)));$   
 $\text{vs-conf } P \text{ h vs; hconf h } \rrbracket$

$\Rightarrow \text{vs-conf } P \text{ h' (w-values } P \text{ vs (take } n \text{ (map NormalAction } \{ta\}_o)))$

**apply**(*erule jvmd-NormalE*)

**apply**(*cases xcp*)

**apply**(*auto intro!*: *JVM-heap-base.exec-1-d.intros simp add: JVM-heap-base.exec-d-def check-def JVM-heap-base.exec-d-def*  
*intro: exec-instr-non-speculative-vs-conf*)

**done**

**end**

**locale** *JVM-allocated-heap-conf* =

*JVM-heap-conf*  
*addr2thread-id thread-id2addr*  
*spurious-wakeups*  
*empty-heap allocate typeof-addr heap-read heap-write hconf*  
*P*

+

*JVM-allocated-heap*  
*addr2thread-id thread-id2addr*  
*spurious-wakeups*  
*empty-heap allocate typeof-addr heap-read heap-write*  
*allocated*  
*P*

**for** *addr2thread-id* :: ('addr :: addr)  $\Rightarrow$  'thread-id

**and** *thread-id2addr* :: 'thread-id  $\Rightarrow$  'addr

**and** *spurious-wakeups* :: bool

**and** *empty-heap* :: 'heap

**and** *allocate* :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set

**and** *typeof-addr* :: 'heap  $\Rightarrow$  'addr  $\rightarrow$  htype

**and** *heap-read* :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool

**and** *heap-write* :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool

**and** *hconf* :: 'heap  $\Rightarrow$  bool

**and** *allocated* :: 'heap  $\Rightarrow$  'addr set

**and** *P* :: 'addr jvm-prog

**begin**

**lemma** *mexecd-known-addrs-typing*:

```

assumes wf: wf-jvm-prog $\Phi$  P
and ok: start-heap-ok
shows known-addr-typing addr2thread-id thread-id2addr empty-heap allocate typeof-addr heap-write
allocated jvm-known-addr JVM-final (mexecd P) ( $\lambda t$  (xcp, frstls) h.  $\Phi \vdash t: (xcp, h, frstls) \checkmark$ ) P
proof –
  from wf obtain wf-md where wf-prog wf-md P by(blast dest: wt-jvm-progD)
  then
  interpret known-addr
    addr2thread-id thread-id2addr
    spurious-wakeups
    empty-heap allocate typeof-addr heap-read heap-write
    allocated jvm-known-addr
    JVM-final mexecd P P
  using ok by(rule mexecd-known-addr)

show ?thesis
proof
  fix t x m ta x' m'
  assume mexecd P t (x, m) ta (x', m')
  thus m  $\sqsubseteq$  m' by(auto simp add: split-beta intro: exec-1-d-heat)
next
  fix t x m ta x' m' vs
  assume exec: mexecd P t (x, m) ta (x', m')
  and ts-ok: ( $\lambda(xcp, frstls) h. \Phi \vdash t:(xcp, h, frstls) \checkmark$ ) x m
  and vs: vs-conf P m vs
  and ns: non-speculative P vs (llist-of (map NormalAction  $\{ta\}_o$ ))

  let ?mexecd = JVM-heap-base.mexecd addr2thread-id thread-id2addr spurious-wakeups empty-heap
  allocate typeof-addr (heap-read-typed P) heap-write P
  have lift: lifting-wf JVM-final ?mexecd ( $\lambda t$  (xcp, frstls) h.  $\Phi \vdash t: (xcp, h, frstls) \checkmark$ )
  by(intro JVM-conf-read.lifting-wf-correct-state-d JVM-conf-read-heap-read-typed wf)

  from exec ns vs ts-ok have exec': ?mexecd t (x, m) ta (x', m')
  by(auto simp add: split-beta correct-state-def dest: mexecd-non-speculative-typeable)
  thus ( $\lambda(xcp, frstls) h. \Phi \vdash t:(xcp, h, frstls) \checkmark$ ) x' m' using ts-ok
  by(rule lifting-wf.preserves-red[OF lift])
  {
    fix t'' x'' m''
    assume New: NewThread t'' x'' m''  $\in$  set  $\{ta\}_t$ 
    with exec have m'' = snd (x', m') by(rule execd-mthr.new-thread-memory)
    thus ( $\lambda(xcp, frstls) h. \Phi \vdash t'':(xcp, h, frstls) \checkmark$ ) x'' m''
    using lifting-wf.preserves-NewThread[where ?r=?mexecd, OF lift exec' ts-ok] New
    by auto }
  { fix t'' x''
    assume ( $\lambda(xcp, frstls) h. \Phi \vdash t'':(xcp, h, frstls) \checkmark$ ) x'' m
    with lift exec' ts-ok show ( $\lambda(xcp, frstls) h. \Phi \vdash t'':(xcp, h, frstls) \checkmark$ ) x'' m'
    by(rule lifting-wf.preserves-other) }
next
  fix t x m ta x' m' vs n
  assume exec: mexecd P t (x, m) ta (x', m')
  and ts-ok: ( $\lambda(xcp, frstls) h. \Phi \vdash t:(xcp, h, frstls) \checkmark$ ) x m
  and vs: vs-conf P m vs
  and ns: non-speculative P vs (llist-of (take n (map NormalAction  $\{ta\}_o$ )))
  thus vs-conf P m' (w-values P vs (take n (map NormalAction  $\{ta\}_o$ )))

```

```

    by(auto simp add: correct-state-def dest: mexecd-non-speculative-vs-conf)
  next
    fix t x m ta x' m' ad al v
    assume mexecd P t (x, m) ta (x', m')
    and  $(\lambda(xcp, frstls) h. \Phi \vdash t:(xcp, h, frstls) \checkmark) x m$ 
    and  $ReadMem\ ad\ al\ v \in set\ \{ta\}_o$ 
    thus  $\exists T. P, m \vdash ad@al : T$ 
    by(auto simp add: correct-state-def split-beta dest: exec-1-d-read-typeable)
  next
    fix t x m ta x' m' ad hT
    assume mexecd P t (x, m) ta (x', m')
    and  $(\lambda(xcp, frstls) h. \Phi \vdash t:(xcp, h, frstls) \checkmark) x m$ 
    and  $NewHeapElem\ ad\ hT \in set\ \{ta\}_o$ 
    thus  $typeof-addr\ m' ad = \lfloor hT \rfloor$ 
    by(auto dest: exec-1-d-New-typeof-addrD[where x=hT] simp add: split-beta correct-state-def)
  qed
qed

lemma executions-sc:
  assumes wf: wf-jvm-prog $\Phi$  P
  and wf-start: wf-start-state P C M vs
  and vs2:  $\bigcup (ka-Val\ 'set\ vs) \subseteq set\ start-addr$ s
  shows executions-sc-hb (JVMd- $\mathcal{E}$  P C M vs status) P
    (is executions-sc-hb ?E P)
proof -
  from wf-start obtain Ts T meth D where ok: start-heap-ok
    and sees:  $P \vdash C\ sees\ M : Ts \rightarrow T = \lfloor meth \rfloor$  in D
    and vs1:  $P, start-heap \vdash vs [\leq] Ts$  by cases

  interpret known-addr-typing
    addr2thread-id thread-id2addr
    spurious-wakeups
    empty-heap allocate typeof-addr heap-read heap-write
    allocated jvm-known-addr
    JVM-final mexecd P  $\lambda t (xcp, frstls) h. \Phi \vdash t : (xcp, h, frstls) \checkmark$  P
    using wf ok by(rule mexecd-known-addr-typing)

  from wf obtain wf-md where wf': wf-prog wf-md P by(blast dest: wt-jvm-progD)
  hence wf-syscls P by(rule wf-prog-wf-syscls)
  thus ?thesis
proof(rule executions-sc-hb)
  from correct-jvm-state-initial[OF wf wf-start]
  show correct-state-ts  $\Phi$  (thr (JVM-start-state P C M vs)) start-heap
    by(simp add: correct-jvm-state-def start-state-def split-beta)
  next
    show jvm-known-addr start-tid (( $\lambda(mxs, mxl0, b) vs. (None, [\square], Null \# vs @ replicate\ mxl0\ undefined-value, fst\ (method\ P\ C\ M), M, 0))$ ) (the (snd (snd (snd (method P C M)))) vs)  $\subseteq allocated\ start-heap$ 
    using vs2
    by(auto simp add: split-beta start-addr-allocated jvm-known-addr-def intro: start-tid-start-addr[OF wf-syscls P] ok])
  qed
qed

```



end

declare *split-paired-Ex* [*simp del*]

declare *eq-upto-seq-inconsist-simps* [*simp*]

context *JVM-progress* begin

abbreviation (input) *jvm-non-speculative-read-bound* :: nat where

*jvm-non-speculative-read-bound*  $\equiv$  2

lemma *exec-instr-non-speculative-read*:

assumes *hrt*: *heap-read-typeable hconf P*

and *vs*: *vs-conf P (shr s) vs*

and *hconf*: *hconf (shr s)*

and *exec-i*:  $(ta, xcp', h', frs') \in \text{exec-instr } i \ P \ t \ (shr \ s) \ stk \ loc \ C \ M \ pc \ frs$

and *check*: *check-instr i P (shr s) stk loc C M pc frs*

and *aok*: *execd-mthr.mthr.if.actions-ok s t ta*

and *i*:  $I < \text{length } \llbracket ta \rrbracket_o$

and *read*:  $\llbracket ta \rrbracket_o ! I = \text{ReadMem } a'' \ al'' \ v$

and *v'*:  $v' \in w\text{-values } P \ vs \ (\text{map NormalAction } (\text{take } I \ \llbracket ta \rrbracket_o)) \ (a'', al'')$

and *ns*: *non-speculative P vs (llist-of (map NormalAction (take I  $\llbracket ta \rrbracket_o$ )))*

shows  $\exists ta' \ xcp'' \ h'' \ frs''. (ta', xcp'', h'', frs'') \in \text{exec-instr } i \ P \ t \ (shr \ s) \ stk \ loc \ C \ M \ pc \ frs \wedge$

*execd-mthr.mthr.if.actions-ok s t ta'  $\wedge$*

$I < \text{length } \llbracket ta' \rrbracket_o \wedge \text{take } I \ \llbracket ta' \rrbracket_o = \text{take } I \ \llbracket ta \rrbracket_o \wedge$

$\llbracket ta' \rrbracket_o ! I = \text{ReadMem } a'' \ al'' \ v' \wedge$

$\text{length } \llbracket ta' \rrbracket_o \leq \max \text{jvm-non-speculative-read-bound } (\text{length } \llbracket ta \rrbracket_o)$

using *exec-i i read*

proof(cases *i*)

case [*simp*]: *ALoad*

let *?a* = *the-Addr (hd (tl stk))*

let *?i* = *the-Intg (hd stk)*

from *exec-i i read* have *Null*: *hd (tl stk)  $\neq$  Null*

and *bounds*:  $0 \leq s \ ?i \ \text{sint } ?i < \text{int } (\text{alen-of-htype } (\text{the } (\text{typeof-addr } (shr \ s) \ ?a)))$

and [*simp*]:  $I = 0 \ a'' = ?a \ al'' = \text{ACell } (\text{nat } (\text{sint } ?i))$

by(*auto split: if-split-asm*)

from *Null check* obtain *a T n*

where *a*:  $\text{length } stk > 1 \ \text{hd } (tl \ stk) = \text{Addr } a$

and *type*:  $\text{typeof-addr } (shr \ s) \ ?a = \lfloor \text{Array-type } T \ n \rfloor$  by(*fastforce simp add: is-Ref-def*)

from *bounds type* have  $\text{nat } (\text{sint } ?i) < n$

by (*simp add: word-sle-eq nat-less-iff*)

with *type* have *adal*:  $P, shr \ s \vdash ?a @ \text{ACell } (\text{nat } (\text{sint } ?i)) : T$

by(*rule addr-loc-type.intros*)

from *v' vs adal* have  $P, shr \ s \vdash v' : \leq T$  by(*auto dest!: vs-confD dest: addr-loc-type-fun*)

with *hrt adal* have *heap-read*  $(shr \ s) \ ?a \ (\text{ACell } (\text{nat } (\text{sint } ?i))) \ v'$  using *hconf* by(*rule heap-read-typeableD*)

with *type Null aok exec-i* show *?thesis* apply *auto* using *bounds* by *fastforce*+

next

case [*simp*]: (*Getfield F D*)

let *?a* = *the-Addr (hd stk)*

from *exec-i i read* have *Null*: *hd stk  $\neq$  Null*

and [*simp*]:  $I = 0 \ a'' = ?a \ al'' = \text{CField } D \ F$

by(*auto split: if-split-asm*)

with *check* obtain *U T fm C' a*

```

    where sees:  $P \vdash D \text{ sees } F:T \text{ (fm) in } D$ 
    and type:  $\text{typeof-addr } (\text{shr } s) \text{ ?}a = \lfloor U \rfloor$ 
    and sub:  $P \vdash \text{class-type-of } U \preceq^* D$ 
    and a:  $\text{hd } \text{stk} = \text{Addr } a \text{ length } \text{stk} > 0$  by(auto simp add: is-Ref-def)
  from has-visible-field[OF sees] sub
  have  $P \vdash \text{class-type-of } U \text{ has } F:T \text{ (fm) in } D$  by(rule has-field-mono)
  with type have adal:  $P, \text{shr } s \vdash \text{?}a @ \text{CField } D \text{ } F : T$ 
    by(rule addr-loc-type.intros)
  from  $v' \text{ vs adal}$  have  $P, \text{shr } s \vdash v' : \leq T$  by(auto dest!: vs-confD dest: addr-loc-type-fun)
  with hrt adal have heap-read ( $\text{shr } s$ )  $\text{?}a$  ( $\text{CField } D \text{ } F$ )  $v'$  using hconf by(rule heap-read-typeableD)
  with type Null aok exec-i show  $\text{?thesis}$  by(fastforce)
next
case [simp]: (CAS F D)
let  $\text{?}a = \text{the-Addr } (\text{hd } (\text{tl } (\text{tl } \text{stk})))$ 

  from exec-i i read have Null:  $\text{hd } (\text{tl } (\text{tl } \text{stk})) \neq \text{Null}$ 
    and [simp]:  $I = 0 \text{ } a'' = \text{?}a \text{ } a'' = \text{CField } D \text{ } F$ 
    by(auto split: if-split-asm simp add: nth-Cons')
  with check obtain  $U \text{ } T \text{ fm } C' \text{ } a$ 
    where sees:  $P \vdash D \text{ sees } F:T \text{ (fm) in } D$ 
    and type:  $\text{typeof-addr } (\text{shr } s) \text{ ?}a = \lfloor U \rfloor$ 
    and sub:  $P \vdash \text{class-type-of } U \preceq^* D$ 
    and a:  $\text{hd } (\text{tl } (\text{tl } \text{stk})) = \text{Addr } a \text{ length } \text{stk} > 2$ 
    and v:  $P, \text{shr } s \vdash \text{hd } \text{stk} : \leq T$ 
    by(auto simp add: is-Ref-def)
  from has-visible-field[OF sees] sub
  have  $P \vdash \text{class-type-of } U \text{ has } F:T \text{ (fm) in } D$  by(rule has-field-mono)
  with type have adal:  $P, \text{shr } s \vdash \text{?}a @ \text{CField } D \text{ } F : T$ 
    by(rule addr-loc-type.intros)
  from  $v' \text{ vs adal}$  have  $P, \text{shr } s \vdash v' : \leq T$  by(auto dest!: vs-confD dest: addr-loc-type-fun)
  with hrt adal have read: heap-read ( $\text{shr } s$ )  $\text{?}a$  ( $\text{CField } D \text{ } F$ )  $v'$  using hconf by(rule heap-read-typeableD)
  show  $\text{?thesis}$ 
  proof(cases v' = hd (tl stk))
    case True
      from heap-write-total[OF hconf adal v] a obtain  $h'$ 
        where heap-write ( $\text{shr } s$ ) a ( $\text{CField } D \text{ } F$ ) ( $\text{hd } \text{stk}$ )  $h'$  by auto
        then show  $\text{?thesis}$  using read a True aok exec-i by fastforce
    case False
      then show  $\text{?thesis}$  using read a aok exec-i
        by(fastforce intro!: disjI2)
  qed
next
case [simp]: (Invoke M n)
let  $\text{?}a = \text{the-Addr } (\text{stk } ! \text{ } n)$ 
let  $\text{?vs} = \text{rev } (\text{take } n \text{ } \text{stk})$ 
from exec-i i read have Null:  $\text{stk } ! \text{ } n \neq \text{Null}$ 
  and iec:  $\text{snd } (\text{snd } (\text{snd } (\text{method } P \text{ (class-type-of (the (typeof-addr (shr } s) \text{ ?}a))) M))) = \text{Native}$ 
  by(auto split: if-split-asm)
with check obtain  $a \text{ } T \text{ } Ts \text{ } Tr \text{ } D$ 
  where a:  $\text{stk } ! \text{ } n = \text{Addr } a \text{ } n < \text{length } \text{stk}$ 
  and type:  $\text{typeof-addr } (\text{shr } s) \text{ ?}a = \lfloor T \rfloor$ 
  and extwt:  $P \vdash \text{class-type-of } T \text{ sees } M:Ts \rightarrow Tr = \text{Native in } D \text{ } D \cdot M(Ts) :: Tr$ 
  by(auto simp add: is-Ref-def has-method-def)

```

```

from extwt have native: is-native  $P\ T\ M$  by(auto simp add: is-native.simps)
from Null iec type exec-i obtain ta' va
  where red:  $(ta', va, h') \in \text{red-external-aggr } P\ t\ ?a\ M\ ?vs\ (\text{shr } s)$ 
  and ta:  $ta = \text{extTA2JVM } P\ ta'$  by(fastforce)
from aok ta have aok':  $\text{execd-mthr.mthr.if.actions-ok } s\ t\ ta'$  by simp
from red-external-aggr-non-speculative-read[OF hrt vs red[unfolded a the-Addr.simps] - aok' hconf,
of  $I\ a''\ al''\ v\ v'$ ]
  native type i read  $v'\ ns\ a\ ta$ 
obtain ta'' va'' h''
  where  $(ta'', va'', h'') \in \text{red-external-aggr } P\ t\ a\ M\ (\text{rev } (\text{take } n\ stk))\ (\text{shr } s)$ 
  and  $\text{execd-mthr.mthr.if.actions-ok } s\ t\ ta''$ 
  and  $I < \text{length } \llbracket ta'' \rrbracket_o$   $\text{take } I\ \llbracket ta'' \rrbracket_o = \text{take } I\ \llbracket ta' \rrbracket_o$ 
  and  $\llbracket ta' \rrbracket_o ! I = \text{ReadMem } a''\ al''\ v'\ \text{length } \llbracket ta'' \rrbracket_o \leq \text{length } \llbracket ta' \rrbracket_o$  by auto
thus ?thesis using Null iec ta extwt a type
  by(cases va'') force+
qed(auto simp add: split-beta split: if-split-asm)

```

```

lemma exec-1-d-non-speculative-read:
  assumes hrt: heap-read-typeable hconf  $P$ 
  and vs: vs-conf  $P\ (\text{shr } s)\ vs$ 
  and exec:  $P, t \vdash \text{Normal } (xcp, \text{shr } s, \text{frs}) - ta - \text{jvmd} \rightarrow \text{Normal } (xcp', h', \text{frs}')$ 
  and aok:  $\text{execd-mthr.mthr.if.actions-ok } s\ t\ ta$ 
  and hconf: hconf  $(\text{shr } s)$ 
  and i:  $I < \text{length } \llbracket ta \rrbracket_o$ 
  and read:  $\llbracket ta \rrbracket_o ! I = \text{ReadMem } a''\ al''\ v$ 
  and  $v': v' \in w\text{-values } P\ vs\ (\text{map NormalAction } (\text{take } I\ \llbracket ta \rrbracket_o))\ (a'', al'')$ 
  and ns: non-speculative  $P\ vs\ (\text{llist-of } (\text{map NormalAction } (\text{take } I\ \llbracket ta \rrbracket_o)))$ 
  shows  $\exists ta'\ xcp''\ h''\ \text{frs}''. P, t \vdash \text{Normal } (xcp, \text{shr } s, \text{frs}) - ta' - \text{jvmd} \rightarrow \text{Normal } (xcp'', h'', \text{frs}'') \wedge$ 
     $\text{execd-mthr.mthr.if.actions-ok } s\ t\ ta' \wedge$ 
     $I < \text{length } \llbracket ta' \rrbracket_o \wedge \text{take } I\ \llbracket ta' \rrbracket_o = \text{take } I\ \llbracket ta \rrbracket_o \wedge$ 
     $\llbracket ta' \rrbracket_o ! I = \text{ReadMem } a''\ al''\ v' \wedge$ 
     $\text{length } \llbracket ta' \rrbracket_o \leq \max \text{jvm-non-speculative-read-bound } (\text{length } \llbracket ta \rrbracket_o)$ 
using assms
apply -
apply(erule jvmd-NormalE)
apply(cases  $(P, t, xcp, \text{shr } s, \text{frs})$  rule: exec.cases)
  apply simp
defer
  apply simp
apply clarsimp
apply(drule (3) exec-instr-non-speculative-read)
  apply(clarsimp simp add: check-def has-method-def)
  apply simp
  apply(rule i)
  apply(rule read)
  apply(rule v')
  apply(rule ns)
apply(clarsimp simp add: exec-1-d.simps exec-d-def)
done

end

```

```

declare split-paired-Ex [simp]
declare eq-upto-seq-inconsist-simps [simp del]

```

**locale** *JVM-allocated-progress* =

*JVM-progress*  
*addr2thread-id thread-id2addr*  
*spurious-wakeups*  
*empty-heap allocate typeof-addr heap-read heap-write hconf*  
*P*

+

*JVM-allocated-heap-conf*  
*addr2thread-id thread-id2addr*  
*spurious-wakeups*  
*empty-heap allocate typeof-addr heap-read heap-write hconf*  
*allocated*  
*P*

**for** *addr2thread-id* :: ('addr :: addr) ⇒ 'thread-id  
**and** *thread-id2addr* :: 'thread-id ⇒ 'addr  
**and** *spurious-wakeups* :: bool  
**and** *empty-heap* :: 'heap  
**and** *allocate* :: 'heap ⇒ htype ⇒ ('heap × 'addr) set  
**and** *typeof-addr* :: 'heap ⇒ 'addr → htype  
**and** *heap-read* :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ bool  
**and** *heap-write* :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap ⇒ bool  
**and** *hconf* :: 'heap ⇒ bool  
**and** *allocated* :: 'heap ⇒ 'addr set  
**and** *P* :: 'addr jvm-prog

**begin**

**lemma** *non-speculative-read*:

**assumes** *wf*: *wf-jvm-prog*<sub>Φ</sub> *P*  
**and** *hrt*: *heap-read-typeable hconf P*  
**and** *wf-start*: *wf-start-state P C M vs*  
**and** *ka*:  $\bigcup (ka-Val \text{ ' set } vs) \subseteq \text{set start-addrs}$   
**shows** *execd-mthr.if.non-speculative-read jvm-non-speculative-read-bound*  
*(init-fin-lift-state status (JVM-start-state P C M vs))*  
*(w-values P (λ-. {})) (map snd (lift-start-obs start-tid start-heap-obs)))*  
*(is execd-mthr.if.non-speculative-read - ?start-state ?start-vs)*  
**proof**(*rule execd-mthr.if.non-speculative-readI*)  
**fix** *ttas s' t x ta x' m' i ad al v v'*

**assume** *τRed*: *execd-mthr.mthr.if.RedT P ?start-state ttas s'*

**and** *sc*: *non-speculative P ?start-vs (llist-of (concat (map (λ(t, ta). {ta}\_o) ttas)))*  
**and** *ts't*: *thr s' t = [(x, no-wait-locks)]*  
**and** *red*: *execd-mthr.init-fin P t (x, shr s') ta (x', m')*  
**and** *aok*: *execd-mthr.mthr.if.actions-ok s' t ta*  
**and** *i*: *i < length {ta}\_o*  
**and** *ns'*: *non-speculative P (w-values P ?start-vs (concat (map (λ(t, ta). {ta}\_o) ttas))) (llist-of (take i {ta}\_o))*  
**and** *read*: *{ta}\_o ! i = NormalAction (ReadMem ad al v)*  
**and** *v'*: *v' ∈ w-values P ?start-vs (concat (map (λ(t, ta). {ta}\_o) ttas) @ take i {ta}\_o) (ad, al)*

**from** *wf-start* **obtain** *Ts T meth D* **where** *ok*: *start-heap-ok*

**and** *sees*: *P ⊢ C sees M:Ts→T = [meth] in D*

**and** *conf*: *P, start-heap ⊢ vs [≤] Ts* **by** *cases*

```

let ?conv = λttas. concat (map (λ(t, ta). ⌊ta⌋o) ttas)
let ?vs' = w-values P ?start-vs (?conv ttas)
let ?wt-ok = init-fin-lift (λt (xcp, frstls) h. Φ ⊢ t: (xcp, h, frstls) √)
let ?start-obs = map snd (lift-start-obs start-tid start-heap-obs)

from wf obtain wf-md where wf': wf-prog wf-md P by(blast dest: wt-jvm-progD)

interpret known-addr-typing
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate type-of-addr heap-read heap-write
  allocated jvm-known-addr
  JVM-final mexecd P λt (xcp, frstls) h. Φ ⊢ t: (xcp, h, frstls) √
using wf ok by(rule mexecd-known-addr-typing)

from conf have len2: length vs = length Ts by(rule list-all2-lengthD)

from correct-jvm-state-initial[OF wf wf-start]
have correct-state-ts Φ (thr (JVM-start-state P C M vs)) start-heap
  by(simp add: correct-jvm-state-def start-state-def split-beta)
hence ts-ok-start: ts-ok ?wt-ok (thr ?start-state) (shr ?start-state)
  unfolding ts-ok-init-fin-lift-init-fin-lift-state by(simp add: start-state-def split-beta)

  have sc': non-speculative P ?start-vs (lmap snd (lconcat (lmap (λ(t, ta). llist-of (map (Pair t)
    ⌊ta⌋o)) (llist-of ttas))))
  using sc by(simp add: lmap-lconcat llist.map-comp o-def split-def lconcat-llist-of[symmetric])
from start-state-vs-conf[OF wf-prog-wf-syscls[OF wf]]
have vs-conf-start: vs-conf P (shr ?start-state) ?start-vs
  by(simp add: init-fin-lift-state-conv-simps start-state-def split-beta)
with τRed ts-ok-start sc
have wt': ts-ok ?wt-ok (thr s') (shr s')
  and vs': vs-conf P (shr s') ?vs' by(rule if-RedT-non-speculative-invar)+

from red i read obtain xcp frs xcp' frs' ta'
  where x: x = (Running, xcp, frs) and x': x' = (Running, xcp', frs')
  and ta: ta = convert-TA-initial (convert-obs-initial ta')
  and red': P, t ⊢ Normal (xcp, shr s', frs) -ta'-jvmd→ Normal (xcp', m', frs')
  by cases fastforce+

from ts't wt' x have hconf: hconf (shr s') by(auto dest!: ts-okD simp add: correct-state-def)

have aok': execd-mthr.mthr.if.actions-ok s' t ta' using aok unfolding ta by simp

from i read v' ns' ta have i < length ⌊ta'⌋o
  and ⌊ta'⌋o ! i = ReadMem ad al v
  and v' ∈ w-values P ?vs' (map NormalAction (take i ⌊ta'⌋o)) (ad, al)
  and non-speculative P ?vs' (llist-of (map NormalAction (take i ⌊ta'⌋o)))
  by(simp-all add: take-map)

from exec-1-d-non-speculative-read[OF hrt vs' red' aok' hconf this]
obtain ta'' xcp'' frs'' h''
  where red'': P, t ⊢ Normal (xcp, shr s', frs) -ta''-jvmd→ Normal (xcp'', h'', frs'')
  and aok'': execd-mthr.mthr.if.actions-ok s' t ta''
  and i'': i < length ⌊ta''⌋o

```

**and**  $eq''$ :  $take\ i\ \llbracket ta'' \rrbracket_o = take\ i\ \llbracket ta' \rrbracket_o$   
**and**  $read''$ :  $\llbracket ta'' \rrbracket_o ! i = ReadMem\ ad\ al\ v'$   
**and**  $len''$ :  $length\ \llbracket ta'' \rrbracket_o \leq max\ jvm\text{-}non\text{-}speculative\text{-}read\text{-}bound\ (length\ \llbracket ta' \rrbracket_o)$  **by** *blast*

**let**  $?x' = (Running, xcp'', frs'')$   
**let**  $?ta' = convert\text{-}TA\text{-}initial\ (convert\text{-}obs\text{-}initial\ ta'')$   
**from**  $red''$  **have**  $execd\text{-}mthr.\text{init}\text{-}fin\ P\ t\ (x, shr\ s')\ ?ta'\ (?x', h'')$   
**unfolding**  $x$  **by**  $-(rule\ execd\text{-}mthr.\text{init}\text{-}fin.\text{NormalAction}, simp)$   
**moreover from**  $aok''$  **have**  $execd\text{-}mthr.\text{mthr}.\text{if}.\text{actions}\text{-}ok\ s'\ t\ ?ta'$  **by** *simp*  
**moreover from**  $i''$  **have**  $i < length\ \llbracket ?ta' \rrbracket_o$  **by** *simp*  
**moreover from**  $eq''$  **have**  $take\ i\ \llbracket ?ta' \rrbracket_o = take\ i\ \llbracket ta \rrbracket_o$  **unfolding**  $ta$  **by**  $(simp\ add: take\text{-}map)$   
**moreover from**  $read''\ i''$  **have**  $\llbracket ?ta' \rrbracket_o ! i = NormalAction\ (ReadMem\ ad\ al\ v')$  **by**  $(simp\ add: nth\text{-}map)$   
**moreover from**  $len''$  **have**  $length\ \llbracket ?ta' \rrbracket_o \leq max\ jvm\text{-}non\text{-}speculative\text{-}read\text{-}bound\ (length\ \llbracket ta \rrbracket_o)$   
**unfolding**  $ta$  **by** *simp*  
**ultimately**  
**show**  $\exists\ ta'\ x''\ m''.\ execd\text{-}mthr.\text{init}\text{-}fin\ P\ t\ (x, shr\ s')\ ta'\ (x'', m'') \wedge$   
 $execd\text{-}mthr.\text{mthr}.\text{if}.\text{actions}\text{-}ok\ s'\ t\ ta' \wedge$   
 $i < length\ \llbracket ta' \rrbracket_o \wedge take\ i\ \llbracket ta' \rrbracket_o = take\ i\ \llbracket ta \rrbracket_o \wedge$   
 $\llbracket ta' \rrbracket_o ! i = NormalAction\ (ReadMem\ ad\ al\ v') \wedge$   
 $length\ \llbracket ta' \rrbracket_o \leq max\ jvm\text{-}non\text{-}speculative\text{-}read\text{-}bound\ (length\ \llbracket ta \rrbracket_o)$   
**by** *blast*

**qed**

**lemma** *JVM-cut-and-update*:

**assumes**  $wf$ :  $wf\text{-}jvm\text{-}prog_{\Phi}\ P$   
**and**  $hrt$ :  $heap\text{-}read\text{-}typeable\ hconf\ P$   
**and**  $wf\text{-}start$ :  $wf\text{-}start\text{-}state\ P\ C\ M\ vs$   
**and**  $ka$ :  $\bigcup (ka\text{-}Val\ 'set\ vs) \subseteq set\ start\text{-}addrs$   
**shows**  $execd\text{-}mthr.\text{if}.\text{cut}\text{-}and\text{-}update\ (init\text{-}fin\text{-}lift\text{-}state\ status\ (JVM\text{-}start\text{-}state\ P\ C\ M\ vs))$   
 $(mrw\text{-}values\ P\ Map.\text{empty}\ (map\ snd\ (lift\text{-}start\text{-}obs\ start\text{-}tid\ start\text{-}heap\text{-}obs)))$

**proof** –

**from**  $wf\text{-}start$  **obtain**  $Ts\ T\ meth\ D$  **where**  $ok$ :  $start\text{-}heap\text{-}ok$   
**and**  $sees$ :  $P \vdash C\ sees\ M:Ts \rightarrow T = \lfloor meth \rfloor$  **in**  $D$   
**and**  $conf$ :  $P, start\text{-}heap \vdash vs\ [\leq]\ Ts$  **by** *cases*

**interpret** *known-addrs-typing*

$addr2thread\text{-}id\ thread\text{-}id2addr$   
 $spurious\text{-}wakeups$   
 $empty\text{-}heap\ allocate\ typeof\text{-}addr\ heap\text{-}read\ heap\text{-}write$   
 $allocated\ jvm\text{-}known\text{-}addrs$   
 $JVM\text{-}final\ mexecd\ P\ \lambda t\ (xcp, frstls)\ h.\ \Phi \vdash t: (xcp, h, frstls)\ \checkmark$   
**using**  $wf\ ok$  **by**  $(rule\ mexecd\text{-}known\text{-}addrs\text{-}typing)$

**let**  $?start\text{-}vs = w\text{-}values\ P\ (\lambda\cdot.\ \{\})\ (map\ snd\ (lift\text{-}start\text{-}obs\ start\text{-}tid\ start\text{-}heap\text{-}obs))$   
**let**  $?wt\text{-}ok = init\text{-}fin\text{-}lift\ (\lambda t\ (xcp, frstls)\ h.\ \Phi \vdash t: (xcp, h, frstls)\ \checkmark)$   
**let**  $?start\text{-}obs = map\ snd\ (lift\text{-}start\text{-}obs\ start\text{-}tid\ start\text{-}heap\text{-}obs)$   
**let**  $?start\text{-}state = init\text{-}fin\text{-}lift\text{-}state\ status\ (JVM\text{-}start\text{-}state\ P\ C\ M\ vs)$

**from**  $wf$  **obtain**  $wf\text{-}md$  **where**  $wf'$ :  $wf\text{-}prog\ wf\text{-}md\ P$  **by**  $(blast\ dest: wt\text{-}jvm\text{-}progD)$   
**hence**  $wf\text{-}syscls\ P$  **by**  $(rule\ wf\text{-}prog\text{-}wf\text{-}syscls)$   
**moreover**  
**note**  $non\text{-}speculative\text{-}read[OF\ wf\ hrt\ wf\text{-}start\ ka]$   
**moreover have**  $ts\text{-}ok\ ?wt\text{-}ok\ (thr\ ?start\text{-}state)\ (shr\ ?start\text{-}state)$

**using** *correct-jvm-state-initial*[*OF wf wf-start*]  
**by**(*simp add: correct-jvm-state-def start-state-def split-beta*)  
**moreover have** *ka: jvm-known-addr start-tid ((λ(mxs, mxl0, b) vs. (None, [([], Null # vs @ replicate mxl0 undefined-value, fst (method P C M), M, 0)])) (the (snd (snd (snd (method P C M)))) vs) ⊆ allocated start-heap*  
**using** *ka by(auto simp add: split-beta start-addr-allocated jvm-known-addr-def intro: start-tid-start-addr[OF <wf-syscls P> ok])*  
**ultimately show** *?thesis by(rule non-speculative-read-into-cut-and-update)*  
**qed**

**lemma** *JVM-drf:*

**assumes** *wf: wf-jvm-prog<sub>Φ</sub> P*  
**and** *hrt: heap-read-typeable hconf P*  
**and** *wf-start: wf-start-state P C M vs*  
**and** *ka: ⋃(ka-Val ‘ set vs) ⊆ set start-addr*  
**shows** *drf (JVMd- $\mathcal{E}$  P C M vs status) P*

**proof** –

**from** *wf-start obtain Ts T meth D where ok: start-heap-ok*  
**and** *sees: P ⊢ C sees M:Ts→T = [meth] in D*  
**and** *conf: P, start-heap ⊢ vs [⋅≤] Ts by cases*

**from** *wf obtain wf-md where wf': wf-prog wf-md P by(blast dest: wt-jvm-progD)*

**hence** *wf-syscls P by(rule wf-prog-wf-syscls)*

**with** *JVM-cut-and-update[OF assms]*

**show** *?thesis*

**proof**(*rule known-addr-typing.drf[OF mexecd-known-addr-typing[OF wf ok]]*)

**from** *correct-jvm-state-initial[OF wf wf-start]*

**show** *correct-state-ts Φ (thr (JVM-start-state P C M vs)) start-heap*

**by**(*simp add: correct-jvm-state-def start-state-def split-beta*)

**next**

**show** *jvm-known-addr start-tid ((λ(mxs, mxl0, b) vs. (None, [([], Null # vs @ replicate mxl0 undefined-value, fst (method P C M), M, 0)])) (the (snd (snd (snd (method P C M)))) vs) ⊆ allocated start-heap*

**using** *ka by(auto simp add: split-beta start-addr-allocated jvm-known-addr-def intro: start-tid-start-addr[OF <wf-syscls P> ok])*

**qed**

**qed**

**lemma** *JVM-sc-legal:*

**assumes** *wf: wf-jvm-prog<sub>Φ</sub> P*  
**and** *hrt: heap-read-typeable hconf P*  
**and** *wf-start: wf-start-state P C M vs*  
**and** *ka: ⋃(ka-Val ‘ set vs) ⊆ set start-addr*  
**shows** *sc-legal (JVMd- $\mathcal{E}$  P C M vs status) P*

**proof** –

**from** *wf-start obtain Ts T meth D where ok: start-heap-ok*

**and** *sees: P ⊢ C sees M:Ts→T = [meth] in D*

**and** *conf: P, start-heap ⊢ vs [⋅≤] Ts by cases*

**interpret** *known-addr-typing*

*addr2thread-id thread-id2addr*

*spurious-wakeups*

*empty-heap allocate type-of-addr heap-read heap-write*

*allocated jvm-known-addr*

$JVM\text{-}final\ mexecd\ P\ \lambda t\ (xcp, frstls)\ h.\ \Phi \vdash t: (xcp, h, frstls)\ \checkmark$   
**using**  $wf\ ok$  **by**(rule  $mexecd\text{-}known\text{-}addrs\text{-}typing$ )

**from**  $wf$  **obtain**  $wf\text{-}md$  **where**  $wf'$ :  $wf\text{-}prog\ wf\text{-}md\ P$  **by**(blast dest:  $wt\text{-}jvm\text{-}progD$ )  
**hence**  $wf\text{-}syscls\ P$  **by**(rule  $wf\text{-}prog\text{-}wf\text{-}syscls$ )

**let**  $?start\text{-}vs = w\text{-}values\ P\ (\lambda\text{-}.\ \{\})\ (map\ snd\ (lift\text{-}start\text{-}obs\ start\text{-}tid\ start\text{-}heap\text{-}obs))$   
**let**  $?wt\text{-}ok = init\text{-}fin\text{-}lift\ (\lambda t\ (xcp, frstls)\ h.\ \Phi \vdash t: (xcp, h, frstls)\ \checkmark)$   
**let**  $?start\text{-}obs = map\ snd\ (lift\text{-}start\text{-}obs\ start\text{-}tid\ start\text{-}heap\text{-}obs)$   
**let**  $?start\text{-}state = init\text{-}fin\text{-}lift\text{-}state\ status\ (JVM\text{-}start\text{-}state\ P\ C\ M\ vs)$

**note**  $\langle wf\text{-}syscls\ P \rangle\ non\text{-}speculative\text{-}read[OF\ wf\ hrt\ wf\text{-}start\ ka]$   
**moreover have**  $ts\text{-}ok\ ?wt\text{-}ok\ (thr\ ?start\text{-}state)\ (shr\ ?start\text{-}state)$   
**using**  $correct\text{-}jvm\text{-}state\text{-}initial[OF\ wf\ wf\text{-}start]$   
**by**(simp add:  $correct\text{-}jvm\text{-}state\text{-}def\ start\text{-}state\text{-}def\ split\text{-}beta$ )  
**moreover**  
**have**  $ka\text{-}allocated: jvm\text{-}known\text{-}addrs\ start\text{-}tid\ ((\lambda(mxs, mxl0, b)\ vs.\ (None, [([], Null\ \# \ vs\ @\ replicate\ mxl0\ undefined\text{-}value, fst\ (method\ P\ C\ M), M, 0)]))\ (the\ (snd\ (snd\ (snd\ (method\ P\ C\ M))))\ vs)\ \subseteq\ allocated\ start\text{-}heap$   
**using**  $ka$  **by**(auto simp add:  $split\text{-}beta\ start\text{-}addrs\text{-}allocated\ jvm\text{-}known\text{-}addrs\text{-}def\ intro: start\text{-}tid\text{-}start\text{-}addrs[OF\ \langle wf\text{-}syscls\ P \rangle\ ok]$ )  
**ultimately have**  $execd\text{-}mthr.\text{if}.\text{hb}\text{-}completion\ ?start\text{-}state\ (lift\text{-}start\text{-}obs\ start\text{-}tid\ start\text{-}heap\text{-}obs)$   
**by**(rule  $non\text{-}speculative\text{-}read\text{-}into\text{-}hb\text{-}completion$ )

**thus**  $?thesis$  **using**  $\langle wf\text{-}syscls\ P \rangle$   
**proof**(rule  $sc\text{-}legal$ )  
**from**  $correct\text{-}jvm\text{-}state\text{-}initial[OF\ wf\ wf\text{-}start]$   
**show**  $correct\text{-}state\text{-}ts\ \Phi\ (thr\ (JVM\text{-}start\text{-}state\ P\ C\ M\ vs))\ start\text{-}heap$   
**by**(simp add:  $correct\text{-}jvm\text{-}state\text{-}def\ start\text{-}state\text{-}def\ split\text{-}beta$ )  
**qed**(rule  $ka\text{-}allocated$ )  
**qed**

**lemma**  $JVM\text{-}jmm\text{-}consistent$ :  
**assumes**  $wf: wf\text{-}jvm\text{-}prog_{\Phi}\ P$   
**and**  $hrt: heap\text{-}read\text{-}typeable\ hconf\ P$   
**and**  $wf\text{-}start: wf\text{-}start\text{-}state\ P\ C\ M\ vs$   
**and**  $ka: \bigcup (ka\text{-}Val\ 'set\ vs) \subseteq set\ start\text{-}addrs$   
**shows**  $jmm\text{-}consistent\ (JVMd\text{-}\mathcal{E}\ P\ C\ M\ vs\ status)\ P$   
**(is**  $jmm\text{-}consistent\ ?\mathcal{E}\ P$ **)**  
**proof** –  
**interpret**  $drf\ ?\mathcal{E}\ P$  **using**  $assms$  **by**(rule  $JVM\text{-}drf$ )  
**interpret**  $sc\text{-}legal\ ?\mathcal{E}\ P$  **using**  $assms$  **by**(rule  $JVM\text{-}sc\text{-}legal$ )  
**show**  $?thesis$  **by**  $unfold\text{-}locales$   
**qed**

**lemma**  $JVM\text{-}ex\text{-}sc\text{-}exec$ :  
**assumes**  $wf: wf\text{-}jvm\text{-}prog_{\Phi}\ P$   
**and**  $hrt: heap\text{-}read\text{-}typeable\ hconf\ P$   
**and**  $wf\text{-}start: wf\text{-}start\text{-}state\ P\ C\ M\ vs$   
**and**  $ka: \bigcup (ka\text{-}Val\ 'set\ vs) \subseteq set\ start\text{-}addrs$   
**shows**  $\exists E\ ws.\ E \in JVMd\text{-}\mathcal{E}\ P\ C\ M\ vs\ status \wedge P \vdash (E, ws)\ \checkmark \wedge sequentially\text{-}consistent\ P\ (E, ws)$   
**(is**  $\exists E\ ws.\ - \in ?\mathcal{E} \wedge -$ **)**  
**proof** –  
**interpret**  $jmm: executions\text{-}sc\text{-}hb\ ?\mathcal{E}\ P$  **using**  $assms$  **by** –(rule  $executions\text{-}sc$ )



```

let ?start-state = init-fin-lift-state status (JVM-start-state P C M vs)
let ?start-mrw = mrw-values P Map.empty (map snd (lift-start-obs start-tid start-heap-obs))

from execd-mthr.if.sequential-completion-Runs[OF execd-mthr.if.cut-and-update-imp-sc-completion[OF
JVM-cut-and-update[OF assms]] ta-seq-consist-convert-RA]
obtain ttas where Red: execd-mthr.mthr.if.mthr.Runs P ?start-state ttas
  and sc: ta-seq-consist P ?start-mrw (lconcat (lmap (λ(t, ta). llist-of {ta}_o) ttas)) by blast
let ?E = lappend (llist-of (lift-start-obs start-tid start-heap-obs)) (lconcat (lmap (λ(t, ta). llist-of
(map (Pair t) {ta}_o) ttas))
from Red have ?E ∈ ?E by (blast intro: execd-mthr.mthr.if.E.intros)
moreover from Red have tsa: thread-start-actions-ok ?E
  by (blast intro: execd-mthr.thread-start-actions-ok-init-fin execd-mthr.mthr.if.E.intros)
from sc have ta-seq-consist P Map.empty (lmap snd ?E)
  unfolding lmap-lappend-distrib lmap-lconcat llist.map-comp split-def o-def lmap-llist-of map-map
snd-conv
  by (simp add: ta-seq-consist-lappend ta-seq-consist-start-heap-obs)
from ta-seq-consist-imp-sequentially-consistent[OF tsa jmm.E-new-actions-for-fun[OF ⟨?E ∈ ?E⟩]
this]
obtain ws where sequentially-consistent P (?E, ws) P ⊢ (?E, ws) ✓ by iprover
ultimately show ?thesis by blast
qed

```

**theorem** JVM-consistent:

```

assumes wf: wf-jvm-progΦ P
and hrt: heap-read-typeable hconf P
and wf-start: wf-start-state P C M vs
and ka: ⋃ (ka-Val ‘ set vs) ⊆ set start-addr
shows ∃ E ws. legal-execution P (JVMd- $\mathcal{E}$  P C M vs status) (E, ws)

```

**proof** –

```

let ?E = JVMd- $\mathcal{E}$  P C M vs status
interpret sc-legal ?E P using assms by (rule JVM-sc-legal)
from JVM-ex-sc-exec[OF assms]
obtain E ws where E ∈ ?E P ⊢ (E, ws) ✓ sequentially-consistent P (E, ws) by blast
hence legal-execution P ?E (E, ws) by (rule SC-is-legal)
thus ?thesis by blast

```

**qed**

**end**

One could now also prove that the aggressive JVM satisfies *drf*. The key would be that *welltyped-commute* also holds for *non-speculative* prefixes from start.

**end**

## 8.16 JMM heap implementation 1

**theory** JMM-Type

**imports**

```

  ../Common/ExternalCallWF
  ../Common/ConformThreaded
  JMM-Heap

```

**begin**

### 8.16.1 Definitions

The JMM heap only stores type information.

**type-synonym**  $'addr \text{ JMM-heap} = 'addr \multimap htype$

**translations**  $(type) 'addr \text{ JMM-heap} \leq (type) 'addr \Rightarrow htype \text{ option}$

**abbreviation**  $jmm\text{-empty} :: 'addr \text{ JMM-heap} \text{ where } jmm\text{-empty} == Map.empty$

**definition**  $jmm\text{-allocate} :: 'addr \text{ JMM-heap} \Rightarrow htype \Rightarrow ('addr \text{ JMM-heap} \times 'addr) \text{ set}$   
**where**  $jmm\text{-allocate } h \ hT = (\lambda a. (h(a \mapsto hT), a)) \text{ ' } \{a. h \ a = None\}$

**definition**  $jmm\text{-typeof-addr} :: 'addr \text{ JMM-heap} \Rightarrow 'addr \multimap htype$   
**where**  $jmm\text{-typeof-addr } h = h$

**definition**  $jmm\text{-heap-read} :: 'addr \text{ JMM-heap} \Rightarrow 'addr \Rightarrow addr\text{-loc} \Rightarrow 'addr \text{ val} \Rightarrow bool$   
**where**  $jmm\text{-heap-read } h \ a \ ad \ v = True$

**context**  
**notes**  $[[inductive\text{-internals}]]$   
**begin**

**inductive**  $jmm\text{-heap-write} :: 'addr \text{ JMM-heap} \Rightarrow 'addr \Rightarrow addr\text{-loc} \Rightarrow 'addr \text{ val} \Rightarrow 'addr \text{ JMM-heap}$   
 $\Rightarrow bool$   
**where**  $jmm\text{-heap-write } h \ a \ ad \ v \ h$

**end**

**definition**  $jmm\text{-hconf} :: 'm \text{ prog} \Rightarrow 'addr \text{ JMM-heap} \Rightarrow bool \ (- \vdash jmm - \sqrt{[51,51]} \ 50)$   
**where**  $P \vdash jmm \ h \ \sqrt{\longleftrightarrow} \ ty\text{-of-} htype \text{ ' } ran \ h \subseteq \{T. is\text{-type } P \ T\}$

**definition**  $jmm\text{-allocated} :: 'addr \text{ JMM-heap} \Rightarrow 'addr \text{ set}$   
**where**  $jmm\text{-allocated } h = dom \ (jmm\text{-typeof-addr } h)$

**definition**  $jmm\text{-spurious-wakeups} :: bool$   
**where**  $jmm\text{-spurious-wakeups} = True$

**lemmas**  $jmm\text{-heap-ops-defs} =$   
 $jmm\text{-allocate-def } jmm\text{-typeof-addr-def}$   
 $jmm\text{-heap-read-def } jmm\text{-heap-write-def}$   
 $jmm\text{-allocated-def } jmm\text{-spurious-wakeups-def}$

**type-synonym**  $'addr \text{ thread-id} = 'addr$

**abbreviation**  $(input) \text{ addr2thread-id} :: 'addr \Rightarrow 'addr \text{ thread-id}$   
**where**  $\text{addr2thread-id} \equiv \lambda x. x$

**abbreviation**  $(input) \text{ thread-id2addr} :: 'addr \text{ thread-id} \Rightarrow 'addr$   
**where**  $\text{thread-id2addr} \equiv \lambda x. x$

**interpretation**  $jmm: heap\text{-base}$   
 $\text{addr2thread-id } \text{thread-id2addr}$   
 $jmm\text{-spurious-wakeups}$   
 $jmm\text{-empty } jmm\text{-allocate } jmm\text{-typeof-addr } jmm\text{-heap-read } jmm\text{-heap-write}$

.

**notation** *jmm.hex*  $(- \trianglelefteq_{jmm} - [51, 51] \ 50)$   
**notation** *jmm.conf*  $(-, - \vdash_{jmm} - : \leq - [51, 51, 51, 51] \ 50)$   
**notation** *jmm.addr-loc-type*  $(-, - \vdash_{jmm} - @ - : - [50, 50, 50, 50, 50] \ 51)$   
**notation** *jmm.confs*  $(-, - \vdash_{jmm} - [:\leq] - [51, 51, 51, 51] \ 50)$   
**notation** *jmm.tconf*  $(-, - \vdash_{jmm} - \sqrt{t} [51, 51, 51] \ 50)$

Now a variation of the JMM with a different read operation that permits to read only type-conformant values

**interpretation** *jmm'*: *heap-base*  
*addr2thread-id thread-id2addr*  
*jmm-spurious-wakeups*  
*jmm-empty jmm-allocate jmm-typeof-addr jmm.heap-read-typed P jmm-heap-write*  
**for** *P* .

**notation** *jmm'.hex*  $(- \trianglelefteq_{jmm''} - [51, 51] \ 50)$   
**notation** *jmm'.conf*  $(-, - \vdash_{jmm''} - : \leq - [51, 51, 51, 51] \ 50)$   
**notation** *jmm'.addr-loc-type*  $(-, - \vdash_{jmm''} - @ - : - [50, 50, 50, 50, 50] \ 51)$   
**notation** *jmm'.confs*  $(-, - \vdash_{jmm''} - [:\leq] - [51, 51, 51, 51] \ 50)$   
**notation** *jmm'.tconf*  $(-, - \vdash_{jmm''} - \sqrt{t} [51, 51, 51] \ 50)$

## 8.16.2 Heap locale interpretations

### 8.16.3 Locale *heap*

**lemma** *jmm-heap*: *heap addr2thread-id thread-id2addr jmm-allocate jmm-typeof-addr jmm-heap-write P*

**proof**  
**fix** *h' a h hT*  
**assume**  $(h', a) \in \text{jmm-allocate } h \ hT$   
**thus** *jmm-typeof-addr* *h' a* =  $\lfloor hT \rfloor$   
**by** (*auto simp add: jmm-heap-ops-defs*)  
**next**  
**fix** *h' :: ('addr :: addr) JMM-heap and h hT a*  
**assume**  $(h', a) \in \text{jmm-allocate } h \ hT$   
**thus**  $h \trianglelefteq_{jmm} h'$   
**by** (*fastforce simp add: jmm-heap-ops-defs intro: jmm.hexI*)  
**next**  
**fix** *h a al v and h' :: ('addr :: addr) JMM-heap*  
**assume** *jmm-heap-write* *h a al v h'*  
**thus**  $h \trianglelefteq_{jmm} h'$  **by** *cases auto*  
**qed** *simp*

**interpretation** *jmm*: *heap*  
*addr2thread-id thread-id2addr*  
*jmm-spurious-wakeups*  
*jmm-empty jmm-allocate jmm-typeof-addr jmm-heap-read jmm-heap-write*  
*P*  
**for** *P*  
**by** (*rule jmm-heap*)

**declare** *jmm.typeof-addr-thread-id2-addr-addr2thread-id* [*simp del*]

**lemmas** *jmm'-heap* = *jmm-heap*

**interpretation** *jmm'*: *heap*  
*addr2thread-id thread-id2addr*  
*jmm-spurious-wakeups*  
*jmm-empty jmm-allocate jmm-typeof-addr jmm.heap-read-typed P jmm-heap-write*  
*P*  
**for** *P*  
**by**(*rule jmm'-heap*)

**declare** *jmm'.typeof-addr-thread-id2-addr-addr2thread-id* [*simp del*]

#### 8.16.4 Locale *heap-conf*

**interpretation** *jmm*: *heap-conf-base*  
*addr2thread-id thread-id2addr*  
*jmm-spurious-wakeups*  
*jmm-empty jmm-allocate jmm-typeof-addr jmm-heap-read jmm-heap-write jmm-hconf P*  
*P*  
**for** *P* .

**abbreviation** (*input*) *jmm'-hconf* :: '*m prog*  $\Rightarrow$  '*addr JMM-heap*  $\Rightarrow$  *bool* ( $\vdash$  *jmm''* -  $\sqrt{[51,51]}$  50)  
**where** *jmm'-hconf* == *jmm-hconf*

**interpretation** *jmm'*: *heap-conf-base*  
*addr2thread-id thread-id2addr*  
*jmm-spurious-wakeups*  
*jmm-empty jmm-allocate jmm-typeof-addr jmm.heap-read-typed P jmm-heap-write jmm'-hconf P*  
*P*  
**for** *P* .

**abbreviation** *jmm-heap-read-typeable* :: ('*addr* :: *addr*) *itself*  $\Rightarrow$  '*m prog*  $\Rightarrow$  *bool*  
**where** *jmm-heap-read-typeable tytok P*  $\equiv$  *jmm.heap-read-typeable (jmm-hconf P :: 'addr JMM-heap  $\Rightarrow$  bool) P*

**abbreviation** *jmm'-heap-read-typeable* :: ('*addr* :: *addr*) *itself*  $\Rightarrow$  '*m prog*  $\Rightarrow$  *bool*  
**where** *jmm'-heap-read-typeable tytok P*  $\equiv$  *jmm'.heap-read-typeable TYPE('m) P (jmm-hconf P :: 'addr JMM-heap  $\Rightarrow$  bool) P*

**lemma** *jmm-heap-read-typeable: jmm-heap-read-typeable tytok P*  
**by**(*rule jmm.heap-read-typeableI*)(*simp add: jmm-heap-read-def*)

**lemma** *jmm'-heap-read-typeable: jmm'-heap-read-typeable tytok P*  
**by**(*rule jmm'.heap-read-typeableI*)(*auto simp add: jmm.heap-read-typed-def jmm-heap-read-def dest: jmm'.addr-loc-type-fun*)

**lemma** *jmm-heap-conf*:  
*heap-conf addr2thread-id thread-id2addr jmm-empty jmm-allocate jmm-typeof-addr jmm-heap-write*  
*(jmm-hconf P) P*  
**proof**  
**show** *P  $\vdash$  jmm jmm-empty  $\checkmark$*   
**by**(*simp add: jmm-hconf-def*)  
**next**  
**fix** *h a hT*

```

assume jmm-typeof-addr h a =  $\lfloor hT \rfloor$  P  $\vdash$  jmm h  $\checkmark$ 
thus is-htype P hT by(auto simp add: jmm-hconf-def jmm-heap-ops-defs intro: ranI)
next
  fix h' h hT a
  assume (h', a)  $\in$  jmm-allocate h hT P  $\vdash$  jmm h  $\checkmark$  is-htype P hT
  thus P  $\vdash$  jmm h'  $\checkmark$ 
    by(fastforce simp add: jmm-hconf-def jmm-heap-ops-defs ran-def split: if-split-asm)
next
  fix h a al v h' T
  assume jmm-heap-write h a al v h' P  $\vdash$  jmm h  $\checkmark$ 
    and jmm.addr-loc-type P h a al T and P, h  $\vdash$  jmm v  $\leq$  T
  thus P  $\vdash$  jmm h'  $\checkmark$  by(cases) simp
qed

```

```

interpretation jmm: heap-conf
  addr2thread-id thread-id2addr
  jmm-spurious-wakeups
  jmm-empty jmm-allocate jmm-typeof-addr jmm-heap-read jmm-heap-write jmm-hconf P
  P
  for P
by(rule jmm-heap-conf)

```

**lemmas** *jmm'-heap-conf* = *jmm-heap-conf*

```

interpretation jmm': heap-conf
  addr2thread-id thread-id2addr
  jmm-spurious-wakeups
  jmm-empty jmm-allocate jmm-typeof-addr jmm.heap-read-typed P jmm-heap-write jmm'-hconf P
  P
  for P
by(rule jmm'-heap-conf)

```

### 8.16.5 Locale *heap-progress*

```

lemma jmm-heap-progress:
  heap-progress addr2thread-id thread-id2addr jmm-empty jmm-allocate jmm-typeof-addr jmm-heap-read
jmm-heap-write (jmm-hconf P) P
proof
  fix h a al T
  assume P  $\vdash$  jmm h  $\checkmark$ 
    and al: jmm.addr-loc-type P h a al T
  show  $\exists v. jmm\text{-heap-read } h \ a \ al \ v \wedge P, h \vdash jmm \ v \leq T$ 
    using jmm.defval-conf[of P h T] unfolding jmm-heap-ops-defs by blast
next
  fix h a al T v
  assume jmm.addr-loc-type P h a al T
  show  $\exists h'. jmm\text{-heap-write } h \ a \ al \ v \ h'$ 
    by(auto intro: jmm-heap-write.intros)
qed

```

```

interpretation jmm: heap-progress
  addr2thread-id thread-id2addr
  jmm-spurious-wakeups
  jmm-empty jmm-allocate jmm-typeof-addr jmm-heap-read jmm-heap-write jmm-hconf P

```

*P*  
**for** *P*  
**by**(*rule jmm-heap-progress*)

**lemma** *jmm'-heap-progress*:

*heap-progress addr2thread-id thread-id2addr jmm-empty jmm-allocate jmm-typeof-addr (jmm.heap-read-typed*  
*P) jmm-heap-write (jmm'-hconf P) P*

**proof**

**fix** *h a al T*  
**assume** *P*  $\vdash$  *jmm'* *h*  $\checkmark$   
**and** *al*: *jmm'.addr-loc-type P h a al T*  
**thus**  $\exists v. \text{jmm.heap-read-typed } P \text{ } h \text{ } a \text{ } al \text{ } v \wedge P, h \vdash \text{jmm}' v : \leq T$   
**unfolding** *jmm.heap-read-typed-def jmm-heap-read-def*  
**by**(*auto dest: jmm'.addr-loc-type-fun intro: jmm'.defval-conf*)

**next**

**fix** *h a al T v*  
**assume** *jmm'.addr-loc-type P h a al T*  
**and** *P, h*  $\vdash$  *jmm'* *v*  $: \leq T$   
**thus**  $\exists h'. \text{jmm-heap-write } h \text{ } a \text{ } al \text{ } v \text{ } h'$   
**by**(*auto intro: jmm-heap-write.intros*)

**qed**

**interpretation** *jmm'*: *heap-progress*

*addr2thread-id thread-id2addr*

*jmm-spurious-wakeups*

*jmm-empty jmm-allocate jmm-typeof-addr jmm.heap-read-typed P jmm-heap-write jmm'-hconf P*  
*P*

**for** *P*

**by**(*rule jmm'-heap-progress*)

### 8.16.6 Locale *heap-conf-read*

**lemma** *jmm'-heap-conf-read*:

*heap-conf-read addr2thread-id thread-id2addr jmm-empty jmm-allocate jmm-typeof-addr (jmm.heap-read-typed*  
*P) jmm-heap-write (jmm'-hconf P) P*

**by**(*rule jmm.heap-conf-read-heap-read-typed*)

**interpretation** *jmm'*: *heap-conf-read*

*addr2thread-id thread-id2addr*

*jmm-spurious-wakeups*

*jmm-empty jmm-allocate jmm-typeof-addr jmm.heap-read-typed P jmm-heap-write jmm'-hconf P*  
*P*

**for** *P*

**by**(*rule jmm'-heap-conf-read*)

**interpretation** *jmm'*: *heap-typesafe*

*addr2thread-id thread-id2addr*

*jmm-spurious-wakeups*

*jmm-empty jmm-allocate jmm-typeof-addr jmm.heap-read-typed P jmm-heap-write jmm'-hconf P*  
*P*

**for** *P*

..

### 8.16.7 Locale *allocated-heap*

**lemma** *jmm-allocated-heap*:

*allocated-heap addr2thread-id thread-id2addr jmm-empty jmm-allocate jmm-typeof-addr jmm-heap-write*  
*jmm-allocated P*

**proof**

**show** *jmm-allocated jmm-empty* = {} **by**(*auto simp add: jmm-heap-ops-defs*)

**next**

**fix** *h' a h hT*

**assume**  $(h', a) \in \text{jmm-allocate } h \ hT$

**thus** *jmm-allocated h' = insert a (jmm-allocated h)  $\wedge$  a  $\notin$  jmm-allocated h*

**by**(*auto simp add: jmm-heap-ops-defs split: if-split-asm*)

**next**

**fix** *h a al v h'*

**assume** *jmm-heap-write h a al v h'*

**thus** *jmm-allocated h' = jmm-allocated h* **by cases simp**

**qed**

**interpretation** *jmm: allocated-heap*

*addr2thread-id thread-id2addr*

*jmm-spurious-wakeups*

*jmm-empty jmm-allocate jmm-typeof-addr jmm-heap-read jmm-heap-write*

*jmm-allocated*

*P*

**for** *P*

**by**(*rule jmm-allocated-heap*)

**lemmas** *jmm'-allocated-heap = jmm-allocated-heap*

**interpretation** *jmm': allocated-heap*

*addr2thread-id thread-id2addr*

*jmm-spurious-wakeups*

*jmm-empty jmm-allocate jmm-typeof-addr jmm.heap-read-typed P jmm-heap-write*

*jmm-allocated*

*P*

**for** *P*

**by**(*rule jmm'-allocated-heap*)

### 8.16.8 Syntax translations

**notation** *jmm'.external-WT' (-,  $\vdash$  jmm'' ( $\dashv$ '(-')) : - [50,0,0,0,50] 60)*

**abbreviation** *jmm'-red-external* ::

*'m prog  $\Rightarrow$  'addr thread-id  $\Rightarrow$  'addr JMM-heap  $\Rightarrow$  'addr  $\Rightarrow$  mname  $\Rightarrow$  'addr val list*

*$\Rightarrow$  ('addr :: addr, 'addr thread-id, 'addr JMM-heap) external-thread-action*

*$\Rightarrow$  'addr extCallRet  $\Rightarrow$  'addr JMM-heap  $\Rightarrow$  bool*

**where** *jmm'-red-external P  $\equiv$  jmm'.red-external (TYPE('m)) P P*

**abbreviation** *jmm'-red-external-syntax* ::

*'m prog  $\Rightarrow$  'addr thread-id  $\Rightarrow$  'addr  $\Rightarrow$  mname  $\Rightarrow$  'addr val list  $\Rightarrow$  'addr JMM-heap*

*$\Rightarrow$  ('addr :: addr, 'addr thread-id, 'addr JMM-heap) external-thread-action*

*$\Rightarrow$  'addr extCallRet  $\Rightarrow$  'addr JMM-heap  $\Rightarrow$  bool*

*(-,  $\vdash$  jmm'' ( $\dashv$ '(-')), /-)  $\dashv$  ext ( $\dashv$ '(-')) [50, 0, 0, 0, 0, 0, 0, 0, 0] 51)*

**where**

*P, t  $\vdash$  jmm'  $\langle a \cdot M(vs), h \rangle$   $\dashv$  ta  $\rightarrow$  ext  $\langle va, h' \rangle \equiv$  jmm'-red-external P t h a M vs ta va h'*

**abbreviation** *jmm'-red-external-aggr* ::

'm prog  $\Rightarrow$  'addr thread-id  $\Rightarrow$  'addr  $\Rightarrow$  mname  $\Rightarrow$  'addr val list  $\Rightarrow$  'addr JMM-heap  
 $\Rightarrow$  (('addr :: addr, 'addr thread-id, 'addr JMM-heap) external-thread-action  $\times$  'addr extCallRet  $\times$  'addr JMM-heap) set

**where** *jmm'-red-external-aggr* *P*  $\equiv$  *jmm'.red-external-aggr* *TYPE('m)* *P* *P*

**abbreviation** *jmm'-heap-copy-loc* ::

'm prog  $\Rightarrow$  'addr  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr JMM-heap  
 $\Rightarrow$  ('addr :: addr, 'addr thread-id) obs-event list  $\Rightarrow$  'addr JMM-heap  $\Rightarrow$  bool

**where** *jmm'-heap-copy-loc*  $\equiv$  *jmm'.heap-copy-loc* *TYPE('m)*

**abbreviation** *jmm'-heap-copies* ::

'm prog  $\Rightarrow$  'addr  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc list  $\Rightarrow$  'addr JMM-heap  
 $\Rightarrow$  ('addr :: addr, 'addr thread-id) obs-event list  $\Rightarrow$  'addr JMM-heap  $\Rightarrow$  bool

**where** *jmm'-heap-copies*  $\equiv$  *jmm'.heap-copies* *TYPE('m)*

**abbreviation** *jmm'-heap-clone* ::

'm prog  $\Rightarrow$  'addr JMM-heap  $\Rightarrow$  'addr  $\Rightarrow$  'addr JMM-heap  
 $\Rightarrow$  (('addr :: addr, 'addr thread-id) obs-event list  $\times$  'addr) option  $\Rightarrow$  bool

**where** *jmm'-heap-clone* *P*  $\equiv$  *jmm'.heap-clone* *TYPE('m)* *P* *P*

**end**

## 8.17 Compiler correctness for the JMM

**theory** *JMM-Compiler* **imports**

*JMM-J*

*JMM-JVM*

*../Compiler/Correctness*

*../Framework/FWBisimLift*

**begin**

**lemma** *action-loc-aux-compP* [*simp*]: *action-loc-aux* (*compP* *f* *P*) = *action-loc-aux* *P*  
**by**(*auto* 4 4 *elim!*: *action-loc-aux-cases*)

**lemma** *action-loc-compP*: *action-loc* (*compP* *f* *P*) = *action-loc* *P*  
**by** *simp*

**lemma** *is-volatile-compP* [*simp*]: *is-volatile* (*compP* *f* *P*) = *is-volatile* *P*

**proof**(*rule ext*)

**fix** *hT*

**show** *is-volatile* (*compP* *f* *P*) *hT* = *is-volatile* *P* *hT*

**by**(*cases* *hT*) *simp-all*

**qed**

**lemma** *saction-compP* [*simp*]: *saction* (*compP* *f* *P*) = *saction* *P*  
**by**(*simp* *add*: *saction.simps* *fun-eq-iff*)

**lemma** *sactions-compP* [*simp*]: *sactions* (*compP* *f* *P*) = *sactions* *P*  
**by**(*rule ext*)(*simp* *only*: *sactions-def*, *simp*)

**lemma** *addr-locs-compP* [*simp*]: *addr-locs* (*compP* *f* *P*) = *addr-locs* *P*



**by**(*rule ext*)(*case-tac x, simp-all*)

**lemma** *synchronizes-with-compP* [*simp*]: *synchronizes-with* (*compP f P*) = *synchronizes-with P*  
**by**(*simp add: synchronizes-with.simps fun-eq-iff*)

**lemma** *sync-order-compP* [*simp*]: *sync-order* (*compP f P*) = *sync-order P*  
**by**(*simp add: sync-order-def fun-eq-iff*)

**lemma** *sync-with-compP* [*simp*]: *sync-with* (*compP f P*) = *sync-with P*  
**by**(*simp add: sync-with-def fun-eq-iff*)

**lemma** *po-sw-compP* [*simp*]: *po-sw* (*compP f P*) = *po-sw P*  
**by**(*simp add: po-sw-def fun-eq-iff*)

**lemma** *happens-before-compP*: *happens-before* (*compP f P*) = *happens-before P*  
**by** *simp*

**lemma** *addr-loc-default-compP* [*simp*]: *addr-loc-default* (*compP f P*) = *addr-loc-default P*  
**proof**(*intro ext*)  
**fix** *hT al*  
**show** *addr-loc-default* (*compP f P*) *hT al* = *addr-loc-default P hT al*  
**by**(*cases (P, hT, al) rule: addr-loc-default.cases*) *simp-all*  
**qed**

**lemma** *value-written-aux-compP* [*simp*]: *value-written-aux* (*compP f P*) = *value-written-aux P*  
**proof**(*intro ext*)  
**fix** *a al*  
**show** *value-written-aux* (*compP f P*) *a al* = *value-written-aux P a al*  
**by**(*cases (P, a, al) rule: value-written-aux.cases*)(*simp-all add: value-written-aux.simps*)  
**qed**

**lemma** *value-written-compP* [*simp*]: *value-written* (*compP f P*) = *value-written P*  
**by**(*simp add: fun-eq-iff value-written.simps*)

**lemma** *is-write-seen-compP* [*simp*]: *is-write-seen* (*compP f P*) = *is-write-seen P*  
**by**(*simp add: fun-eq-iff is-write-seen-def*)

**lemma** *justification-well-formed-compP* [*simp*]:  
*justification-well-formed* (*compP f P*) = *justification-well-formed P*  
**by**(*simp add: fun-eq-iff justification-well-formed-def*)

**lemma** *happens-before-committed-compP* [*simp*]:  
*happens-before-committed* (*compP f P*) = *happens-before-committed P*  
**by**(*simp add: fun-eq-iff happens-before-committed-def*)

**lemma** *happens-before-committed-weak-compP* [*simp*]:  
*happens-before-committed-weak* (*compP f P*) = *happens-before-committed-weak P*  
**by**(*simp add: fun-eq-iff happens-before-committed-weak-def*)

**lemma** *sync-order-committed-compP* [*simp*]:  
*sync-order-committed* (*compP f P*) = *sync-order-committed P*  
**by**(*simp add: fun-eq-iff sync-order-committed-def*)

**lemma** *value-written-committed-compP* [*simp*]:

*value-written-committed* (*compP f P*) = *value-written-committed P*  
**by**(*simp add: fun-eq-iff value-written-committed-def*)

**lemma** *uncommitted-reads-see-hb-compP* [*simp*]:  
*uncommitted-reads-see-hb* (*compP f P*) = *uncommitted-reads-see-hb P*  
**by**(*simp add: fun-eq-iff uncommitted-reads-see-hb-def*)

**lemma** *external-actions-committed-compP* [*simp*]:  
*external-actions-committed* (*compP f P*) = *external-actions-committed P*  
**by**(*simp add: fun-eq-iff external-actions-committed-def*)

**lemma** *is-justified-by-compP* [*simp*]: *is-justified-by* (*compP f P*) = *is-justified-by P*  
**by**(*simp add: fun-eq-iff is-justified-by.simps*)

**lemma** *is-weakly-justified-by-compP* [*simp*]: *is-weakly-justified-by* (*compP f P*) = *is-weakly-justified-by P*  
**by**(*simp add: fun-eq-iff is-weakly-justified-by.simps*)

**lemma** *legal-execution-compP*: *legal-execution* (*compP f P*) = *legal-execution P*  
**by**(*simp add: fun-eq-iff gen-legal-execution.simps*)

**lemma** *weakly-legal-execution-compP*: *weakly-legal-execution* (*compP f P*) = *weakly-legal-execution P*  
**by**(*simp add: fun-eq-iff gen-legal-execution.simps*)

**lemma** *most-recent-write-for-compP* [*simp*]:  
*most-recent-write-for* (*compP f P*) = *most-recent-write-for P*  
**by**(*simp add: fun-eq-iff most-recent-write-for.simps*)

**lemma** *sequentially-consistent-compP* [*simp*]:  
*sequentially-consistent* (*compP f P*) = *sequentially-consistent P*  
**by**(*simp add: sequentially-consistent-def split-beta*)

**lemma** *conflict-compP* [*simp*]: *non-volatile-conflict* (*compP f P*) = *non-volatile-conflict P*  
**by**(*simp add: fun-eq-iff non-volatile-conflict-def*)

**lemma** *correctly-synchronized-compP* [*simp*]:  
*correctly-synchronized* (*compP f P*) = *correctly-synchronized P*  
**by**(*simp add: fun-eq-iff correctly-synchronized-def*)

**lemma** (**in** *heap-base*) *heap-read-typed-compP* [*simp*]:  
*heap-read-typed* (*compP f P*) = *heap-read-typed P*  
**by**(*intro ext*)(*simp add: heap-read-typed-def*)

**context** *J-JVM-heap-conf-base* **begin**

**definition** *if-bisimJ2JVM* ::

((('addr,'thread-id,status × 'addr expr × 'addr locals,'heap,'addr) state,  
('addr,'thread-id,status × 'addr option × 'addr frame list,'heap,'addr) state) *bisim*

**where**

*if-bisimJ2JVM* =

*FWbisimulation-base.mbisim red-red0.init-fin-bisim red-red0.init-fin-bisim-wait*  $\circ_B$

*FWbisimulation-base.mbisim red0-Red1'.init-fin-bisim red0-Red1'.init-fin-bisim-wait*  $\circ_B$

*if-mbisim-Red1'-Red1*  $\circ_B$

*FWbisimulation-base.mbisim Red1-execd.init-fin-bisim Red1-execd.init-fin-bisim-wait*

**definition** *if-tlsimJ2JVM* ::

$(\text{'thread-id} \times (\text{'addr}, \text{'thread-id}, \text{status} \times \text{'addr expr} \times \text{'addr locals},$   
 $\text{'heap}, \text{'addr}, (\text{'addr}, \text{'thread-id}) \text{obs-event action}) \text{thread-action},$   
 $\text{'thread-id} \times (\text{'addr}, \text{'thread-id}, \text{status} \times \text{'addr jvm-thread-state},$   
 $\text{'heap}, \text{'addr}, (\text{'addr}, \text{'thread-id}) \text{obs-event action}) \text{thread-action}) \text{bisim}$

**where**

*if-tlsimJ2JVM* =  
 $\text{FWbisimulation-base.mta-bisim red-red0.init-fin-bisim} \circ_B$   
 $\text{FWbisimulation-base.mta-bisim red0-Red1'.init-fin-bisim} \circ_B (=) \circ_B$   
 $\text{FWbisimulation-base.mta-bisim Red1-execd.init-fin-bisim}$

**end**

**sublocale** *J-JVM-conf-read* < *red-mthr*: *if- $\tau$ multithreaded-wf final-expr mred P convert-RA  $\tau$ MOVE*  
*P*

**by**(*unfold-locales*)

**sublocale** *J-JVM-conf-read* < *execd-mthr*:

*if- $\tau$ multithreaded-wf*  
*JVM-final*  
*mexecd (compP2 (compP1 P))*  
*convert-RA*  
 *$\tau$ MOVE2 (compP2 (compP1 P))*

**by**(*unfold-locales*)

**context** *J-JVM-conf-read* **begin**

**theorem** *if-bisimJ2JVM-weak-bisim*:

**assumes** *wf*: *wf-J-prog P*

**shows** *delay-bisimulation-diverge-final*

$(\text{red-mthr.mthr.if.redT } P) (\text{execd-mthr.mthr.if.redT } (J2JVM P)) \text{if-bisimJ2JVM if-tlsimJ2JVM}$   
 $\text{red-mthr.if.m}\tau\text{move execd-mthr.if.m}\tau\text{move red-mthr.mthr.if.mfinal execd-mthr.mthr.if.mfinal}$

**apply** (*simp only: if-bisimJ2JVM-def if-tlsimJ2JVM-def J2JVM-def o-apply*)

**apply**(*rule delay-bisimulation-diverge-final-compose*)

**apply**(*rule FWdelay-bisimulation-diverge.mthr-delay-bisimulation-diverge-final*)

**apply**(*rule FWdelay-bisimulation-diverge.init-fin-FWdelay-bisimulation-diverge*)

**apply**(*rule red-red0-FWbisim[OF wf-prog-wwf-prog[OF wf]]*)

**apply**(*rule delay-bisimulation-diverge-final-compose*)

**apply**(*rule FWdelay-bisimulation-diverge.mthr-delay-bisimulation-diverge-final*)

**apply**(*rule FWdelay-bisimulation-diverge.init-fin-FWdelay-bisimulation-diverge*)

**apply**(*rule red0-Red1'-FWweak-bisim[OF wf]*)

**apply**(*rule delay-bisimulation-diverge-final-compose*)

**apply**(*rule delay-bisimulation-diverge-final.intro*)

**apply**(*rule bisimulation-into-delay.delay-bisimulation*)

**apply**(*rule if-Red1'-Red1-bisim-into-weak[OF compP1-pres-wf[OF wf]]*)

**apply**(*rule bisimulation-final.delay-bisimulation-final-base*)

**apply**(*rule if-Red1'-Red1-bisimulation-final[OF compP1-pres-wf[OF wf]]*)

**apply**(*rule FWdelay-bisimulation-diverge.mthr-delay-bisimulation-diverge-final*)

**apply**(*rule FWdelay-bisimulation-diverge.init-fin-FWdelay-bisimulation-diverge*)

**apply**(*rule Red1-exec1-FWwbisim[OF compP1-pres-wf[OF wf]]*)

**done**

**lemma** *if-bisimJ2JVM-start*:

```

assumes wf: wf-J-prog P
and wf-start: wf-start-state P C M vs
shows if-bisimJ2JVM (init-fin-lift-state Running (J-start-state P C M vs))
    (init-fin-lift-state Running (JVM-start-state (J2JVM P) C M vs))

using assms
unfolding if-bisimJ2JVM-def J2JVM-def o-apply
apply(intro bisim-composeI)
  apply(rule FWbisimulation-base.init-fin-lift-state-mbisimI)
  apply(erule (1) bisim-J-J0-start[OF wf-prog-wwf-prog])
  apply(rule FWbisimulation-base.init-fin-lift-state-mbisimI)
  apply(erule (1) bisim-J0-J1-start)
  apply(erule if-bisim-J1-J1-start[OF compP1-pres-wf])
apply simp
apply(rule FWbisimulation-base.init-fin-lift-state-mbisimI)
apply(erule bisim-J1-JVM-start[OF compP1-pres-wf])
apply simp
done

lemma red-Runs-eq-mexecd-Runs:
  fixes C M vs
  defines s: s  $\equiv$  init-fin-lift-state Running (J-start-state P C M vs)
  and comps: cs  $\equiv$  init-fin-lift-state Running (JVM-start-state (J2JVM P) C M vs)
  assumes wf: wf-J-prog P
  and wf-start: wf-start-state P C M vs
  shows red-mthr.mthr.if. $\mathcal{E}$  P s = execd-mthr.mthr.if. $\mathcal{E}$  (J2JVM P) cs
proof –
  from wf wf-start have bisim: if-bisimJ2JVM s cs
    unfolding s comps by(rule if-bisimJ2JVM-start)

  interpret divfin: delay-bisimulation-diverge-final
    red-mthr.mthr.if.redT P
    execd-mthr.mthr.if.redT (J2JVM P)
    if-bisimJ2JVM
    if-tlsimJ2JVM
    red-mthr.if.m $\tau$ move
    execd-mthr.if.m $\tau$ move
    red-mthr.mthr.if.mfinal
    execd-mthr.mthr.if.mfinal
    using wf by(rule if-bisimJ2JVM-weak-bisim)

  show ?thesis (is ?lhs = ?rhs)
proof(intro equalityI subsetI)
  fix E
  assume E  $\in$  ?lhs
  then obtain E' where E: E = lconcat (lmap ( $\lambda(t, ta).$  llist-of (map (Pair t)  $\{ta\}_o$ )) (llist-of-tlhist E'))
    and E': red-mthr.if.mthr. $\tau$ Runs s E'
    unfolding red-mthr.if. $\mathcal{E}$ -conv-Runs by blast
  from divfin.simulation- $\tau$ Runs1[OF bisim E']
  obtain E'' where E'': execd-mthr.if.mthr. $\tau$ Runs cs E''
    and tlsim: tlhist-all2 if-tlsimJ2JVM (option.rel-option if-bisimJ2JVM) E' E''
    unfolding J2JVM-def o-apply by blast
  let ?E = lconcat (lmap ( $\lambda(t, ta).$  llist-of (map (Pair t)  $\{ta\}_o$ )) (llist-of-tlhist E'))
  from tlsim have llist-all2 if-tlsimJ2JVM (llist-of-tlhist E') (llist-of-tlhist E'')

```



```

and wf-start: wf-start-state  $P\ C\ M\ vs$ 
shows correctly-synchronized  $(J2JVM\ P)\ (JVMd\mathcal{E}\ (J2JVM\ P)\ C\ M\ vs\ Running) \longleftrightarrow$ 
      correctly-synchronized  $P\ (J\mathcal{E}\ P\ C\ M\ vs\ Running)$ 
by(simp only: red- $\mathcal{E}$ -eq-mexecd- $\mathcal{E}$ [OF assms] J2JVM-def o-apply compP1-def compP2-def correctly-synchronized-com)
end

end

```

## 8.18 JMM heap implementation 2

```

theory JMM-Type2
imports
  ../Common/ExternalCallWF
  ../Common/ConformThreaded
  JMM-Heap
begin

```

### 8.18.1 Definitions

**datatype** *addr* = *Address htype nat* — heap type and sequence number

```

lemma rec-addr-conv-case-addr [simp]: rec-addr = case-addr
by(auto intro!: ext split: addr.split)

```

**instantiation** *addr* :: *addr* **begin**

**definition** *hash-addr* (*a* :: *addr*) = (case *a* of *Address ht n*  $\Rightarrow$  *int n*)

**definition** *monitor-funfun-to-list* (*ls* :: *addr*  $\Rightarrow$  *f nat*) = (*SOME xs. set xs* = {*x. finfun-dom ls* \$ *x* })

**instance**

**proof**

**fix** *ls* :: *addr*  $\Rightarrow$  *f nat*

**show** *set (monitor-funfun-to-list ls)* = *Collect (( $\$$ ) (finfun-dom ls))*

**unfolding** *monitor-funfun-to-list-addr-def*

**using** *finite-list*[OF *finite-funfun-dom*, **where** *?f.1* = *ls*]

**by**(rule *someI-ex*)

**qed**

**end**

**primrec** *the-Address* :: *addr*  $\Rightarrow$  *htype*  $\times$  *nat*

**where** *the-Address* (*Address hT n*) = (*hT*, *n*)

The JMM heap only stores which sequence numbers of a given *htype* have already been allocated.

**type-synonym** *JMM-heap* = *htype*  $\Rightarrow$  *nat set*

**translations** (*type*) *JMM-heap* <= (*type*) *htype*  $\Rightarrow$  *nat set*

**definition** *jmm-allocate* :: *JMM-heap*  $\Rightarrow$  *htype*  $\Rightarrow$  (*JMM-heap*  $\times$  *addr*) *set*

**where** *jmm-allocate* *h hT* = (let *hhT* = *h hT* in ( $\lambda n. (h(hT := insert\ n\ hhT),\ Address\ hT\ n)$ ) ‘ (*h hT*))

**abbreviation** *jmm-empty* :: *JMM-heap* **where** *jmm-empty* == ( $\lambda -. \{\}$ )

**definition** *jmm-typeof-addr* :: '*m prog*  $\Rightarrow$  *JMM-heap*  $\Rightarrow$  *addr*  $\rightarrow$  *htype*

where  $jmm\text{-}typeof\text{-}addr\ P\ h = (\lambda hT. \text{ if is-htype } P\ hT \text{ then Some } hT \text{ else None}) \circ \text{fst} \circ \text{the-Address}$

**definition**  $jmm\text{-}typeof\text{-}addr' :: 'm\ \text{prog} \Rightarrow \text{addr} \rightarrow \text{htype}$

where  $jmm\text{-}typeof\text{-}addr' P = (\lambda hT. \text{ if is-htype } P\ hT \text{ then Some } hT \text{ else None}) \circ \text{fst} \circ \text{the-Address}$

**lemma**  $jmm\text{-}typeof\text{-}addr'\text{-}conv\text{-}jmm\text{-}typeof\text{-}addr: jmm\text{-}typeof\text{-}addr' P = jmm\text{-}typeof\text{-}addr P\ h$   
**by**(*simp add: jmm-typeof-addr-def jmm-typeof-addr'-def*)

**lemma**  $jmm\text{-}typeof\text{-}addr'\text{-}conv\text{-}jmm\text{-}typeof\text{-}addr: (\lambda -. jmm\text{-}typeof\text{-}addr' P) = jmm\text{-}typeof\text{-}addr P$   
**by**(*simp add: jmm-typeof-addr-def jmm-typeof-addr'-def fun-eq-iff*)

**lemma**  $jmm\text{-}typeof\text{-}addr\text{-}conv\text{-}jmm\text{-}typeof\text{-}addr': jmm\text{-}typeof\text{-}addr = (\lambda P -. jmm\text{-}typeof\text{-}addr' P)$   
**by**(*simp add: jmm-typeof-addr'-conv-jmm-typeof-addr*)

**definition**  $jmm\text{-}heap\text{-}read :: JMM\text{-}heap \Rightarrow \text{addr} \Rightarrow \text{addr-loc} \Rightarrow \text{addr val} \Rightarrow \text{bool}$   
 where  $jmm\text{-}heap\text{-}read\ h\ a\ ad\ v = \text{True}$

**context**

**notes**  $[[\text{inductive-internals}]]$

**begin**

**inductive**  $jmm\text{-}heap\text{-}write :: JMM\text{-}heap \Rightarrow \text{addr} \Rightarrow \text{addr-loc} \Rightarrow \text{addr val} \Rightarrow JMM\text{-}heap \Rightarrow \text{bool}$   
 where  $jmm\text{-}heap\text{-}write\ h\ a\ ad\ v\ h$

**end**

**definition**  $jmm\text{-}hconf :: JMM\text{-}heap \Rightarrow \text{bool}$   
 where  $jmm\text{-}hconf\ h \longleftrightarrow \text{True}$

**definition**  $jmm\text{-}allocated :: JMM\text{-}heap \Rightarrow \text{addr set}$   
 where  $jmm\text{-}allocated\ h = \{\text{Address } CTn\ n \mid CTn\ n. n \in h\ CTn\}$

**definition**  $jmm\text{-}spurious\text{-}wakeups :: \text{bool}$   
 where  $jmm\text{-}spurious\text{-}wakeups = \text{True}$

**lemmas**  $jmm\text{-}heap\text{-}ops\text{-}defs =$   
 $jmm\text{-}allocate\text{-}def\ jmm\text{-}typeof\text{-}addr\text{-}def$   
 $jmm\text{-}heap\text{-}read\text{-}def\ jmm\text{-}heap\text{-}write\text{-}def$   
 $jmm\text{-}allocated\text{-}def\ jmm\text{-}spurious\text{-}wakeups\text{-}def$

**type-synonym**  $thread\text{-}id = \text{addr}$

**abbreviation** (*input*)  $addr2thread\text{-}id :: \text{addr} \Rightarrow \text{thread-id}$   
 where  $addr2thread\text{-}id \equiv \lambda x. x$

**abbreviation** (*input*)  $thread\text{-}id2addr :: \text{thread-id} \Rightarrow \text{addr}$   
 where  $thread\text{-}id2addr \equiv \lambda x. x$

**interpretation**  $jmm: \text{heap-base}$   
 $addr2thread\text{-}id\ thread\text{-}id2addr$   
 $jmm\text{-}spurious\text{-}wakeups$   
 $jmm\text{-}empty\ jmm\text{-}allocate\ jmm\text{-}typeof\text{-}addr\ P\ jmm\text{-}heap\text{-}read\ jmm\text{-}heap\text{-}write$   
**for**  $P$

.

**abbreviation**  $jmm\text{-}hext :: 'm \text{ prog} \Rightarrow JMM\text{-}heap \Rightarrow JMM\text{-}heap \Rightarrow bool$   $(- \vdash - \sqsubseteq_{jmm} - [51, 51, 51] 50)$   
**where**  $jmm\text{-}hext \equiv jmm.hext \text{ TYPE}('m)$

**abbreviation**  $jmm\text{-}conf :: 'm \text{ prog} \Rightarrow JMM\text{-}heap \Rightarrow addr \text{ val} \Rightarrow ty \Rightarrow bool$   
 $(-, - \vdash_{jmm} - : \leq - [51, 51, 51, 51] 50)$   
**where**  $jmm\text{-}conf P \equiv jmm.conf \text{ TYPE}('m) P P$

**abbreviation**  $jmm\text{-}addr\text{-}loc\text{-}type :: 'm \text{ prog} \Rightarrow JMM\text{-}heap \Rightarrow addr \Rightarrow addr\text{-}loc \Rightarrow ty \Rightarrow bool$   
 $(-, - \vdash_{jmm} - @ - : - [50, 50, 50, 50, 50] 51)$   
**where**  $jmm\text{-}addr\text{-}loc\text{-}type P \equiv jmm.addr\text{-}loc\text{-}type \text{ TYPE}('m) P P$

**abbreviation**  $jmm\text{-}confs :: 'm \text{ prog} \Rightarrow JMM\text{-}heap \Rightarrow addr \text{ val list} \Rightarrow ty \text{ list} \Rightarrow bool$   
 $(-, - \vdash_{jmm} - [: \leq] - [51, 51, 51, 51] 50)$   
**where**  $jmm\text{-}confs P \equiv jmm.confs \text{ TYPE}('m) P P$

**abbreviation**  $jmm\text{-}tconf :: 'm \text{ prog} \Rightarrow JMM\text{-}heap \Rightarrow addr \Rightarrow bool$   $(-, - \vdash_{jmm} - \sqrt{t} [51, 51, 51] 50)$   
**where**  $jmm\text{-}tconf P \equiv jmm.tconf \text{ TYPE}('m) P P$

**interpretation**  $jmm$ : *allocated-heap-base*  
 $addr2thread\text{-}id \text{ thread}\text{-}id2addr$   
 $jmm\text{-}spurious\text{-}wakeups$   
 $jmm\text{-}empty \text{ jmm}\text{-}allocate \text{ jmm}\text{-}typeof\text{-}addr P \text{ jmm}\text{-}heap\text{-}read \text{ jmm}\text{-}heap\text{-}write$   
 $jmm\text{-}allocated$   
**for**  $P$   
 .

Now a variation of the JMM with a different read operation that permits to read only type-conformant values

**abbreviation**  $jmm\text{-}heap\text{-}read\text{-}typed :: 'm \text{ prog} \Rightarrow JMM\text{-}heap \Rightarrow addr \Rightarrow addr\text{-}loc \Rightarrow addr \text{ val} \Rightarrow bool$   
**where**  $jmm\text{-}heap\text{-}read\text{-}typed P \equiv jmm.heap\text{-}read\text{-}typed \text{ TYPE}('m) P P$

**interpretation**  $jmm'$ : *heap-base*  
 $addr2thread\text{-}id \text{ thread}\text{-}id2addr$   
 $jmm\text{-}spurious\text{-}wakeups$   
 $jmm\text{-}empty \text{ jmm}\text{-}allocate \text{ jmm}\text{-}typeof\text{-}addr P \text{ jmm}\text{-}heap\text{-}read\text{-}typed P \text{ jmm}\text{-}heap\text{-}write$   
**for**  $P$  .

**abbreviation**  $jmm'\text{-}hext :: 'm \text{ prog} \Rightarrow JMM\text{-}heap \Rightarrow JMM\text{-}heap \Rightarrow bool$   $(- \vdash - \sqsubseteq_{jmm''} - [51, 51, 51] 50)$   
**where**  $jmm'\text{-}hext \equiv jmm'.hext \text{ TYPE}('m)$

**abbreviation**  $jmm'\text{-}conf :: 'm \text{ prog} \Rightarrow JMM\text{-}heap \Rightarrow addr \text{ val} \Rightarrow ty \Rightarrow bool$   
 $(-, - \vdash_{jmm''} - : \leq - [51, 51, 51, 51] 50)$   
**where**  $jmm'\text{-}conf P \equiv jmm'.conf \text{ TYPE}('m) P P$

**abbreviation**  $jmm'\text{-}addr\text{-}loc\text{-}type :: 'm \text{ prog} \Rightarrow JMM\text{-}heap \Rightarrow addr \Rightarrow addr\text{-}loc \Rightarrow ty \Rightarrow bool$   
 $(-, - \vdash_{jmm''} - @ - : - [50, 50, 50, 50, 50] 51)$   
**where**  $jmm'\text{-}addr\text{-}loc\text{-}type P \equiv jmm'.addr\text{-}loc\text{-}type \text{ TYPE}('m) P P$

**abbreviation**  $jmm'\text{-}confs :: 'm \text{ prog} \Rightarrow JMM\text{-}heap \Rightarrow addr \text{ val list} \Rightarrow ty \text{ list} \Rightarrow bool$   
 $(-, - \vdash_{jmm''} - [: \leq] - [51, 51, 51, 51] 50)$   
**where**  $jmm'\text{-}confs P \equiv jmm'.confs \text{ TYPE}('m) P P$



**abbreviation**  $jmm'\text{-tconf} :: 'm \text{ prog} \Rightarrow JMM\text{-heap} \Rightarrow \text{addr} \Rightarrow \text{bool} \ (-, - \vdash jmm'' - \sqrt{t} [51, 51, 51] 50)$   
**where**  $jmm'\text{-tconf } P \equiv jmm'.\text{tconf } TYPE('m) P P$

## 8.18.2 Heap locale interpretations

### 8.18.3 Locale *heap*

**lemma**  $jmm\text{-heap}: \text{heap } \text{addr2thread-id } \text{thread-id2addr } jmm\text{-allocate } (jmm\text{-typeof-addr } P) jmm\text{-heap-write } P$

**proof**

```

fix  $h' a h hT$ 
assume  $(h', a) \in jmm\text{-allocate } h hT \text{ is-htype } P hT$ 
thus  $jmm\text{-typeof-addr } P h' a = \lfloor hT \rfloor$ 
by( $\text{auto simp add: } jmm\text{-heap-ops-defs}$ )
next
fix  $h hT h' a$ 
assume  $(h', a) \in jmm\text{-allocate } h hT$ 
thus  $P \vdash h \trianglelefteq_{jmm} h' \text{ by } (\text{auto simp add: } jmm\text{-heap-ops-defs intro: } jmm.\text{heapI})$ 
next
fix  $h a al v h'$ 
assume  $jmm\text{-heap-write } h a al v h'$ 
thus  $P \vdash h \trianglelefteq_{jmm} h' \text{ by cases auto}$ 
qed simp

```

**interpretation**  $jmm: \text{heap}$   
 $\text{addr2thread-id } \text{thread-id2addr}$   
 $jmm\text{-spurious-wakeups}$   
 $jmm\text{-empty } jmm\text{-allocate } jmm\text{-typeof-addr } P jmm\text{-heap-read } jmm\text{-heap-write } P$   
**for**  $P$   
**by**( $\text{rule } jmm\text{-heap}$ )

**declare**  $jmm.\text{typeof-addr-thread-id2-addr-addr2thread-id} [\text{simp del}]$

**lemmas**  $jmm'\text{-heap} = jmm\text{-heap}$

**interpretation**  $jmm': \text{heap}$   
 $\text{addr2thread-id } \text{thread-id2addr}$   
 $jmm\text{-spurious-wakeups}$   
 $jmm\text{-empty } jmm\text{-allocate } jmm\text{-typeof-addr } P jmm\text{-heap-read-typed } P jmm\text{-heap-write } P$   
**for**  $P$   
**by**( $\text{rule } jmm'\text{-heap}$ )

**declare**  $jmm'.\text{typeof-addr-thread-id2-addr-addr2thread-id} [\text{simp del}]$

**lemma**  $jmm\text{-heap-read-typed-default-val}:$   
 $\text{heap-base.heap-read-typed } \text{typeof-addr } jmm\text{-heap-read } P h a al$   
 $(\text{default-val } (THE T. \text{heap-base.addr-loc-type } \text{typeof-addr } P h a al T))$   
**by**( $\text{rule heap-base.heap-read-typedI}(\text{simp-all add: heap-base.THE-addr-loc-type } jmm\text{-heap-read-def heap-base.defval-conf})$ )

**lemma**  $jmm\text{-allocate-Eps}:$   
 $(\text{SOME } ha. ha \in jmm\text{-allocate } h hT) = (h', a')$   
 $\implies jmm\text{-allocate } h hT \neq \{\} \longrightarrow (h', a') \in jmm\text{-allocate } h hT$

**by**(*auto dest: jmm.allocate-Eps*)

**lemma** *jmm-allocate-eq-empty*:  $jmm\text{-}allocate\ h\ hT = \{\} \longleftrightarrow h\ hT = UNIV$   
**by**(*auto simp add: jmm-allocate-def*)

**lemma** *jmm-allocate-otherD*:  
 $(h', a) \in jmm\text{-}allocate\ h\ hT \implies \forall hT'. hT' \neq hT \longrightarrow h'\ hT' = h\ hT'$   
**by**(*auto simp add: jmm-allocate-def*)

**lemma** *jmm-start-heap-ok*: *jmm.start-heap-ok*  
**apply**(*simp add: jmm.start-heap-ok-def jmm.start-heap-data-def initialization-list-def sys-xcpts-list-def*  
*jmm.create-initial-object-simps*)  
**apply**(*split prod.split, clarify, clarsimp simp add: jmm.create-initial-object-simps jmm-allocate-eq-empty*  
*Thread-neq-sys-xcpts sys-xcpts-neqs dest!: jmm-allocate-Eps jmm-allocate-otherD*)  
**done**

#### 8.18.4 Locale *heap-conf*

**interpretation** *jmm*: *heap-conf-base*  
*addr2thread-id thread-id2addr*  
*jmm-spurious-wakeups*  
*jmm-empty jmm-allocate jmm-typeof-addr P jmm-heap-read jmm-heap-write jmm-hconf*  
*P*  
**for** *P* .

**abbreviation** (*input*) *jmm'-hconf* ::  $JMM\text{-}heap \Rightarrow bool$   
**where** *jmm'-hconf* == *jmm-hconf*

**interpretation** *jmm'*: *heap-conf-base*  
*addr2thread-id thread-id2addr*  
*jmm-spurious-wakeups*  
*jmm-empty jmm-allocate jmm-typeof-addr P jmm-heap-read-typed P jmm-heap-write jmm'-hconf*  
*P*  
**for** *P* .

**abbreviation** *jmm-heap-read-typeable* ::  $'m\ prog \Rightarrow bool$   
**where** *jmm-heap-read-typeable*  $P \equiv jmm.\text{heap-read-typeable}\ TYPE('m)\ P\ jmm\text{-}hconf\ P$

**abbreviation** *jmm'-heap-read-typeable* ::  $'m\ prog \Rightarrow bool$   
**where** *jmm'-heap-read-typeable*  $P \equiv jmm'.\text{heap-read-typeable}\ TYPE('m)\ P\ jmm\text{-}hconf\ P$

**lemma** *jmm-heap-read-typeable*: *jmm-heap-read-typeable* *P*  
**by**(*rule jmm.heap-read-typeableI*)(*simp add: jmm-heap-read-def*)

**lemma** *jmm'-heap-read-typeable*: *jmm'-heap-read-typeable* *P*  
**by**(*rule jmm'.heap-read-typeableI*)(*auto simp add: jmm-heap-read-def jmm.heap-read-typed-def dest:*  
*jmm'.addr-loc-type-fun*)

**lemma** *jmm-heap-conf*:  
*heap-conf addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr P) jmm-heap-write*  
*jmm-hconf P*  
**by**(*unfold-locales*)(*simp-all add: jmm-hconf-def jmm-heap-ops-defs split: if-split-asm*)

**interpretation** *jmm*: *heap-conf*

```

  addr2thread-id thread-id2addr
  jmm-spurious-wakeups
  jmm-empty jmm-allocate jmm-typeof-addr P jmm-heap-read jmm-heap-write jmm-hconf
  P
  for P
  by(rule jmm-heap-conf)

```

**lemmas**  $jmm'\text{-heap-conf} = jmm\text{-heap-conf}$

```

interpretation jmm': heap-conf
  addr2thread-id thread-id2addr
  jmm-spurious-wakeups
  jmm-empty jmm-allocate jmm-typeof-addr P jmm-heap-read-typed P jmm-heap-write jmm'-hconf
  P
  for P
  by(rule jmm'-heap-conf)

```

### 8.18.5 Locale *heap-progress*

```

lemma jmm-heap-progress:
  heap-progress addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr P) jmm-heap-read
  jmm-heap-write jmm-hconf P
proof
  fix h a al T
  assume jmm-hconf h
  and al: P,h ⊢ jmm a@al : T
  show ∃ v. jmm-heap-read h a al v ∧ P,h ⊢ jmm v :≤ T
  using jmm.defval-conf[of P P h T] unfolding jmm-heap-ops-defs by blast
next
  fix h a al T v
  assume P,h ⊢ jmm a@al : T
  show ∃ h'. jmm-heap-write h a al v h'
  by(auto intro: jmm-heap-write.intros)
qed

```

```

interpretation jmm: heap-progress
  addr2thread-id thread-id2addr
  jmm-spurious-wakeups
  jmm-empty jmm-allocate jmm-typeof-addr P jmm-heap-read jmm-heap-write jmm-hconf
  P
  for P
  by(rule jmm-heap-progress)

```

```

lemma jmm'-heap-progress:
  heap-progress addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr P) (jmm-heap-read-typed
  P) jmm-heap-write jmm'-hconf P
proof
  fix h a al T
  assume jmm'-hconf h
  and al: P,h ⊢ jmm' a@al : T
  thus ∃ v. jmm-heap-read-typed P h a al v ∧ P,h ⊢ jmm' v :≤ T
  unfolding jmm-heap-read-def jmm.heap-read-typed-def
  by(blast dest: jmm'.addr-loc-type-fun intro: jmm'.defval-conf)+
next

```

```

fix  $h\ a\ al\ T\ v$ 
assume  $P, h \vdash jmm'\ a@al : T$ 
  and  $P, h \vdash jmm'\ v : \leq T$ 
thus  $\exists h'.\ jmm\text{-heap-write}\ h\ a\ al\ v\ h'$ 
  by(auto intro: jmm-heap-write.intros)
qed

```

```

interpretation  $jmm'$ : heap-progress
  addr2thread-id thread-id2addr
  jmm-spurious-wakeups
  jmm-empty jmm-allocate jmm-typeof-addr P jmm-heap-read-typed P jmm-heap-write jmm'-hconf
   $P$ 
  for  $P$ 
by(rule jmm'-heap-progress)

```

### 8.18.6 Locale *heap-conf-read*

```

lemma  $jmm'\text{-heap-conf-read}$ :
  heap-conf-read addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr P) (jmm-heap-read-typed
P) jmm-heap-write jmm'-hconf P
by(rule jmm.heap-conf-read-heap-read-typed)

```

```

interpretation  $jmm'$ : heap-conf-read
  addr2thread-id thread-id2addr
  jmm-spurious-wakeups
  jmm-empty jmm-allocate jmm-typeof-addr P jmm-heap-read-typed P jmm-heap-write jmm'-hconf
   $P$ 
  for  $P$ 
by(rule jmm'-heap-conf-read)

```

```

interpretation  $jmm'$ : heap-typesafe
  addr2thread-id thread-id2addr
  jmm-spurious-wakeups
  jmm-empty jmm-allocate jmm-typeof-addr P jmm-heap-read-typed P jmm-heap-write jmm'-hconf
   $P$ 
  for  $P$ 
..

```

### 8.18.7 Locale *allocated-heap*

```

lemma  $jmm\text{-allocated-heap}$ :
  allocated-heap addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr P) jmm-heap-write
jmm-allocated P
proof
  show  $jmm\text{-allocated}\ jmm\text{-empty} = \{\}$  by(simp add: jmm-allocated-def)
next
  fix  $h'\ a\ h\ hT$ 
  assume  $(h', a) \in jmm\text{-allocate}\ h\ hT$ 
  thus  $jmm\text{-allocated}\ h' = insert\ a\ (jmm\text{-allocated}\ h) \wedge a \notin jmm\text{-allocated}\ h$ 
  by(auto simp add: jmm-heap-ops-defs split: if-split-asm)
next
  fix  $h\ a\ al\ v\ h'$ 
  assume  $jmm\text{-heap-write}\ h\ a\ al\ v\ h'$ 
  thus  $jmm\text{-allocated}\ h' = jmm\text{-allocated}\ h$  by cases simp

```

qed

**interpretation** *jmm*: *allocated-heap*  
*addr2thread-id thread-id2addr*  
*jmm-spurious-wakeups*  
*jmm-empty jmm-allocate jmm-typeof-addr P jmm-heap-read jmm-heap-write*  
*jmm-allocated*  
*P*  
**for** *P*  
**by**(*rule jmm-allocated-heap*)

**lemmas** *jmm'-allocated-heap = jmm-allocated-heap*

**interpretation** *jmm'*: *allocated-heap*  
*addr2thread-id thread-id2addr*  
*jmm-spurious-wakeups*  
*jmm-empty jmm-allocate jmm-typeof-addr P jmm-heap-read-typed P jmm-heap-write*  
*jmm-allocated*  
*P*  
**for** *P*  
**by**(*rule jmm'-allocated-heap*)

### 8.18.8 Syntax translations

**notation** *jmm'.external-WT'* ( $\cdot, \vdash jmm''$  ( $\dashv'(-')$ )) : - [50,0,0,0,50] 60)

**abbreviation** *jmm'-red-external* ::  
*'m prog  $\Rightarrow$  thread-id  $\Rightarrow$  JMM-heap  $\Rightarrow$  addr  $\Rightarrow$  mname  $\Rightarrow$  addr val list*  
 $\Rightarrow$  (*addr, thread-id, JMM-heap*) *external-thread-action*  
 $\Rightarrow$  *addr extCallRet  $\Rightarrow$  JMM-heap  $\Rightarrow$  bool*  
**where** *jmm'-red-external P  $\equiv$  jmm'.red-external (TYPE('m)) P P*

**abbreviation** *jmm'-red-external-syntax* ::  
*'m prog  $\Rightarrow$  thread-id  $\Rightarrow$  addr  $\Rightarrow$  mname  $\Rightarrow$  addr val list  $\Rightarrow$  JMM-heap*  
 $\Rightarrow$  (*addr, thread-id, JMM-heap*) *external-thread-action*  
 $\Rightarrow$  *addr extCallRet  $\Rightarrow$  JMM-heap  $\Rightarrow$  bool*  
 $(\cdot, \vdash jmm'' ((\dashv'(-')),/-)) \dashrightarrow \text{ext } ((\cdot,/-)) [50, 0, 0, 0, 0, 0, 0, 0, 0] 51)$   
**where**

$P, t \vdash jmm' \langle a \cdot M(vs), h \rangle \dashrightarrow \text{ext } \langle va, h' \rangle \equiv jmm'\text{-red-external } P \text{ } t \text{ } h \text{ } a \text{ } M \text{ } vs \text{ } ta \text{ } va \text{ } h'$

**abbreviation** *jmm'-red-external-aggr* ::  
*'m prog  $\Rightarrow$  thread-id  $\Rightarrow$  addr  $\Rightarrow$  mname  $\Rightarrow$  addr val list  $\Rightarrow$  JMM-heap*  
 $\Rightarrow$  ((*addr, thread-id, JMM-heap*) *external-thread-action*  $\times$  *addr extCallRet*  $\times$  *JMM-heap*) *set*  
**where** *jmm'-red-external-aggr P  $\equiv$  jmm'.red-external-aggr TYPE('m) P P*

**abbreviation** *jmm'-heap-copy-loc* ::  
*'m prog  $\Rightarrow$  addr  $\Rightarrow$  addr  $\Rightarrow$  addr-loc  $\Rightarrow$  JMM-heap*  
 $\Rightarrow$  (*addr, thread-id*) *obs-event list  $\Rightarrow$  JMM-heap  $\Rightarrow$  bool*  
**where** *jmm'-heap-copy-loc  $\equiv$  jmm'.heap-copy-loc TYPE('m)*

**abbreviation** *jmm'-heap-copies* ::  
*'m prog  $\Rightarrow$  addr  $\Rightarrow$  addr  $\Rightarrow$  addr-loc list  $\Rightarrow$  JMM-heap*  
 $\Rightarrow$  (*addr, thread-id*) *obs-event list  $\Rightarrow$  JMM-heap  $\Rightarrow$  bool*  
**where** *jmm'-heap-copies  $\equiv$  jmm'.heap-copies TYPE('m)*

**abbreviation** *jmm'-heap-clone* ::  
*'m prog*  $\Rightarrow$  *JMM-heap*  $\Rightarrow$  *addr*  $\Rightarrow$  *JMM-heap*  
 $\Rightarrow ((addr, thread-id) \text{ obs-event list} \times addr) \text{ option} \Rightarrow \text{bool}$   
**where** *jmm'-heap-clone* *P*  $\equiv$  *jmm'.heap-clone* *TYPE('m)* *P* *P*  
**end**

**theory** *JMM-Interp* **imports**

*JMM-Compiler*  
 $\dots/J/Deadlocked$   
 $\dots/BV/JVMDeadlocked$   
*JMM-Type2*  
*DRF-J*  
*DRF-JVM*

**begin**

**lemma** *jmm'-J-typesafe*:

*J-typesafe* *addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr P) (jmm-heap-read-typed P) jmm-heap-write jmm-hconf P*  
**by** *unfold-locales*

**lemma** *jmm'-JVM-typesafe*:

*JVM-typesafe* *addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr P) (jmm-heap-read-typed P) jmm-heap-write jmm-hconf P*  
**by** *unfold-locales*

**lemma** *jmm-typeof-addr-compP* [*simp*]:

*jmm-typeof-addr (compP f P) = jmm-typeof-addr P*  
**by**(*simp add: jmm-typeof-addr-def fun-eq-iff*)

**lemma** *compP2-compP1-consvs*:

*is-type (compP2 (compP1 P)) = is-type P*  
*is-class (compP2 (compP1 P)) = is-class P*  
*jmm'-addr-loc-type (compP2 (compP1 P)) = jmm'-addr-loc-type P*  
*jmm'-conf (compP2 (compP1 P)) = jmm'-conf P*  
**by**(*simp-all add: compP2-def heap-base.compP-conf heap-base.compP-addr-loc-type fun-eq-iff split: addr-loc.splits*)

**lemma** *jmm'-J-JVM-conf-read*:

*J-JVM-conf-read* *addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr P) (jmm-heap-read-typed P) jmm-heap-write jmm-hconf P*  
**apply**(*rule J-JVM-conf-read.intro*)  
**apply**(*rule J1-JVM-conf-read.intro*)  
**apply**(*rule JVM-conf-read.intro*)  
**prefer** 2  
**apply**(*rule JVM-heap-conf.intro*)  
**apply**(*rule JVM-heap-conf-base'.intro*)  
**apply**(*unfold compP2-def compP1-def compP-heap compP-heap-conf compP-heap-conf-read jmm-typeof-addr-comp*)  
**apply** *unfold-locales*  
**done**

**lemma** *jmm-J-allocated-progress*:

*J-allocated-progress* *addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr P)*

*jmm-heap-read jmm-heap-write jmm-hconf jmm-allocated P*  
**by** *unfold-locales*

**lemma** *jmm'-J-allocated-progress:*

*J-allocated-progress addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr P)*  
*(jmm-heap-read-typed P) jmm-heap-write jmm-hconf jmm-allocated P*  
**by**(*unfold-locales*)

**lemma** *jmm-JVM-allocated-progress:*

*JVM-allocated-progress addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr P)*  
*jmm-heap-read jmm-heap-write jmm-hconf jmm-allocated P*  
**by** *unfold-locales*

**lemma** *jmm'-JVM-allocated-progress:*

*JVM-allocated-progress addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr P)*  
*(jmm-heap-read-typed P) jmm-heap-write jmm-hconf jmm-allocated P*  
**by**(*unfold-locales*)

**end**

## 8.19 Specialize type safety for JMM heap implementation 2

**theory** *JMM-Typesafe2*

**imports**

*JMM-Type2*

*JMM-Common*

**begin**

**interpretation** *jmm: heap'*

*addr2thread-id thread-id2addr*

*jmm-spurious-wakeups*

*jmm-empty jmm-allocate jmm-typeof-addr' P jmm-heap-read jmm-heap-write*

**for** *P*

**by**(*rule heap'.intro*)(*unfold jmm-typeof-addr'-conv-jmm-typeof-addr, unfold-locales*)

**abbreviation** *jmm-addr-loc-type' :: 'm prog  $\Rightarrow$  addr  $\Rightarrow$  addr-loc  $\Rightarrow$  ty  $\Rightarrow$  bool (-  $\vdash$  jmm -@- : - [50, 50, 50] 51)*

**where** *jmm-addr-loc-type' P  $\equiv$  jmm.addr-loc-type TYPE('m) P P*

**lemma** *jmm-addr-loc-type-conv-jmm-addr-loc-type' [simp, heap-independent]:*

*jmm-addr-loc-type P h = jmm-addr-loc-type' P*

**by**(*metis jmm-typeof-addr'-conv-jmm-typeof-addr heap-base'.addr-loc-type-conv-addr-loc-type*)

**abbreviation** *jmm-conf' :: 'm prog  $\Rightarrow$  addr val  $\Rightarrow$  ty  $\Rightarrow$  bool (-  $\vdash$  jmm - : $\leq$  - [51,51,51] 50)*

**where** *jmm-conf' P  $\equiv$  jmm.conf TYPE('m) P P*

**lemma** *jmm-conf-conv-jmm-conf' [simp, heap-independent]:*

*jmm-conf P h = jmm-conf' P*

**by** (*metis jmm-typeof-addr'-conv-jmm-typeof-addr heap-base'.conf-conv-conf*)

**lemma** *jmm-heap'': heap'' addr2thread-id thread-id2addr jmm-allocate (jmm-typeof-addr' P) jmm-heap-write P*

**by**(*unfold-locales*)(*auto simp add: jmm-typeof-addr'-def jmm-allocate-def split: if-split-asm*)

**interpretation** *jmm*: heap''  
 addr2thread-id thread-id2addr  
 jmm-spurious-wakeups  
 jmm-empty jmm-allocate jmm-typeof-addr' P jmm-heap-read jmm-heap-write  
 for P  
 by(rule jmm-heap'')

**interpretation** *jmm'*: heap''  
 addr2thread-id thread-id2addr  
 jmm-spurious-wakeups  
 jmm-empty jmm-allocate jmm-typeof-addr' P jmm-heap-read-typed P jmm-heap-write  
 for P  
 by(rule jmm-heap'')

**abbreviation** *jmm-wf-start-state* :: 'm prog  $\Rightarrow$  cname  $\Rightarrow$  mname  $\Rightarrow$  addr val list  $\Rightarrow$  bool  
 where *jmm-wf-start-state* P  $\equiv$  *jmm.wf-start-state* TYPE('m) P P

**abbreviation** *if-heap-read-typed* ::  
 ('x  $\Rightarrow$  bool)  $\Rightarrow$  ('l, 't, 'x, 'heap, 'w, ('addr :: addr, 'thread-id) obs-event) semantics  
 $\Rightarrow$  ('addr  $\Rightarrow$  htype option)  
 $\Rightarrow$  'm prog  $\Rightarrow$  ('l, 't, status  $\times$  'x, 'heap, 'w, ('addr, 'thread-id) obs-event action) semantics

where

$\bigwedge$ final. *if-heap-read-typed* final r typeof-addr P t xh ta x'h'  $\equiv$   
 multithreaded-base.init-fin final r t xh ta x'h'  $\wedge$   
 ( $\forall$  ad al v T. NormalAction (ReadMem ad al v)  $\in$  set  $\{ta\}_o \longrightarrow$  heap-base'.addr-loc-type TYPE('heap)  
 typeof-addr P ad al T  $\longrightarrow$  heap-base'.conf TYPE('heap) typeof-addr P v T)

**lemma** *if-mthr-Runs-heap-read-typedI*:

**fixes** final **and** r :: ('addr, 't, 'x, 'heap, 'w, ('addr :: addr, 'thread-id) obs-event) semantics  
**assumes** trsys.Runs (multithreaded-base.redT (final-thread.init-fin-final final) (multithreaded-base.init-fin  
 final r) (map NormalAction  $\circ$  convert-RA)) s  $\xi$   
 (is trsys.Runs ?redT - -)  
**and**  $\bigwedge$ ad al v T.  $\llbracket$  NormalAction (ReadMem ad al v)  $\in$  lset (lconcat (lmap (llist-of  $\circ$  obs-a  $\circ$  snd)  
 $\xi$ ); heap-base'.addr-loc-type TYPE('heap) typeof-addr P ad al T  $\rrbracket \implies$  heap-base'.conf TYPE('heap)  
 typeof-addr P v T  
 (is  $\bigwedge$ ad al v T.  $\llbracket$  ?obs  $\xi$  ad al v; ?adal ad al T  $\rrbracket \implies$  ?conf v T)  
**shows** trsys.Runs (multithreaded-base.redT (final-thread.init-fin-final final) (if-heap-read-typed final  
 r typeof-addr P) (map NormalAction  $\circ$  convert-RA)) s  $\xi$   
 (is trsys.Runs ?redT' - -)

**using** *assms*

**proof**(coinduction arbitrary: s  $\xi$  rule: trsys.Runs.coinduct[consumes 1, case-names Runs, case-conclusion  
 Runs Stuck Step])

**case** (Runs s  $\xi$ )

**let** ?read =  $\lambda\xi$ . ( $\forall$  ad al v T. ?obs  $\xi$  ad al v  $\longrightarrow$  ?adal ad al T  $\longrightarrow$  ?conf v T)

**note** read = Runs(2)

**from** Runs(1) **show** ?case

**proof**(cases rule: trsys.Runs.cases[consumes 1, case-names Stuck Step])

**case** (Stuck S)

{ **fix** tta s'

**from**  $\neg$  ?redT S tta s' **have**  $\neg$  ?redT' S tta s'

**by**(rule contrapos-nn)(fastforce simp add: multithreaded-base.redT.simps) }

**hence** ?Stuck **using**  $\langle\xi = LNil\rangle$  **unfolding**  $\langle s = S\rangle$  **by** blast



```

  thus ?thesis ..
next
  case (Step S s' ttas tta)
  from ⟨ξ = LCons tta ttas⟩ read
  have read1:  $\bigwedge ad\ al\ v\ T. \llbracket NormalAction\ (ReadMem\ ad\ al\ v) \in set\ \{\text{snd}\ tta\}_o; ?adal\ ad\ al\ T \rrbracket$ 
 $\implies ?conf\ v\ T$ 
    and read2: ?read ttas by (auto simp add: o-def)
  from ⟨?redT S tta s'⟩ read1
  have ?redT' S tta s' by (fastforce simp add: multithreaded-base.redT.simps)
  hence ?Step using Step read2 ⟨s = S⟩ by blast
  thus ?thesis ..
qed
qed

```

**lemma** *if-mthr-Runs-heap-read-typedD*:

```

  fixes final and r :: ('addr, 't, 'x, 'heap, 'w, ('addr :: addr, 'thread-id) obs-event) semantics
  assumes Runs': trsys.Runs (multithreaded-base.redT (final-thread.init-fin-final final) (if-heap-read-typed
final r typeof-addr P) (map NormalAction ◦ convert-RA)) s ξ
  (is ?Runs' s ξ)
  and stuck:  $\bigwedge ttas\ s'\ tta\ s''. \llbracket$ 
    multithreaded-base.RedT (final-thread.init-fin-final final) (if-heap-read-typed final r typeof-addr P)
  (map NormalAction ◦ convert-RA) s ttas s';
    multithreaded-base.redT (final-thread.init-fin-final final) (multithreaded-base.init-fin final r) (map
NormalAction ◦ convert-RA) s' tta s''  $\rrbracket$ 
 $\implies \exists tta\ s''. multithreaded-base.redT\ (final-thread.init-fin-final\ final)\ (if-heap-read-typed\ final\ r\ typeof-addr\ P)\ (map\ NormalAction\ \circ\ convert-RA)\ s'\ tta\ s''$ 
  (is  $\bigwedge ttas\ s'\ tta\ s''. \llbracket ?RedT'\ s\ ttas\ s'; ?redT\ s'\ tta\ s'' \rrbracket \implies \exists tta\ s''. ?redT'\ s'\ tta\ s''$ )
  shows trsys.Runs (multithreaded-base.redT (final-thread.init-fin-final final) (multithreaded-base.init-fin
final r) (map NormalAction ◦ convert-RA)) s ξ
  (is ?Runs s ξ)

```

**proof** –

```

  define s' where s' = s
  with Runs' have  $\exists ttas. ?RedT'\ s\ ttas\ s' \wedge ?Runs'\ s'\ \xi$ 
  by (auto simp add: multithreaded-base.RedT-def o-def)
  thus ?Runs s' ξ
  proof (coinduct rule: trsys.Runs.coinduct[consumes 1, case-names Runs, case-conclusion Runs Stuck Step])
    case (Runs s' ξ)
    then obtain ttas where RedT': ?RedT' s ttas s'
    and Runs': ?Runs' s' ξ by blast
    from Runs' show ?case
  proof (cases rule: trsys.Runs.cases[consumes 1, case-names Stuck Step])
    case (Stuck S)
    have  $\bigwedge tta\ s''. \neg ?redT\ s'\ tta\ s''$ 
  proof
    fix tta s''
    assume ?redT s' tta s''
    from stuck[OF RedT' this]
    obtain tta s'' where ?redT' s' tta s'' by blast
    with Stuck(3)[of tta s''] show False
    unfolding ⟨s' = S⟩ by contradiction
  qed
  with Stuck(1–2) have ?Stuck by simp
  thus ?thesis by (rule disjI1)

```

```

next
  case (Step S s'' ξ' tta)
  note Step = Step(2-)[folded ⟨s' = S⟩]
  from ⟨?redT' s' tta s''⟩ have ?redT s' tta s''
    by(fastforce simp add: multithreaded-base.redT.simps)
  moreover from RedT' ⟨?redT' s' tta s''⟩
  have ?RedT' s (ttas @ [tta]) s''
    unfolding multithreaded-base.RedT-def by(rule rtrancl3p-step)
  ultimately have ?Step using ⟨ξ = LCons tta ξ'⟩ ⟨?Runs' s'' ξ'⟩ by blast
  thus ?thesis by(rule disjI2)
qed
qed
qed

```

**lemma** *heap-copy-loc-heap-read-typed*:

```

heap-base.heap-copy-loc (heap-base.heap-read-typed (λ- :: 'heap. typeof-addr) heap-read P) heap-write
a a' al h obs h' ⟷
  heap-base.heap-copy-loc heap-read heap-write a a' al h obs h' ∧
  (∀ ad al v T. ReadMem ad al v ∈ set obs ⟶ heap-base'.addr-loc-type TYPE('heap) typeof-addr P ad
al T ⟶ heap-base'.conf TYPE('heap) typeof-addr P v T)
by(auto elim!: heap-base.heap-copy-loc.cases intro!: heap-base.heap-copy-loc.intros dest: heap-base.heap-read-typed-i
heap-base.heap-read-typed-typed intro: heap-base.heap-read-typedI simp add: heap-base'.addr-loc-type-conv-addr-loc-t
heap-base'.conf-conv-conf)

```

**lemma** *heap-copies-heap-read-typed*:

```

heap-base.heap-copies (heap-base.heap-read-typed (λ- :: 'heap. typeof-addr) heap-read P) heap-write a
a' als h obs h' ⟷
  heap-base.heap-copies heap-read heap-write a a' als h obs h' ∧
  (∀ ad al v T. ReadMem ad al v ∈ set obs ⟶ heap-base'.addr-loc-type TYPE('heap) typeof-addr P ad
al T ⟶ heap-base'.conf TYPE('heap) typeof-addr P v T)
(is ?lhs ⟷ ?rhs)

```

**proof**

assume ?lhs thus ?rhs

```

by(induct rule: heap-base.heap-copies.induct[consumes 1])(auto intro!: heap-base.heap-copies.intros
simp add: heap-copy-loc-heap-read-typed)

```

**next**

assume ?rhs thus ?lhs

```

by(rule conjE)(induct rule: heap-base.heap-copies.induct[consumes 1], auto intro!: heap-base.heap-copies.intros
simp add: heap-copy-loc-heap-read-typed)

```

**qed**

**lemma** *heap-clone-heap-read-typed*:

```

heap-base.heap-clone allocate (λ- :: 'heap. typeof-addr) (heap-base.heap-read-typed (λ- :: 'heap. typeof-addr)
heap-read P) heap-write P a h h' obs ⟷
  heap-base.heap-clone allocate (λ- :: 'heap. typeof-addr) heap-read heap-write P a h h' obs ∧
  (∀ ad al v T obs' a'. obs = [(obs', a')] ⟶ ReadMem ad al v ∈ set obs' ⟶ heap-base'.addr-loc-type
TYPE('heap) typeof-addr P ad al T ⟶ heap-base'.conf TYPE('heap) typeof-addr P v T)
by(auto elim!: heap-base.heap-clone.cases intro: heap-base.heap-clone.intros simp add: heap-copies-heap-read-typed)

```

**lemma** *red-external-heap-read-typed*:

```

heap-base.red-external addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate (λ- ::
'heap. typeof-addr) (heap-base.heap-read-typed (λ- :: 'heap. typeof-addr) heap-read P) heap-write P t
h a M vs ta va h' ⟷
  heap-base.red-external addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate (λ- ::

```

'heap. typeof-addr) heap-read heap-write  $P \ t \ h \ a \ M \ vs \ ta \ va \ h' \wedge$   
 $(\forall ad \ al \ v \ T \ obs' \ a'. \text{ReadMem } ad \ al \ v \in \text{set } \{\{ta\}_o \longrightarrow \text{heap-base'.addr-loc-type } TYPE('heap)$   
 $\text{typeof-addr } P \ ad \ al \ T \longrightarrow \text{heap-base'.conf } TYPE('heap) \ \text{typeof-addr } P \ v \ T)$   
**by**(auto elim!: heap-base.red-external.cases intro: heap-base.red-external.intros simp add: heap-clone-heap-read-typed)

**lemma** red-external-aggr-heap-read-typed:

$(ta, va, h') \in \text{heap-base.red-external-aggr addr2thread-id thread-id2addr spurious-wakeups empty-heap}$   
 $\text{allocate } (\lambda- :: 'heap. \text{typeof-addr}) \ (\text{heap-base.heap-read-typed } (\lambda- :: 'heap. \text{typeof-addr}) \ \text{heap-read } P)$   
 $\text{heap-write } P \ t \ h \ a \ M \ vs \longleftrightarrow$

$(ta, va, h') \in \text{heap-base.red-external-aggr addr2thread-id thread-id2addr spurious-wakeups empty-heap}$   
 $\text{allocate } (\lambda- :: 'heap. \text{typeof-addr}) \ \text{heap-read heap-write } P \ t \ h \ a \ M \ vs \wedge$

$(\forall ad \ al \ v \ T \ obs' \ a'. \text{ReadMem } ad \ al \ v \in \text{set } \{\{ta\}_o \longrightarrow \text{heap-base'.addr-loc-type } TYPE('heap)$   
 $\text{typeof-addr } P \ ad \ al \ T \longrightarrow \text{heap-base'.conf } TYPE('heap) \ \text{typeof-addr } P \ v \ T)$

**by**(auto simp add: heap-base.red-external-aggr-def heap-clone-heap-read-typed split del: if-split split: if-split-asm)

**lemma** jmm'-heap-copy-locI:

$\exists obs \ h'. \text{heap-base.heap-copy-loc } (\text{heap-base.heap-read-typed } \text{typeof-addr } \text{jmm-heap-read } P) \ \text{jmm-heap-write}$   
 $a \ a' \ al \ h \ obs \ h'$

**by**(auto intro!: heap-base.heap-copy-loc.intros jmm-heap-read-typed-default-val intro: jmm-heap-write.intros)

**lemma** jmm'-heap-copiesI:

$\exists obs :: (\text{addr}, 'thread-id) \ \text{obs-event list.}$

$\exists h'. \text{heap-base.heap-copies } (\text{heap-base.heap-read-typed } \text{typeof-addr } \text{jmm-heap-read } P) \ \text{jmm-heap-write}$   
 $a \ a' \ als \ h \ obs \ h'$

**proof**(induction als arbitrary: h)

**case** Nil

**thus** ?case **by**(blast intro: heap-base.heap-copies.intros)

**next**

**case** (Cons al als)

**from** jmm'-heap-copy-locI[of typeof-addr P a a' al h]

**obtain** ob :: (addr, 'thread-id) obs-event list **and** h'

**where** heap-base.heap-copy-loc (heap-base.heap-read-typed typeof-addr jmm-heap-read P) jmm-heap-write  
 $a \ a' \ al \ h \ ob \ h'$

**by** blast

**with** Cons.IH[of h'] **show** ?case

**by**(auto 4 4 intro: heap-base.heap-copies.intros)

**qed**

**lemma** jmm'-heap-cloneI:

**fixes** obsa :: ((addr, 'thread-id) obs-event list  $\times$  addr) option

**assumes** heap-base.heap-clone allocate typeof-addr jmm-heap-read jmm-heap-write P h a h' obsa

**shows**  $\exists h'. \exists obsa :: ((addr, 'thread-id) \ \text{obs-event list} \times \text{addr}) \ \text{option.}$

$\text{heap-base.heap-clone allocate typeof-addr } (\text{heap-base.heap-read-typed } \text{typeof-addr } \text{jmm-heap-read}$   
 $P) \ \text{jmm-heap-write } P \ h \ a \ h' \ \text{obsa}$

**using** assms

**proof**(cases rule: heap-base.heap-clone.cases[consumes 1, case-names Fail Obj Arr])

**case** Fail

**thus** ?thesis **by**(blast intro: heap-base.heap-clone.intros)

**next**

**case** (Obj C h' a' FDTs obs h')

**with** jmm'-heap-copiesI[of typeof-addr P a a' map  $(\lambda((F, D), \text{Tfm}). \text{CFIELD } D \ F) \ \text{FDTs } h')$

```

show ?thesis by(blast intro: heap-base.heap-clone.intros)
next
  case (Arr T n h' a' FDTs obs h'')
  with jmm'-heap-copiesI[of typeof-addr P a a' map ( $\lambda((F, D), Tfm).$  CField D F) FDTs @ map
    ACell [0.. $n$ ]]
  show ?thesis by(blast intro: heap-base.heap-clone.intros)
qed

```

**lemma** jmm'-red-externalI:

```

 $\wedge$ final.
 $\llbracket$  heap-base.red-external addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr
jmm-heap-read jmm-heap-write P t h a M vs ta va h';
  final-thread.actions-ok final s t ta  $\rrbracket$ 
 $\implies \exists ta va h'. \text{heap-base.red-external addr2thread-id thread-id2addr spurious-wakeups empty-heap}$ 
 $\text{allocate typeof-addr (heap-base.heap-read-typed typeof-addr jmm-heap-read P) jmm-heap-write P t h a}$ 
 $M \text{ vs ta va } h' \wedge \text{final-thread.actions-ok final s t ta}$ 
proof(erule heap-base.red-external.cases, goal-cases)
  case 19
  thus ?case apply –
    apply(drule jmm'-heap-cloneI, clarify)
    apply(rename-tac obsa', case-tac obsa')
    by(auto 4 4 intro: heap-base.red-external.intros simp add: final-thread.actions-ok-iff simp del:
split-paired-Ex)
next
  case 20
  thus ?case apply –
    apply(drule jmm'-heap-cloneI, clarify)
    apply(rename-tac obsa', case-tac obsa')
    by(auto 4 4 intro: heap-base.red-external.intros simp add: final-thread.actions-ok-iff simp del:
split-paired-Ex)
qed(blast intro: heap-base.red-external.intros)+

```

**lemma** red-external-aggr-heap-read-typedI:

```

 $\wedge$ final.
 $\llbracket (ta, vah') \in \text{heap-base.red-external-aggr addr2thread-id thread-id2addr spurious-wakeups empty-heap}$ 
 $\text{allocate typeof-addr jmm-heap-read jmm-heap-write P t h a M vs};$ 
  final-thread.actions-ok final s t ta
 $\rrbracket$ 
 $\implies \exists ta vah'. (ta, vah') \in \text{heap-base.red-external-aggr addr2thread-id thread-id2addr spurious-wakeups}$ 
 $\text{empty-heap allocate typeof-addr (heap-base.heap-read-typed typeof-addr jmm-heap-read P) jmm-heap-write}$ 
 $P t h a M \text{ vs} \wedge \text{final-thread.actions-ok final s t ta}$ 
apply(simp add: heap-base.red-external-aggr-def split-beta split del: if-split split: if-split-asm del: split-paired-Ex)
apply(auto simp del: split-paired-Ex)
apply(drule jmm'-heap-cloneI)
apply(clarify)
apply(rename-tac obsa, case-tac obsa)
apply(force simp add: final-thread.actions-ok-iff del: disjCI intro: disjI1 disjI2 simp del: split-paired-Ex)
apply(force simp add: final-thread.actions-ok-iff del: disjCI intro: disjI1 disjI2 simp del: split-paired-Ex)
apply(drule jmm'-heap-cloneI)
apply clarify
apply(rename-tac obsa, case-tac obsa)
apply(force simp add: final-thread.actions-ok-iff del: disjCI intro: disjI1 disjI2 simp del: split-paired-Ex)
apply(force simp add: final-thread.actions-ok-iff del: disjCI intro: disjI1 disjI2 simp del: split-paired-Ex)
done

```

end

## 8.20 JMM type safety for source code

**theory** *JMM-J-Typesafe* **imports**

*JMM-Typesafe2*

*DRF-J*

**begin**

**locale** *J-allocated-heap-conf'* =

*h*: *J-heap-conf*

*addr2thread-id thread-id2addr*

*spurious-wakeups*

*empty-heap allocate*  $\lambda\cdot$ . *typeof-addr heap-read heap-write hconf*

*P*

+

*h*: *J-allocated-heap*

*addr2thread-id thread-id2addr*

*spurious-wakeups*

*empty-heap allocate*  $\lambda\cdot$ . *typeof-addr heap-read heap-write*

*allocated*

*P*

+

*heap''*

*addr2thread-id thread-id2addr*

*spurious-wakeups*

*empty-heap allocate typeof-addr heap-read heap-write*

*P*

**for** *addr2thread-id* :: (*'addr* :: *addr*)  $\Rightarrow$  *'thread-id*

**and** *thread-id2addr* :: *'thread-id*  $\Rightarrow$  *'addr*

**and** *spurious-wakeups* :: *bool*

**and** *empty-heap* :: *'heap*

**and** *allocate* :: *'heap*  $\Rightarrow$  *htype*  $\Rightarrow$  (*'heap*  $\times$  *'addr*) *set*

**and** *typeof-addr* :: *'addr*  $\rightarrow$  *htype*

**and** *heap-read* :: *'heap*  $\Rightarrow$  *'addr*  $\Rightarrow$  *addr-loc*  $\Rightarrow$  *'addr val*  $\Rightarrow$  *bool*

**and** *heap-write* :: *'heap*  $\Rightarrow$  *'addr*  $\Rightarrow$  *addr-loc*  $\Rightarrow$  *'addr val*  $\Rightarrow$  *'heap*  $\Rightarrow$  *bool*

**and** *hconf* :: *'heap*  $\Rightarrow$  *bool*

**and** *allocated* :: *'heap*  $\Rightarrow$  *'addr set*

**and** *P* :: *'addr J-prog*

**sublocale** *J-allocated-heap-conf'* < *h*: *J-allocated-heap-conf*

*addr2thread-id thread-id2addr*

*spurious-wakeups*

*empty-heap allocate*  $\lambda\cdot$ . *typeof-addr heap-read heap-write hconf allocated*

*P*

**by**(*unfold-locales*)

**context** *J-allocated-heap-conf'* **begin**

**lemma** *red-New-type-match*:

$\llbracket h.\text{red}' P t e s ta e' s'; \text{NewHeapElem } ad \ C Tn \in \text{set } \{ta\}_o; \text{typeof-addr } ad \neq \text{None} \rrbracket$

$\implies \text{typeof-addr } ad = \lfloor C Tn \rfloor$

**and** *reds-New-type-match*:  
 $\llbracket h.\text{reds}' P \text{ } t \text{ } es \text{ } s \text{ } ta \text{ } es' s'; \text{NewHeapElem } ad \text{ } CTn \in \text{set } \{ta\}_o; \text{typeof-addr } ad \neq \text{None} \rrbracket$   
 $\implies \text{typeof-addr } ad = \lfloor CTn \rfloor$   
**by**(*induct rule: h.red-reds.inducts*)(*auto dest: allocate-typeof-addr-SomeD red-external-New-type-match*)

**lemma** *mred-known-addr-typing'*:  
**assumes** *wf: wf-J-prog P*  
**and** *ok: h.start-heap-ok*  
**shows** *known-addr-typing' addr2thread-id thread-id2addr empty-heap allocate typeof-addr heap-write*  
*allocated h.J-known-addr final-expr (h.mred P) ( $\lambda t \ x \ h. \exists ET. h.sconf\text{-}type\text{-}ok \ ET \ t \ x \ h$ ) P*  
**proof** –  
**interpret** *known-addr-typing*  
*addr2thread-id thread-id2addr*  
*spurious-wakeups*  
*empty-heap allocate  $\lambda\cdot$ . typeof-addr heap-read heap-write*  
*allocated h.J-known-addr*  
*final-expr h.mred P  $\lambda t \ x \ h. \exists ET. h.sconf\text{-}type\text{-}ok \ ET \ t \ x \ h$*   
*P*  
**using** *assms* **by**(*rule h.mred-known-addr-typing*)

**show** *?thesis* **by** *unfold-locales(auto dest: red-New-type-match)*  
**qed**

**lemma** *J-legal-read-value-typeable*:  
**assumes** *wf: wf-J-prog P*  
**and** *wf-start: h.wf-start-state P C M vs*  
**and** *legal: weakly-legal-execution P (h.J- $\mathcal{E}$  P C M vs status) (E, ws)*  
**and** *a: enat a < llength E*  
**and** *read: action-obs E a = NormalAction (ReadMem ad al v)*  
**shows**  $\exists T. P \vdash ad@al : T \wedge P \vdash v : \leq T$   
**proof** –  
**note** *wf*  
**moreover from** *wf-start* **have** *h.start-heap-ok* **by** *cases*  
**moreover from** *wf wf-start*  
**have** *ts-ok ( $\lambda t \ x \ h. \exists ET. h.sconf\text{-}type\text{-}ok \ ET \ t \ x \ h$ ) (thr (h.J-start-state P C M vs)) h.start-heap*  
**by**(*rule h.J-start-state-sconf-type-ok*)  
**moreover from** *wf* **have** *wf-syscls P* **by**(*rule wf-prog-wf-syscls*)  
**ultimately show** *?thesis* **using** *legal a read*  
**by**(*rule known-addr-typing'.weakly-legal-read-value-typeable[OF mred-known-addr-typing']*)  
**qed**

**end**

### 8.20.1 Specific part for JMM implementation 2

**abbreviation** *jmm-J- $\mathcal{E}$*

$:: \text{addr } J\text{-prog} \Rightarrow \text{cname} \Rightarrow \text{mname} \Rightarrow \text{addr val list} \Rightarrow \text{status} \Rightarrow (\text{addr} \times (\text{addr}, \text{addr}) \text{ obs-event action) llist set}$

**where**

*jmm-J- $\mathcal{E}$  P*  $\equiv$   
*J-heap-base.J- $\mathcal{E}$  addr2thread-id thread-id2addr jmm-spurious-wakeups jmm-empty jmm-allocate (jmm-typeof-addr P) jmm-heap-read jmm-heap-write P*

**abbreviation** *jmm'-J- $\mathcal{E}$*

$:: \text{addr } J\text{-prog} \Rightarrow \text{cname} \Rightarrow \text{mname} \Rightarrow \text{addr val list} \Rightarrow \text{status} \Rightarrow (\text{addr} \times (\text{addr}, \text{addr}) \text{ obs-event action}) \text{ llist set}$

**where**

$jmm'\text{-}J\text{-}\mathcal{E} \ P \equiv$   
 $J\text{-heap-base.}J\text{-}\mathcal{E} \ \text{addr2thread-id thread-id2addr jmm-spurious-wakeups jmm-empty jmm-allocate (jmm-typeof-addr } P)$   
 $P) \ (jmm\text{-heap-read-typed } P) \ jmm\text{-heap-write } P$

**lemma** *jmm-J-heap-conf*:

$J\text{-heap-conf addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr } P) \ jmm\text{-heap-write}$   
 $jmm\text{-hconf } P$

**by**(*unfold-locales*)

**lemma** *jmm-J-allocated-heap-conf*:  $J\text{-allocated-heap-conf addr2thread-id thread-id2addr jmm-empty jmm-allocate}$   
 $(jmm\text{-typeof-addr } P) \ jmm\text{-heap-write jmm-hconf jmm-allocated } P$

**by**(*unfold-locales*)

**lemma** *jmm-J-allocated-heap-conf'*:

$J\text{-allocated-heap-conf}' \ \text{addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr}' \ P)$   
 $jmm\text{-heap-write jmm-hconf jmm-allocated } P$

**apply**(*rule J-allocated-heap-conf'.intro*)

**apply**(*unfold jmm-typeof-addr'-conv-jmm-typeof-addr*)

**apply**(*unfold-locales*)

**done**

**lemma** *red-heap-read-typedD*:

$J\text{-heap-base.red}' \ \text{addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda\text{-} :: 'heap.$   
 $\text{typeof-addr}) \ (\text{heap-base.heap-read-typed } (\lambda\text{-} :: 'heap. \ \text{typeof-addr}) \ \text{heap-read } P) \ \text{heap-write } P \ t \ e \ s \ ta \ e'$   
 $s' \longleftrightarrow$

$J\text{-heap-base.red}' \ \text{addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda\text{-} :: 'heap.$   
 $\text{typeof-addr}) \ \text{heap-read heap-write } P \ t \ e \ s \ ta \ e' \ s' \wedge$

$(\forall \text{ad al v } T. \ \text{ReadMem ad al v} \in \text{set } \{\!|ta|\!\}_o \longrightarrow \text{heap-base'.addr-loc-type TYPE('heap) typeof-addr } P$   
 $\text{ad al } T \longrightarrow \text{heap-base'.conf TYPE('heap) typeof-addr } P \ v \ T)$

**(is**  $?lhs1 \longleftrightarrow ?rhs1a \wedge ?rhs1b)$

**and** *reds-heap-read-typedD*:

$J\text{-heap-base.reds}' \ \text{addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda\text{-} :: 'heap.$   
 $\text{typeof-addr}) \ (\text{heap-base.heap-read-typed } (\lambda\text{-} :: 'heap. \ \text{typeof-addr}) \ \text{heap-read } P) \ \text{heap-write } P \ t \ e \ s \ ta$   
 $es' \ s' \longleftrightarrow$

$J\text{-heap-base.reds}' \ \text{addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda\text{-} :: 'heap.$   
 $\text{typeof-addr}) \ \text{heap-read heap-write } P \ t \ e \ s \ ta \ es' \ s' \wedge$

$(\forall \text{ad al v } T. \ \text{ReadMem ad al v} \in \text{set } \{\!|ta|\!\}_o \longrightarrow \text{heap-base'.addr-loc-type TYPE('heap) typeof-addr } P$   
 $\text{ad al } T \longrightarrow \text{heap-base'.conf TYPE('heap) typeof-addr } P \ v \ T)$

**(is**  $?lhs2 \longleftrightarrow ?rhs2a \wedge ?rhs2b)$

**proof** –

**have**  $(?lhs1 \longrightarrow ?rhs1a \wedge ?rhs1b) \wedge (?lhs2 \longrightarrow ?rhs2a \wedge ?rhs2b)$

**apply**(*induct rule: J-heap-base.red-reds.induct*)

**prefer** 50

**apply**(*subst (asm) red-external-heap-read-typed*)

**apply**(*fastforce intro!: J-heap-base.red-reds.RedCallExternal simp add: convert-extTA-def*)

**prefer** 49

**apply**(*fastforce dest: J-heap-base.red-reds.RedCall*)

**apply**(*auto intro: J-heap-base.red-reds.intros dest: heap-base.heap-read-typed-into-heap-read heap-base.heap-read-*  
*dest: heap-base'.addr-loc-type-conv-addr-loc-type[THEN fun-cong, THEN fun-cong, THEN fun-cong,*  
*THEN iffD2] heap-base'.conf-conv-conf[THEN fun-cong, THEN fun-cong, THEN iffD1]*)  
**done**  
**moreover have** ( $?rhs1a \longrightarrow ?rhs1b \longrightarrow ?lhs1$ )  $\wedge$  ( $?rhs2a \longrightarrow ?rhs2b \longrightarrow ?lhs2$ )  
**apply**(*induct rule: J-heap-base.red-reds.induct*)  
**prefer 50**  
**apply simp**  
**apply**(*intro strip*)  
**apply**(*erule (1) J-heap-base.red-reds.RedCallExternal*)  
**apply**(*subst red-external-heap-read-typed, erule conjI*)  
**apply**(*blast+*)[4]  
  
**prefer 49**  
**apply**(*fastforce dest: J-heap-base.red-reds.RedCall*)  
  
**apply**(*auto intro: J-heap-base.red-reds.intros intro!: heap-base.heap-read-typedI dest: heap-base'.addr-loc-type-conv-*  
*fun-cong, THEN fun-cong, THEN fun-cong, THEN iffD1] intro: heap-base'.conf-conv-conf[THEN fun-cong,*  
*THEN fun-cong, THEN iffD2]*)  
**done**  
**ultimately show**  $?lhs1 \longleftrightarrow ?rhs1a \wedge ?rhs1b$   $?lhs2 \longleftrightarrow ?rhs2a \wedge ?rhs2b$  **by** *blast+*  
**qed**

**lemma** *if-mred-heap-read-typedD:*

*multithreaded-base.init-fin final-expr (J-heap-base.mred addr2thread-id thread-id2addr spurious-wakeups*  
*empty-heap allocate ( $\lambda - :: 'heap. \text{typeof-addr}$ ) (heap-base.heap-read-typed ( $\lambda - :: 'heap. \text{typeof-addr}$ )*  
*heap-read P) heap-write P) t xh ta x'h'  $\longleftrightarrow$*   
*if-heap-read-typed final-expr (J-heap-base.mred addr2thread-id thread-id2addr spurious-wakeups empty-heap*  
*allocate ( $\lambda - :: 'heap. \text{typeof-addr}$ ) heap-read heap-write P) typeof-addr P t xh ta x'h'*  
**unfolding** *multithreaded-base.init-fin.simps*  
**by**(*subst red-heap-read-typedD*) *fastforce*

**lemma** *J- $\mathcal{E}$ -heap-read-typedI:*

$\llbracket E \in J\text{-heap-base.}J\text{-}\mathcal{E} \text{ addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda - ::$   
 $'heap. \text{typeof-addr}) \text{ heap-read heap-write } P \ C \ M \text{ vs status};$   
 $\bigwedge ad \text{ al } v \ T. \llbracket \text{NormalAction (ReadMem } ad \text{ al } v) \in \text{snd 'lset } E; \text{ heap-base'.addr-loc-type TYPE('heap)}$   
 $\text{typeof-addr } P \text{ ad al } T \rrbracket \implies \text{heap-base'.conf TYPE('heap) typeof-addr } P \ v \ T \rrbracket$   
 $\implies E \in J\text{-heap-base.}J\text{-}\mathcal{E} \text{ addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda - ::$   
 $'heap. \text{typeof-addr}) \text{ (heap-base.heap-read-typed } (\lambda - :: 'heap. \text{typeof-addr}) \text{ heap-read } P) \text{ heap-write } P \ C$   
 $M \text{ vs status}$   
**apply**(*erule imageE, hypsubst*)  
**apply**(*rule imageI*)  
**apply**(*erule multithreaded-base. $\mathcal{E}$ .cases, hypsubst*)  
**apply**(*rule multithreaded-base. $\mathcal{E}$ .intros*)  
**apply**(*subst if-mred-heap-read-typedD[abs-def]*)  
**apply**(*erule if-mthr-Runs-heap-read-typedI*)  
**apply**(*auto simp add: image-Un lset-lmap[symmetric] lmap-lconcat llist.map-comp o-def split-def simp*  
*del: lset-lmap*)  
**done**

**lemma** *jmm'-redI:*

$\llbracket J\text{-heap-base.red' addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr}$   
 $jmm\text{-heap-read } jmm\text{-heap-write } P \ t \ e \ s \text{ ta } e' \ s';$



```

    final-thread.actions-ok (final-thread.init-fin-final final-expr) S t ta ]]
    ==> ∃ ta e' s'. J-heap-base.red' addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate
    typeof-addr (heap-base.heap-read-typed typeof-addr jmm-heap-read P) jmm-heap-write P t e s ta e' s'
    ∧ final-thread.actions-ok (final-thread.init-fin-final final-expr) S t ta
    (is [[ ?red'; ?aok ]] ==> ?concl)
    and jmm'-redsI:
    [[ J-heap-base.reds' addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr
    jmm-heap-read jmm-heap-write P t e s ta es' s';
    final-thread.actions-ok (final-thread.init-fin-final final-expr) S t ta ]]
    ==> ∃ ta es' s'. J-heap-base.reds' addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate
    typeof-addr (heap-base.heap-read-typed typeof-addr jmm-heap-read P) jmm-heap-write P t e s ta es' s'
    ∧
    final-thread.actions-ok (final-thread.init-fin-final final-expr) S t ta
    (is [[ ?reds'; ?aoks ]] ==> ?concls)
  proof -
  note [simp del] = split-paired-Ex
  and [simp add] = final-thread.actions-ok-iff heap-base.THE-addr-loc-type heap-base.defval-conf
  and [intro] = jmm-heap-read-typed-default-val

  let ?v = λh a al. default-val (THE T. heap-base.addr-loc-type typeof-addr P h a al T)

  have (?red' → ?aok → ?concl) ∧ (?reds' → ?aoks → ?concls)
  proof(induct rule: J-heap-base.red-reds.induct)
  case (23 h a T n i v l)
  thus ?case by(auto 4 6 intro: J-heap-base.red-reds.RedAAcc[where v=?v h a (ACell (nat (sint
i)))]))
  next
  case (35 h a D F v l)
  thus ?case by(auto 4 5 intro: J-heap-base.red-reds.RedFAcc[where v=?v h a (CField D F)])
  next
  case RedCASSucceed: (45 h a D F v v' h')
  thus ?case
  proof(cases v = ?v h a (CField D F))
  case True
  with RedCASSucceed show ?thesis
  by(fastforce intro: J-heap-base.red-reds.RedCASSucceed[where v=?v h a (CField D F)])
  next
  case False
  with RedCASSucceed show ?thesis
  by(fastforce intro: J-heap-base.red-reds.RedCASFail[where v'=?v h a (CField D F)])
  qed
  next
  case RedCASFail: (46 h a D F v'' v v' l)
  thus ?case
  proof(cases v = ?v h a (CField D F))
  case True
  with RedCASFail show ?thesis
  by(fastforce intro: J-heap-base.red-reds.RedCASSucceed[where v=?v h a (CField D F)] jmm-heap-write.intros)
  next
  case False
  with RedCASFail show ?thesis
  by(fastforce intro: J-heap-base.red-reds.RedCASFail[where v'=?v h a (CField D F)])
  qed
  next

```

```

case (50 s a hU M Ts T D vs ta va h' ta' e' s')
thus ?case
  apply clarify
  apply (drule jmm'-red-externalI, simp)
  apply (auto 4 4 intro: J-heap-base.red-reds.RedCallExternal)
  done
next
case (52 e h l V vo ta e' h' l' T)
thus ?case
  by (clarify) (iprover intro: J-heap-base.red-reds.BlockRed)
qed (blast intro: J-heap-base.red-reds.intros)+
thus [ [ ?red'; ?aok ] ==> ?concl and [ [ ?reds'; ?aoks ] ==> ?concls by blast+
qed

```

**lemma** *if-mred-heap-read-not-stuck:*

```

[ multithreaded-base.init-fin final-expr (J-heap-base.mred addr2thread-id thread-id2addr spurious-wakeups
empty-heap allocate typeof-addr jmm-heap-read jmm-heap-write P) t xh ta x'h';
  final-thread.actions-ok (final-thread.init-fin-final final-expr) s t ta ]
==>
  ∃ ta x'h'. multithreaded-base.init-fin final-expr (J-heap-base.mred addr2thread-id thread-id2addr spu-
rious-wakeups empty-heap allocate typeof-addr (heap-base.heap-read-typed typeof-addr jmm-heap-read
P) jmm-heap-write P) t xh ta x'h' ∧ final-thread.actions-ok (final-thread.init-fin-final final-expr) s t ta
apply (erule multithreaded-base.init-fin.cases)
apply hypsubst
apply clarify
apply (drule jmm'-redI)
  apply (simp add: final-thread.actions-ok-iff)
apply clarify
apply (subst (2) split-paired-Ex)
apply (subst (2) split-paired-Ex)
apply (subst (2) split-paired-Ex)
apply (rule exI conjI)+
  apply (rule multithreaded-base.init-fin.intros)
  apply (simp)
  apply (simp add: final-thread.actions-ok-iff)
apply (blast intro: multithreaded-base.init-fin.intros)
apply (blast intro: multithreaded-base.init-fin.intros)
done

```

**lemma** *if-mredT-heap-read-not-stuck:*

```

multithreaded-base.redT (final-thread.init-fin-final final-expr) (multithreaded-base.init-fin final-expr
(J-heap-base.mred addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr jmm-heap-read
jmm-heap-write P)) convert-RA' s tta s'
==> ∃ tta s'. multithreaded-base.redT (final-thread.init-fin-final final-expr) (multithreaded-base.init-fin
final-expr (J-heap-base.mred addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr
(heap-base.heap-read-typed typeof-addr jmm-heap-read P) jmm-heap-write P)) convert-RA' s tta s'
apply (erule multithreaded-base.redT.cases)
apply hypsubst
apply (drule (1) if-mred-heap-read-not-stuck)
apply (erule exE)+
  apply (rename-tac ta' x'h')
  apply (insert redT-updWs-total)
  apply (erule-tac x=t in meta-allE)
  apply (erule-tac x=wset s in meta-allE)

```

```

apply(erule-tac  $x = \llbracket ta \rrbracket_w$  in meta-allE)
apply clarsimp
apply(rule exI)+
apply(auto intro!: multithreaded-base.redT.intros)[1]
apply hypsubst
apply(rule exI conjI)+
apply(rule multithreaded-base.redT.redT-acquire)
apply assumption+
done

```

**lemma** *J- $\mathcal{E}$ -heap-read-typedD*:

$E \in J\text{-heap-base.}J\text{-}\mathcal{E} \text{ addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda\cdot. \text{typeof-addr})$   
 $(\text{heap-base.heap-read-typed } (\lambda\cdot. \text{typeof-addr}) \text{ jmm-heap-read } P) \text{ jmm-heap-write } P \ C \ M \text{ vs status}$   
 $\implies E \in J\text{-heap-base.}J\text{-}\mathcal{E} \text{ addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate } (\lambda\cdot. \text{typeof-addr})$   
 $\text{jmm-heap-read jmm-heap-write } P \ C \ M \text{ vs status}$

```

apply(erule imageE, hypsubst)
apply(rule imageI)
apply(erule multithreaded-base. $\mathcal{E}$ .cases, hypsubst)
apply(rule multithreaded-base. $\mathcal{E}$ .intros)
apply(subst (asm) if-mred-heap-read-typedD[abs-def])
apply(erule if-mthr-Runs-heap-read-typedD)
apply(erule if-mredT-heap-read-not-stuck[where typeof-addr= $\lambda\cdot. \text{typeof-addr}$ , unfolded if-mred-heap-read-typedD[abs-def]])
done

```

**lemma** *J- $\mathcal{E}$ -typesafe-subset*:  $\text{jmm}'\text{-}J\text{-}\mathcal{E} \ P \ C \ M \text{ vs status} \subseteq \text{jmm-}J\text{-}\mathcal{E} \ P \ C \ M \text{ vs status}$

**unfolding**  $\text{jmm-typeof-addr-def}[abs\text{-def}]$   
**by**(rule subsetI)(erule J- $\mathcal{E}$ -heap-read-typedD)

**lemma** *J-legal-typesafe1*:

**assumes** wfP: wf-J-prog P  
**and** ok:  $\text{jmm-wf-start-state } P \ C \ M \text{ vs}$   
**and** legal: legal-execution P ( $\text{jmm-}J\text{-}\mathcal{E} \ P \ C \ M \text{ vs status}$ ) (E, ws)  
**shows** legal-execution P ( $\text{jmm}'\text{-}J\text{-}\mathcal{E} \ P \ C \ M \text{ vs status}$ ) (E, ws)

**proof** –

```

let ? $\mathcal{E}$  =  $\text{jmm-}J\text{-}\mathcal{E} \ P \ C \ M \text{ vs status}$ 
let ? $\mathcal{E}'$  =  $\text{jmm}'\text{-}J\text{-}\mathcal{E} \ P \ C \ M \text{ vs status}$ 
from legal obtain J
  where justified:  $P \vdash (E, ws) \text{ justified-by } J$ 
  and range:  $\text{range } (\text{justifying-exec} \circ J) \subseteq ?\mathcal{E}$ 
  and E:  $E \in ?\mathcal{E}$  and wf:  $P \vdash (E, ws) \checkmark$  by(auto simp add: gen-legal-execution.simps)
let ?J =  $J(0 := \llbracket committed = \{\} \rrbracket, \text{justifying-exec} = \text{justifying-exec } (J \ 1), \text{justifying-ws} = \text{justifying-ws } (J \ 1), \text{action-translation} = id)$ 

```

**from** wfP **have** wf-sys: wf-syscls P **by**(rule wf-prog-wf-syscls)

**from** justified **have**  $P \vdash (\text{justifying-exec } (J \ 1), \text{justifying-ws } (J \ 1)) \checkmark$

**by**(simp add: justification-well-formed-def)

**with** justified **have**  $P \vdash (E, ws) \text{ justified-by } ?J$  **by**(rule drop-0th-justifying-exec)

**moreover** **have**  $\text{range } (\text{justifying-exec} \circ ?J) \subseteq ?\mathcal{E}'$

**proof**

**fix**  $\xi$

**assume**  $\xi \in \text{range } (\text{justifying-exec} \circ ?J)$

**then** **obtain** n **where**  $\xi = \text{justifying-exec } (?J \ n)$  **by** auto

**then** **obtain** n **where**  $\xi: \xi = \text{justifying-exec } (J \ n)$  **and** n:  $n > 0$  **by**(auto split: if-split-asm)

```

from range  $\xi$  have  $\xi \in ?\mathcal{E}$  by auto
thus  $\xi \in ?\mathcal{E}'$  unfolding jmm-typeof-addr'-conv-jmm-type-addr[symmetric, abs-def]
proof(rule J-E-heap-read-typedI)
  fix ad al v T
  assume read: NormalAction (ReadMem ad al v)  $\in$  snd ' lset  $\xi$ 
  and adal:  $P \vdash_{\text{jmm}} \text{ad}@al : T$ 
  from read obtain a where a: enat a < llength  $\xi$  action-obs  $\xi$  a = NormalAction (ReadMem ad
al v)
    unfolding lset-conv-lnth by(auto simp add: action-obs-def)
  with J-allocated-heap-conf'.mred-known-addr-typing'[OF jmm-J-allocated-heap-conf' wfP jmm-start-heap-ok]
J-heap-conf.J-start-state-sconf-type-ok[OF jmm-J-heap-conf wfP ok]
wf-sys is-justified-by-imp-is-weakly-justified-by[OF justified wf] range n
  have  $\exists T. P \vdash_{\text{jmm}} \text{ad}@al : T \wedge P \vdash_{\text{jmm}} v : \leq T$ 
    unfolding jmm-typeof-addr'-conv-jmm-type-addr[symmetric, abs-def]  $\xi$ 
    by(rule known-addr-typing'.read-value-typeable-justifying)
  thus  $P \vdash_{\text{jmm}} v : \leq T$  using adal
    by(auto dest: jmm.addr-loc-type-fun[unfolded jmm-typeof-addr-conv-jmm-typeof-addr', unfolded
heap-base'.addr-loc-type-conv-addr-loc-type])
  qed
qed
moreover from E have  $E \in ?\mathcal{E}'$ 
  unfolding jmm-typeof-addr'-conv-jmm-type-addr[symmetric, abs-def]
proof(rule J-E-heap-read-typedI)
  fix ad al v T
  assume read: NormalAction (ReadMem ad al v)  $\in$  snd ' lset  $E$ 
  and adal:  $P \vdash_{\text{jmm}} \text{ad}@al : T$ 
  from read obtain a where a: enat a < llength  $E$  action-obs  $E$  a = NormalAction (ReadMem ad
al v)
    unfolding lset-conv-lnth by(auto simp add: action-obs-def)
  with jmm-J-allocated-heap-conf' wfP ok legal-imp-weakly-legal-execution[OF legal]
  have  $\exists T. P \vdash_{\text{jmm}} \text{ad}@al : T \wedge P \vdash_{\text{jmm}} v : \leq T$ 
    unfolding jmm-typeof-addr'-conv-jmm-type-addr[symmetric, abs-def]
    by(rule J-allocated-heap-conf'.J-legal-read-value-typeable)
  thus  $P \vdash_{\text{jmm}} v : \leq T$  using adal
    by(auto dest: jmm.addr-loc-type-fun[unfolded jmm-typeof-addr-conv-jmm-typeof-addr', unfolded
heap-base'.addr-loc-type-conv-addr-loc-type])
  qed
  ultimately show ?thesis using wf unfolding gen-legal-execution.simps by blast
qed

```

**lemma** *J-weakly-legal-typesafe1:*

```

assumes wfP: wf-J-prog P
and ok: jmm-wf-start-state P C M vs
and legal: weakly-legal-execution P (jmm-J-E P C M vs status) (E, ws)
shows weakly-legal-execution P (jmm'-J-E P C M vs status) (E, ws)
proof –
  let  $?E = \text{jmm-J-E } P \ C \ M \ \text{vs} \ \text{status}$ 
  let  $?E' = \text{jmm'-J-E } P \ C \ M \ \text{vs} \ \text{status}$ 
  from legal obtain J
    where justified:  $P \vdash (E, ws)$  weakly-justified-by J
    and range: range (justifying-exec  $\circ$  J)  $\subseteq$  ?E
    and E:  $E \in ?E$  and wf:  $P \vdash (E, ws) \checkmark$  by (auto simp add: gen-legal-execution.simps)
  let  $?J = J(0 := \{\text{committed} = \{\}, \text{justifying-exec} = \text{justifying-exec } (J \ 1), \text{justifying-ws} = \text{justifying-ws}$ 
(J 1), action-translation = id\})

```

```

from wfP have wf-sys: wf-syscls P by(rule wf-prog-wf-syscls)

from justified have  $P \vdash (\text{justifying-exec } (J \ 1), \text{justifying-ws } (J \ 1)) \ \checkmark$ 
  by(simp add: justification-well-formed-def)
with justified have  $P \vdash (E, \text{ws}) \text{ weakly-justified-by } ?J$  by(rule drop-0th-weakly-justifying-exec)
moreover have  $\text{range } (\text{justifying-exec} \circ ?J) \subseteq ?\mathcal{E}'$ 
proof
  fix  $\xi$ 
  assume  $\xi \in \text{range } (\text{justifying-exec} \circ ?J)$ 
  then obtain  $n$  where  $\xi = \text{justifying-exec } (?J \ n)$  by auto
  then obtain  $n$  where  $\xi: \xi = \text{justifying-exec } (J \ n)$  and  $n: n > 0$  by(auto split: if-split-asm)
  from range  $\xi$  have  $\xi \in ?\mathcal{E}$  by auto
  thus  $\xi \in ?\mathcal{E}'$  unfolding jmm-typeof-addr'-conv-jmm-type-addr[symmetric, abs-def]
  proof(rule J-E-heap-read-typedI)
    fix ad al v T
    assume read: NormalAction (ReadMem ad al v)  $\in \text{snd } \text{'lset } \xi$ 
    and adal:  $P \vdash \text{jmm } \text{ad}@al : T$ 
    from read obtain  $a$  where  $a: \text{enat } a < \text{length } \xi \text{ action-obs } \xi \ a = \text{NormalAction } (\text{ReadMem } \text{ad}$ 
al v)
    unfolding lset-conv-lnth by(auto simp add: action-obs-def)
    with J-allocated-heap-conf'.mred-known-addr-typing'[OF jmm-J-allocated-heap-conf' wfP jmm-start-heap-ok]
J-heap-conf.J-start-state-sconf-type-ok[OF jmm-J-heap-conf wfP ok]
wf-sys justified range n
    have  $\exists T. P \vdash \text{jmm } \text{ad}@al : T \wedge P \vdash \text{jmm } v : \leq T$ 
    unfolding jmm-typeof-addr'-conv-jmm-type-addr[symmetric, abs-def]  $\xi$ 
    by(rule known-addr-typing'.read-value-typeable-justifying)
    thus  $P \vdash \text{jmm } v : \leq T$  using adal
    by(auto dest: jmm.addr-loc-type-fun[unfolded jmm-typeof-addr-conv-jmm-typeof-addr', unfolded
heap-base'.addr-loc-type-conv-addr-loc-type])
    qed
  qed
  moreover from E have  $E \in ?\mathcal{E}'$ 
    unfolding jmm-typeof-addr'-conv-jmm-type-addr[symmetric, abs-def]
  proof(rule J-E-heap-read-typedI)
    fix ad al v T
    assume read: NormalAction (ReadMem ad al v)  $\in \text{snd } \text{'lset } E$ 
    and adal:  $P \vdash \text{jmm } \text{ad}@al : T$ 
    from read obtain  $a$  where  $a: \text{enat } a < \text{length } E \text{ action-obs } E \ a = \text{NormalAction } (\text{ReadMem } \text{ad}$ 
al v)
    unfolding lset-conv-lnth by(auto simp add: action-obs-def)
    with jmm-J-allocated-heap-conf' wfP ok legal
    have  $\exists T. P \vdash \text{jmm } \text{ad}@al : T \wedge P \vdash \text{jmm } v : \leq T$ 
    unfolding jmm-typeof-addr'-conv-jmm-type-addr[symmetric, abs-def]
    by(rule J-allocated-heap-conf'.J-legal-read-value-typeable)
    thus  $P \vdash \text{jmm } v : \leq T$  using adal
    by(auto dest: jmm.addr-loc-type-fun[unfolded jmm-typeof-addr-conv-jmm-typeof-addr', unfolded
heap-base'.addr-loc-type-conv-addr-loc-type])
    qed
    ultimately show ?thesis using wf unfolding gen-legal-execution.simps by blast
  qed

lemma J-legal-typesafe2:
  assumes legal: legal-execution P (jmm'-J-E P C M vs status) (E, ws)

```

**shows** *legal-execution*  $P$  (*jmm-J- $\mathcal{E}$*   $P$   $C$   $M$  *vs status*) ( $E$ ,  $ws$ )  
**proof** –  
**let**  $? \mathcal{E} = \text{jmm-J-}\mathcal{E} \ P \ C \ M \ \text{vs status}$   
**let**  $? \mathcal{E}' = \text{jmm'-J-}\mathcal{E} \ P \ C \ M \ \text{vs status}$   
**from** *legal* **obtain**  $J$   
**where** *justified*:  $P \vdash (E, ws)$  *justified-by*  $J$   
**and** *range*:  $\text{range}(\text{justifying-exec} \circ J) \subseteq ? \mathcal{E}'$   
**and**  $E: E \in ? \mathcal{E}'$  **and** *wf*:  $P \vdash (E, ws) \checkmark$  **by**(*auto simp add: gen-legal-execution.simps*)  
**from** *range*  $E$  **have**  $\text{range}(\text{justifying-exec} \circ J) \subseteq ? \mathcal{E} \ E \in ? \mathcal{E}$   
**using** *J- $\mathcal{E}$ -typesafe-subset*[*of*  $P$  *status*  $C$   $M$  *vs*] **by** *blast+*  
**with** *justified wf*  
**show** *?thesis* **by**(*auto simp add: gen-legal-execution.simps*)  
**qed**

**lemma** *J-weakly-legal-typesafe2*:

**assumes** *legal*: *weakly-legal-execution*  $P$  (*jmm'-J- $\mathcal{E}$*   $P$   $C$   $M$  *vs status*) ( $E$ ,  $ws$ )  
**shows** *weakly-legal-execution*  $P$  (*jmm-J- $\mathcal{E}$*   $P$   $C$   $M$  *vs status*) ( $E$ ,  $ws$ )  
**proof** –  
**let**  $? \mathcal{E} = \text{jmm-J-}\mathcal{E} \ P \ C \ M \ \text{vs status}$   
**let**  $? \mathcal{E}' = \text{jmm'-J-}\mathcal{E} \ P \ C \ M \ \text{vs status}$   
**from** *legal* **obtain**  $J$   
**where** *justified*:  $P \vdash (E, ws)$  *weakly-justified-by*  $J$   
**and** *range*:  $\text{range}(\text{justifying-exec} \circ J) \subseteq ? \mathcal{E}'$   
**and**  $E: E \in ? \mathcal{E}'$  **and** *wf*:  $P \vdash (E, ws) \checkmark$  **by**(*auto simp add: gen-legal-execution.simps*)  
**from** *range*  $E$  **have**  $\text{range}(\text{justifying-exec} \circ J) \subseteq ? \mathcal{E} \ E \in ? \mathcal{E}$   
**using** *J- $\mathcal{E}$ -typesafe-subset*[*of*  $P$  *status*  $C$   $M$  *vs*] **by** *blast+*  
**with** *justified wf*  
**show** *?thesis* **by**(*auto simp add: gen-legal-execution.simps*)  
**qed**

**theorem** *J-weakly-legal-typesafe*:

**assumes** *wf-J-prog*  $P$   
**and** *jmm-wf-start-state*  $P$   $C$   $M$  *vs*  
**shows** *weakly-legal-execution*  $P$  (*jmm-J- $\mathcal{E}$*   $P$   $C$   $M$  *vs status*) = *weakly-legal-execution*  $P$  (*jmm'-J- $\mathcal{E}$*   $P$   $C$   $M$  *vs status*)  
**apply**(*rule ext iffI*) +  
**apply**(*clarify, erule J-weakly-legal-typesafe1*[*OF assms*])  
**apply**(*clarify, erule J-weakly-legal-typesafe2*)  
**done**

**theorem** *J-legal-typesafe*:

**assumes** *wf-J-prog*  $P$   
**and** *jmm-wf-start-state*  $P$   $C$   $M$  *vs*  
**shows** *legal-execution*  $P$  (*jmm-J- $\mathcal{E}$*   $P$   $C$   $M$  *vs status*) = *legal-execution*  $P$  (*jmm'-J- $\mathcal{E}$*   $P$   $C$   $M$  *vs status*)  
**apply**(*rule ext iffI*) +  
**apply**(*clarify, erule J-legal-typesafe1*[*OF assms*])  
**apply**(*clarify, erule J-legal-typesafe2*)  
**done**

**end**

## 8.21 JMM type safety for bytecode

```

theory JMM-JVM-Typesafe
imports
  JMM-Typesafe2
  DRF-JVM
begin

locale JVM-allocated-heap-conf' =
  h: JVM-heap-conf
    addr2thread-id thread-id2addr
    spurious-wakeups
    empty-heap allocate  $\lambda\cdot$ . typeof-addr heap-read heap-write hconf
    P
  +
  h: JVM-allocated-heap
    addr2thread-id thread-id2addr
    spurious-wakeups
    empty-heap allocate  $\lambda\cdot$ . typeof-addr heap-read heap-write
    allocated
    P
  +
  heap''
    addr2thread-id thread-id2addr
    spurious-wakeups
    empty-heap allocate typeof-addr heap-read heap-write
    P
for addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
and spurious-wakeups :: bool
and empty-heap :: 'heap
and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
and typeof-addr :: 'addr  $\rightarrow$  htype
and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  bool
and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap  $\Rightarrow$  bool
and hconf :: 'heap  $\Rightarrow$  bool
and allocated :: 'heap  $\Rightarrow$  'addr set
and P :: 'addr jvm-prog

sublocale JVM-allocated-heap-conf' < h: JVM-allocated-heap-conf
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate  $\lambda\cdot$ . typeof-addr heap-read heap-write hconf allocated
  P
by(unfold-locales)

context JVM-allocated-heap-conf' begin

lemma exec-instr-New-type-match:
   $\llbracket (ta, s') \in h.exec\text{-}instr\ i\ P\ t\ h\ stk\ loc\ C\ M\ pc\ frs; NewHeapElem\ ad\ CTh \in set\ \llbracket ta \rrbracket_o; typeof\text{-}addr\ ad \neq None \rrbracket$ 
   $\implies typeof\text{-}addr\ ad = \lfloor CTh \rfloor$ 
by(cases i)(auto split: if-split-asm prod.split-asm dest: allocate-typeof-addr-SomeD red-external-aggr-New-type-match)

```

**lemma** *mexecd-New-type-match*:

$\llbracket h.\text{mexecd } P \ t \ (xcpfrs, h) \ ta \ (xcpfrs', h'); \text{NewHeapElem } ad \ CTh \in \text{set } \{\{ta\}_o; \text{typeof-addr } ad \neq \text{None} \} \rrbracket$

$\implies \text{typeof-addr } ad = \lfloor CTh \rfloor$

**apply**(*cases xcpfrs*)

**apply**(*cases xcpfrs'*)

**apply**(*simp add: split-beta*)

**apply**(*erule h.jvmd-NormalE*)

**apply**(*cases fst xcpfrs*)

**apply**(*auto 4 3 dest: exec-instr-New-type-match*)

**done**

**lemma** *mexecd-known-addr-typing'*:

**assumes** *wf*: *wf-jvm-prog* $\Phi$  *P*

**and** *ok*: *h.start-heap-ok*

**shows** *known-addr-typing'* *addr2thread-id thread-id2addr empty-heap allocate typeof-addr heap-write allocated h.jvm-known-addr JVM-final (h.mexecd P) ( $\lambda t \ (xcp, frs) \ h. \ h.\text{correct-state } \Phi \ t \ (xcp, h, frs)$ )* *P*

**proof** –

**interpret** *known-addr-typing*

*addr2thread-id thread-id2addr*

*spurious-wakeups*

*empty-heap allocate*  $\lambda\cdot.$  *typeof-addr heap-read heap-write*

*allocated h.jvm-known-addr*

*JVM-final h.mexecd P  $\lambda t \ (xcp, frs) \ h. \ h.\text{correct-state } \Phi \ t \ (xcp, h, frs)$*

*P*

**using** *assms* **by**(*rule h.mexecd-known-addr-typing*)

**show** *?thesis* **by**(*unfold-locales*)(*erule mexecd-New-type-match*)

**qed**

**lemma** *JVM-weakly-legal-read-value-typeable*:

**assumes** *wf*: *wf-jvm-prog* $\Phi$  *P*

**and** *wf-start*: *h.wf-start-state P C M vs*

**and** *legal*: *weakly-legal-execution P (h.JVMd- $\mathcal{E}$  P C M vs status) (E, ws)*

**and** *a*: *enat a < llength E*

**and** *read*: *action-obs E a = NormalAction (ReadMem ad al v)*

**shows**  $\exists T. P \vdash ad@al : T \wedge P \vdash v : \leq T$

**proof** –

**note** *wf*

**moreover from** *wf-start* **have** *h.start-heap-ok* **by** *cases*

**moreover from** *wf wf-start*

**have** *ts-ok ( $\lambda t \ (xcp, frs) \ h. \ h.\text{correct-state } \Phi \ t \ (xcp, h, frs)$ ) (thr (h.JVM-start-state P C M vs))*

*h.start-heap*

**using** *h.correct-jvm-state-initial[OF wf wf-start]*

**by**(*simp add: h.correct-jvm-state-def h.start-state-def split-beta*)

**moreover from** *wf* **obtain** *wf-md* **where** *wf'*: *wf-prog wf-md P* **by**(*blast dest: wt-jvm-progD*)

**hence** *wf-syscls P* **by**(*rule wf-prog-wf-syscls*)

**ultimately show** *?thesis* **using** *legal a read*

**by**(*rule known-addr-typing'.weakly-legal-read-value-typeable[OF mexecd-known-addr-typing']*)

**qed**

**end**



**abbreviation** *jmm-JVMd- $\mathcal{E}$* 

$:: \text{addr jvm-prog} \Rightarrow \text{cname} \Rightarrow \text{mname} \Rightarrow \text{addr val list} \Rightarrow \text{status} \Rightarrow (\text{addr} \times (\text{addr}, \text{addr}) \text{ obs-event action}) \text{ llist set}$

**where**

$\text{jmm-JVMd-}\mathcal{E} \ P \equiv$   
 $\text{JVM-heap-base.JVMd-}\mathcal{E} \ \text{addr2thread-id thread-id2addr jmm-spurious-wakeups jmm-empty jmm-allocate}$   
 $(\text{jmm-typeof-addr } P) \ \text{jmm-heap-read jmm-heap-write } P$

**abbreviation** *jmm'-JVMd- $\mathcal{E}$* 

$:: \text{addr jvm-prog} \Rightarrow \text{cname} \Rightarrow \text{mname} \Rightarrow \text{addr val list} \Rightarrow \text{status} \Rightarrow (\text{addr} \times (\text{addr}, \text{addr}) \text{ obs-event action}) \text{ llist set}$

**where**

$\text{jmm'-JVMd-}\mathcal{E} \ P \equiv$   
 $\text{JVM-heap-base.JVMd-}\mathcal{E} \ \text{addr2thread-id thread-id2addr jmm-spurious-wakeups jmm-empty jmm-allocate}$   
 $(\text{jmm-typeof-addr } P) \ (\text{jmm-heap-read-typed } P) \ \text{jmm-heap-write } P$

**abbreviation** *jmm-JVM-start-state*

$:: \text{addr jvm-prog} \Rightarrow \text{cname} \Rightarrow \text{mname} \Rightarrow \text{addr val list} \Rightarrow (\text{addr, thread-id, addr jvm-thread-state, JMM-heap, addr})$   
 $\text{state}$

**where**  $\text{jmm-JVM-start-state} \equiv \text{JVM-heap-base.JVM-start-state addr2thread-id jmm-empty jmm-allocate}$

**lemma** *jmm-JVM-heap-conf:*

$\text{JVM-heap-conf addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr } P) \ \text{jmm-heap-write}$   
 $\text{jmm-hconf } P$

**by**(*unfold-locales*)

**lemma** *jmm-JVMd-allocated-heap-conf':*

$\text{JVM-allocated-heap-conf'} \ \text{addr2thread-id thread-id2addr jmm-empty jmm-allocate (jmm-typeof-addr'}$   
 $P) \ \text{jmm-heap-write jmm-hconf jmm-allocated } P$

**apply**(*rule JVM-allocated-heap-conf'.intro*)

**apply**(*unfold jmm-typeof-addr'-conv-jmm-typeof-addr*)

**apply**(*unfold-locales*)

**done**

**lemma** *exec-instr-heap-read-typed:*

$(\text{ta}, \text{xcphfrs}') \in \text{JVM-heap-base.exec-instr addr2thread-id thread-id2addr spurious-wakeups empty-heap}$   
 $\text{allocate } (\lambda- :: \text{'heap. typeof-addr}) \ (\text{heap-base.heap-read-typed } (\lambda- :: \text{'heap. typeof-addr}) \ \text{heap-read } P)$   
 $\text{heap-write } i \ P \ t \ h \ \text{stk loc } C \ M \ \text{pc frs} \longleftrightarrow$

$(\text{ta}, \text{xcphfrs}') \in \text{JVM-heap-base.exec-instr addr2thread-id thread-id2addr spurious-wakeups empty-heap}$   
 $\text{allocate } (\lambda- :: \text{'heap. typeof-addr}) \ \text{heap-read heap-write } i \ P \ t \ h \ \text{stk loc } C \ M \ \text{pc frs} \wedge$

$(\forall \text{ad al v } T. \ \text{ReadMem ad al v} \in \text{set } \{\text{ta}\}_o \longrightarrow \text{heap-base'.addr-loc-type TYPE('heap) typeof-addr}$   
 $P \ \text{ad al } T \longrightarrow \text{heap-base'.conf TYPE('heap) typeof-addr } P \ v \ T)$

**apply**(*cases i*)

**apply**(*simp-all add: JVM-heap-base.exec-instr.simps split-beta cong: conj-cong*)

**apply**(*auto dest: heap-base.heap-read-typed-into-heap-read del: disjCI*)[5]

**apply**(*blast dest: heap-base.heap-read-typed-typed heap-base'.addr-loc-type-conv-addr-loc-type[THEN*  
 $\text{fun-cong, THEN fun-cong, THEN fun-cong, THEN iffD2}] \ \text{heap-base'.conf-conv-conf[THEN fun-cong,}$   
 $\text{THEN fun-cong, THEN iffD1}]$ )

**apply**(*auto dest: heap-base'.addr-loc-type-conv-addr-loc-type[THEN fun-cong, THEN fun-cong, THEN*  
 $\text{fun-cong, THEN iffD1}] \ \text{intro: heap-base'.conf-conv-conf[THEN fun-cong, THEN fun-cong, THEN iffD2}]$   
 $\text{heap-base.heap-read-typedI}[1]$ )

**apply**(blast dest: heap-base.heap-read-typed-typed heap-base'.addr-loc-type-conv-addr-loc-type[THEN fun-cong, THEN fun-cong, THEN fun-cong, THEN iffD2] heap-base'.conf-conv-conf[THEN fun-cong, THEN fun-cong, THEN iffD1])  
**apply**(auto dest: heap-base'.addr-loc-type-conv-addr-loc-type[THEN fun-cong, THEN fun-cong, THEN fun-cong, THEN iffD1] intro: heap-base'.conf-conv-conf[THEN fun-cong, THEN fun-cong, THEN iffD2] heap-base.heap-read-typedI)[1]  
**apply**(blast dest: heap-base.heap-read-typed-typed heap-base'.addr-loc-type-conv-addr-loc-type[THEN fun-cong, THEN fun-cong, THEN fun-cong, THEN iffD2] heap-base'.conf-conv-conf[THEN fun-cong, THEN fun-cong, THEN iffD1])  
**subgoal by**(auto dest: heap-base.heap-read-typed-into-heap-read)  
**apply**(blast dest: heap-base.heap-read-typed-typed heap-base'.addr-loc-type-conv-addr-loc-type[THEN fun-cong, THEN fun-cong, THEN fun-cong, THEN iffD2] heap-base'.conf-conv-conf[THEN fun-cong, THEN fun-cong, THEN iffD1])  
**subgoal by**(auto dest: heap-base'.addr-loc-type-conv-addr-loc-type[THEN fun-cong, THEN fun-cong, THEN fun-cong, THEN iffD1] intro: heap-base'.conf-conv-conf[THEN fun-cong, THEN fun-cong, THEN iffD2] heap-base.heap-read-typedI)[1]  
**subgoal by**(auto 4 3 dest: heap-base.heap-read-typed-into-heap-read intro: heap-base.heap-read-typedI simp add: heap-base'.conf-conv-conf heap-base'.addr-loc-type-conv-addr-loc-type)

**apply**(subst red-external-aggr-heap-read-typed)  
**apply**(fastforce)  
**apply** auto  
**done**

**lemma** exec-heap-read-typed:

$(ta, xcphfrs') \in JVM\text{-}heap\text{-}base.exec\ addr2thread\text{-}id\ thread\text{-}id2addr\ spurious\text{-}wakeup\ empty\text{-}heap\ allocate\ (\lambda\text{-} :: 'heap.\ typeof\text{-}addr)\ (heap\text{-}base.heap\text{-}read\text{-}typed\ (\lambda\text{-}.\ typeof\text{-}addr)\ heap\text{-}read\ P)\ heap\text{-}write\ P\ t\ xcphfrs \longleftrightarrow$

$(ta, xcphfrs') \in JVM\text{-}heap\text{-}base.exec\ addr2thread\text{-}id\ thread\text{-}id2addr\ spurious\text{-}wakeup\ empty\text{-}heap\ allocate\ (\lambda\text{-} :: 'heap.\ typeof\text{-}addr)\ heap\text{-}read\ heap\text{-}write\ P\ t\ xcphfrs \wedge$

$(\forall ad\ al\ v\ T.\ ReadMem\ ad\ al\ v \in set\ \{\!\{ta\}\!\}_o \longrightarrow heap\text{-}base'.addr\text{-}loc\text{-}type\ TYPE('heap)\ typeof\text{-}addr\ P\ ad\ al\ T \longrightarrow heap\text{-}base'.conf\ TYPE('heap)\ typeof\text{-}addr\ P\ v\ T)$

**apply**(cases xcphfrs)  
**apply**(cases fst xcphfrs)  
**apply**(case-tac [!] snd (snd xcphfrs))  
**apply**(auto simp add: JVM-heap-base.exec.simps exec-instr-heap-read-typed)  
**done**

**lemma** exec-1-d-heap-read-typed:

$JVM\text{-}heap\text{-}base.exec\text{-}1\text{-}d\ addr2thread\text{-}id\ thread\text{-}id2addr\ spurious\text{-}wakeup\ empty\text{-}heap\ allocate\ (\lambda\text{-} :: 'heap.\ typeof\text{-}addr)\ (heap\text{-}base.heap\text{-}read\text{-}typed\ (\lambda\text{-}.\ typeof\text{-}addr)\ heap\text{-}read\ P)\ heap\text{-}write\ P\ t\ (Normal\ xcphfrs)\ ta\ (Normal\ xcphfrs') \longleftrightarrow$

$JVM\text{-}heap\text{-}base.exec\text{-}1\text{-}d\ addr2thread\text{-}id\ thread\text{-}id2addr\ spurious\text{-}wakeup\ empty\text{-}heap\ allocate\ (\lambda\text{-} :: 'heap.\ typeof\text{-}addr)\ heap\text{-}read\ heap\text{-}write\ P\ t\ (Normal\ xcphfrs)\ ta\ (Normal\ xcphfrs') \wedge$

$(\forall ad\ al\ v\ T.\ ReadMem\ ad\ al\ v \in set\ \{\!\{ta\}\!\}_o \longrightarrow heap\text{-}base'.addr\text{-}loc\text{-}type\ TYPE('heap)\ typeof\text{-}addr\ P\ ad\ al\ T \longrightarrow heap\text{-}base'.conf\ TYPE('heap)\ typeof\text{-}addr\ P\ v\ T)$

**by**(auto elim!: JVM-heap-base.jvmd-NormalE intro: JVM-heap-base.exec-1-d-NormalI simp add: exec-heap-read-typed JVM-heap-base.exec-d-def)

**lemma** mexecd-heap-read-typed:

$JVM\text{-}heap\text{-}base.mexecd\ addr2thread\text{-}id\ thread\text{-}id2addr\ spurious\text{-}wakeup\ empty\text{-}heap\ allocate\ (\lambda\text{-} :: 'heap.\ typeof\text{-}addr)\ (heap\text{-}base.heap\text{-}read\text{-}typed\ (\lambda\text{-} :: 'heap.\ typeof\text{-}addr)\ heap\text{-}read\ P)\ heap\text{-}write\ P\ t\ xcprsh\ ta\ xcprsh' \longleftrightarrow$

$JVM\text{-}heap\text{-}base.mexecd\ addr2thread\text{-}id\ thread\text{-}id2addr\ spurious\text{-}wakeup\ empty\text{-}heap\ allocate\ (\lambda\text{-} ::$

'heap. typeof-addr) heap-read heap-write  $P$   $t$  xcpfrsh  $ta$  xcpfrsh'  $\wedge$   
 $(\forall ad\ al\ v\ T. \text{ReadMem } ad\ al\ v \in \text{set } \{\{ta\}\}_o \longrightarrow \text{heap-base'.addr-loc-type TYPE('heap) typeof-addr}$   
 $P\ ad\ al\ T \longrightarrow \text{heap-base'.conf TYPE('heap) typeof-addr } P\ v\ T)$   
**by**(simp add: split-beta exec-1-d-heap-read-typed)

**lemma** if-mexecd-heap-read-typed:

multithreaded-base.init-fin JVM-final (JVM-heap-base.mexecd addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate  $(\lambda- :: \text{'heap. typeof-addr})$  (heap-base.heap-read-typed  $(\lambda- :: \text{'heap. typeof-addr})$  heap-read  $P$ ) heap-write  $P$ )  $t\ xh\ ta\ x'h' \longleftrightarrow$

if-heap-read-typed JVM-final (JVM-heap-base.mexecd addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate  $(\lambda- :: \text{'heap. typeof-addr})$  heap-read heap-write  $P$ ) typeof-addr  $P\ t\ xh\ ta\ x'h'$

**unfolding** multithreaded-base.init-fin.simps

**by**(subst mexecd-heap-read-typed) fastforce

**lemma** JVMd- $\mathcal{E}$ -heap-read-typedI:

$\llbracket E \in \text{JVM-heap-base.JVMd-}\mathcal{E}\ \text{addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate}$   
 $(\lambda- :: \text{'heap. typeof-addr})\ \text{heap-read heap-write } P\ C\ M\ \text{vs status};$

$\bigwedge ad\ al\ v\ T. \llbracket \text{NormalAction (ReadMem } ad\ al\ v) \in \text{snd 'lset } E; \text{heap-base'.addr-loc-type TYPE('heap)}$   
 $\text{typeof-addr } P\ ad\ al\ T \rrbracket \implies \text{heap-base'.conf TYPE('heap) typeof-addr } P\ v\ T \rrbracket$

$\implies E \in \text{JVM-heap-base.JVMd-}\mathcal{E}\ \text{addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate}$   
 $(\lambda- :: \text{'heap. typeof-addr})\ (\text{heap-base.heap-read-typed } (\lambda- :: \text{'heap. typeof-addr})\ \text{heap-read } P)\ \text{heap-write}$   
 $P\ C\ M\ \text{vs status}$

**apply**(erule imageE, hypsubst)

**apply**(rule imageI)

**apply**(erule multithreaded-base. $\mathcal{E}$ .cases, hypsubst)

**apply**(rule multithreaded-base. $\mathcal{E}$ .intros)

**apply**(subst if-mexecd-heap-read-typed[abs-def])

**apply**(erule if-mthr-Runs-heap-read-typedI)

**apply**(auto simp add: image-Un lset-lmap[symmetric] lmap-lconcat llist.map-comp o-def split-def simp  
del: lset-lmap)

**done**

**lemma** jmm'-exec-instrI:

$\llbracket (ta, xcpfrs) \in \text{JVM-heap-base.exec-instr addr2thread-id thread-id2addr spurious-wakeups empty-heap}$   
 $\text{allocate typeof-addr jmm-heap-read jmm-heap-write } i\ P\ t\ h\ stk\ loc\ C\ M\ pc\ frs;$

$\text{final-thread.actions-ok (final-thread.init-fin-final JVM-final) } s\ t\ ta \rrbracket$

$\implies \exists ta\ xcpfrs. (ta, xcpfrs) \in \text{JVM-heap-base.exec-instr addr2thread-id thread-id2addr spuri-}$   
 $\text{ous-wakeups empty-heap allocate typeof-addr (heap-base.heap-read-typed typeof-addr jmm-heap-read } P)$   
 $\text{jmm-heap-write } i\ P\ t\ h\ stk\ loc\ C\ M\ pc\ frs \wedge \text{final-thread.actions-ok (final-thread.init-fin-final JVM-final)}$   
 $s\ t\ ta$

**apply**(cases  $i$ )

**apply**(auto simp add: JVM-heap-base.exec-instr.simps split-beta final-thread.actions-ok-iff intro!: jmm-heap-read-typed-default-  
rev-image-eqI simp del: split-paired-Ex split: if-split-asm)

**subgoal for**  $F\ D$

**apply**(cases  $hd\ (tl\ stk) = (\text{default-val (THE } T. \text{heap-base.addr-loc-type typeof-addr } P\ h\ (\text{the-Addr}$   
 $(hd\ (tl\ (tl\ stk)))) (CField\ D\ F)\ T)))$

**subgoal by**(auto simp add: JVM-heap-base.exec-instr.simps split-beta final-thread.actions-ok-iff intro!: jmm-heap-read-typed-default-val rev-image-eqI simp del: split-paired-Ex split: if-split-asm del: dis-  
jCI intro!: disjI1 exI)

**subgoal by**(auto simp add: JVM-heap-base.exec-instr.simps split-beta final-thread.actions-ok-iff intro!: jmm-heap-read-typed-default-val rev-image-eqI simp del: split-paired-Ex split: if-split-asm del: dis-  
jCI intro!: disjI2 exI)

**done**

**subgoal for**  $F\ D$

**apply**(cases *hd* (*tl stk*) = (default-val (*THE T. heap-base.addr-loc-type* typeof-addr *P h* (the-Addr (*hd* (*tl* (*tl stk*)))) (*CField D F*) *T*)))

**subgoal by**(auto simp add: *JVM-heap-base.exec-instr.simps* split-beta final-thread.actions-ok-iff intro!: *jmm-heap-read-typed-default-val rev-image-eqI* simp del: split-paired-Ex split: if-split-asm del: disjCI intro!: disjI1 exI *jmm-heap-write.intros*)

**subgoal by**(rule exI conjI disjI2 refl *jmm-heap-read-typed-default-val|assumption*) + auto

**done**

**subgoal by**(drule red-external-aggr-heap-read-typedI)(fastforce simp add: final-thread.actions-ok-iff simp del: split-paired-Ex) +

**done**

**lemma** *jmm'-execI*:

$\llbracket (ta, xcphfrs') \in \text{JVM-heap-base.exec addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr jmm-heap-read jmm-heap-write } P \ t \ xcphfrs;$

$\text{final-thread.actions-ok (final-thread.init-fin-final JVM-final) } s \ t \ ta \rrbracket$

$\implies \exists ta \ xcphfrs'. (ta, xcphfrs') \in \text{JVM-heap-base.exec addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr (heap-base.heap-read-typed typeof-addr jmm-heap-read } P) \text{ jmm-heap-write } P \ t \ xcphfrs \wedge \text{final-thread.actions-ok (final-thread.init-fin-final JVM-final) } s \ t \ ta$

**apply**(cases *xcphfrs*)

**apply**(cases *snd* (*snd xcphfrs*))

**apply**(simp add: *JVM-heap-base.exec.simps*)

**apply**(cases *fst xcphfrs*)

**apply**(fastforce simp add: *JVM-heap-base.exec.simps* dest!: *jmm'-exec-instrI*) +

**done**

**lemma** *jmm'-execdI*:

$\llbracket \text{JVM-heap-base.exec-1-d addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr jmm-heap-read jmm-heap-write } P \ t \ (\text{Normal } xcphfrs) \ ta \ (\text{Normal } xcphfrs');$

$\text{final-thread.actions-ok (final-thread.init-fin-final JVM-final) } s \ t \ ta \rrbracket$

$\implies \exists ta \ xcphfrs'. \text{JVM-heap-base.exec-1-d addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr (heap-base.heap-read-typed typeof-addr jmm-heap-read } P) \text{ jmm-heap-write } P \ t \ (\text{Normal } xcphfrs) \ ta \ (\text{Normal } xcphfrs') \wedge \text{final-thread.actions-ok (final-thread.init-fin-final JVM-final) } s \ t \ ta$

**apply**(erule *JVM-heap-base.jvmd-NormalE*)

**apply**(drule (1) *jmm'-execI*)

**apply**(force intro: *JVM-heap-base.exec-1-d-NormalI* simp add: *JVM-heap-base.exec-d-def*)

**done**

**lemma** *jmm'-mexecdI*:

$\llbracket \text{JVM-heap-base.mexecd addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr jmm-heap-read jmm-heap-write } P \ t \ xcpfrsh \ ta \ xcpfrsh';$

$\text{final-thread.actions-ok (final-thread.init-fin-final JVM-final) } s \ t \ ta \rrbracket$

$\implies \exists ta \ xcpfrsh'. \text{JVM-heap-base.mexecd addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr (heap-base.heap-read-typed typeof-addr jmm-heap-read } P) \text{ jmm-heap-write } P \ t \ xcpfrsh \ ta \ xcpfrsh' \wedge \text{final-thread.actions-ok (final-thread.init-fin-final JVM-final) } s \ t \ ta$

**by**(simp add: split-beta)(drule (1) *jmm'-execdI*, auto 4 10)

**lemma** *if-mexecd-heap-read-not-stuck*:

$\llbracket \text{multithreaded-base.init-fin JVM-final (JVM-heap-base.mexecd addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr jmm-heap-read jmm-heap-write } P) \ t \ xh \ ta \ x'h';$

$\text{final-thread.actions-ok (final-thread.init-fin-final JVM-final) } s \ t \ ta \rrbracket$

$\implies \exists ta \ x'h'. \text{multithreaded-base.init-fin JVM-final (JVM-heap-base.mexecd addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr (heap-base.heap-read-typed typeof-addr jmm-heap-read } P) \text{ jmm-heap-write } P) \ t \ xh \ ta \ x'h' \wedge \text{final-thread.actions-ok (final-thread.init-fin-final JVM-final) } s \ t \ ta$

```

apply(erule multithreaded-base.init-fin.cases)
  apply hypsubst
  apply(drule jmm'-mexecdI)
  apply(simp add: final-thread.actions-ok-iff)
  apply clarify
  apply(subst (2) split-paired-Ex)
  apply(subst (2) split-paired-Ex)
  apply(subst (2) split-paired-Ex)
  apply(rule exI conjI)+
  apply(rule multithreaded-base.init-fin.intros)
  apply simp
  apply(simp add: final-thread.actions-ok-iff)
apply(blast intro: multithreaded-base.init-fin.intros)
apply(blast intro: multithreaded-base.init-fin.intros)
done

```

**lemma** *if-mExecd-heap-read-not-stuck*:

*multithreaded-base.redT (final-thread.init-fin-final JVM-final) (multithreaded-base.init-fin JVM-final (JVM-heap-base.mexecd addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr jmm-heap-read jmm-heap-write P)) convert-RA' s tta s'*

$\implies \exists tta s'. \text{multithreaded-base.redT (final-thread.init-fin-final JVM-final) (multithreaded-base.init-fin JVM-final (JVM-heap-base.mexecd addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate typeof-addr (heap-base.heap-read-typed typeof-addr jmm-heap-read P) jmm-heap-write P)) convert-RA' s tta s'}$

```

apply(erule multithreaded-base.redT.cases)
apply hypsubst
thm if-mexecd-heap-read-not-stuck
apply(drule (1) if-mexecd-heap-read-not-stuck)
apply(erule exE)+
apply(rename-tac ta' x'h')
apply(insert redT-updWs-total)
apply(erule-tac x=t in meta-allE)
apply(erule-tac x=wset s in meta-allE)
apply(erule-tac x={ta'}_w in meta-allE)
apply clarsimp
apply(rule exI)+
apply(auto intro!: multithreaded-base.redT.intros)[1]
apply hypsubst
apply(rule exI conjI)+
apply(rule multithreaded-base.redT.redT-acquire)
apply assumption+
done

```

**lemma** *JVM-legal-typesafe1*:

**assumes** *wfP*: wf-jvm-prog *P*  
**and** *ok*: jmm-wf-start-state *P C M vs*  
**and** *legal*: legal-execution *P (jmm-JVMd- $\mathcal{E}$  P C M vs status) (E, ws)*  
**shows** legal-execution *P (jmm'-JVMd- $\mathcal{E}$  P C M vs status) (E, ws)*

**proof** –

**let**  $?E = \text{jmm-JVMd-}\mathcal{E} \ P \ C \ M \ \text{vs} \ \text{status}$   
**let**  $?E' = \text{jmm'-JVMd-}\mathcal{E} \ P \ C \ M \ \text{vs} \ \text{status}$   
**from** *legal* **obtain** *J*  
**where** *justified*:  $P \vdash (E, ws) \text{ justified-by } J$   
**and** *range*:  $\text{range} (\text{justifying-exec} \circ J) \subseteq ?E$

**and**  $E: E \in ?\mathcal{E}$  **and**  $wf: P \vdash (E, ws) \checkmark$  **by**  $(auto \text{ simp add: gen-legal-execution.simps})$   
**let**  $?J = J(0 := \{\}, committed = \{\}, justifying-exec = justifying-exec(J\ 1), justifying-ws = justifying-ws(J\ 1), action-translation = id)$

**from**  $wfP$  **obtain**  $\Phi$  **where**  $\Phi: wf-jvm-prog_{\Phi} P$  **by**  $(auto \text{ simp add: wf-jvm-prog-def})$   
**hence**  $wf-sys: wf-syscls P$  **by**  $(auto \text{ dest: wt-jvm-progD intro: wf-prog-wf-syscls})$

**from**  $justified$  **have**  $P \vdash (justifying-exec(J\ 1), justifying-ws(J\ 1)) \checkmark$  **by**  $(simp \text{ add: justification-well-formed-def})$   
**with**  $justified$  **have**  $P \vdash (E, ws)$   $justified-by\ ?J$  **by**  $(rule \text{ drop-0th-justifying-exec})$   
**moreover** **have**  $range(justifying-exec \circ ?J) \subseteq ?\mathcal{E}'$

**proof**  
**fix**  $\xi$   
**assume**  $\xi \in range(justifying-exec \circ ?J)$   
**then obtain**  $n$  **where**  $\xi = justifying-exec(?J\ n)$  **by**  $auto$   
**then obtain**  $n$  **where**  $\xi: \xi = justifying-exec(J\ n)$  **and**  $n: n > 0$  **by**  $(auto \text{ split: if-split-asm})$   
**from**  $range\ \xi$  **have**  $\xi \in ?\mathcal{E}$  **by**  $auto$   
**thus**  $\xi \in ?\mathcal{E}'$  **unfolding**  $jmm-typeof-addr'-conv-jmm-type-addr[symmetric, abs-def]$   
**proof**  $(rule \text{ JVMd-}\mathcal{E}\text{-heap-read-typedI})$   
**fix**  $ad\ al\ v\ T$   
**assume**  $read: NormalAction(ReadMem\ ad\ al\ v) \in snd\ 'lset\ \xi$   
**and**  $adal: P \vdash jmm\ ad@al : T$   
**from**  $read$  **obtain**  $a$  **where**  $a: enat\ a < llength\ \xi$   $action-obs\ \xi\ a = NormalAction(ReadMem\ ad\ al\ v)$   
**unfolding**  $lset-conv-lnth$  **by**  $(auto \text{ simp add: action-obs-def})$

**have**  $ts-ok(\lambda t(xcp, frs)\ h. JVM\text{-heap-conf-base.correct-state}\ addr2thread\text{-id}\ jmm\text{-empty}\ jmm\text{-allocate}(\lambda\text{-}. jmm\text{-typeof-addr}'\ P)\ jmm\text{-hconf}\ P\ \Phi\ t(xcp, h, frs))\ (thr\ (jmm\text{-JVM-start-state}\ P\ C\ M\ vs))\ jmm.start\text{-heap})$

**using**  $JVM\text{-heap-conf.correct-jvm-state-initial}[OF\ jmm\text{-JVM-heap-conf}\ \Phi\ ok]$   
**by**  $(simp \text{ add: JVM-heap-conf-base.correct-jvm-state-def}\ jmm\text{-typeof-addr}'\text{-conv-jmm-typeof-addr-heap-base.start-state-def}\ split\text{-beta})$   
**with**  $JVM\text{-allocated-heap-conf'}.mexecd\text{-known-addr}\text{-typing}'[OF\ jmm\text{-JVMd-allocated-heap-conf}'\ \Phi\ jmm\text{-start-heap-ok}]$   
**have**  $\exists T. P \vdash jmm\ ad@al : T \wedge P \vdash jmm\ v : \leq T$   
**using**  $wf\text{-sys}\ is\text{-justified-by-imp-is-weakly-justified-by}[OF\ justified\ wf]\ range\ n\ a$   
**unfolding**  $jmm\text{-typeof-addr}'\text{-conv-jmm-type-addr}[symmetric, abs-def]\ \xi$   
**by**  $(rule\ known\text{-addr}\text{-typing}'.read\text{-value-typeable-justifying})$   
**thus**  $P \vdash jmm\ v : \leq T$  **using**  $adal$   
**by**  $(auto \text{ dest: jmm.addr-loc-type-fun}[unfolded\ jmm\text{-typeof-addr-conv-jmm-typeof-addr}', unfolded\ heap\text{-base}'.addr\text{-loc-type-conv-addr-loc-type}])$

**qed**  
**qed**  
**moreover from**  $E$  **have**  $E \in ?\mathcal{E}'$   
**unfolding**  $jmm\text{-typeof-addr}'\text{-conv-jmm-type-addr}[symmetric, abs-def]$   
**proof**  $(rule \text{ JVMd-}\mathcal{E}\text{-heap-read-typedI})$   
**fix**  $ad\ al\ v\ T$   
**assume**  $read: NormalAction(ReadMem\ ad\ al\ v) \in snd\ 'lset\ E$   
**and**  $adal: P \vdash jmm\ ad@al : T$   
**from**  $read$  **obtain**  $a$  **where**  $a: enat\ a < llength\ E$   $action-obs\ E\ a = NormalAction(ReadMem\ ad\ al\ v)$   
**unfolding**  $lset-conv-lnth$  **by**  $(auto \text{ simp add: action-obs-def})$   
**with**  $jmm\text{-JVMd-allocated-heap-conf}'\ \Phi\ ok\ legal\text{-imp-weakly-legal-execution}[OF\ legal]$

```

have  $\exists T. P \vdash_{jmm} ad@al : T \wedge P \vdash_{jmm} v : \leq T$ 
  unfolding jmm-typeof-addr'-conv-jmm-typeof-addr[symmetric, abs-def]
  by(rule JVM-allocated-heap-conf'.JVM-weakly-legal-read-value-typeable)
thus  $P \vdash_{jmm} v : \leq T$  using adal
  by(auto dest: jmm.addr-loc-type-fun[unfolded jmm-typeof-addr-conv-jmm-typeof-addr', unfolded
heap-base'.addr-loc-type-conv-addr-loc-type])
qed
ultimately show ?thesis using wf unfolding gen-legal-execution.simps by blast
qed

```

lemma *JVM-weakly-legal-typesafe1*:

```

assumes wfP: wf-jvm-prog P
and ok: jmm-wf-start-state P C M vs
and legal: weakly-legal-execution P (jmm-JVMd- $\mathcal{E}$  P C M vs status) (E, ws)
shows weakly-legal-execution P (jmm'-JVMd- $\mathcal{E}$  P C M vs status) (E, ws)
proof -
  let  $?E = jmm-JVMd-\mathcal{E} P C M vs status$ 
  let  $?E' = jmm'-JVMd-\mathcal{E} P C M vs status$ 
  from legal obtain J
    where justified:  $P \vdash (E, ws)$  weakly-justified-by J
    and range:  $range (justifying-exec \circ J) \subseteq ?E$ 
    and E:  $E \in ?E$  and wf:  $P \vdash (E, ws) \checkmark$  by(auto simp add: gen-legal-execution.simps)
  let  $?J = J(0 := \{\}, justifying-exec = justifying-exec (J 1), justifying-ws = justifying-ws$ 
  ( $J 1$ ), action-translation = id)

```

```

from wfP obtain  $\Phi$  where  $\Phi$ : wf-jvm-prog $_{\Phi}$  P by(auto simp add: wf-jvm-prog-def)
hence wf-sys: wf-syscls P by(auto dest: wt-jvm-progD intro: wf-prog-wf-syscls)

```

```

from justified have  $P \vdash (justifying-exec (J 1), justifying-ws (J 1)) \checkmark$  by(simp add: justification-well-formed-def)

```

```

with justified have  $P \vdash (E, ws)$  weakly-justified-by  $?J$  by(rule drop-0th-weakly-justifying-exec)
moreover have  $range (justifying-exec \circ ?J) \subseteq ?E'$ 

```

proof

```

  fix  $\xi$ 
  assume  $\xi \in range (justifying-exec \circ ?J)$ 
  then obtain n where  $\xi = justifying-exec (?J n)$  by auto
  then obtain n where  $\xi$ :  $\xi = justifying-exec (J n)$  and n:  $n > 0$  by(auto split: if-split-asm)
  from range  $\xi$  have  $\xi \in ?E$  by auto
  thus  $\xi \in ?E'$  unfolding jmm-typeof-addr'-conv-jmm-type-addr[symmetric, abs-def]
  proof(rule JVMd- $\mathcal{E}$ -heap-read-typedI)
    fix ad al v T
    assume read: NormalAction (ReadMem ad al v)  $\in snd \text{ ' lset } \xi$ 
    and adal:  $P \vdash_{jmm} ad@al : T$ 
    from read obtain a where a:  $enat a < llength \xi$  action-obs  $\xi a = NormalAction (ReadMem ad$ 
  al v)
    unfolding lset-conv-lnth by(auto simp add: action-obs-def)

```

```

  have ts-ok ( $\lambda t (xcp, frs) h. JVM\text{-}heap\text{-}conf\text{-}base.correct\text{-}state\ addr2thread\text{-}id\ jmm\text{-}empty\ jmm\text{-}allocate$ 
  ( $\lambda\text{-}. jmm\text{-}typeof\text{-}addr' P) jmm\text{-}hconf P \Phi t (xcp, h, frs)) (thr (jmm\text{-}JVM\text{-}start\text{-}state P C M vs))$ 
  jmm.start-heap

```

```

  using JVM-heap-conf.correct-jvm-state-initial[OF jmm-JVM-heap-conf \Phi ok]
  by(simp add: JVM-heap-conf-base.correct-jvm-state-def jmm-typeof-addr'-conv-jmm-typeof-addr
  heap-base.start-state-def split-beta)

```

**with** *JVM-allocated-heap-conf'.mexecd-known-addr-typing'*[*OF jmm-JVMd-allocated-heap-conf'*  
 $\Phi$  *jmm-start-heap-ok*]  
**have**  $\exists T. P \vdash_{jmm} ad@al : T \wedge P \vdash_{jmm} v : \leq T$   
**using** *wf-sys justified range n a*  
**unfolding** *jmm-typeof-addr'-conv-jmm-type-addr*[*symmetric, abs-def*]  $\xi$   
**by**(*rule known-addr-typing'.read-value-typeable-justifying*)  
**thus**  $P \vdash_{jmm} v : \leq T$  **using** *adal*  
**by**(*auto dest: jmm.addr-loc-type-fun[unfolded jmm-typeof-addr-conv-jmm-typeof-addr', unfolded*  
*heap-base'.addr-loc-type-conv-addr-loc-type]*)  
**qed**  
**qed**  
**moreover from** *E* **have**  $E \in ?\mathcal{E}'$   
**unfolding** *jmm-typeof-addr'-conv-jmm-type-addr*[*symmetric, abs-def*]  
**proof**(*rule JVMd- $\mathcal{E}$ -heap-read-typedI*)  
**fix** *ad al v T*  
**assume** *read: NormalAction (ReadMem ad al v)  $\in$  snd ' lset E*  
**and** *adal: P  $\vdash_{jmm}$  ad@al : T*  
**from** *read obtain a where a: enat a < llength E action-obs E a = NormalAction (ReadMem ad*  
*al v)*  
**unfolding** *lset-conv-lnth* **by**(*auto simp add: action-obs-def*)  
**with** *jmm-JVMd-allocated-heap-conf'  $\Phi$  ok legal*  
**have**  $\exists T. P \vdash_{jmm} ad@al : T \wedge P \vdash_{jmm} v : \leq T$   
**unfolding** *jmm-typeof-addr'-conv-jmm-typeof-addr*[*symmetric, abs-def*]  
**by**(*rule JVM-allocated-heap-conf'.JVM-weakly-legal-read-value-typeable*)  
**thus**  $P \vdash_{jmm} v : \leq T$  **using** *adal*  
**by**(*auto dest: jmm.addr-loc-type-fun[unfolded jmm-typeof-addr-conv-jmm-typeof-addr', unfolded*  
*heap-base'.addr-loc-type-conv-addr-loc-type]*)  
**qed**  
**ultimately show** *?thesis using wf unfolding gen-legal-execution.simps by blast*  
**qed**

**lemma** *JVMd- $\mathcal{E}$ -heap-read-typedD:*

$E \in \text{JVM-heap-base.JVMd-}\mathcal{E} \text{ addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate}$   
 $(\lambda-. \text{typeof-addr}) (\text{heap-base.heap-read-typed } (\lambda-. \text{typeof-addr}) \text{ jmm-heap-read } P) \text{ jmm-heap-write } P \text{ C } M \text{ vs status}$

$\implies E \in \text{JVM-heap-base.JVMd-}\mathcal{E} \text{ addr2thread-id thread-id2addr spurious-wakeups empty-heap allocate}$   
 $(\lambda-. \text{typeof-addr}) \text{ jmm-heap-read jmm-heap-write } P \text{ C } M \text{ vs status}$

**apply**(*erule imageE, hypsubst*)

**apply**(*rule imageI*)

**apply**(*erule multithreaded-base. $\mathcal{E}$ .cases, hypsubst*)

**apply**(*rule multithreaded-base. $\mathcal{E}$ .intros*)

**apply**(*subst (asm) if-mexecd-heap-read-typed[abs-def]*)

**apply**(*erule if-mthr-Runs-heap-read-typedD*)

**apply**(*erule if-mExecd-heap-read-not-stuck[where typeof-addr= $\lambda-. \text{typeof-addr}$ , unfolded if-mexecd-heap-read-typed*  
**done**)

**lemma** *JVMd- $\mathcal{E}$ -typesafe-subset: jmm'-JVMd- $\mathcal{E}$  P C M vs status  $\subseteq$  jmm-JVMd- $\mathcal{E}$  P C M vs status*

**unfolding** *jmm-typeof-addr-def[abs-def]*

**by**(*rule subsetI*)(*erule JVMd- $\mathcal{E}$ -heap-read-typedD*)

**lemma** *JVMd-legal-typesafe2:*

**assumes** *legal: legal-execution P (jmm'-JVMd- $\mathcal{E}$  P C M vs status) (E, ws)*

**shows** *legal-execution P (jmm-JVMd- $\mathcal{E}$  P C M vs status) (E, ws)*

**proof** –



```

let ?E = jmm-JVMd-E P C M vs status
let ?E' = jmm'-JVMd-E P C M vs status
from legal obtain J
  where justified: P ⊢ (E, ws) justified-by J
  and range: range (justifying-exec ∘ J) ⊆ ?E'
  and E: E ∈ ?E' and wf: P ⊢ (E, ws) ✓ by (auto simp add: gen-legal-execution.simps)
from range E have range (justifying-exec ∘ J) ⊆ ?E E ∈ ?E
  using JVMd-E-typesafe-subset[of P status C M vs] by blast+
with justified wf
show ?thesis by (auto simp add: gen-legal-execution.simps)
qed

```

**theorem** *JVMd-weakly-legal-typesafe2:*

```

assumes legal: weakly-legal-execution P (jmm'-JVMd-E P C M vs status) (E, ws)
shows weakly-legal-execution P (jmm-JVMd-E P C M vs status) (E, ws)

```

**proof** –

```

let ?E = jmm-JVMd-E P C M vs status
let ?E' = jmm'-JVMd-E P C M vs status
from legal obtain J
  where justified: P ⊢ (E, ws) weakly-justified-by J
  and range: range (justifying-exec ∘ J) ⊆ ?E'
  and E: E ∈ ?E' and wf: P ⊢ (E, ws) ✓ by (auto simp add: gen-legal-execution.simps)
from range E have range (justifying-exec ∘ J) ⊆ ?E E ∈ ?E
  using JVMd-E-typesafe-subset[of P status C M vs] by blast+
with justified wf
show ?thesis by (auto simp add: gen-legal-execution.simps)
qed

```

**theorem** *JVMd-weakly-legal-typesafe:*

```

assumes wf-jvm-prog P
and jmm-wf-start-state P C M vs
shows weakly-legal-execution P (jmm-JVMd-E P C M vs status) = weakly-legal-execution P (jmm'-JVMd-E
P C M vs status)
apply (rule ext iffI) +
  apply (clarify, erule JVM-weakly-legal-typesafe1[OF assms])
apply (clarify, erule JVMd-weakly-legal-typesafe2)
done

```

**theorem** *JVMd-legal-typesafe:*

```

assumes wf-jvm-prog P
and jmm-wf-start-state P C M vs
shows legal-execution P (jmm-JVMd-E P C M vs status) = legal-execution P (jmm'-JVMd-E P C
M vs status)
apply (rule ext iffI) +
  apply (clarify, erule JVM-legal-typesafe1[OF assms])
apply (clarify, erule JVMd-legal-typesafe2)
done

```

**end**

## 8.22 Compiler correctness for JMM heap implementation 2

**theory** *JMM-Compiler-Type2*

**imports**

*JMM-Compiler*  
*JMM-J-Typesafe*  
*JMM-JVM-Typesafe*  
*JMM-Interp*

**begin**

**theorem** *J2JVM-jmm-correct*:

**assumes** *wf*: *wf-J-prog P*

**and** *wf-start*: *jmm-wf-start-state P C M vs*

**shows** *legal-execution P (jmm-J- $\mathcal{E}$  P C M vs Running) (E, ws)  $\longleftrightarrow$*

*legal-execution (J2JVM P) (jmm-JVMd- $\mathcal{E}$  (J2JVM P) C M vs Running) (E, ws)*

**using** *JVMd-legal-typesafe[OF wt-J2JVM[OF wf], of C M vs Running, symmetric] wf-start*

**by**(*simp only: J-legal-typesafe[OF assms] J-JVM-conf-read.red- $\mathcal{E}$ -eq-mexecd- $\mathcal{E}$ [OF jmm'-J-JVM-conf-read assms] J2JVM-def o-apply compP1-def compP2-def legal-execution-compP heap-base.wf-start-state-compP jmm-typeof-addr-compP heap-base.heap-read-typed-compP*)

**theorem** *J2JVM-jmm-correct-weak*:

**assumes** *wf*: *wf-J-prog P*

**and** *wf-start*: *jmm-wf-start-state P C M vs*

**shows** *weakly-legal-execution P (jmm-J- $\mathcal{E}$  P C M vs Running) (E, ws)  $\longleftrightarrow$*

*weakly-legal-execution (J2JVM P) (jmm-JVMd- $\mathcal{E}$  (J2JVM P) C M vs Running) (E, ws)*

**using** *JVMd-weakly-legal-typesafe[OF wt-J2JVM[OF wf], of C M vs Running, symmetric] wf-start*

**by**(*simp only: J-weakly-legal-typesafe[OF assms] J-JVM-conf-read.red- $\mathcal{E}$ -eq-mexecd- $\mathcal{E}$ [OF jmm'-J-JVM-conf-read assms] J2JVM-def o-apply compP1-def compP2-def weakly-legal-execution-compP heap-base.wf-start-state-compP jmm-typeof-addr-compP heap-base.heap-read-typed-compP*)

**theorem** *J2JVM-jmm-correctly-synchronized*:

**assumes** *wf*: *wf-J-prog P*

**and** *wf-start*: *jmm-wf-start-state P C M vs*

**and** *ka*:  $\bigcup (ka-Val \text{ ' set vs}) \subseteq \text{set jmm.start-addrs}$

**shows** *correctly-synchronized (J2JVM P) (jmm-JVMd- $\mathcal{E}$  (J2JVM P) C M vs Running)  $\longleftrightarrow$*

*correctly-synchronized P (jmm-J- $\mathcal{E}$  P C M vs Running)*

(**is** *?lhs  $\longleftrightarrow$  ?rhs*)

**proof**

**assume** *?lhs*

**show** *?rhs unfolding correctly-synchronized-def*

**proof**(*intro strip*)

**fix** *E ws a a'*

**assume** *E*: *E  $\in$  jmm-J- $\mathcal{E}$  P C M vs Running*

**and** *wf-exec*: *P  $\vdash$  (E, ws)  $\checkmark$*

**and** *sc*: *sequentially-consistent P (E, ws)*

**and** *actions*: *a  $\in$  actions E a'  $\in$  actions E*

**and** *conflict*: *P, E  $\vdash$  a  $\dagger$  a'*

**from** *E wf-exec sc*

**have** *legal-execution P (jmm-J- $\mathcal{E}$  P C M vs Running) (E, ws)*

**by**(*rule sc-legal.SC-is-legal[OF J-allocated-progress.J-sc-legal[OF jmm-J-allocated-progress wf jmm-heap-read-typeable wf-start ka]]*)

**hence** *legal-execution (J2JVM P) (jmm-JVMd- $\mathcal{E}$  (J2JVM P) C M vs Running) (E, ws)*

**by**(*simp only: J2JVM-jmm-correct[OF wf wf-start]*)

**hence** *E  $\in$  jmm-JVMd- $\mathcal{E}$  (J2JVM P) C M vs Running J2JVM P  $\vdash$  (E, ws)  $\checkmark$*

**by**(*simp-all add: gen-legal-execution.simps*)

**moreover from** *sc have sequentially-consistent (J2JVM P) (E, ws)*

```

    by(simp add: J2JVM-def compP2-def)
  moreover from conflict have J2JVM P,E ⊢ a † a'
    by(simp add: J2JVM-def compP2-def)
  ultimately have J2JVM P,E ⊢ a ≤hb a' ∨ J2JVM P,E ⊢ a' ≤hb a
    using ‹?lhs› actions by(auto simp add: correctly-synchronized-def)
  thus P,E ⊢ a ≤hb a' ∨ P,E ⊢ a' ≤hb a
    by(simp add: J2JVM-def compP2-def)
qed
next
assume ?rhs
show ?lhs unfolding correctly-synchronized-def
proof(intro strip)
  fix E ws a a'
  assume E: E ∈ jmm-JVMd-ℰ (J2JVM P) C M vs Running
    and wf-exec: J2JVM P ⊢ (E, ws) ✓
    and sc: sequentially-consistent (J2JVM P) (E, ws)
    and actions: a ∈ actions E a' ∈ actions E
    and conflict: J2JVM P,E ⊢ a † a'

  from wf have wf-jvm-prog (J2JVM P) by(rule wt-J2JVM)
  then obtain Φ where wf': wf-jvm-progΦ (J2JVM P)
    by(auto simp add: wf-jvm-prog-def)
  from wf-start have wf-start': jmm-wf-start-state (J2JVM P) C M vs
    by(simp add: J2JVM-def compP2-def heap-base.wf-start-state-compP)
  from E wf-exec sc
  have legal-execution (J2JVM P) (jmm-JVMd-ℰ (J2JVM P) C M vs Running) (E, ws)
    by(rule sc-legal.SC-is-legal[OF JVM-allocated-progress.JVM-sc-legal[OF jmm-JVM-allocated-progress
wf' jmm-heap-read-typeable wf-start' ka]])
  hence legal-execution P (jmm-J-ℰ P C M vs Running) (E, ws)
    by(simp only: J2JVM-jmm-correct[OF wf wf-start'])
  hence E ∈ jmm-J-ℰ P C M vs Running P ⊢ (E, ws) ✓
    by(simp-all add: gen-legal-execution.simps)
  moreover from sc have sequentially-consistent P (E, ws)
    by(simp add: J2JVM-def compP2-def)
  moreover from conflict have P,E ⊢ a † a'
    by(simp add: J2JVM-def compP2-def)
  ultimately have P,E ⊢ a ≤hb a' ∨ P,E ⊢ a' ≤hb a
    using ‹?rhs› actions by(auto simp add: correctly-synchronized-def)
  thus J2JVM P,E ⊢ a ≤hb a' ∨ J2JVM P,E ⊢ a' ≤hb a
    by(simp add: J2JVM-def compP2-def)
qed
qed
end

```

**theory JMM**

**imports**

JMM-DRF

SC-Legal

DRF-J

DRF-JVM

JMM-Type

JMM-Interp

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*JMM-Typesafe*  
*JMM-J-Typesafe*  
*JMM-JVM-Typesafe*  
*JMM-Compiler-Type2*

**begin**

**end**

**theory** *MM-Main*

**imports**

*SC*  
*SC-Interp*  
*SC-Collections*  
*JMM*

**begin**

**end**

## Chapter 9

# Schedulers

### 9.1 Refinement for multithreaded states

```
theory State-Refinement
imports
  ../Framework/FWSemantics
  ../Common/StartConfig
begin

type-synonym
  ('l,'t,'m,'m-t,'m-w,'s-i) state-refine = ('l,'t) locks × ('m-t × 'm) × 'm-w × 's-i

locale state-refine-base =
  fixes final :: 'x ⇒ bool
  and r :: 't ⇒ ('x × 'm) ⇒ (('l,'t,'x,'m,'w,'o) thread-action × 'x × 'm) Predicate.pred
  and convert-RA :: 'l released-locks ⇒ 'o list
  and thr-α :: 'm-t ⇒ ('l,'t,'x) thread-info
  and thr-invar :: 'm-t ⇒ bool
  and ws-α :: 'm-w ⇒ ('w,'t) wait-sets
  and ws-invar :: 'm-w ⇒ bool
  and is-α :: 's-i ⇒ 't interrupts
  and is-invar :: 's-i ⇒ bool
begin

fun state-α :: ('l,'t,'m,'m-t,'m-w, 's-i) state-refine ⇒ ('l,'t,'x,'m,'w) state
where state-α (ls, (ts, m), ws, is) = (ls, (thr-α ts, m), ws-α ws, is-α is)

lemma state-α-conv [simp]:
  locks (state-α s) = locks s
  thr (state-α s) = thr-α (thr s)
  shr (state-α s) = shr s
  wset (state-α s) = ws-α (wset s)
  interrupts (state-α s) = is-α (interrupts s)
by(case-tac [!]) s) auto

inductive state-invar :: ('l,'t,'m,'m-t,'m-w,'s-i) state-refine ⇒ bool
where [| thr-invar ts; ws-invar ws; is-invar is |] ⇒ state-invar (ls, (ts, m), ws, is)

inductive-simps state-invar-simps [simp]:
  state-invar (ls, (ts, m), ws, is)
```

```

lemma state-invarD [simp]:
  assumes state-invar s
  shows thr-invar (thr s) ws-invar (wset s) is-invar (interrupts s)
using assms by(case-tac [!] s) auto

end

sublocale state-refine-base < α: final-thread final .
sublocale state-refine-base < α:
  multithreaded-base
  final
   $\lambda t\ xm\ ta\ x'm'. \text{Predicate.eval } (r\ t\ xm)\ (ta,\ x'm')$ 
  .

definition (in heap-base) start-state-refine ::
   $'m\text{-}t \Rightarrow ('thread\text{-}id \Rightarrow ('x \times 'addr\ released\ locks) \Rightarrow 'm\text{-}t \Rightarrow 'm\text{-}t) \Rightarrow 'm\text{-}w \Rightarrow 's\text{-}i$ 
 $\Rightarrow (cname \Rightarrow mname \Rightarrow ty\ list \Rightarrow ty \Rightarrow 'md \Rightarrow 'addr\ val\ list \Rightarrow 'x) \Rightarrow 'md\ prog \Rightarrow cname \Rightarrow mname$ 
 $\Rightarrow 'addr\ val\ list$ 
 $\Rightarrow ('addr,\ 'thread\text{-}id,\ 'heap,\ 'm\text{-}t,\ 'm\text{-}w,\ 's\text{-}i)\ state\text{-}refine$ 
where
   $\bigwedge is\text{-}empty.$ 
  start-state-refine thr-empty thr-update ws-empty is-empty f P C M vs =
  (let (D, Ts, T, m) = method P C M
    in (K$ None, (thr-update start-tid (f D M Ts T (the m) vs, no-wait-locks) thr-empty, start-heap),
    ws-empty, is-empty))

definition Jinja-output ::
   $'s \Rightarrow 'thread\text{-}id \Rightarrow ('addr,\ 'thread\text{-}id,\ 'x,\ 'heap,\ 'addr,\ ('addr,\ 'thread\text{-}id)\ obs\text{-}event)\ thread\text{-}action$ 
 $\Rightarrow ('thread\text{-}id \times ('addr,\ 'thread\text{-}id)\ obs\text{-}event\ list)\ option$ 
where Jinja-output σ t ta = (if  $\llbracket ta \rrbracket_o = []$  then None else Some (t,  $\llbracket ta \rrbracket_o$ ))

lemmas [code] =
  heap-base.start-state-refine-def

end

```

## 9.2 Abstract scheduler

```

theory Scheduler
imports
  State-Refinement
  ../Framework/FWProgressAux
  ../Framework/FWLTS

  ../Basic/JT-ICF

```

**begin**

Define an unfold operation that puts everything into one function to avoid duplicate evaluation.

```

definition unfold-tllist' :: ('a  $\Rightarrow$  'b  $\times$  'a + 'c)  $\Rightarrow$  'a  $\Rightarrow$  ('b, 'c) tllist
where [code del]:

```

*unfold-tllist'* *f* =  
*unfold-tllist* ( $\lambda a. \exists c. f\ a = \text{Inr}\ c$ ) (*projr*  $\circ$  *f*) (*fst*  $\circ$  *projl*  $\circ$  *f*) (*snd*  $\circ$  *projl*  $\circ$  *f*)

**lemma** *unfold-tllist'* [*code*]:

*unfold-tllist'* *f* *a* =  
 (case *f* *a* of *Inr* *c*  $\Rightarrow$  *TNil* *c* | *Inl* (*b*, *a'*)  $\Rightarrow$  *TCons* *b* (*unfold-tllist'* *f* *a'*))  
**by**(rule *tllist.expand*)(auto simp add: *unfold-tllist'-def* *split*: *sum.split-asm*)

**type-synonym**

(*l*,*t*,*x*,*m*,*w*,*o*,*m-t*,*m-w*,*s-i*,*s*) *scheduler* =  
*s*  $\Rightarrow$  (*l*,*t*,*m*,*m-t*,*m-w*,*s-i*) *state-refine*  $\Rightarrow$  (*t*  $\times$  ((*l*,*t*,*x*,*m*,*w*,*o*) *thread-action*  $\times$  *x*  $\times$  *m*) *option*  
 $\times$  *s*) *option*

**locale** *scheduler-spec-base* =

*state-refine-base*  
*final* *r* *convert-RA*  
*thr- $\alpha$*  *thr-invar*  
*ws- $\alpha$*  *ws-invar*  
*is- $\alpha$*  *is-invar*  
**for** *final* :: *x*  $\Rightarrow$  *bool*  
**and** *r* :: *t*  $\Rightarrow$  (*x*  $\times$  *m*)  $\Rightarrow$  ((*l*,*t*,*x*,*m*,*w*,*o*) *thread-action*  $\times$  *x*  $\times$  *m*) *Predicate.pred*  
**and** *convert-RA* :: *l* *released-locks*  $\Rightarrow$  *o* *list*  
**and** *schedule* :: (*l*,*t*,*x*,*m*,*w*,*o*,*m-t*,*m-w*,*s-i*,*s*) *scheduler*  
**and**  *$\sigma$ -invar* :: *s*  $\Rightarrow$  *t* *set*  $\Rightarrow$  *bool*  
**and** *thr- $\alpha$*  :: *m-t*  $\Rightarrow$  (*l*,*t*,*x*) *thread-info*  
**and** *thr-invar* :: *m-t*  $\Rightarrow$  *bool*  
**and** *ws- $\alpha$*  :: *m-w*  $\Rightarrow$  (*w*,*t*) *wait-sets*  
**and** *ws-invar* :: *m-w*  $\Rightarrow$  *bool*  
**and** *is- $\alpha$*  :: *s-i*  $\Rightarrow$  *t* *interrupts*  
**and** *is-invar* :: *s-i*  $\Rightarrow$  *bool*

**locale** *scheduler-spec* =

*scheduler-spec-base*  
*final* *r* *convert-RA*  
*schedule*  *$\sigma$ -invar*  
*thr- $\alpha$*  *thr-invar*  
*ws- $\alpha$*  *ws-invar*  
*is- $\alpha$*  *is-invar*  
**for** *final* :: *x*  $\Rightarrow$  *bool*  
**and** *r* :: *t*  $\Rightarrow$  (*x*  $\times$  *m*)  $\Rightarrow$  ((*l*,*t*,*x*,*m*,*w*,*o*) *thread-action*  $\times$  *x*  $\times$  *m*) *Predicate.pred*  
**and** *convert-RA* :: *l* *released-locks*  $\Rightarrow$  *o* *list*  
**and** *schedule* :: (*l*,*t*,*x*,*m*,*w*,*o*,*m-t*,*m-w*,*s-i*,*s*) *scheduler*  
**and**  *$\sigma$ -invar* :: *s*  $\Rightarrow$  *t* *set*  $\Rightarrow$  *bool*  
**and** *thr- $\alpha$*  :: *m-t*  $\Rightarrow$  (*l*,*t*,*x*) *thread-info*  
**and** *thr-invar* :: *m-t*  $\Rightarrow$  *bool*  
**and** *ws- $\alpha$*  :: *m-w*  $\Rightarrow$  (*w*,*t*) *wait-sets*  
**and** *ws-invar* :: *m-w*  $\Rightarrow$  *bool*  
**and** *is- $\alpha$*  :: *s-i*  $\Rightarrow$  *t* *interrupts*  
**and** *is-invar* :: *s-i*  $\Rightarrow$  *bool*  
 +  
**fixes** *invariant* :: (*l*,*t*,*x*,*m*,*w*) *state set*  
**assumes** *schedule-NoneD*:  
 $\llbracket$  *schedule*  *$\sigma$*  *s* = *None*; *state-invar* *s*;  *$\sigma$ -invar*  *$\sigma$*  (*dom* (*thr- $\alpha$*  (*thr* *s*))); *state- $\alpha$*  *s*  $\in$  *invariant*  $\rrbracket$

$\Rightarrow \alpha.\text{active-threads}(\text{state-}\alpha\ s) = \{\}$   
**and** *schedule-Some-NoneD*:  
 $\llbracket \text{schedule } \sigma\ s = \lfloor (t, \text{None}, \sigma') \rfloor; \text{state-invar } s; \sigma\text{-invar } \sigma (\text{dom } (\text{thr-}\alpha (\text{thr } s))); \text{state-}\alpha\ s \in \text{invariant} \rrbracket$   
 $\Rightarrow \exists x\ ln\ n. \text{thr-}\alpha (\text{thr } s)\ t = \lfloor (x, ln) \rfloor \wedge ln\ \$\ n > 0 \wedge \neg \text{waiting } (ws\text{-}\alpha (wset\ s)\ t) \wedge \text{may-acquire-all } (locks\ s)\ t\ ln$   
**and** *schedule-Some-SomeD*:  
 $\llbracket \text{schedule } \sigma\ s = \lfloor (t, \lfloor (ta, x', m') \rfloor, \sigma') \rfloor; \text{state-invar } s; \sigma\text{-invar } \sigma (\text{dom } (\text{thr-}\alpha (\text{thr } s))); \text{state-}\alpha\ s \in \text{invariant} \rrbracket$   
 $\Rightarrow \exists x. \text{thr-}\alpha (\text{thr } s)\ t = \lfloor (x, \text{no-wait-locks}) \rfloor \wedge \text{Predicate.eval } (r\ t\ (x, shr\ s))\ (ta, x', m') \wedge \alpha.\text{actions-ok } (\text{state-}\alpha\ s)\ t\ ta$   
**and** *schedule-invar-None*:  
 $\llbracket \text{schedule } \sigma\ s = \lfloor (t, \text{None}, \sigma') \rfloor; \text{state-invar } s; \sigma\text{-invar } \sigma (\text{dom } (\text{thr-}\alpha (\text{thr } s))); \text{state-}\alpha\ s \in \text{invariant} \rrbracket$   
 $\Rightarrow \sigma\text{-invar } \sigma' (\text{dom } (\text{thr-}\alpha (\text{thr } s)))$   
**and** *schedule-invar-Some*:  
 $\llbracket \text{schedule } \sigma\ s = \lfloor (t, \lfloor (ta, x', m') \rfloor, \sigma') \rfloor; \text{state-invar } s; \sigma\text{-invar } \sigma (\text{dom } (\text{thr-}\alpha (\text{thr } s))); \text{state-}\alpha\ s \in \text{invariant} \rrbracket$   
 $\Rightarrow \sigma\text{-invar } \sigma' (\text{dom } (\text{thr-}\alpha (\text{thr } s)) \cup \{t. \exists x\ m. \text{NewThread } t\ x\ m \in \text{set } \{ta\}_t\})$

**locale** *pick-wakeup-spec-base* =  
*state-refine-base*  
*final* *r* *convert-RA*  
*thr-}\alpha\ thr-invar*  
*ws-}\alpha\ ws-invar*  
*is-}\alpha\ is-invar*  
**for** *final* :: 'x  $\Rightarrow$  bool  
**and** *r* :: 't  $\Rightarrow$  ('x  $\times$  'm)  $\Rightarrow$  (('l, 't, 'x, 'm, 'w, 'o) thread-action  $\times$  'x  $\times$  'm) Predicate.pred  
**and** *convert-RA* :: 'l released-locks  $\Rightarrow$  'o list  
**and** *pick-wakeup* :: 's  $\Rightarrow$  't  $\Rightarrow$  'w  $\Rightarrow$  'm-w  $\Rightarrow$  't option  
**and**  $\sigma\text{-invar}$  :: 's  $\Rightarrow$  't set  $\Rightarrow$  bool  
**and** *thr-}\alpha* :: 'm-t  $\Rightarrow$  ('l, 't, 'x) thread-info  
**and** *thr-invar* :: 'm-t  $\Rightarrow$  bool  
**and** *ws-}\alpha* :: 'm-w  $\Rightarrow$  ('w, 't) wait-sets  
**and** *ws-invar* :: 'm-w  $\Rightarrow$  bool  
**and** *is-}\alpha* :: 's-i  $\Rightarrow$  't interrupts  
**and** *is-invar* :: 's-i  $\Rightarrow$  bool

**locale** *pick-wakeup-spec* =  
*pick-wakeup-spec-base*  
*final* *r* *convert-RA*  
*pick-wakeup*  $\sigma\text{-invar}$   
*thr-}\alpha\ thr-invar*  
*ws-}\alpha\ ws-invar*  
*is-}\alpha\ is-invar*  
**for** *final* :: 'x  $\Rightarrow$  bool  
**and** *r* :: 't  $\Rightarrow$  ('x  $\times$  'm)  $\Rightarrow$  (('l, 't, 'x, 'm, 'w, 'o) thread-action  $\times$  'x  $\times$  'm) Predicate.pred  
**and** *convert-RA* :: 'l released-locks  $\Rightarrow$  'o list  
**and** *pick-wakeup* :: 's  $\Rightarrow$  't  $\Rightarrow$  'w  $\Rightarrow$  'm-w  $\Rightarrow$  't option  
**and**  $\sigma\text{-invar}$  :: 's  $\Rightarrow$  't set  $\Rightarrow$  bool  
**and** *thr-}\alpha* :: 'm-t  $\Rightarrow$  ('l, 't, 'x) thread-info  
**and** *thr-invar* :: 'm-t  $\Rightarrow$  bool  
**and** *ws-}\alpha* :: 'm-w  $\Rightarrow$  ('w, 't) wait-sets  
**and** *ws-invar* :: 'm-w  $\Rightarrow$  bool



```

and  $is-\alpha :: 's-i \Rightarrow 't \text{ interrupts}$ 
and  $is-invar :: 's-i \Rightarrow \text{bool}$ 
+
assumes pick-wakeup-NoneD:
 $\llbracket \text{pick-wakeup } \sigma \ t \ w \ ws = \text{None}; ws-invar \ ws; \sigma-invar \ \sigma \ T; \text{dom } (ws-\alpha \ ws) \subseteq T; t \in T \rrbracket$ 
 $\implies InWS \ w \notin \text{ran } (ws-\alpha \ ws)$ 
and pick-wakeup-SomeD:
 $\llbracket \text{pick-wakeup } \sigma \ t \ w \ ws = \lfloor t' \rfloor; ws-invar \ ws; \sigma-invar \ \sigma \ T; \text{dom } (ws-\alpha \ ws) \subseteq T; t \in T \rrbracket$ 
 $\implies ws-\alpha \ ws \ t' = \lfloor InWS \ w \rfloor$ 

```

```

locale scheduler-base-aux =
  state-refine-base
  final r convert-RA
  thr- $\alpha$  thr-invar
  ws- $\alpha$  ws-invar
  is- $\alpha$  is-invar
for final ::  $'x \Rightarrow \text{bool}$ 
and  $r :: 't \Rightarrow ('x \times 'm) \Rightarrow (('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \times 'x \times 'm) \text{ Predicate.pred}$ 
and convert-RA ::  $'l \text{ released-locks} \Rightarrow 'o \text{ list}$ 
and  $thr-\alpha :: 'm-t \Rightarrow ('l, 't, 'x) \text{ thread-info}$ 
and  $thr-invar :: 'm-t \Rightarrow \text{bool}$ 
and  $thr-lookup :: 't \Rightarrow 'm-t \rightarrow ('x \times 'l \text{ released-locks})$ 
and  $thr-update :: 't \Rightarrow 'x \times 'l \text{ released-locks} \Rightarrow 'm-t \Rightarrow 'm-t$ 
and  $ws-\alpha :: 'm-w \Rightarrow ('w, 't) \text{ wait-sets}$ 
and  $ws-invar :: 'm-w \Rightarrow \text{bool}$ 
and  $ws-lookup :: 't \Rightarrow 'm-w \rightarrow 'w \text{ wait-set-status}$ 
and  $is-\alpha :: 's-i \Rightarrow 't \text{ interrupts}$ 
and  $is-invar :: 's-i \Rightarrow \text{bool}$ 
and  $is-memb :: 't \Rightarrow 's-i \Rightarrow \text{bool}$ 
and  $is-ins :: 't \Rightarrow 's-i \Rightarrow 's-i$ 
and  $is-delete :: 't \Rightarrow 's-i \Rightarrow 's-i$ 
begin

```

```

definition free-thread-id ::  $'m-t \Rightarrow 't \Rightarrow \text{bool}$ 
where free-thread-id  $ts \ t \longleftrightarrow thr-lookup \ t \ ts = \text{None}$ 

```

```

fun redT-updT ::  $'m-t \Rightarrow ('t, 'x, 'm) \text{ new-thread-action} \Rightarrow 'm-t$ 
where
  redT-updT  $ts \ (\text{NewThread } t' \ x \ m) = thr-update \ t' \ (x, \text{no-wait-locks}) \ ts$ 
| redT-updT  $ts \ - = ts$ 

```

```

definition redT-updTs ::  $'m-t \Rightarrow ('t, 'x, 'm) \text{ new-thread-action list} \Rightarrow 'm-t$ 
where redT-updTs = foldl redT-updT

```

```

primrec thread-ok ::  $'m-t \Rightarrow ('t, 'x, 'm) \text{ new-thread-action} \Rightarrow \text{bool}$ 
where
  thread-ok  $ts \ (\text{NewThread } t \ x \ m) = \text{free-thread-id } ts \ t$ 
| thread-ok  $ts \ (\text{ThreadExists } t \ b) = (b \neq \text{free-thread-id } ts \ t)$ 

```

We use *local.redT-updT* in *thread-ok* instead of *redT-updT'* like in theory *JinjaThreads.FWThread*. This fixes *'x* in the  $('t, 'x, 'm) \text{ new-thread-action list}$  type, but avoids *undefined*, which raises an exception during execution in the generated code.

```

primrec thread-oks ::  $'m-t \Rightarrow ('t, 'x, 'm) \text{ new-thread-action list} \Rightarrow \text{bool}$ 
where

```

$thread\text{-}oks\ ts\ [] = True$   
 $| thread\text{-}oks\ ts\ (ta\#\ tas) = (thread\text{-}ok\ ts\ ta \wedge thread\text{-}oks\ (redT\text{-}updT\ ts\ ta)\ tas)$

**definition**  $wset\text{-}actions\text{-}ok :: 'm\text{-}w \Rightarrow 't \Rightarrow ('t, 'w)\ wait\text{-}set\text{-}action\ list \Rightarrow bool$   
**where**

$wset\text{-}actions\text{-}ok\ ws\ t\ was \longleftrightarrow$   
 $ws\text{-}lookup\ t\ ws =$   
 $(if\ Notified \in set\ was\ then\ \lfloor PostWS\ WSNotified \rfloor$   
 $\quad else\ if\ WokenUp \in set\ was\ then\ \lfloor PostWS\ WSWokenUp \rfloor$   
 $\quad else\ None)$

**primrec**  $cond\text{-}action\text{-}ok :: ('l, 't, 'm, 'm\text{-}t, 'm\text{-}w, 's\text{-}i)\ state\text{-}refine \Rightarrow 't \Rightarrow 't\ conditional\text{-}action \Rightarrow bool$   
**where**

$\bigwedge ln.\ cond\text{-}action\text{-}ok\ s\ t\ (Join\ T) =$   
 $(case\ thr\text{-}lookup\ T\ (thr\ s)$   
 $\quad of\ None \Rightarrow True$   
 $\quad | \lfloor (x, ln) \rfloor \Rightarrow t \neq T \wedge final\ x \wedge ln = no\text{-}wait\text{-}locks \wedge ws\text{-}lookup\ T\ (wset\ s) = None)$   
 $| cond\text{-}action\text{-}ok\ s\ t\ Yield = True$

**definition**  $cond\text{-}action\text{-}oks :: ('l, 't, 'm, 'm\text{-}t, 'm\text{-}w, 's\text{-}i)\ state\text{-}refine \Rightarrow 't \Rightarrow 't\ conditional\text{-}action\ list \Rightarrow bool$

**where**

$cond\text{-}action\text{-}oks\ s\ t\ cts = list\text{-}all\ (cond\text{-}action\text{-}ok\ s\ t)\ cts$

**primrec**  $redT\text{-}updI :: 's\text{-}i \Rightarrow 't\ interrupt\text{-}action \Rightarrow 's\text{-}i$   
**where**

$redT\text{-}updI\ is\ (Interrupt\ t) = is\text{-}ins\ t\ is$   
 $| redT\text{-}updI\ is\ (ClearInterrupt\ t) = is\text{-}delete\ t\ is$   
 $| redT\text{-}updI\ is\ (IsInterrupted\ t\ b) = is$

**primrec**  $redT\text{-}updIs :: 's\text{-}i \Rightarrow 't\ interrupt\text{-}action\ list \Rightarrow 's\text{-}i$   
**where**

$redT\text{-}updIs\ is\ [] = is$   
 $| redT\text{-}updIs\ is\ (ia\ \# ias) = redT\text{-}updIs\ (redT\text{-}updI\ is\ ia)\ ias$

**primrec**  $interrupt\text{-}action\text{-}ok :: 's\text{-}i \Rightarrow 't\ interrupt\text{-}action \Rightarrow bool$   
**where**

$interrupt\text{-}action\text{-}ok\ is\ (Interrupt\ t) = True$   
 $| interrupt\text{-}action\text{-}ok\ is\ (ClearInterrupt\ t) = True$   
 $| interrupt\text{-}action\text{-}ok\ is\ (IsInterrupted\ t\ b) = (b = (is\text{-}memb\ t\ is))$

**primrec**  $interrupt\text{-}actions\text{-}ok :: 's\text{-}i \Rightarrow 't\ interrupt\text{-}action\ list \Rightarrow bool$   
**where**

$interrupt\text{-}actions\text{-}ok\ is\ [] = True$   
 $| interrupt\text{-}actions\text{-}ok\ is\ (ia\ \# ias) \longleftrightarrow interrupt\text{-}action\text{-}ok\ is\ ia \wedge interrupt\text{-}actions\text{-}ok\ (redT\text{-}updI\ is\ ia)\ ias$

**definition**  $actions\text{-}ok :: ('l, 't, 'm, 'm\text{-}t, 'm\text{-}w, 's\text{-}i)\ state\text{-}refine \Rightarrow 't \Rightarrow ('l, 't, 'x, 'm, 'w, 'o')\ thread\text{-}action \Rightarrow bool$

**where**

$actions\text{-}ok\ s\ t\ ta \longleftrightarrow$   
 $lock\text{-}ok\text{-}las\ (locks\ s)\ t\ \{\!\!\{ta\}\!\!\}_l \wedge$   
 $thread\text{-}oks\ (thr\ s)\ \{\!\!\{ta\}\!\!\}_t \wedge$   
 $cond\text{-}action\text{-}oks\ s\ t\ \{\!\!\{ta\}\!\!\}_c \wedge$

*wset-actions-ok* (*wset s*) *t*  $\{ta\}_w \wedge$   
*interrupt-actions-ok* (*interrupts s*)  $\{ta\}_i$

**end**

**locale** *scheduler-base* =

*scheduler-base-aux*

*final r convert-RA*

*thr- $\alpha$  thr-invar thr-lookup thr-update*

*ws- $\alpha$  ws-invar ws-lookup*

*is- $\alpha$  is-invar is-memb is-ins is-delete*

+

*scheduler-spec-base*

*final r convert-RA*

*schedule  $\sigma$ -invar*

*thr- $\alpha$  thr-invar*

*ws- $\alpha$  ws-invar*

*is- $\alpha$  is-invar*

+

*pick-wakeup-spec-base*

*final r convert-RA*

*pick-wakeup  $\sigma$ -invar*

*thr- $\alpha$  thr-invar*

*ws- $\alpha$  ws-invar*

*is- $\alpha$  is-invar*

**for** *final* :: '*x*  $\Rightarrow$  bool

**and** *r* :: '*t*  $\Rightarrow$  ('*x*  $\times$  '*m*)  $\Rightarrow$  (('l,'t,'x,'m,'w,'o) thread-action  $\times$  '*x*  $\times$  '*m*) Predicate.pred

**and** *convert-RA* :: '*l* released-locks  $\Rightarrow$  '*o* list

**and** *schedule* :: ('l,'t,'x,'m,'w,'o,'m-t,'m-w,'s-i,'s) scheduler

**and** *output* :: '*s*  $\Rightarrow$  '*t*  $\Rightarrow$  ('l,'t,'x,'m,'w,'o) thread-action  $\Rightarrow$  '*q* option

**and** *pick-wakeup* :: '*s*  $\Rightarrow$  '*t*  $\Rightarrow$  '*w*  $\Rightarrow$  '*m-w*  $\Rightarrow$  '*t* option

**and**  *$\sigma$ -invar* :: '*s*  $\Rightarrow$  '*t* set  $\Rightarrow$  bool

**and** *thr- $\alpha$*  :: '*m-t*  $\Rightarrow$  ('l,'t,'x) thread-info

**and** *thr-invar* :: '*m-t*  $\Rightarrow$  bool

**and** *thr-lookup* :: '*t*  $\Rightarrow$  '*m-t*  $\rightarrow$  ('*x*  $\times$  '*l* released-locks)

**and** *thr-update* :: '*t*  $\Rightarrow$  '*x*  $\times$  '*l* released-locks  $\Rightarrow$  '*m-t*  $\Rightarrow$  '*m-t*

**and** *ws- $\alpha$*  :: '*m-w*  $\Rightarrow$  ('w,'t) wait-sets

**and** *ws-invar* :: '*m-w*  $\Rightarrow$  bool

**and** *ws-lookup* :: '*t*  $\Rightarrow$  '*m-w*  $\rightarrow$  '*w* wait-set-status

**and** *ws-update* :: '*t*  $\Rightarrow$  '*w* wait-set-status  $\Rightarrow$  '*m-w*  $\Rightarrow$  '*m-w*

**and** *ws-delete* :: '*t*  $\Rightarrow$  '*m-w*  $\Rightarrow$  '*m-w*

**and** *ws-iterate* :: '*m-w*  $\Rightarrow$  ('t  $\times$  '*w* wait-set-status, '*m-w*) set-iterator

**and** *is- $\alpha$*  :: '*s-i*  $\Rightarrow$  '*t* interrupts

**and** *is-invar* :: '*s-i*  $\Rightarrow$  bool

**and** *is-memb* :: '*t*  $\Rightarrow$  '*s-i*  $\Rightarrow$  bool

**and** *is-ins* :: '*t*  $\Rightarrow$  '*s-i*  $\Rightarrow$  '*s-i*

**and** *is-delete* :: '*t*  $\Rightarrow$  '*s-i*  $\Rightarrow$  '*s-i*

**begin**

**primrec** *exec-updW* :: '*s*  $\Rightarrow$  '*t*  $\Rightarrow$  '*m-w*  $\Rightarrow$  ('t,'w) wait-set-action  $\Rightarrow$  '*m-w*

**where**

*exec-updW*  $\sigma$  *t ws* (*Notify w*) =

(*case pick-wakeup*  $\sigma$  *t w ws*

of *None*  $\Rightarrow$  *ws*

$| \text{Some } t \Rightarrow \text{ws-update } t \text{ (PostWS WSNotified) } ws)$   
 $| \text{exec-updW } \sigma \ t \ ws \text{ (NotifyAll } w) =$   
 $\quad \text{ws-iterate } ws \ (\lambda\cdot. \text{True}) \ (\lambda(t, w') \text{ ws'}. \text{if } w' = \text{InWS } w \text{ then } \text{ws-update } t \text{ (PostWS WSNotified) } ws'$   
 $\text{else } ws')$   
 $\quad \text{ws}$   
 $| \text{exec-updW } \sigma \ t \ ws \text{ (Suspend } w) = \text{ws-update } t \text{ (InWS } w) \ ws$   
 $| \text{exec-updW } \sigma \ t \ ws \text{ (WakeUp } t') =$   
 $\quad (\text{case } \text{ws-lookup } t' \ ws \text{ of } [\text{InWS } w] \Rightarrow \text{ws-update } t' \text{ (PostWS WSWokenUp) } ws \mid - \Rightarrow ws)$   
 $| \text{exec-updW } \sigma \ t \ ws \text{ Notified} = \text{ws-delete } t \ ws$   
 $| \text{exec-updW } \sigma \ t \ ws \text{ WokenUp} = \text{ws-delete } t \ ws$

**definition**  $\text{exec-updWs} :: 's \Rightarrow 't \Rightarrow 'm\text{-}w \Rightarrow ('t, 'w) \text{ wait-set-action list} \Rightarrow 'm\text{-}w$   
**where**  $\text{exec-updWs } \sigma \ t = \text{foldl } (\text{exec-updW } \sigma \ t)$

**definition**  $\text{exec-upd} ::$

$'s \Rightarrow ('l, 't, 'm, 'm\text{-}t, 'm\text{-}w, 's\text{-}i) \text{ state-refine} \Rightarrow 't \Rightarrow ('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \Rightarrow 'x \Rightarrow 'm$   
 $\Rightarrow ('l, 't, 'm, 'm\text{-}t, 'm\text{-}w, 's\text{-}i) \text{ state-refine}$

**where**  $[\text{simp}]$ :

$\text{exec-upd } \sigma \ s \ t \ ta \ x' \ m' =$   
 $\quad (\text{redT-updLs } (\text{locks } s) \ t \ \{\!\!\{ta}\!\!\}_l,$   
 $\quad (\text{thr-update } t \ (x', \text{redT-updLns } (\text{locks } s) \ t \ (\text{snd } (\text{the } (\text{thr-lookup } t \ (\text{thr } s)))) \ \{\!\!\{ta}\!\!\}_l) \ (\text{redT-updTs } (\text{thr}$   
 $\quad s) \ \{\!\!\{ta}\!\!\}_t), \ m'),$   
 $\quad \text{exec-updWs } \sigma \ t \ (\text{wset } s) \ \{\!\!\{ta}\!\!\}_w, \text{redT-updIs } (\text{interrupts } s) \ \{\!\!\{ta}\!\!\}_i)$

**definition**  $\text{execT} ::$

$'s \Rightarrow ('l, 't, 'm, 'm\text{-}t, 'm\text{-}w, 's\text{-}i) \text{ state-refine}$   
 $\Rightarrow ('s \times 't \times ('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \times ('l, 't, 'm, 'm\text{-}t, 'm\text{-}w, 's\text{-}i) \text{ state-refine}) \text{ option}$

**where**

$\text{execT } \sigma \ s =$   
 $\quad (\text{do } \{$   
 $\quad \quad (t, \text{tax}'m', \sigma') \leftarrow \text{schedule } \sigma \ s;$   
 $\quad \quad \text{case } \text{tax}'m' \text{ of}$   
 $\quad \quad \quad \text{None} \Rightarrow$   
 $\quad \quad \quad (\text{let } (x, \text{ln}) = \text{the } (\text{thr-lookup } t \ (\text{thr } s));$   
 $\quad \quad \quad \text{ta} = (K\$ \ [], \ [], \ [], \ [], \ [], \ \text{convert-RA } \text{ln});$   
 $\quad \quad \quad s' = (\text{acquire-all } (\text{locks } s) \ t \ \text{ln}, (\text{thr-update } t \ (x, \text{no-wait-locks}) \ (\text{thr } s), \text{shr } s), \text{wset } s, \text{interrupts}$   
 $\quad \quad s)$   
 $\quad \quad \text{in } [(\sigma', t, \text{ta}, s')])$   
 $\quad \mid [(\text{ta}, x', m')] \Rightarrow [(\sigma', t, \text{ta}, \text{exec-upd } \sigma \ s \ t \ ta \ x' \ m')]$   
 $\quad \})$

**primrec**  $\text{exec-step} ::$

$'s \times ('l, 't, 'm, 'm\text{-}t, 'm\text{-}w, 's\text{-}i) \text{ state-refine} \Rightarrow$   
 $(('s \times 't \times ('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action}) \times 's \times ('l, 't, 'm, 'm\text{-}t, 'm\text{-}w, 's\text{-}i) \text{ state-refine}) + ('l, 't, 'm, 'm\text{-}t, 'm\text{-}w, 's\text{-}i)$   
 $\text{state-refine}$

**where**

$\text{exec-step } (\sigma, s) =$   
 $\quad (\text{case } \text{execT } \sigma \ s \text{ of}$   
 $\quad \quad \text{None} \Rightarrow \text{Inr } s$   
 $\quad \mid \text{Some } (\sigma', t, \text{ta}, s') \Rightarrow \text{Inl } ((\sigma, t, \text{ta}), \sigma', s'))$

**declare**  $\text{exec-step.simps} [\text{simp del}]$

**definition**  $\text{exec-aux} ::$

$'s \times ('l, 't, 'm, 'm-t, 'm-w, 's-i)$  state-refine  
 $\Rightarrow ('s \times 't \times ('l, 't, 'x, 'm, 'w, 'o)$  thread-action,  $('l, 't, 'm, 'm-t, 'm-w, 's-i)$  state-refine) tllist  
**where**  
 $exec\_aux \sigma s = unfold\_tllist' exec\_step \sigma s$

**definition**  $exec :: 's \Rightarrow ('l, 't, 'm, 'm-t, 'm-w, 's-i)$  state-refine  $\Rightarrow ('q, ('l, 't, 'm, 'm-t, 'm-w, 's-i)$  state-refine)  
tllist  
**where**  
 $exec \sigma s = tmap the id (tfilter undefined (\lambda q. q \neq None) (tmap (\lambda(\sigma, t, ta). output \sigma t ta) id$   
 $(exec\_aux (\sigma, s))))$

**end**

Implement *pick-wakeup* by *map-sel'*

**definition** *pick-wakeup-via-sel* ::  
 $('m-w \Rightarrow ('t \Rightarrow 'w$  wait-set-status  $\Rightarrow bool) \rightarrow 't \times 'w$  wait-set-status)  
 $\Rightarrow 's \Rightarrow 't \Rightarrow 'w \Rightarrow 'm-w \Rightarrow 't$  option  
**where** *pick-wakeup-via-sel*  $ws\_sel \sigma t w ws = map\_option fst (ws\_sel ws (\lambda t w'. w' = InWS w))$

**lemma** *pick-wakeup-spec-via-sel*:  
**assumes** *sel*: *map-sel'*  $ws-\alpha$  *ws-invar* *ws-sel*  
**shows** *pick-wakeup-spec* (*pick-wakeup-via-sel*  $(\lambda s P. ws\_sel s (\lambda(k, v). P k v))$ )  $\sigma$ -invar  $ws-\alpha$  *ws-invar*  
**proof** –  
**interpret** *ws*: *map-sel'*  $ws-\alpha$  *ws-invar* *ws-sel* **by**(rule *sel*)  
**show** ?thesis  
**by**(unfold-locales)(auto simp add: *pick-wakeup-via-sel-def* *ran-def* dest: *ws.sel'-noneD* *ws.sel'-SomeD*)  
**qed**

**locale** *scheduler-ext-base* =  
*scheduler-base-aux*  
*final* *r* *convert-RA*  
*thr- $\alpha$*  *thr-invar* *thr-lookup* *thr-update*  
*ws- $\alpha$*  *ws-invar* *ws-lookup*  
*is- $\alpha$*  *is-invar* *is-memb* *is-ins* *is-delete*  
**for** *final* ::  $'x \Rightarrow bool$   
**and** *r* ::  $'t \Rightarrow ('x \times 'm) \Rightarrow (('l, 't, 'x, 'm, 'w, 'o)$  thread-action  $\times 'x \times 'm)$  Predicate.pred  
**and** *convert-RA* ::  $'l$  released-locks  $\Rightarrow 'o$  list  
**and** *thr- $\alpha$*  ::  $'m-t \Rightarrow ('l, 't, 'x)$  thread-info  
**and** *thr-invar* ::  $'m-t \Rightarrow bool$   
**and** *thr-lookup* ::  $'t \Rightarrow 'm-t \rightarrow ('x \times 'l$  released-locks)  
**and** *thr-update* ::  $'t \Rightarrow 'x \times 'l$  released-locks  $\Rightarrow 'm-t \Rightarrow 'm-t$   
**and** *thr-iterate* ::  $'m-t \Rightarrow ('t \times ('x \times 'l$  released-locks),  $'s-t)$  set-iterator  
**and** *ws- $\alpha$*  ::  $'m-w \Rightarrow ('w, 't)$  wait-sets  
**and** *ws-invar* ::  $'m-w \Rightarrow bool$   
**and** *ws-lookup* ::  $'t \Rightarrow 'm-w \rightarrow 'w$  wait-set-status  
**and** *ws-update* ::  $'t \Rightarrow 'w$  wait-set-status  $\Rightarrow 'm-w \Rightarrow 'm-w$   
**and** *ws-sel* ::  $'m-w \Rightarrow ('t \times 'w$  wait-set-status  $\Rightarrow bool) \rightarrow ('t \times 'w$  wait-set-status)  
**and** *is- $\alpha$*  ::  $'s-i \Rightarrow 't$  interrupts  
**and** *is-invar* ::  $'s-i \Rightarrow bool$   
**and** *is-memb* ::  $'t \Rightarrow 's-i \Rightarrow bool$   
**and** *is-ins* ::  $'t \Rightarrow 's-i \Rightarrow 's-i$   
**and** *is-delete* ::  $'t \Rightarrow 's-i \Rightarrow 's-i$   
+  
**fixes** *thr'- $\alpha$*  ::  $'s-t \Rightarrow 't$  set

```

and thr'-invar :: 's-t ⇒ bool
and thr'-empty :: unit ⇒ 's-t
and thr'-ins-dj :: 't ⇒ 's-t ⇒ 's-t
begin

abbreviation pick-wakeup :: 's ⇒ 't ⇒ 'w ⇒ 'm-w ⇒ 't option
where pick-wakeup ≡ pick-wakeup-via-sel (λs P. ws-sel s (λ(k,v). P k v))

fun active-threads :: ('l,'t,'m,'m-t,'m-w,'s-i) state-refine ⇒ 's-t
where
  active-threads (ls, (ts, m), ws, is) =
    thr-iterate ts (λ-. True)
      (λ(t, (x, ln)) ts'. if ln = no-wait-locks
        then if Predicate.holds
          (do {
            (ta, -) ← r t (x, m);
            Predicate.if-pred (actions-ok (ls, (ts, m), ws, is) t ta)
          })
          then thr'-ins-dj t ts'
          else ts'
        else if ¬ waiting (ws-lookup t ws) ∧ may-acquire-all ls t ln then thr'-ins-dj t ts' else
ts')
    (thr'-empty ())

end

locale scheduler-aux =
  scheduler-base-aux
  final r convert-RA
  thr-α thr-invar thr-lookup thr-update
  ws-α ws-invar ws-lookup
  is-α is-invar is-memb is-ins is-delete
  +
  thr: finite-map thr-α thr-invar +
  thr: map-lookup thr-α thr-invar thr-lookup +
  thr: map-update thr-α thr-invar thr-update +
  ws: map ws-α ws-invar +
  ws: map-lookup ws-α ws-invar ws-lookup +
  is: set is-α is-invar +
  is: set-memb is-α is-invar is-memb +
  is: set-ins is-α is-invar is-ins +
  is: set-delete is-α is-invar is-delete
  for final :: 'x ⇒ bool
  and r :: 't ⇒ ('x × 'm) ⇒ (('l,'t,'x,'m,'w,'o) thread-action × 'x × 'm) Predicate.pred
  and convert-RA :: 'l released-locks ⇒ 'o list
  and thr-α :: 'm-t ⇒ ('l,'t,'x) thread-info
  and thr-invar :: 'm-t ⇒ bool
  and thr-lookup :: 't ⇒ 'm-t → ('x × 'l released-locks)
  and thr-update :: 't ⇒ 'x × 'l released-locks ⇒ 'm-t ⇒ 'm-t
  and ws-α :: 'm-w ⇒ ('w,'t) wait-sets
  and ws-invar :: 'm-w ⇒ bool
  and ws-lookup :: 't ⇒ 'm-w → 'w wait-set-status
  and is-α :: 's-i ⇒ 't interrupts
  and is-invar :: 's-i ⇒ bool

```

```

and is-memb :: 't ⇒ 's-i ⇒ bool
and is-ins :: 't ⇒ 's-i ⇒ 's-i
and is-delete :: 't ⇒ 's-i ⇒ 's-i
begin

```

```

lemma free-thread-id-correct [simp]:
  thr-invar ts ⇒ free-thread-id ts = FWThread.free-thread-id (thr-α ts)
by(auto simp add: free-thread-id-def fun-eq-iff thr.lookup-correct intro: free-thread-id.intros)

```

```

lemma redT-updT-correct [simp]:
  assumes thr-invar ts
  shows thr-α (redT-updT ts nta) = FWThread.redT-updT (thr-α ts) nta
  and thr-invar (redT-updT ts nta)
by(case-tac [!]) nta)(simp-all add: thr.update-correct assms)

```

```

lemma redT-updT-s-correct [simp]:
  assumes thr-invar ts
  shows thr-α (redT-updT-s ts ntas) = FWThread.redT-updT-s (thr-α ts) ntas
  and thr-invar (redT-updT-s ts ntas)
using assms
by(induct ntas arbitrary: ts)(simp-all add: redT-updT-s-def)

```

```

lemma thread-ok-correct [simp]:
  thr-invar ts ⇒ thread-ok ts nta ⇔ FWThread.thread-ok (thr-α ts) nta
by(cases nta) simp-all

```

```

lemma thread-oks-correct [simp]:
  thr-invar ts ⇒ thread-oks ts ntas ⇔ FWThread.thread-oks (thr-α ts) ntas
by(induct ntas arbitrary: ts) simp-all

```

```

lemma wset-actions-ok-correct [simp]:
  ws-invar ws ⇒ wset-actions-ok ws t was ⇔ FWWait.wset-actions-ok (ws-α ws) t was
by(simp add: wset-actions-ok-def FWWait.wset-actions-ok-def ws.lookup-correct)

```

```

lemma cond-action-ok-correct [simp]:
  state-invar s ⇒ cond-action-ok s t cta ⇔ α.cond-action-ok (state-α s) t cta
by(cases s,cases cta)(auto simp add: thr.lookup-correct ws.lookup-correct)

```

```

lemma cond-action-oks-correct [simp]:
  assumes state-invar s
  shows cond-action-oks s t ctas ⇔ α.cond-action-oks (state-α s) t ctas
by(induct ctas)(simp-all add: cond-action-oks-def assms)

```

```

lemma redT-updI-correct [simp]:
  assumes is-invar is
  shows is-α (redT-updI is ia) = FWInterrupt.redT-updI (is-α is) ia
  and is-invar (redT-updI is ia)
using assms
by(case-tac [!]) ia)(auto simp add: is.ins-correct is.delete-correct)

```

```

lemma redT-updIs-correct [simp]:
  assumes is-invar is
  shows is-α (redT-updIs is ias) = FWInterrupt.redT-updIs (is-α is) ias
  and is-invar (redT-updIs is ias)

```

**using** *assms*

**by**(*induct ias arbitrary: is*)(*auto*)

**lemma** *interrupt-action-ok-correct* [*simp*]:

*is-invar is*  $\implies$  *interrupt-action-ok is ia*  $\longleftrightarrow$  *FWInterrupt.interrupt-action-ok (is- $\alpha$  is) ia*

**by**(*cases ia*)(*auto simp add: is.memb-correct*)

**lemma** *interrupt-actions-ok-correct* [*simp*]:

*is-invar is*  $\implies$  *interrupt-actions-ok is ias*  $\longleftrightarrow$  *FWInterrupt.interrupt-actions-ok (is- $\alpha$  is) ias*

**by**(*induct ias arbitrary:is*) *simp-all*

**lemma** *actions-ok-correct* [*simp*]:

*state-invar s*  $\implies$  *actions-ok s t ta*  $\longleftrightarrow$   *$\alpha$ .actions-ok (state- $\alpha$  s) t ta*

**by**(*auto simp add: actions-ok-def*)

**end**

**locale** *scheduler* =

*scheduler-base*

*final r convert-RA*

*schedule output pick-wakeup  $\sigma$ -invar*

*thr- $\alpha$  thr-invar thr-lookup thr-update*

*ws- $\alpha$  ws-invar ws-lookup ws-update ws-delete ws-iterate*

*is- $\alpha$  is-invar is-memb is-ins is-delete*

+

*scheduler-aux*

*final r convert-RA*

*thr- $\alpha$  thr-invar thr-lookup thr-update*

*ws- $\alpha$  ws-invar ws-lookup*

*is- $\alpha$  is-invar is-memb is-ins is-delete*

+

*scheduler-spec*

*final r convert-RA*

*schedule  $\sigma$ -invar*

*thr- $\alpha$  thr-invar*

*ws- $\alpha$  ws-invar*

*is- $\alpha$  is-invar*

*invariant*

+

*pick-wakeup-spec*

*final r convert-RA*

*pick-wakeup  $\sigma$ -invar*

*thr- $\alpha$  thr-invar*

*ws- $\alpha$  ws-invar*

*is- $\alpha$  is-invar*

+

*ws: map-update ws- $\alpha$  ws-invar ws-update* +

*ws: map-delete ws- $\alpha$  ws-invar ws-delete* +

*ws: map-iteratei ws- $\alpha$  ws-invar ws-iterate*

**for** *final* :: '*x*  $\Rightarrow$  bool

**and** *r* :: '*t*  $\Rightarrow$  ('*x*  $\times$  '*m*)  $\Rightarrow$  (('l,'t,'x,'m,'w,'o) thread-action  $\times$  '*x*  $\times$  '*m*) Predicate.pred

**and** *convert-RA* :: '*l* released-locks  $\Rightarrow$  '*o* list

**and** *schedule* :: ('l,'t,'x,'m,'w,'o,'m-t,'m-w,'s-i,'s) *scheduler*

**and** *output* :: '*s*  $\Rightarrow$  '*t*  $\Rightarrow$  ('l,'t,'x,'m,'w,'o) thread-action  $\Rightarrow$  '*q* option



```

and pick-wakeup :: 's ⇒ 't ⇒ 'w ⇒ 'm-w ⇒ 't option
and σ-invar :: 's ⇒ 't set ⇒ bool
and thr-α :: 'm-t ⇒ ('l,'t,'x) thread-info
and thr-invar :: 'm-t ⇒ bool
and thr-lookup :: 't ⇒ 'm-t → ('x × 'l released-locks)
and thr-update :: 't ⇒ 'x × 'l released-locks ⇒ 'm-t ⇒ 'm-t
and ws-α :: 'm-w ⇒ ('w,'t) wait-sets
and ws-invar :: 'm-w ⇒ bool
and ws-lookup :: 't ⇒ 'm-w → 'w wait-set-status
and ws-update :: 't ⇒ 'w wait-set-status ⇒ 'm-w ⇒ 'm-w
and ws-delete :: 't ⇒ 'm-w ⇒ 'm-w
and ws-iterate :: 'm-w ⇒ ('t × 'w wait-set-status, 'm-w) set-iterator
and is-α :: 's-i ⇒ 't interrupts
and is-invar :: 's-i ⇒ bool
and is-memb :: 't ⇒ 's-i ⇒ bool
and is-ins :: 't ⇒ 's-i ⇒ 's-i
and is-delete :: 't ⇒ 's-i ⇒ 's-i
and invariant :: ('l,'t,'x,'m,'w) state set
+
assumes invariant: invariant3p α.redT invariant
begin

```

lemma *exec-updW-correct*:

```

assumes invar: ws-invar ws σ-invar σ T dom (ws-α ws) ⊆ T t ∈ T
shows redT-updW t (ws-α ws) wa (ws-α (exec-updW σ t ws wa)) (is ?thesis1)
and ws-invar (exec-updW σ t ws wa) (is ?thesis2)
proof -
  from invar have ?thesis1 ∧ ?thesis2
  proof(cases wa)
    case [simp]: (Notify w)
    show ?thesis
    proof(cases pick-wakeup σ t w ws)
      case (Some t')
      hence ws-α ws t' = [InWS w] using invar by(rule pick-wakeup-SomeD)
      with Some show ?thesis using invar by(auto simp add: ws.update-correct)
    next
      case None
      hence InWS w ∉ ran (ws-α ws) using invar by(rule pick-wakeup-NoneD)
      with None show ?thesis using invar by(auto simp add: ran-def)
    qed
  next
    case [simp]: (NotifyAll w)
    let ?f = λ(t, w') ws'. if w' = InWS w then ws-update t (PostWS WSNotified) ws' else ws'
    let ?I = λT ws'. (∀ k. if k ∉ T ∧ ws-α ws k = [InWS w] then ws-α ws' k = [PostWS WSNotified]
    else ws-α ws' k = ws-α ws k) ∧ ws-invar ws'
    from invar have ?I (dom (ws-α ws)) ws by(auto simp add: ws.lookup-correct)
    with ⟨ws-invar ws⟩ have ?I {} (ws-iterate ws (λ-. True) ?f ws)
    proof(rule ws.iterate-rule-P[where I=?I])
      fix t w' T ws'
      assume t: t ∈ T and w': ws-α ws t = [w']
      and T: T ⊆ dom (ws-α ws) and I: ?I T ws'
      { fix t'
        assume t' ∉ T - {t} ws-α ws t' = [InWS w]
        with t I w' invar have ws-α (?f (t, w') ws') t' = [PostWS WSNotified]

```

```

    by(auto)(simp-all add: ws.update-correct) }
  moreover {
    fix t'
    assume t' ∈ T - {t} ∨ ws-α ws t' ≠ [InWS w]
    with t I w' invar have ws-α (?f (t,w') ws') t' = ws-α ws t'
    by(auto simp add: ws.update-correct) }
  moreover
  have ws-invar (?f (t, w') ws') using I by(simp add: ws.update-correct)
  ultimately show ?I (T - {t}) (?f (t, w') ws') by safe simp
qed
hence ws-α (ws-iterate ws (λ-. True) ?f ws) = (λt. if ws-α ws t = [InWS w] then [PostWS
WSNotified] else ws-α ws t)
and ws-invar (ws-iterate ws (λ-. True) ?f ws) by(simp-all add: fun-eq-iff)
thus ?thesis by simp
next
case WakeUp thus ?thesis using assms
  by(auto simp add: ws.lookup-correct ws.update-correct split: wait-set-status.split)
qed(simp-all add: ws.update-correct ws.delete-correct map-upd-eq-restrict)
thus ?thesis1 ?thesis2 by simp-all
qed

```

lemma *exec-updWs-correct*:

```

  assumes ws-invar ws σ-invar σ T dom (ws-α ws) ⊆ T t ∈ T
  shows redT-updWs t (ws-α ws) was (ws-α (exec-updWs σ t ws was)) (is ?thesis1)
  and ws-invar (exec-updWs σ t ws was) (is ?thesis2)
proof -
  from ⟨ws-invar ws⟩ ⟨dom (ws-α ws) ⊆ T⟩
  have ?thesis1 ∧ ?thesis2
  proof(induct was arbitrary: ws)
    case Nil thus ?case by(auto simp add: exec-updWs-def redT-updWs-def)
  next
    case (Cons wa was)
    let ?ws' = exec-updW σ t ws wa
    from ⟨ws-invar ws⟩ ⟨σ-invar σ T⟩ ⟨dom (ws-α ws) ⊆ T⟩ ⟨t ∈ T⟩
    have invar': ws-invar ?ws' and red: redT-updW t (ws-α ws) wa (ws-α ?ws')
    by(rule exec-updW-correct)+
    have dom (ws-α ?ws') ⊆ T
    proof
      fix t' assume t' ∈ dom (ws-α ?ws')
      with red have t' ∈ dom (ws-α ws) ∨ t = t'
      by(auto dest!: redT-updW-Some-otherD split: wait-set-status.split-asm)
      with ⟨dom (ws-α ws) ⊆ T⟩ ⟨t ∈ T⟩ show t' ∈ T by auto
    qed
    with invar' have redT-updWs t (ws-α ?ws') was (ws-α (exec-updWs σ t ?ws' was)) ∧ ws-invar
    (exec-updWs σ t ?ws' was)
    by(rule Cons.hyps)
    thus ?case using red
    by(auto simp add: exec-updWs-def redT-updWs-def intro: rtrancl3p-step-converse)
  qed
  thus ?thesis1 ?thesis2 by simp-all
qed

```

lemma *exec-upd-correct*:

```

  assumes state-invar s σ-invar σ (dom (thr-α (thr s))) t ∈ (dom (thr-α (thr s)))

```

**and** *wset-thread-ok* (*ws-α* (*wset s*)) (*thr-α* (*thr s*))  
**shows** *redT-upd* (*state-α s*) *t ta x' m'* (*state-α* (*exec-upd σ s t ta x' m'*))  
**and** *state-invar* (*exec-upd σ s t ta x' m'*)  
**using** *assms unfolding wset-thread-ok-conv-dom*  
**by**(*auto simp add: thr.update-correct thr.lookup-correct intro: exec-updWs-correct*)

**lemma** *execT-None*:

**assumes** *invar*: *state-invar s σ-invar σ* (*dom* (*thr-α* (*thr s*))) *state-α s*  $\in$  *invariant*  
**and** *exec*: *execT σ s* = *None*  
**shows**  $\alpha$ .*active-threads* (*state-α s*) = {}  
**using** *assms*  
**by**(*cases schedule σ s*)(*fastforce simp add: execT-def thr.lookup-correct dest: schedule-Some-NoneD schedule-NoneD*)+

**lemma** *execT-Some*:

**assumes** *invar*: *state-invar s σ-invar σ* (*dom* (*thr-α* (*thr s*))) *state-α s*  $\in$  *invariant*  
**and** *wstok*: *wset-thread-ok* (*ws-α* (*wset s*)) (*thr-α* (*thr s*))  
**and** *exec*: *execT σ s* =  $\lfloor (\sigma', t, ta, s') \rfloor$   
**shows**  $\alpha$ .*redT* (*state-α s*) (*t, ta*) (*state-α s'*) (**is** *?thesis1*)  
**and** *state-invar s'* (**is** *?thesis2*)  
**and**  $\sigma$ -*invar σ'* (*dom* (*thr-α* (*thr s'*))) (**is** *?thesis3*)  
**proof** –  
**note** [*simp del*] = *redT-upd-simps exec-upd-def*  
  
**have** *?thesis1*  $\wedge$  *?thesis2*  $\wedge$  *?thesis3*  
**proof**(*cases fst (snd (the (schedule σ s)))*)  
**case** *None*  
**with** *exec invar* **have** *schedule*: *schedule σ s* =  $\lfloor (t, \text{None}, \sigma') \rfloor$   
**and** *ta*: *ta* = (*K\$* [], [], [], [], *convert-RA* (*snd (the (thr-α (thr s) t))*))  
**and** *s'*: *s'* = (*acquire-all* (*locks s*) *t (snd (the (thr-α (thr s) t)))*, (*thr-update t (fst (the (thr-α (thr s) t))*, *no-wait-locks*) (*thr s*), *shr s*), *wset s*, *interrupts s*)  
**by**(*auto simp add: execT-def bind-eq-Some-conv thr.lookup-correct split-beta split del: option.split-asm*)  
**from** *schedule-Some-NoneD[OF schedule invar]*  
  
**obtain** *x ln n* **where** *t*: *thr-α* (*thr s*) *t* =  $\lfloor (x, \text{ln}) \rfloor$   
**and**  $0 < \text{ln} \ \$ \ n \neg$  *waiting* (*ws-α* (*wset s*) *t*) *may-acquire-all* (*locks s*) *t ln* **by** *blast*  
**hence** *?thesis1* **using** *ta s' invar* **by**(*auto intro: α.redT.redT-acquire simp add: thr.update-correct*)  
**moreover from** *invar s'* **have** *?thesis2* **by**(*simp add: thr.update-correct*)  
**moreover from** *t s' invar* **have** *dom* (*thr-α* (*thr s'*)) = *dom* (*thr-α* (*thr s*)) **by**(*auto simp add: thr.update-correct*)  
**hence** *?thesis3* **using** *invar schedule* **by**(*auto intro: schedule-invar-None*)  
**ultimately show** *?thesis* **by** *simp*  
**next**  
**case** (*Some tarm*)  
**with** *exec invar* **obtain** *x' m'*  
**where** *schedule*: *schedule σ s* =  $\lfloor (t, \lfloor (ta, x', m') \rfloor, \sigma') \rfloor$   
**and** *s'*: *s'* = *exec-upd σ s t ta x' m'*  
**by**(*cases tarm*)(*fastforce simp add: execT-def bind-eq-Some-conv split del: option.split-asm*)  
**from** *schedule-Some-SomeD[OF schedule invar]*  
**obtain** *x* **where** *t*: *thr-α* (*thr s*) *t* =  $\lfloor (x, \text{no-wait-locks}) \rfloor$   
**and** *Predicate.eval* (*r t* (*x, shr s*)) (*ta, x', m'*)  
**and** *aok*:  $\alpha$ .*actions-ok* (*state-α s*) *t ta* **by** *blast*  
**with** *s'* **have** *?thesis1* **using** *invar wstok*  
**by**(*fastforce intro: α.redT.intros exec-upd-correct*)

```

moreover from invar s' t wstok have ?thesis2 by(auto intro: exec-upd-correct)
moreover {
  from schedule invar
  have  $\sigma$ -invar  $\sigma'$  ( $\text{dom } (\text{thr-}\alpha \text{ } (\text{thr } s)) \cup \{t. \exists x m. \text{NewThread } t \ x \ m \in \text{set } \llbracket ta \rrbracket_t\}$ )
  by(rule schedule-invar-Some)
  also have  $\text{dom } (\text{thr-}\alpha \text{ } (\text{thr } s)) \cup \{t. \exists x m. \text{NewThread } t \ x \ m \in \text{set } \llbracket ta \rrbracket_t\} = \text{dom } (\text{thr-}\alpha \text{ } (\text{thr } s'))$ 
  using invar s' aok t
  by(auto simp add: exec-upd-def thr.lookup-correct thr.update-correct simp del: split-paired-Ex)(fastforce
dest: redT-updTs-new-thread intro: redT-updTs-Some1 redT-updTs-new-thread-ts simp del: split-paired-Ex)+
  finally have  $\sigma$ -invar  $\sigma'$  ( $\text{dom } (\text{thr-}\alpha \text{ } (\text{thr } s'))$ ) . }
  ultimately show ?thesis by simp
qed
thus ?thesis1 ?thesis2 ?thesis3 by simp-all
qed

```

**lemma** *exec-step-into-redT*:

```

assumes invar: state-invar s  $\sigma$ -invar  $\sigma$  ( $\text{dom } (\text{thr-}\alpha \text{ } (\text{thr } s))$ ) state- $\alpha$  s  $\in$  invariant
and wstok: wset-thread-ok ( $\text{ws-}\alpha \text{ } (\text{wset } s)$ ) ( $\text{thr-}\alpha \text{ } (\text{thr } s)$ )
and exec: exec-step ( $\sigma, s$ ) = Inl ( $(\sigma'', t, ta), \sigma', s'$ )
shows  $\alpha.\text{redT } (\text{state-}\alpha \text{ } s) (t, ta) (\text{state-}\alpha \text{ } s') \sigma'' = \sigma$ 
and state-invar s'  $\sigma$ -invar  $\sigma'$  ( $\text{dom } (\text{thr-}\alpha \text{ } (\text{thr } s'))$ ) state- $\alpha$  s'  $\in$  invariant

```

**proof** –

```

from exec have execT: execT  $\sigma$   $s = \lfloor (\sigma', t, ta, s') \rfloor$ 
  and q:  $\sigma'' = \sigma$  by(auto simp add: exec-step.simps split-beta)
from invar wstok execT show red:  $\alpha.\text{redT } (\text{state-}\alpha \text{ } s) (t, ta) (\text{state-}\alpha \text{ } s')$ 
  and invar': state-invar s'  $\sigma$ -invar  $\sigma'$  ( $\text{dom } (\text{thr-}\alpha \text{ } (\text{thr } s'))$ )  $\sigma'' = \sigma$ 
  by(rule execT-Some)+(rule q)
from invariant red  $\langle \text{state-}\alpha \text{ } s \in \text{invariant} \rangle$ 
show state- $\alpha$  s'  $\in$  invariant by(rule invariant3pD)

```

**qed**

**lemma** *exec-step-InrD*:

```

assumes state-invar s  $\sigma$ -invar  $\sigma$  ( $\text{dom } (\text{thr-}\alpha \text{ } (\text{thr } s))$ ) state- $\alpha$  s  $\in$  invariant
and exec-step ( $\sigma, s$ ) = Inr  $s'$ 
shows  $\alpha.\text{active-threads } (\text{state-}\alpha \text{ } s) = \{\}$ 
and  $s' = s$ 

```

**using** *assms*

**by**(*auto simp add: exec-step-def dest: execT-None*)

**lemma** (*in multithreaded-base*) *red-in-active-threads*:

```

assumes  $s -t>ta\rightarrow s'$ 
shows  $t \in \text{active-threads } s$ 

```

**using** *assms*

**by** *cases*(*auto intro: active-threads.intros*)

**lemma** *exec-aux-into-Runs*:

```

assumes state-invar s  $\sigma$ -invar  $\sigma$  ( $\text{dom } (\text{thr-}\alpha \text{ } (\text{thr } s))$ ) state- $\alpha$  s  $\in$  invariant
and wset-thread-ok ( $\text{ws-}\alpha \text{ } (\text{wset } s)$ ) ( $\text{thr-}\alpha \text{ } (\text{thr } s)$ )
shows  $\alpha.\text{mthr.Runs } (\text{state-}\alpha \text{ } s) (\text{lmap snd } (\text{llist-of-tllist } (\text{exec-aux } (\sigma, s))))$  (is ?thesis1)
and  $t\text{finite } (\text{exec-aux } (\sigma, s)) \implies \text{state-invar } (\text{terminal } (\text{exec-aux } (\sigma, s)))$  (is  $- \implies$  ?thesis2)

```

**proof** –

```

from assms show ?thesis1
proof(coinduction arbitrary:  $\sigma$  s)
  case (Runs  $\sigma$   $s$ )

```

```

note invar =  $\langle \text{state-invar } s \rangle \langle \sigma\text{-invar } \sigma \text{ (dom (thr-}\alpha \text{ (thr } s)) \rangle \langle \text{state-}\alpha \text{ } s \in \text{invariant} \rangle$ 
  and wstok =  $\langle \text{wset-thread-ok (ws-}\alpha \text{ (wset } s)) \text{ (thr-}\alpha \text{ (thr } s)) \rangle$ 
show ?case
proof(cases exec-aux ( $\sigma$ ,  $s$ ))
  case (TNil  $s'$ )
    hence  $\alpha.\text{active-threads (state-}\alpha \text{ } s) = \{\}$   $s' = s$ 
    by(auto simp add: exec-aux-def unfold-tllist' split: sum.split-asm dest: exec-step-InrD[OF invar])
    hence ?Stuck using TNil by(auto dest: \alpha.red-in-active-threads)
    thus ?thesis ..
  next
    case (TCons  $\sigma tta \text{ } t t s'$ )
    then obtain  $t \text{ } ta \text{ } \sigma' \text{ } s' \text{ } \sigma''$ 
      where [simp]:  $\sigma tta = (\sigma'', t, ta)$ 
      and [simp]:  $t t s' = \text{exec-aux } (\sigma', s')$ 
      and step:  $\text{exec-step } (\sigma, s) = \text{Inl } ((\sigma'', t, ta), \sigma', s')$ 
      unfolding exec-aux-def by(subst (asm) (2) unfold-tllist' (fastforce split: sum.split-asm))
    from invar wstok step
    have redT:  $\alpha.\text{redT (state-}\alpha \text{ } s) (t, ta) (\text{state-}\alpha \text{ } s')$ 
      and [simp]:  $\sigma'' = \sigma$ 
      and invar':  $\text{state-invar } s' \text{ } \sigma\text{-invar } \sigma' \text{ (dom (thr-}\alpha \text{ (thr } s')) \text{ } \text{state-}\alpha \text{ } s' \in \text{invariant}$ 
      by(rule exec-step-into-redT)+
    from wstok \alpha.redT-preserves-wset-thread-ok[OF redT]
    have wset-thread-ok ( $\text{ws-}\alpha \text{ (wset } s') \text{ (thr-}\alpha \text{ (thr } s'))$ ) by simp
    with invar' redT TCons have ?Step by(auto simp del: split-paired-Ex)
    thus ?thesis ..
  qed
qed
next
  assume tfinite (exec-aux ( $\sigma$ ,  $s$ ))
  thus ?thesis2 using assms
proof(induct exec-aux ( $\sigma$ ,  $s$ ) arbitrary: \sigma s rule: tfinite-induct)
  case TNil thus ?case
    by(auto simp add: exec-aux-def unfold-tllist' split-beta split: sum.split-asm dest: exec-step-InrD)
  next
    case (TCons  $\sigma tta \text{ } t t s$ )
    from  $\langle \text{TCons } \sigma tta \text{ } t t s = \text{exec-aux } (\sigma, s) \rangle$ 
    obtain  $\sigma'' \text{ } t \text{ } ta \text{ } \sigma' \text{ } s'$ 
      where [simp]:  $\sigma tta = (\sigma'', t, ta)$ 
      and ttls:  $t t s = \text{exec-aux } (\sigma', s')$ 
      and step:  $\text{exec-step } (\sigma, s) = \text{Inl } ((\sigma'', t, ta), \sigma', s')$ 
      unfolding exec-aux-def by(subst (asm) (2) unfold-tllist' (fastforce split: sum.split-asm))
    note ttls moreover
    from  $\langle \text{state-invar } s \rangle \langle \sigma\text{-invar } \sigma \text{ (dom (thr-}\alpha \text{ (thr } s)) \rangle \langle \text{state-}\alpha \text{ } s \in \text{invariant} \rangle \langle \text{wset-thread-ok (ws-}\alpha \text{ (wset } s)) \text{ (thr-}\alpha \text{ (thr } s)) \rangle \text{ } \text{step}$ 
    have [simp]:  $\sigma'' = \sigma$ 
      and invar':  $\text{state-invar } s' \text{ } \sigma\text{-invar } \sigma' \text{ (dom (thr-}\alpha \text{ (thr } s')) \text{ } \text{state-}\alpha \text{ } s' \in \text{invariant}$ 
      and redT:  $\alpha.\text{redT (state-}\alpha \text{ } s) (t, ta) (\text{state-}\alpha \text{ } s')$ 
      by(rule exec-step-into-redT)+
    note invar' moreover
    from  $\alpha.\text{redT-preserves-wset-thread-ok[OF redT]} \langle \text{wset-thread-ok (ws-}\alpha \text{ (wset } s)) \text{ (thr-}\alpha \text{ (thr } s)) \rangle$ 
    have wset-thread-ok ( $\text{ws-}\alpha \text{ (wset } s') \text{ (thr-}\alpha \text{ (thr } s'))$ ) by simp
    ultimately have state-invar (terminal (exec-aux ( $\sigma'$ ,  $s'$ ))) by(rule TCons)
    with  $\langle \text{TCons } \sigma tta \text{ } t t s = \text{exec-aux } (\sigma, s) \rangle$  [symmetric]
    show ?case unfolding ttls by simp

```

qed  
qed

end

**locale** *scheduler-ext-aux* =  
*scheduler-ext-base*  
*final r convert-RA*  
*thr-α thr-invar thr-lookup thr-update thr-iterate*  
*ws-α ws-invar ws-lookup ws-update ws-sel*  
*is-α is-invar is-memb is-ins is-delete*  
*thr'-α thr'-invar thr'-empty thr'-ins-dj*  
+  
*scheduler-aux*  
*final r convert-RA*  
*thr-α thr-invar thr-lookup thr-update*  
*ws-α ws-invar ws-lookup*  
*is-α is-invar is-memb is-ins is-delete*  
+  
*thr*: *map-iteratei thr-α thr-invar thr-iterate* +  
*ws*: *map-update ws-α ws-invar ws-update* +  
*ws*: *map-sel' ws-α ws-invar ws-sel* +  
*thr'*: *finite-set thr'-α thr'-invar* +  
*thr'*: *set-empty thr'-α thr'-invar thr'-empty* +  
*thr'*: *set-ins-dj thr'-α thr'-invar thr'-ins-dj*  
**for** *final* :: '*x* ⇒ *bool*  
**and** *r* :: '*t* ⇒ ('*x* × '*m*) ⇒ (('l,'t,'x,'m,'w,'o) *thread-action* × '*x* × '*m*) *Predicate.pred*  
**and** *convert-RA* :: '*l* *released-locks* ⇒ '*o* *list*  
**and** *thr-α* :: '*m-t* ⇒ ('l,'t,'x) *thread-info*  
**and** *thr-invar* :: '*m-t* ⇒ *bool*  
**and** *thr-lookup* :: '*t* ⇒ '*m-t* → ('*x* × '*l* *released-locks*)  
**and** *thr-update* :: '*t* ⇒ '*x* × '*l* *released-locks* ⇒ '*m-t* ⇒ '*m-t*  
**and** *thr-iterate* :: '*m-t* ⇒ ('*t* × ('*x* × '*l* *released-locks*), '*s-t*) *set-iterator*  
**and** *ws-α* :: '*m-w* ⇒ ('w,'t) *wait-sets*  
**and** *ws-invar* :: '*m-w* ⇒ *bool*  
**and** *ws-lookup* :: '*t* ⇒ '*m-w* → '*w* *wait-set-status*  
**and** *ws-update* :: '*t* ⇒ '*w* *wait-set-status* ⇒ '*m-w* ⇒ '*m-w*  
**and** *ws-sel* :: '*m-w* ⇒ (('t × '*w* *wait-set-status*) ⇒ *bool*) → ('t × '*w* *wait-set-status*)  
**and** *is-α* :: '*s-i* ⇒ '*t* *interrupts*  
**and** *is-invar* :: '*s-i* ⇒ *bool*  
**and** *is-memb* :: '*t* ⇒ '*s-i* ⇒ *bool*  
**and** *is-ins* :: '*t* ⇒ '*s-i* ⇒ '*s-i*  
**and** *is-delete* :: '*t* ⇒ '*s-i* ⇒ '*s-i*  
**and** *thr'-α* :: '*s-t* ⇒ '*t* *set*  
**and** *thr'-invar* :: '*s-t* ⇒ *bool*  
**and** *thr'-empty* :: *unit* ⇒ '*s-t*  
**and** *thr'-ins-dj* :: '*t* ⇒ '*s-t* ⇒ '*s-t*  
**begin**  
  
**lemma** *active-threads-correct* [*simp*]:  
**assumes** *state-invar s*  
**shows** *thr'-α* (*active-threads s*) = *α.active-threads* (*state-α s*) (**is** ?thesis1)  
**and** *thr'-invar* (*active-threads s*) (**is** ?thesis2)  
**proof** –

**obtain**  $ls\ ts\ m\ ws\ is$  **where**  $s: s = (ls, (ts, m), ws, is)$  **by**  $(cases\ s)\ fastforce$   
**let**  $?f = \lambda(t, (x, ln))\ TS.$  *if*  $ln = no\_wait\_locks$   
     *then if*  $Predicate.holds\ (do\ \{ (ta, -) \leftarrow r\ t\ (x, m); Predicate.if\_pred\ (actions\_ok\ (ls, (ts, m),$   
 $ws, is)\ t\ ta)\ \})$   
         *then*  $thr'-ins-dj\ t\ TS$  *else*  $TS$   
         *else if*  $\neg waiting\ (ws\_lookup\ t\ ws) \wedge may\_acquire\_all\ ls\ t\ ln$  *then*  $thr'-ins-dj\ t\ TS$  *else*  $TS$   
**let**  $?I = \lambda T\ TS.$   $thr'-invar\ TS \wedge thr'-\alpha\ TS \subseteq dom\ (thr-\alpha\ ts) - T \wedge (\forall t. t \notin T \longrightarrow t \in thr'-\alpha\ TS$   
 $\longleftrightarrow t \in \alpha.active\_threads\ (state-\alpha\ s))$

**from**  $assms\ s$  **have**  $thr-invar\ ts$  **by**  $simp$   
**moreover** **have**  $?I\ (dom\ (thr-\alpha\ ts))\ (thr'-empty\ ())$   
     **by**  $(auto\ simp\ add: thr'.empty-correct\ s\ elim: \alpha.active\_threads.cases)$   
**ultimately** **have**  $?I\ \{\}\ (thr-iterate\ ts\ (\lambda-. True)\ ?f\ (thr'-empty\ ()))$   
**proof**  $(rule\ thr.iterate-rule-P[where\ I=?I])$

**fix**  $t\ xln\ T\ TS$

**assume**  $tT: t \in T$

**and**  $tst: thr-\alpha\ ts\ t = \lfloor xln \rfloor$

**and**  $Tdom: T \subseteq dom\ (thr-\alpha\ ts)$

**and**  $I: ?I\ T\ TS$

**obtain**  $x\ ln$  **where**  $xln: xln = (x, ln)$  **by**  $(cases\ xln)$

**from**  $tT\ I$  **have**  $t: t \notin thr'-\alpha\ TS$  **by**  $blast$

**from**  $I$  **have**  $invar: thr'-invar\ TS ..$

**hence**  $thr'-invar\ (?f\ (t, xln)\ TS)$  **using**  $t$

**unfolding**  $xln$  **by**  $(auto\ simp\ add: thr'.ins-dj-correct)$

**moreover** **from**  $I$  **have**  $thr'-\alpha\ TS \subseteq dom\ (thr-\alpha\ ts) - T$  **by**  $blast$

**hence**  $thr'-\alpha\ (?f\ (t, xln)\ TS) \subseteq dom\ (thr-\alpha\ ts) - (T - \{t\})$

**using**  $invar\ tst\ t$  **by**  $(auto\ simp\ add: xln\ thr'.ins-dj-correct)$

**moreover**

**{**

**fix**  $t'$

**assume**  $t': t' \notin T - \{t\}$

**have**  $t' \in thr'-\alpha\ (?f\ (t, xln)\ TS) \longleftrightarrow t' \in \alpha.active\_threads\ (state-\alpha\ s)$  **(is**  $?lhs \longleftrightarrow ?rhs)$

**proof**  $(cases\ t' = t)$

**case**  $True$

**show**  $?thesis$

**proof**

**assume**  $?lhs$

**with**  $True\ xln\ invar\ tst\ \langle state-invar\ s \rangle\ t$  **show**  $?rhs$

**by**  $(fastforce\ simp\ add: holds-eq\ thr'.ins-dj-correct\ s\ split-beta\ ws.lookup-correct\ split: if-split-asm$

$elim!: bindE\ if-predE\ intro: \alpha.active\_threads.intros)$

**next**

**assume**  $?rhs$

**with**  $True\ xln\ invar\ tst\ \langle state-invar\ s \rangle\ t$  **show**  $?lhs$

**by**  $(fastforce\ elim!: \alpha.active\_threads.cases\ simp\ add: holds-eq\ s\ thr'.ins-dj-correct\ ws.lookup-correct$

$elim!: bindE\ if-predE\ intro: bindI\ if-predI)$

**qed**

**next**

**case**  $False$

**with**  $t'$  **have**  $t' \notin T$  **by**  $simp$

**with**  $I$  **have**  $t' \in thr'-\alpha\ TS \longleftrightarrow t' \in \alpha.active\_threads\ (state-\alpha\ s)$  **by**  $blast$

**thus**  $?thesis$  **using**  $xln\ False\ invar\ t$  **by**  $(auto\ simp\ add: thr'.ins-dj-correct)$

**qed**

**}**

```

    ultimately show ?I (T - {t}) (?f (t, xln) TS) by blast
qed
thus ?thesis1 ?thesis2 by(auto simp add: s)
qed

end

locale scheduler-ext =
  scheduler-ext-aux
  final r convert-RA
  thr- $\alpha$  thr-invar thr-lookup thr-update thr-iterate
  ws- $\alpha$  ws-invar ws-lookup ws-update ws-sel
  is- $\alpha$  is-invar is-memb is-ins is-delete
  thr'- $\alpha$  thr'-invar thr'-empty thr'-ins-dj
+
  scheduler-spec
  final r convert-RA
  schedule  $\sigma$ -invar
  thr- $\alpha$  thr-invar
  ws- $\alpha$  ws-invar
  is- $\alpha$  is-invar
  invariant
+
  ws: map-delete ws- $\alpha$  ws-invar ws-delete +
  ws: map-iteratei ws- $\alpha$  ws-invar ws-iterate
  for final :: 'x  $\Rightarrow$  bool
  and r :: 't  $\Rightarrow$  ('x  $\times$  'm)  $\Rightarrow$  (('l,'t,'x,'m,'w,'o) thread-action  $\times$  'x  $\times$  'm) Predicate.pred
  and convert-RA :: 'l released-locks  $\Rightarrow$  'o list
  and schedule :: ('l,'t,'x,'m,'w,'o,'m-t,'m-w,'s-i,'s) scheduler
  and output :: 's  $\Rightarrow$  't  $\Rightarrow$  ('l,'t,'x,'m,'w,'o) thread-action  $\Rightarrow$  'q option
  and  $\sigma$ -invar :: 's  $\Rightarrow$  't set  $\Rightarrow$  bool
  and thr- $\alpha$  :: 'm-t  $\Rightarrow$  ('l,'t,'x) thread-info
  and thr-invar :: 'm-t  $\Rightarrow$  bool
  and thr-lookup :: 't  $\Rightarrow$  'm-t  $\rightarrow$  ('x  $\times$  'l released-locks)
  and thr-update :: 't  $\Rightarrow$  'x  $\times$  'l released-locks  $\Rightarrow$  'm-t  $\Rightarrow$  'm-t
  and thr-iterate :: 'm-t  $\Rightarrow$  ('t  $\times$  ('x  $\times$  'l released-locks), 's-t) set-iterator
  and ws- $\alpha$  :: 'm-w  $\Rightarrow$  ('w,'t) wait-sets
  and ws-invar :: 'm-w  $\Rightarrow$  bool
  and ws-empty :: unit  $\Rightarrow$  'm-w
  and ws-lookup :: 't  $\Rightarrow$  'm-w  $\rightarrow$  'w wait-set-status
  and ws-update :: 't  $\Rightarrow$  'w wait-set-status  $\Rightarrow$  'm-w  $\Rightarrow$  'm-w
  and ws-delete :: 't  $\Rightarrow$  'm-w  $\Rightarrow$  'm-w
  and ws-iterate :: 'm-w  $\Rightarrow$  ('t  $\times$  'w wait-set-status, 'm-w) set-iterator
  and ws-sel :: 'm-w  $\Rightarrow$  ('t  $\times$  'w wait-set-status  $\Rightarrow$  bool)  $\rightarrow$  ('t  $\times$  'w wait-set-status)
  and is- $\alpha$  :: 's-i  $\Rightarrow$  't interrupts
  and is-invar :: 's-i  $\Rightarrow$  bool
  and is-memb :: 't  $\Rightarrow$  's-i  $\Rightarrow$  bool
  and is-ins :: 't  $\Rightarrow$  's-i  $\Rightarrow$  's-i
  and is-delete :: 't  $\Rightarrow$  's-i  $\Rightarrow$  's-i
  and thr'- $\alpha$  :: 's-t  $\Rightarrow$  't set
  and thr'-invar :: 's-t  $\Rightarrow$  bool
  and thr'-empty :: unit  $\Rightarrow$  's-t
  and thr'-ins-dj :: 't  $\Rightarrow$  's-t  $\Rightarrow$  's-t
  and invariant :: ('l,'t,'x,'m,'w) state set

```



```

+
assumes invariant: invariant3p  $\alpha$ .redT invariant

sublocale scheduler-ext <
  pick-wakeup-spec
  final r convert-RA
  pick-wakeup  $\sigma$ -invar
  thr- $\alpha$  thr-invar
  ws- $\alpha$  ws-invar
by(rule pick-wakeup-spec-via-sel)(unfold-locales)

sublocale scheduler-ext <
  scheduler
  final r convert-RA
  schedule output pick-wakeup  $\sigma$ -invar
  thr- $\alpha$  thr-invar thr-lookup thr-update
  ws- $\alpha$  ws-invar ws-lookup ws-update ws-delete ws-iterate
  is- $\alpha$  is-invar is-memb is-ins is-delete
  invariant
by(unfold-locales)(rule invariant)

```

### 9.2.1 Schedulers for deterministic small-step semantics

The default code equations for *Predicate.the* impose the type class constraint *eq* on the predicate elements. For the semantics, which contains the heap, there might be no such instance, so we use new constants for which other code equations can be used. These do not add the type class constraint, but may fail more often with non-uniqueness exception.

**definition** *singleton2* **where** [*simp*]: *singleton2* = *Predicate.singleton*

**definition** *the-only2* **where** [*simp*]: *the-only2* = *Predicate.the-only*

**definition** *the2* **where** [*simp*]: *the2* = *Predicate.the*

**context** *multithreaded-base* **begin**

**definition** *step-thread* ::

$((l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \Rightarrow 's) \Rightarrow (l, 't, 'x, 'm, 'w) \text{ state} \Rightarrow 't$   
 $\Rightarrow ('t \times ((l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \times 'x \times 'm) \text{ option} \times 's) \text{ option}$

**where**

```

 $\bigwedge ln. \text{ step-thread update-state } s \ t =$ 
  (case thr s t of
    [(x, ln)]  $\Rightarrow$ 
      if ln = no-wait-locks then
        if  $\exists ta \ x' \ m'. \ t \vdash (x, \text{shr } s) -ta \rightarrow (x', m') \wedge \text{actions-ok } s \ t \ ta$  then
          let
            (ta, x', m') = THE (ta, x', m').  $t \vdash (x, \text{shr } s) -ta \rightarrow (x', m') \wedge \text{actions-ok } s \ t \ ta$ 
          in
            [(t, [(ta, x', m')], update-state ta)]
        else
          None
      else if may-acquire-all (locks s) t ln  $\wedge \neg \text{waiting } (\text{wset } s \ t)$  then
        [(t, None, update-state (K$ [], [], [], [], [], convert-RA ln))]
      else
        None
  | None  $\Rightarrow$  None)

```

**lemma** *step-thread-NoneD*:

*step-thread update-state s t = None  $\implies$  t  $\notin$  active-threads s*

**unfolding** *step-thread-def*

**by**(*fastforce simp add: split-beta elim!: active-threads.cases split: if-split-asm*)

**lemma** *inactive-step-thread-eq-NoneI*:

*t  $\notin$  active-threads s  $\implies$  step-thread update-state s t = None*

**unfolding** *step-thread-def*

**by**(*fastforce simp add: split-beta split: if-split-asm intro: active-threads.intros*)

**lemma** *step-thread-eq-None-conv*:

*step-thread update-state s t = None  $\longleftrightarrow$  t  $\notin$  active-threads s*

**by**(*blast dest: step-thread-NoneD intro: inactive-step-thread-eq-NoneI*)

**lemma** *step-thread-eq-Some-activeD*:

*step-thread update-state s t =  $\lfloor (t', \text{taxm}\sigma') \rfloor$*

*$\implies$  t' = t  $\wedge$  t  $\in$  active-threads s*

**unfolding** *step-thread-def*

**by**(*fastforce split: if-split-asm simp add: split-beta intro: active-threads.intros*)

**declare** *actions-ok-iff* [*simp del*]

**declare** *actions-ok.cases* [*rule del*]

**lemma** *step-thread-Some-NoneD*:

*step-thread update-state s t' =  $\lfloor (t, \text{None}, \sigma') \rfloor$*

*$\implies \exists x \ln n. \text{thr } s \ t = \lfloor (x, \ln) \rfloor \wedge \ln \ \$ \ n > 0 \wedge \neg \text{waiting } (wset \ s \ t) \wedge \text{may-acquire-all } (\text{locks } s) \ t \ \ln$*

*$\wedge \sigma' = \text{update-state } (K\$ \ \lfloor, \lfloor, \lfloor, \lfloor, \lfloor, \text{convert-RA } \ln)$*

**unfolding** *step-thread-def*

**by**(*auto split: if-split-asm simp add: split-beta elim!: neq-no-wait-locksE*)

**lemma** *step-thread-Some-SomeD*:

*$\llbracket \text{deterministic } I; \text{step-thread update-state } s \ t' = \lfloor (t, \lfloor (ta, x', m') \rfloor, \sigma') \rfloor; s \in I \rrbracket$*

*$\implies \exists x. \text{thr } s \ t = \lfloor (x, \text{no-wait-locks}) \rfloor \wedge t \vdash \langle x, \text{shr } s \rangle -ta \rightarrow \langle x', m' \rangle \wedge \text{actions-ok } s \ t \ ta \wedge \sigma' = \text{update-state } ta$*

**unfolding** *step-thread-def*

**by**(*auto simp add: split-beta deterministic-THE split: if-split-asm*)

**end**

**context** *scheduler-base-aux* **begin**

**definition** *step-thread* ::

*((l', t', x', m', w', o) thread-action  $\Rightarrow$  's)  $\Rightarrow$  (l', t', m', m-t, m-w, s-i) state-refine  $\Rightarrow$  't  $\Rightarrow$*

*('t  $\times$  ((l', t', x', m', w', o) thread-action  $\times$  'x  $\times$  'm) option  $\times$  's) option*

**where**

*$\bigwedge \ln. \text{step-thread update-state } s \ t =$*

*(case thr-lookup t (thr s) of*

*$\lfloor (x, \ln) \rfloor \Rightarrow$*

*if  $\ln = \text{no-wait-locks}$  then*

*let*

*reds = do {*

*(ta, x', m')  $\leftarrow$  r t (x, shr s);*

*if actions-ok s t ta then Predicate.single (ta, x', m') else bot*

```

    }
  in
    if Predicate.holds (reds  $\gg$  ( $\lambda$ -. Predicate.single ())) then
      let
        (ta, x', m') = the2 reds
      in
        [(t, [(ta, x', m')], update-state ta)]
      else
        None
    else if may-acquire-all (locks s) t ln  $\wedge$   $\neg$  waiting (ws-lookup t (wset s)) then
      [(t, None, update-state (K$ [], [], [], [], [], convert-RA ln))]
    else
      None
  | None  $\Rightarrow$  None)

```

**end**

**context scheduler-aux begin**

**lemma deterministic-THE2:**

```

  assumes  $\alpha$ .deterministic I
  and tst: thr- $\alpha$  (thr s) t = [(x, no-wait-locks)]
  and red: Predicate.eval (r t (x, shr s)) (ta, x', m')
  and aok:  $\alpha$ .actions-ok (state- $\alpha$  s) t ta
  and I: state- $\alpha$  s  $\in$  I
  shows Predicate.the (r t (x, shr s)  $\gg$  ( $\lambda$ (ta, x', m'). if  $\alpha$ .actions-ok (state- $\alpha$  s) t ta then Predicate.single (ta, x', m') else bot)) = (ta, x', m')

```

**proof** –

```

  show ?thesis unfolding the-def
  apply(rule the-equality)
  apply(rule bindI[OF red])
  apply(simp add: singleI aok)
  apply(erule bindE)
  apply(clarsimp split: if-split-asm)
  apply(drule (1)  $\alpha$ .deterministicD[OF  $\langle \alpha$ .deterministic I  $\rangle$ , where s=state- $\alpha$  s, simplified, OF red
- tst aok])
  apply(rule I)
  apply simp
done
qed

```

**lemma step-thread-correct:**

```

  assumes det:  $\alpha$ .deterministic I
  and invar:  $\sigma$ -invar  $\sigma$  (dom (thr- $\alpha$  (thr s))) state-invar s state- $\alpha$  s  $\in$  I
  shows
    map-option (apsnd (apsnd  $\sigma$ - $\alpha$ )) (step-thread update-state s t) =  $\alpha$ .step-thread ( $\sigma$ - $\alpha$   $\circ$  update-state)
    (state- $\alpha$  s) t (is ?thesis1)
  and ( $\bigwedge$  ta. FWThread.thread-oks (thr- $\alpha$  (thr s))  $\{ta\}_t \Rightarrow \sigma$ -invar (update-state ta) (dom (thr- $\alpha$ 
    (thr s))  $\cup \{t. \exists x m. NewThread t x m \in \text{set } \{ta\}_t\}) \Rightarrow \text{case-option True } (\lambda(t, t\alpha m, \sigma). \sigma$ -invar
     $\sigma$  (case t\alpha m of None  $\Rightarrow$  dom (thr- $\alpha$  (thr s)) | Some (ta, x', m')  $\Rightarrow$  dom (thr- $\alpha$  (thr s))  $\cup \{t. \exists x m.
    NewThread t x m \in \text{set } \{ta\}_t\})$ ) (step-thread update-state s t)
    (is ( $\bigwedge$  ta. ?tso ta  $\Rightarrow$  ?inv ta)  $\Rightarrow$  ?thesis2))

```

**proof** –

```

  have ?thesis1  $\wedge$  (( $\forall$  ta. ?tso ta  $\longrightarrow$  ?inv ta)  $\longrightarrow$  ?thesis2)

```

```

proof(cases step-thread update-state s t)
  case None
  with invar show ?thesis
    apply (auto simp add: thr.lookup-correct  $\alpha$ .step-thread-def step-thread-def ws.lookup-correct
      split-beta holds-eq split: if-split-asm cong del: image-cong-simp)
    apply metis
    apply metis
    done
  next
  case (Some a)
  then obtain t' tasm  $\sigma'$ 
    where rrs: step-thread update-state s t = [(t', tasm,  $\sigma'$ )] by(cases a) auto
  show ?thesis
  proof(cases tasm)
    case None
    with rrs invar have ?thesis1
      by(auto simp add: thr.lookup-correct ws.lookup-correct  $\alpha$ .step-thread-def step-thread-def split-beta
        split: if-split-asm)
    moreover {
      let ?ta = (K$ [], [], [], [], [], convert-RA (snd (the (thr-lookup t (thr s)))))
      assume ?tso ?ta  $\longrightarrow$  ?inv ?ta
      hence ?thesis2 using None rrs
      by(auto simp add: thr.lookup-correct ws.lookup-correct  $\alpha$ .step-thread-def step-thread-def split-beta
        split: if-split-asm) }
    ultimately show ?thesis by blast
  next
  case (Some a)
  with rrs obtain ta x' m'
    where rrs: step-thread update-state s t = [(t', [(ta, x', m')],  $\sigma'$ )]
    by(cases a) fastforce
  with invar have ?thesis1
    by (auto simp add: thr.lookup-correct ws.lookup-correct  $\alpha$ .step-thread-def step-thread-def
      split-beta  $\alpha$ .deterministic-THE [OF det, where s=state- $\alpha$  s, simplified]
      deterministic-THE2[OF det] holds-eq split: if-split-asm
      cong del: image-cong-simp) blast+
  moreover {
    assume ?tso ta  $\longrightarrow$  ?inv ta
    hence ?thesis2 using rrs invar
    by(auto simp add: thr.lookup-correct ws.lookup-correct  $\alpha$ .step-thread-def step-thread-def split-beta
       $\alpha$ .deterministic-THE[OF det, where s=state- $\alpha$  s, simplified] deterministic-THE2[OF det] holds-eq
      split: if-split-asm)(auto simp add:  $\alpha$ .actions-ok-iff)
  }
  ultimately show ?thesis by blast
  qed
qed
thus ?thesis1 ( $\bigwedge$ ta. ?tso ta  $\implies$  ?inv ta)  $\implies$  ?thesis2 by blast+
qed

```

**lemma** step-thread-eq-None-conv:

```

assumes det:  $\alpha$ .deterministic I
and invar: state-invar s state- $\alpha$  s  $\in$  I
shows step-thread update-state s t = None  $\longleftrightarrow$  t  $\notin$   $\alpha$ .active-threads (state- $\alpha$  s)
using assms step-thread-correct(1)[OF det - invar(1), of  $\lambda$ - . True, of id update-state t]
by(simp add: map-option.id  $\alpha$ .step-thread-eq-None-conv)

```

**lemma** *step-thread-Some-NoneD*:

**assumes** *det*:  $\alpha.\text{deterministic } I$   
**and** *step*:  $\text{step-thread update-state } s \ t' = \lfloor (t, \text{None}, \sigma') \rfloor$   
**and** *invar*:  $\text{state-invar } s \ \text{state-}\alpha \ s \in I$   
**shows**  $\exists x \ln n. \text{thr-}\alpha \ (\text{thr } s) \ t = \lfloor (x, \ln) \rfloor \wedge \ln \ \$ \ n > 0 \wedge \neg \text{waiting} \ (\text{ws-}\alpha \ (\text{wset } s) \ t) \wedge \text{may-acquire-all}$   
 $(\text{locks } s) \ t \ \ln \wedge \sigma' = \text{update-state} \ (K\$ \ [], [], [], [], \text{convert-RA } \ln)$   
**using** *assms*  $\text{step-thread-correct}(1)[\text{OF } \text{det} - \text{invar}(1), \text{ of } \lambda - . \text{True}, \text{ of id update-state } t]$   
**by**  $(\text{fastforce simp add: map-option.id dest: } \alpha.\text{step-thread-Some-NoneD}[\text{OF sym}])$

**lemma** *step-thread-Some-SomeD*:

**assumes** *det*:  $\alpha.\text{deterministic } I$   
**and** *step*:  $\text{step-thread update-state } s \ t' = \lfloor (t, \lfloor (ta, x', m') \rfloor, \sigma') \rfloor$   
**and** *invar*:  $\text{state-invar } s \ \text{state-}\alpha \ s \in I$   
**shows**  $\exists x. \text{thr-}\alpha \ (\text{thr } s) \ t = \lfloor (x, \text{no-wait-locks}) \rfloor \wedge \text{Predicate.eval} \ (r \ t \ (x, \text{shr } s)) \ (ta, x', m') \wedge$   
 $\text{actions-ok } s \ t \ ta \wedge \sigma' = \text{update-state } ta$   
**using** *assms*  $\text{step-thread-correct}(1)[\text{OF } \text{det} - \text{invar}(1), \text{ of } \lambda - . \text{True}, \text{ of id update-state } t]$   
**by**  $(\text{auto simp add: map-option.id dest: } \alpha.\text{step-thread-Some-SomeD}[\text{OF det sym}])$

**end**

## 9.2.2 Code Generator setup

**lemmas** *[code]* =

*scheduler-base-aux.free-thread-id-def*  
*scheduler-base-aux.redT-updT.simps*  
*scheduler-base-aux.redT-updTs-def*  
*scheduler-base-aux.thread-ok.simps*  
*scheduler-base-aux.thread-oks.simps*  
*scheduler-base-aux.wset-actions-ok-def*  
*scheduler-base-aux.cond-action-ok.simps*  
*scheduler-base-aux.cond-action-oks-def*  
*scheduler-base-aux.redT-updI.simps*  
*scheduler-base-aux.redT-updIs.simps*  
*scheduler-base-aux.interrupt-action-ok.simps*  
*scheduler-base-aux.interrupt-actions-ok.simps*  
*scheduler-base-aux.actions-ok-def*  
*scheduler-base-aux.step-thread-def*

**lemmas** *[code]* =

*scheduler-base.exec-updW.simps*  
*scheduler-base.exec-updWs-def*  
*scheduler-base.exec-upd-def*  
*scheduler-base.execT-def*  
*scheduler-base.exec-step.simps*  
*scheduler-base.exec-aux-def*  
*scheduler-base.exec-def*

**lemmas** *[code]* =

*scheduler-ext-base.active-threads.simps*

**lemma** *singleton2-code* *[code]*:

*singleton2 dfault*  $(\text{Predicate.Seq } f) =$   
 $(\text{case } f \ () \ \text{of})$

```

    Predicate.Empty  $\Rightarrow$  dfault ()
  | Predicate.Insert x P  $\Rightarrow$ 
    if Predicate.is-empty P then x else Code.abort (STR "singleton2 not unique") ( $\lambda$ -. singleton2 dfault
(Predicate.Seq f))
  | Predicate.Join P xq  $\Rightarrow$ 
    if Predicate.is-empty P then
      the-only2 dfault xq
    else if Predicate.null xq then singleton2 dfault P else Code.abort (STR "singleton2 not unique")
( $\lambda$ -. singleton2 dfault (Predicate.Seq f)))
unfolding singleton2-def the-only2-def
by(auto simp only: singleton-code Code.abort-def split: seq.split if-split)

```

**lemma** the-only2-code [code]:

```

the-only2 dfault Predicate.Empty = Code.abort (STR "the-only2 empty") dfault
the-only2 dfault (Predicate.Insert x P) =
  (if Predicate.is-empty P then x else Code.abort (STR "the-only2 not unique") ( $\lambda$ -. the-only2 dfault
(Predicate.Insert x P)))
the-only2 dfault (Predicate.Join P xq) =
  (if Predicate.is-empty P then
    the-only2 dfault xq
  else if Predicate.null xq then
    singleton2 dfault P
  else
    Code.abort (STR "the-only2 not unique") ( $\lambda$ -. the-only2 dfault (Predicate.Join P xq)))
unfolding singleton2-def the-only2-def by simp-all

```

**lemma** the2-eq [code]:

```

the2 A = singleton2 ( $\lambda$ x. Code.abort (STR "not-unique") ( $\lambda$ -. the2 A)) A
unfolding the2-def singleton2-def by(rule the-eq)

```

**end**

### 9.3 Random scheduler

**theory** Random-Scheduler

**imports**

Scheduler

**begin**

**type-synonym** random-scheduler = Random.seed

**abbreviation** (input)

random-scheduler-invar :: random-scheduler  $\Rightarrow$  't set  $\Rightarrow$  bool

**where** random-scheduler-invar  $\equiv \lambda$ -. True

**locale** random-scheduler-base =

scheduler-ext-base

final r convert-RA

thr- $\alpha$  thr-invar thr-lookup thr-update thr-iterate

ws- $\alpha$  ws-invar ws-lookup ws-update ws-sel

is- $\alpha$  is-invar is-memb is-ins is-delete

thr'- $\alpha$  thr'-invar thr'-empty thr'-ins-dj

**for** final :: 'x  $\Rightarrow$  bool

```

and  $r :: 't \Rightarrow ('x \times 'm) \Rightarrow (('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \times 'x \times 'm) \text{ Predicate.pred}$ 
and  $\text{convert-RA} :: 'l \text{ released-locks} \Rightarrow 'o \text{ list}$ 
and  $\text{output} :: \text{random-scheduler} \Rightarrow 't \Rightarrow ('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \Rightarrow 'q \text{ option}$ 
and  $\text{thr-}\alpha :: 'm\text{-}t \Rightarrow ('l, 't, 'x) \text{ thread-info}$ 
and  $\text{thr-invar} :: 'm\text{-}t \Rightarrow \text{bool}$ 
and  $\text{thr-lookup} :: 't \Rightarrow 'm\text{-}t \rightarrow ('x \times 'l \text{ released-locks})$ 
and  $\text{thr-update} :: 't \Rightarrow 'x \times 'l \text{ released-locks} \Rightarrow 'm\text{-}t \Rightarrow 'm\text{-}t$ 
and  $\text{thr-iterate} :: 'm\text{-}t \Rightarrow ('t \times ('x \times 'l \text{ released-locks}), 's\text{-}t) \text{ set-iterator}$ 
and  $\text{ws-}\alpha :: 'm\text{-}w \Rightarrow ('w, 't) \text{ wait-sets}$ 
and  $\text{ws-invar} :: 'm\text{-}w \Rightarrow \text{bool}$ 
and  $\text{ws-lookup} :: 't \Rightarrow 'm\text{-}w \rightarrow 'w \text{ wait-set-status}$ 
and  $\text{ws-update} :: 't \Rightarrow 'w \text{ wait-set-status} \Rightarrow 'm\text{-}w \Rightarrow 'm\text{-}w$ 
and  $\text{ws-delete} :: 't \Rightarrow 'm\text{-}w \Rightarrow 'm\text{-}w$ 
and  $\text{ws-iterate} :: 'm\text{-}w \Rightarrow ('t \times 'w \text{ wait-set-status}, 'm\text{-}w) \text{ set-iterator}$ 
and  $\text{ws-sel} :: 'm\text{-}w \Rightarrow ('t \times 'w \text{ wait-set-status} \Rightarrow \text{bool}) \rightarrow ('t \times 'w \text{ wait-set-status})$ 
and  $\text{is-}\alpha :: 's\text{-}i \Rightarrow 't \text{ interrupts}$ 
and  $\text{is-invar} :: 's\text{-}i \Rightarrow \text{bool}$ 
and  $\text{is-memb} :: 't \Rightarrow 's\text{-}i \Rightarrow \text{bool}$ 
and  $\text{is-ins} :: 't \Rightarrow 's\text{-}i \Rightarrow 's\text{-}i$ 
and  $\text{is-delete} :: 't \Rightarrow 's\text{-}i \Rightarrow 's\text{-}i$ 
and  $\text{thr}'\text{-}\alpha :: 's\text{-}t \Rightarrow 't \text{ set}$ 
and  $\text{thr}'\text{-invar} :: 's\text{-}t \Rightarrow \text{bool}$ 
and  $\text{thr}'\text{-empty} :: \text{unit} \Rightarrow 's\text{-}t$ 
and  $\text{thr}'\text{-ins-dj} :: 't \Rightarrow 's\text{-}t \Rightarrow 's\text{-}t$ 
+
fixes  $\text{thr}'\text{-to-list} :: 's\text{-}t \Rightarrow 't \text{ list}$ 
begin

```

```

definition  $\text{next-thread} :: \text{random-scheduler} \Rightarrow 's\text{-}t \Rightarrow ('t \times \text{random-scheduler}) \text{ option}$ 
where
   $\text{next-thread seed active} =$ 
   $(\text{let } ts = \text{thr}'\text{-to-list active}$ 
     $\text{in if } ts = [] \text{ then None else Some (Random.select (thr}'\text{-to-list active) seed)})$ 

```

```

definition  $\text{random-scheduler} :: ('l, 't, 'x, 'm, 'w, 'o, 'm\text{-}t, 'm\text{-}w, 's\text{-}i, \text{random-scheduler}) \text{ scheduler}$ 
where
   $\text{random-scheduler seed } s =$ 
   $(\text{do } \{$ 
     $(t, \text{seed}') \leftarrow \text{next-thread seed (active-threads } s);$ 
     $\text{step-thread } (\lambda ta. \text{seed}') s t$ 
   $\})$ 

```

**end**

```

locale  $\text{random-scheduler} =$ 
   $\text{random-scheduler-base}$ 
   $\text{final } r \text{ convert-RA output}$ 
   $\text{thr-}\alpha \text{ thr-invar thr-lookup thr-update thr-iterate}$ 
   $\text{ws-}\alpha \text{ ws-invar ws-lookup ws-update ws-delete ws-iterate ws-sel}$ 
   $\text{is-}\alpha \text{ is-invar is-memb is-ins is-delete}$ 
   $\text{thr}'\text{-}\alpha \text{ thr}'\text{-invar thr}'\text{-empty thr}'\text{-ins-dj thr}'\text{-to-list}$ 
+
   $\text{scheduler-ext-aux}$ 
   $\text{final } r \text{ convert-RA}$ 

```

```

  thr-α thr-invar thr-lookup thr-update thr-iterate
  ws-α ws-invar ws-lookup ws-update ws-sel
  is-α is-invar is-memb is-ins is-delete
  thr'-α thr'-invar thr'-empty thr'-ins-dj
+
  ws: map-delete ws-α ws-invar ws-delete +
  ws: map-iteratei ws-α ws-invar ws-iterate +
  thr': set-to-list thr'-α thr'-invar thr'-to-list
for final :: 'x ⇒ bool
and r :: 't ⇒ ('x × 'm) ⇒ (('l,'t,'x,'m,'w,'o) thread-action × 'x × 'm) Predicate.pred
and convert-RA :: 'l released-locks ⇒ 'o list
and output :: random-scheduler ⇒ 't ⇒ ('l,'t,'x,'m,'w,'o) thread-action ⇒ 'q option
and thr-α :: 'm-t ⇒ ('l,'t,'x) thread-info
and thr-invar :: 'm-t ⇒ bool
and thr-lookup :: 't ⇒ 'm-t → ('x × 'l released-locks)
and thr-update :: 't ⇒ 'x × 'l released-locks ⇒ 'm-t ⇒ 'm-t
and thr-iterate :: 'm-t ⇒ ('t × ('x × 'l released-locks), 's-t) set-iterator
and ws-α :: 'm-w ⇒ ('w,'t) wait-sets
and ws-invar :: 'm-w ⇒ bool
and ws-lookup :: 't ⇒ 'm-w → 'w wait-set-status
and ws-update :: 't ⇒ 'w wait-set-status ⇒ 'm-w ⇒ 'm-w
and ws-delete :: 't ⇒ 'm-w ⇒ 'm-w
and ws-iterate :: 'm-w ⇒ ('t × 'w wait-set-status, 'm-w) set-iterator
and ws-sel :: 'm-w ⇒ ('t × 'w wait-set-status ⇒ bool) → ('t × 'w wait-set-status)
and is-α :: 's-i ⇒ 't interrupts
and is-invar :: 's-i ⇒ bool
and is-memb :: 't ⇒ 's-i ⇒ bool
and is-ins :: 't ⇒ 's-i ⇒ 's-i
and is-delete :: 't ⇒ 's-i ⇒ 's-i
and thr'-α :: 's-t ⇒ 't set
and thr'-invar :: 's-t ⇒ bool
and thr'-empty :: unit ⇒ 's-t
and thr'-ins-dj :: 't ⇒ 's-t ⇒ 's-t
and thr'-to-list :: 's-t ⇒ 't list
begin

```

**lemma** *next-thread-eq-None-iff*:

**assumes** thr'-invar active random-scheduler-invar seed T  
**shows** next-thread seed active = None  $\longleftrightarrow$  thr'-α active = {}

**using** thr'.to-list-correct[OF assms(1)]

**by**(auto simp add: next-thread-def neq-Nil-conv)

**lemma** *next-thread-eq-SomeD*:

**assumes** next-thread seed active = Some (t, seed')  
**and** thr'-invar active random-scheduler-invar seed T  
**shows** t ∈ thr'-α active

**using** assms

**by**(auto simp add: next-thread-def thr'.to-list-correct split: if-split-asm dest: select[of - seed])

**lemma** *random-scheduler-spec*:

**assumes** det: α.deterministic I

**shows** scheduler-spec final r random-scheduler random-scheduler-invar thr-α thr-invar ws-α ws-invar  
 is-α is-invar I

**proof**



```

fix  $\sigma$   $s$ 
assume  $rr$ : random-scheduler  $\sigma$   $s = \text{None}$ 
  and  $invar$ : random-scheduler-invar  $\sigma$  ( $\text{dom } (thr-\alpha \ (thr \ s))$ ) state-invar  $s$   $state-\alpha \ s \in I$ 
from  $invar(2)$  have  $thr'-invar$  (active-threads  $s$ ) by(rule active-threads-correct)
thus  $\alpha.active-threads$  ( $state-\alpha \ s$ ) =  $\{\}$  using  $rr \ invar$ 
  by(auto simp add: random-scheduler-def Option-bind-eq-None-conv next-thread-eq-None-iff step-thread-eq-None-conv[OF det] dest: next-thread-eq-SomeD)
next
  fix  $\sigma \ s \ t \ \sigma'$ 
  assume  $rr$ : random-scheduler  $\sigma \ s = \lfloor (t, \text{None}, \sigma') \rfloor$ 
  and  $invar$ : random-scheduler-invar  $\sigma$  ( $\text{dom } (thr-\alpha \ (thr \ s))$ ) state-invar  $s$   $state-\alpha \ s \in I$ 
  thus  $\exists x \ln n. thr-\alpha \ (thr \ s) \ t = \lfloor (x, \ln) \rfloor \wedge 0 < \ln \ \$ \ n \wedge \neg \text{waiting } (ws-\alpha \ (wset \ s) \ t) \wedge \text{may-acquire-all}$ 
    (locks  $s$ )  $t \ \ln$ 
  by(fastforce simp add: random-scheduler-def bind-eq-Some-conv dest: step-thread-Some-NoneD[OF det])
next
  fix  $\sigma \ s \ t \ ta \ x' \ m' \ \sigma'$ 
  assume  $rr$ : random-scheduler  $\sigma \ s = \lfloor (t, \lfloor (ta, x', m') \rfloor, \sigma') \rfloor$ 
  and  $invar$ : random-scheduler-invar  $\sigma$  ( $\text{dom } (thr-\alpha \ (thr \ s))$ ) state-invar  $s$   $state-\alpha \ s \in I$ 
  thus  $\exists x. thr-\alpha \ (thr \ s) \ t = \lfloor (x, \text{no-wait-locks}) \rfloor \wedge \text{Predicate.eval } (r \ t \ (x, shr \ s)) \ (ta, x', m') \wedge$ 
     $\alpha.actions-ok$  ( $state-\alpha \ s$ )  $t \ ta$ 
  by(auto simp add: random-scheduler-def bind-eq-Some-conv dest: step-thread-Some-SomeD[OF det])
qed simp-all

end

sublocale random-scheduler-base <
  scheduler-base
  final r convert-RA
  random-scheduler output pick-wakeup-via-sel ( $\lambda s \ P. ws-sel \ s \ (\lambda(k,v). P \ k \ v)$ ) random-scheduler-invar
  thr-\alpha thr-invar thr-lookup thr-update
  ws-\alpha ws-invar ws-lookup ws-update ws-delete ws-iterate
  is-\alpha is-invar is-memb is-ins is-delete
for  $n0$  .

sublocale random-scheduler <
  pick-wakeup-spec
  final r convert-RA
  pick-wakeup-via-sel ( $\lambda s \ P. ws-sel \ s \ (\lambda(k,v). P \ k \ v)$ ) random-scheduler-invar
  thr-\alpha thr-invar
  ws-\alpha ws-invar
  is-\alpha is-invar
by(rule pick-wakeup-spec-via-sel)(unfold-locales)

context random-scheduler begin

lemma random-scheduler-scheduler:
assumes  $det$ :  $\alpha.deterministic \ I$ 
shows
  scheduler
  final r convert-RA
  random-scheduler (pick-wakeup-via-sel ( $\lambda s \ P. ws-sel \ s \ (\lambda(k,v). P \ k \ v)$ )) random-scheduler-invar
  thr-\alpha thr-invar thr-lookup thr-update
  ws-\alpha ws-invar ws-lookup ws-update ws-delete ws-iterate

```

```

    is- $\alpha$  is-invar is-memb is-ins is-delete
  I
proof —
  interpret scheduler-spec
    final r convert-RA
    random-scheduler random-scheduler-invar
    thr- $\alpha$  thr-invar
    ws- $\alpha$  ws-invar
    is- $\alpha$  is-invar
  I
  using det by(rule random-scheduler-spec)

  show ?thesis by(unfold-locales)(rule  $\alpha$ .deterministic-invariant3p[OF det])
qed

end

```

### 9.3.1 Code generator setup

```

lemmas [code] =
  random-scheduler-base.next-thread-def
  random-scheduler-base.random-scheduler-def

end

```

## 9.4 Round robin scheduler

```

theory Round-Robin
imports
  Scheduler
begin

```

A concrete scheduler must pick one possible reduction step from the small-step semantics for individual threads. Currently, this is only possible if there is only one such by using *Predicate.the*.

### 9.4.1 Concrete schedulers

### 9.4.2 Round-robin schedulers

```

type-synonym 'queue round-robin = 'queue  $\times$  nat
  — Waiting queue of threads and remaining number of steps of the first thread until it has to return
  resources

primrec enqueue-new-thread :: 't list  $\Rightarrow$  ('t,'x,'m) new-thread-action  $\Rightarrow$  't list
where
  enqueue-new-thread queue (NewThread t x m) = queue @ [t]
| enqueue-new-thread queue (ThreadExists t b) = queue

definition enqueue-new-threads :: 't list  $\Rightarrow$  ('t,'x,'m) new-thread-action list  $\Rightarrow$  't list
where
  enqueue-new-threads = foldl enqueue-new-thread

```

**primrec** *round-robin-update-state* ::  $\text{nat} \Rightarrow 't \text{ list round-robin} \Rightarrow 't \Rightarrow ('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \Rightarrow 't \text{ list round-robin}$

**where**

*round-robin-update-state*  $n0$  (*queue*,  $n$ )  $t$   $ta =$   
 (let  $queue' = \text{enqueue-new-threads } queue \llbracket ta \rrbracket_t$   
 in if  $n = 0 \vee \text{Yield} \in \text{set } \llbracket ta \rrbracket_c$  then (*rotate1*  $queue'$ ,  $n0$ ) else ( $queue'$ ,  $n - 1$ ))

**context** *multithreaded-base* **begin**

**abbreviation** *round-robin-step* ::  $\text{nat} \Rightarrow 't \text{ list round-robin} \Rightarrow ('l, 't, 'x, 'm, 'w) \text{ state} \Rightarrow 't \Rightarrow ('t \times (( 'l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \times 'x \times 'm) \text{ option} \times 't \text{ list round-robin}) \text{ option}$

**where**

*round-robin-step*  $n0$   $\sigma$   $s$   $t \equiv \text{step-thread } (\text{round-robin-update-state } n0 \sigma t) s t$

**partial-function** (*option*) *round-robin-reschedule* ::  $'t \Rightarrow$

$'t \text{ list} \Rightarrow \text{nat} \Rightarrow ('l, 't, 'x, 'm, 'w) \text{ state} \Rightarrow ('t \times (( 'l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \times 'x \times 'm) \text{ option} \times 't \text{ list round-robin}) \text{ option}$

**where**

*round-robin-reschedule*  $t0$  *queue*  $n0$   $s =$   
 (let  
    $t = \text{hd } queue;$   
    $queue' = \text{tl } queue$   
 in  
   if  $t = t0$  then  
     None  
   else  
     case *round-robin-step*  $n0$  ( $t \# queue'$ ,  $n0$ )  $s$   $t$  of  
       None  $\Rightarrow \text{round-robin-reschedule } t0$  ( $queue' @ [t]$ )  $n0$   $s$   
       |  $\llbracket ttaxm\sigma \rrbracket \Rightarrow \llbracket ttaxm\sigma \rrbracket$ )

**fun** *round-robin* ::  $\text{nat} \Rightarrow 't \text{ list round-robin} \Rightarrow ('l, 't, 'x, 'm, 'w) \text{ state} \Rightarrow ('t \times (( 'l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \times 'x \times 'm) \text{ option} \times 't \text{ list round-robin}) \text{ option}$

**where**

*round-robin*  $n0$  ( $[], n$ )  $s = \text{None}$   
 | *round-robin*  $n0$  ( $t \# queue$ ,  $n$ )  $s =$   
   (case *round-robin-step*  $n0$  ( $t \# queue$ ,  $n$ )  $s$   $t$  of  
      $\llbracket ttaxm\sigma \rrbracket \Rightarrow \llbracket ttaxm\sigma \rrbracket$   
     | None  $\Rightarrow \text{round-robin-reschedule } t$  ( $queue @ [t]$ )  $n0$   $s$ )

**end**

**primrec** *round-robin-invar* ::  $'t \text{ list round-robin} \Rightarrow 't \text{ set} \Rightarrow \text{bool}$

**where** *round-robin-invar* (*queue*,  $n$ )  $T \longleftrightarrow \text{set } queue = T \wedge \text{distinct } queue$

**lemma** *set-enqueue-new-thread*:

$\text{set } (\text{enqueue-new-thread } queue \text{ nta}) = \text{set } queue \cup \{t. \exists x m. \text{nta} = \text{NewThread } t x m\}$

**by**(cases *nta*) *auto*

**lemma** *set-enqueue-new-threads*:

$\text{set } (\text{enqueue-new-threads } queue \text{ ntas}) = \text{set } queue \cup \{t. \exists x m. \text{NewThread } t x m \in \text{set } ntas\}$

**apply**(*induct* *ntas* *arbitrary*: *queue*)

**apply**(*auto simp add*: *enqueue-new-threads-def* *set-enqueue-new-thread*)

**done**

**lemma** *enqueue-new-thread-eq-Nil* [simp]:

*enqueue-new-thread queue nta* = []  $\longleftrightarrow$  *queue* = []  $\wedge$  ( $\exists t b. nta = \text{ThreadExists } t b$ )  
**by**(cases nta) simp-all

**lemma** *enqueue-new-threads-eq-Nil* [simp]:

*enqueue-new-threads queue ntas* = []  $\longleftrightarrow$  *queue* = []  $\wedge$  *set ntas*  $\subseteq$  {*ThreadExists t b* | *t b. True*}  
**apply**(induct ntas arbitrary: queue)  
**apply**(auto simp add: enqueue-new-threads-def)  
**done**

**lemma** *distinct-enqueue-new-threads*:

**fixes** *ts* :: ('l,'t,'x) *thread-info*  
**and** *ntas* :: ('t,'x,'m) *new-thread-action list*  
**assumes** *thread-oks ts ntas set queue = dom ts distinct queue*  
**shows** *distinct (enqueue-new-threads queue ntas)*  
**using** *assms*  
**proof**(induct ntas arbitrary: ts queue)  
**case Nil thus ?case** **by**(simp add: enqueue-new-threads-def)  
**next**  
**case** (*Cons nt ntas*)  
**from**  $\langle \text{thread-oks } ts (nt \# ntas) \rangle$   
**have** *thread-ok ts nt and thread-oks (redT-updT ts nt) ntas* **by** simp-all  
**from**  $\langle \text{thread-ok } ts nt \rangle \langle \text{set queue = dom ts} \rangle \langle \text{distinct queue} \rangle$   
**have** *set (enqueue-new-thread queue nt) = dom (redT-updT ts nt)  $\wedge$  distinct (enqueue-new-thread queue nt)*  
**by**(cases nt)(auto)  
**with**  $\langle \text{thread-oks (redT-updT ts nt) ntas} \rangle$   
**have** *distinct (enqueue-new-threads (enqueue-new-thread queue nt) ntas)*  
**by**(blast intro: Cons.hyps)  
**thus ?case** **by**(simp add: enqueue-new-threads-def)  
**qed**

**lemma** *round-robin-reschedule-induct* [consumes 1, case-names head rotate]:

**assumes** *major: t0  $\in$  set queue*  
**and** *head:  $\bigwedge \text{queue}. P (t0 \# \text{queue})$*   
**and** *rotate:  $\bigwedge \text{queue } t. [\![ t \neq t0; t0 \in \text{set queue}; P (\text{queue} @ [t]) ]\!] \implies P (t \# \text{queue})$*   
**shows** *P queue*  
**using** *major*  
**proof**(induct  $n \equiv \text{length (takeWhile } (\lambda x. x \neq t0) \text{ queue})$  arbitrary: queue)  
**case 0**  
**then obtain queue' where queue = t0 # queue'**  
**by**(cases queue)(auto split: if-split-asm)  
**thus ?case** **by**(simp add: head)  
**next**  
**case** (*Suc n*)  
**then obtain t queue' where** [simp]: *queue = t # queue'*  
**and** *t: t  $\neq$  t0 and n: n = length (takeWhile (lambda x. x  $\neq$  t0) queue')*  
**and** *t0: t0  $\in$  set queue'*  
**by**(cases queue)(auto split: if-split-asm)  
**from** *n t0 have n = length (takeWhile (lambda x. x  $\neq$  t0) (queue' @ [t]))* **by**(simp)  
**moreover from t0 have t0  $\in$  set (queue' @ [t])** **by** simp  
**ultimately have P (queue' @ [t])** **by**(rule Suc.hyps)  
**with t t0 show ?case** **by**(simp add: rotate)  
**qed**

**context** *multithreaded-base* **begin**

**declare** *actions-ok-iff* [*simp del*]  
**declare** *actions-ok.cases* [*rule del*]

**lemma** *round-robin-step-invar-None*:

$\llbracket \text{round-robin-step } n0 \ \sigma \ s \ t' = \lfloor (t, \text{None}, \sigma') \rfloor; \text{round-robin-invar } \sigma \ (\text{dom } (\text{thr } s)) \rrbracket$   
 $\implies \text{round-robin-invar } \sigma' \ (\text{dom } (\text{thr } s))$

**by**(*cases*  $\sigma$ )(*auto dest: step-thread-Some-NoneD simp add: set-enqueue-new-threads distinct-enqueue-new-threads*)

**lemma** *round-robin-step-invar-Some*:

$\llbracket \text{deterministic } I; \text{round-robin-step } n0 \ \sigma \ s \ t' = \lfloor (t, \lfloor (ta, x', m') \rfloor, \sigma') \rfloor; \text{round-robin-invar } \sigma \ (\text{dom } (\text{thr } s)); s \in I \rrbracket$   
 $\implies \text{round-robin-invar } \sigma' \ (\text{dom } (\text{thr } s) \cup \{t. \exists x \ m. \text{NewThread } t \ x \ m \in \text{set } \llbracket ta \rrbracket_t\})$

**apply**(*cases*  $\sigma$ )

**apply** *clarsimp*

**apply**(*frule* (1) *step-thread-Some-SomeD*)

**apply**(*auto split: if-split-asm simp add: split-beta set-enqueue-new-threads deterministic-THE*)

**apply**(*auto simp add: actions-ok-iff distinct-enqueue-new-threads*)

**done**

**lemma** *round-robin-reschedule-Cons*:

*round-robin-reschedule*  $t0 \ (t0 \# \text{queue}) \ n0 \ s = \text{None}$   
 $t \neq t0 \implies \text{round-robin-reschedule } t0 \ (t \# \text{queue}) \ n0 \ s =$   
 (*case* *round-robin-step*  $n0 \ (t \# \text{queue}, n0) \ s \ t$  *of*  
    $\text{None} \Rightarrow \text{round-robin-reschedule } t0 \ (\text{queue} @ [t]) \ n0 \ s$   
    $| \text{Some } ttaxm\sigma \Rightarrow \text{Some } ttaxm\sigma$ )

**by**(*simp-all add: round-robin-reschedule.simps*)

**lemma** *round-robin-reschedule-NoneD*:

**assumes** *rrr*: *round-robin-reschedule*  $t0 \ \text{queue} \ n0 \ s = \text{None}$   
**and** *t0*:  $t0 \in \text{set } \text{queue}$   
**shows**  $\text{set } (\text{takeWhile } (\lambda t'. t' \neq t0) \ \text{queue}) \cap \text{active-threads } s = \{\}$

**using** *t0 rrr*

**proof**(*induct queue rule: round-robin-reschedule-induct*)

**case** (*head queue*)

**thus** *?case* **by** *simp*

**next**

**case** (*rotate queue t*)

**from**  $\langle \text{round-robin-reschedule } t0 \ (t \# \text{queue}) \ n0 \ s = \text{None} \rangle \langle t \neq t0 \rangle$

**have** *round-robin-step*  $n0 \ (t \# \text{queue}, n0) \ s \ t = \text{None}$

**and** *round-robin-reschedule*  $t0 \ (\text{queue} @ [t]) \ n0 \ s = \text{None}$

**by**(*simp-all add: round-robin-reschedule-Cons*)

**from** *this*(1) **have**  $t \notin \text{active-threads } s$  **by**(*rule step-thread-NoneD*)

**moreover from**  $\langle \text{round-robin-reschedule } t0 \ (\text{queue} @ [t]) \ n0 \ s = \text{None} \rangle$

**have**  $\text{set } (\text{takeWhile } (\lambda t'. t' \neq t0) \ (\text{queue} @ [t])) \cap \text{active-threads } s = \{\}$

**by**(*rule rotate.hyps*)

**moreover have**  $\text{takeWhile } (\lambda t'. t' \neq t0) \ (\text{queue} @ [t]) = \text{takeWhile } (\lambda t'. t' \neq t0) \ \text{queue}$

**using**  $\langle t0 \in \text{set } \text{queue} \rangle$  **by** *simp*

**ultimately show** *?case* **using**  $\langle t \neq t0 \rangle$  **by** *simp*

**qed**

**lemma** *round-robin-reschedule-Some-NoneD*:

```

assumes rrr: round-robin-reschedule t0 queue n0 s = [(t, None, σ')]
and t0: t0 ∈ set queue
shows ∃ x ln n. thr s t = [(x, ln)] ∧ ln $ n > 0 ∧ ¬ waiting (wset s t) ∧ may-acquire-all (locks s)
t ln
using t0 rrr
proof(induct queue rule: round-robin-reschedule-induct)
  case head thus ?case by(simp add: round-robin-reschedule-Cons)
next
  case (rotate queue t')
  show ?case
  proof(cases round-robin-step n0 (t' # queue, n0) s t')
    case None
    with ⟨round-robin-reschedule t0 (t' # queue) n0 s = [(t, None, σ')]⟩ ⟨t' ≠ t0⟩
    have round-robin-reschedule t0 (queue @ [t']) n0 s = [(t, None, σ')]
      by(simp add: round-robin-reschedule-Cons)
    thus ?thesis by(rule rotate.hyps)
  next
  case (Some a)
  with ⟨round-robin-reschedule t0 (t' # queue) n0 s = [(t, None, σ')]⟩ ⟨t' ≠ t0⟩
  have round-robin-step n0 (t' # queue, n0) s t' = [(t, None, σ')]
    by(simp add: round-robin-reschedule-Cons)
  thus ?thesis by(blast dest: step-thread-Some-NoneD)
qed
qed

```

**lemma** round-robin-reschedule-Some-SomeD:

```

assumes deterministic I
and rrr: round-robin-reschedule t0 queue n0 s = [(t, [(ta, x', m'), σ'])]
and t0: t0 ∈ set queue
and I: s ∈ I
shows ∃ x. thr s t = [(x, no-wait-locks)] ∧ t ⊢ ⟨x, shr s⟩ -ta→ ⟨x', m'⟩ ∧ actions-ok s t ta
using t0 rrr
proof(induct queue rule: round-robin-reschedule-induct)
  case head thus ?case by(simp add: round-robin-reschedule-Cons)
next
  case (rotate queue t')
  show ?case
  proof(cases round-robin-step n0 (t' # queue, n0) s t')
    case None
    with ⟨round-robin-reschedule t0 (t' # queue) n0 s = [(t, [(ta, x', m'), σ'])]⟩ ⟨t' ≠ t0⟩
    have round-robin-reschedule t0 (queue @ [t']) n0 s = [(t, [(ta, x', m'), σ'])]
      by(simp add: round-robin-reschedule-Cons)
    thus ?thesis by(rule rotate.hyps)
  next
  case (Some a)
  with ⟨round-robin-reschedule t0 (t' # queue) n0 s = [(t, [(ta, x', m'), σ'])]⟩ ⟨t' ≠ t0⟩
  have round-robin-step n0 (t' # queue, n0) s t' = [(t, [(ta, x', m'), σ'])]
    by(simp add: round-robin-reschedule-Cons)
  thus ?thesis using I by(blast dest: step-thread-Some-SomeD[OF ⟨deterministic I⟩])
qed
qed

```

**lemma** round-robin-reschedule-invar-None:

```

assumes rrr: round-robin-reschedule t0 queue n0 s = [(t, None, σ')]

```

```

and invar: round-robin-invar (queue, n0) (dom (thr s))
and t0: t0 ∈ set queue
shows round-robin-invar σ' (dom (thr s))
using t0 rrr invar
proof(induct queue rule: round-robin-reschedule-induct)
  case head thus ?case by(simp add: round-robin-reschedule-Cons)
next
  case (rotate queue t')
  show ?case
  proof(cases round-robin-step n0 (t' # queue, n0) s t')
    case None
    with ⟨round-robin-reschedule t0 (t' # queue) n0 s = [(t, None, σ')], ⟨t' ≠ t0⟩
    have round-robin-reschedule t0 (queue @ [t']) n0 s = [(t, None, σ')]
      by(simp add: round-robin-reschedule-Cons)
    moreover from ⟨round-robin-invar (t' # queue, n0) (dom (thr s))⟩
    have round-robin-invar (queue @ [t'], n0) (dom (thr s)) by simp
    ultimately show ?thesis by(rule rotate.hyps)
  next
    case (Some a)
    with ⟨round-robin-reschedule t0 (t' # queue) n0 s = [(t, None, σ')], ⟨t' ≠ t0⟩
    have round-robin-step n0 (t' # queue, n0) s t' = [(t, None, σ')]
      by(simp add: round-robin-reschedule-Cons)
    thus ?thesis using ⟨round-robin-invar (t' # queue, n0) (dom (thr s))⟩
      by(rule round-robin-step-invar-None)
  qed
qed

lemma round-robin-reschedule-invar-Some:
  assumes deterministic I
  and rrr: round-robin-reschedule t0 queue n0 s = [(t, [(ta, x', m'), σ'])]
  and invar: round-robin-invar (queue, n0) (dom (thr s))
  and t0: t0 ∈ set queue
  and s ∈ I
  shows round-robin-invar σ' (dom (thr s) ∪ {t. ∃ x m. NewThread t x m ∈ set {ta}_t})
using t0 rrr invar
proof(induct queue rule: round-robin-reschedule-induct)
  case head thus ?case by(simp add: round-robin-reschedule-Cons)
next
  case (rotate queue t')
  show ?case
  proof(cases round-robin-step n0 (t' # queue, n0) s t')
    case None
    with ⟨round-robin-reschedule t0 (t' # queue) n0 s = [(t, [(ta, x', m'), σ']), ⟨t' ≠ t0⟩
    have round-robin-reschedule t0 (queue @ [t']) n0 s = [(t, [(ta, x', m'), σ'])]
      by(simp add: round-robin-reschedule-Cons)
    moreover from ⟨round-robin-invar (t' # queue, n0) (dom (thr s))⟩
    have round-robin-invar (queue @ [t'], n0) (dom (thr s)) by simp
    ultimately show ?thesis by(rule rotate.hyps)
  next
    case (Some a)
    with ⟨round-robin-reschedule t0 (t' # queue) n0 s = [(t, [(ta, x', m'), σ']), ⟨t' ≠ t0⟩
    have round-robin-step n0 (t' # queue, n0) s t' = [(t, [(ta, x', m'), σ'])]
      by(simp add: round-robin-reschedule-Cons)
    thus ?thesis using ⟨round-robin-invar (t' # queue, n0) (dom (thr s))⟩ ⟨s ∈ I⟩

```

by(rule round-robin-step-invar-Some[OF  $\langle$ deterministic  $I\rangle$ ])  
 qed  
 qed

lemma round-robin-NoneD:

assumes rr: round-robin n0  $\sigma$  s = None  
 and invar: round-robin-invar  $\sigma$  (dom (thr s))  
 shows active-threads s = {}

proof –

obtain queue n where  $\sigma$ :  $\sigma = (\text{queue}, n)$  by(cases  $\sigma$ )  
 show ?thesis  
 proof(cases queue)  
 case Nil  
 thus ?thesis using invar  $\sigma$  by(fastforce elim: active-threads.cases)  
 next  
 case (Cons t queue')  
 with rr  $\sigma$  have round-robin-step n0 (t # queue', n) s t = None  
 and round-robin-reschedule t (queue' @ [t]) n0 s = None by simp-all  
 from  $\langle$ round-robin-step n0 (t # queue', n) s t = None $\rangle$   
 have  $t \notin \text{active-threads } s$  by(rule step-thread-NoneD)  
 moreover from  $\langle$ round-robin-reschedule t (queue' @ [t]) n0 s = None $\rangle$   
 have set (takeWhile ( $\lambda x. x \neq t$ ) (queue' @ [t]))  $\cap$  active-threads s = {}  
 by(rule round-robin-reschedule-NoneD) simp  
 moreover from invar  $\sigma$  Cons  
 have takeWhile ( $\lambda x. x \neq t$ ) (queue' @ [t]) = queue'  
 by(subst takeWhile-append2) auto  
 moreover from invar have active-threads s  $\subseteq$  set queue  
 using  $\sigma$  by(auto elim: active-threads.cases)  
 ultimately show ?thesis using Cons by auto  
 qed  
 qed

lemma round-robin-Some-NoneD:

assumes rr: round-robin n0  $\sigma$  s =  $\lfloor (t, \text{None}, \sigma') \rfloor$   
 shows  $\exists x \ln n. \text{thr } s \ t = \lfloor (x, \ln) \rfloor \wedge \ln \ \$ \ n > 0 \wedge \neg \text{waiting } (wset \ s \ t) \wedge \text{may-acquire-all } (\text{locks } s)$   
 t ln

proof –

obtain queue n where  $\sigma$ :  $\sigma = (\text{queue}, n)$  by(cases  $\sigma$ )  
 with rr have queue  $\neq []$  by clarsimp  
 then obtain t' queue' where queue: queue = t' # queue'  
 by(auto simp add: neq-Nil-conv)  
 show ?thesis  
 proof(cases round-robin-step n0 (t' # queue', n) s t')  
 case (Some a)  
 with rr queue  $\sigma$  have round-robin-step n0 (t' # queue', n) s t' =  $\lfloor (t, \text{None}, \sigma') \rfloor$  by simp  
 thus ?thesis by(blast dest: step-thread-Some-NoneD)  
 next  
 case None  
 with rr queue  $\sigma$  have round-robin-reschedule t' (queue' @ [t']) n0 s =  $\lfloor (t, \text{None}, \sigma') \rfloor$  by simp  
 thus ?thesis by(rule round-robin-reschedule-Some-NoneD) simp  
 qed  
 qed

lemma round-robin-Some-SomeD:



**assumes** *deterministic I*  
**and** *rr: round-robin n0 σ s = [(t, [(ta, x', m')], σ')]*  
**and** *s ∈ I*  
**shows**  $\exists x. \text{thr } s \text{ } t = [(x, \text{no-wait-locks})] \wedge t \vdash \langle x, \text{shr } s \rangle \text{--}ta \rightarrow \langle x', m' \rangle \wedge \text{actions-ok } s \text{ } t \text{ } ta$   
**proof** –  
**obtain** *queue n where σ: σ = (queue, n) by (cases σ)*  
**with** *rr have queue ≠ [] by clarsimp*  
**then obtain** *t' queue' where queue: queue = t' # queue'*  
**by** (*auto simp add: neq-Nil-conv*)  
**show** ?thesis  
**proof** (*cases round-robin-step n0 (t' # queue', n) s t'*)  
**case** (*Some a*)  
**with** *rr queue σ have round-robin-step n0 (t' # queue', n) s t' = [(t, [(ta, x', m')], σ')]* **by** *simp*  
**thus** ?thesis **using**  $\langle s \in I \rangle$  **by** (*blast dest: step-thread-Some-SomeD[OF ⟨deterministic I⟩]*)  
**next**  
**case** *None*  
**with** *rr queue σ have round-robin-reschedule t' (queue' @ [t']) n0 s = [(t, [(ta, x', m')], σ')]* **by** *simp*  
**thus** ?thesis **by** (*rule round-robin-reschedule-Some-SomeD[OF ⟨deterministic I⟩]*) (*simp-all add: ⟨s ∈ I⟩*)  
**qed**  
**qed**

**lemma** *round-robin-invar-None:*

**assumes** *rr: round-robin n0 σ s = [(t, None, σ')]*  
**and** *invar: round-robin-invar σ (dom (thr s))*  
**shows** *round-robin-invar σ' (dom (thr s))*  
**proof** –  
**obtain** *queue n where σ: σ = (queue, n) by (cases σ)*  
**with** *rr have queue ≠ [] by clarsimp*  
**then obtain** *t' queue' where queue: queue = t' # queue'*  
**by** (*auto simp add: neq-Nil-conv*)  
**show** ?thesis  
**proof** (*cases round-robin-step n0 (t' # queue', n) s t'*)  
**case** (*Some a*)  
**with** *rr queue σ have round-robin-step n0 (t' # queue', n) s t' = [(t, None, σ')]* **by** *simp*  
**thus** ?thesis **using** *invar unfolding σ queue by (rule round-robin-step-invar-None)*  
**next**  
**case** *None*  
**with** *rr queue σ have round-robin-reschedule t' (queue' @ [t']) n0 s = [(t, None, σ')]* **by** *simp*  
**moreover from** *invar queue σ have round-robin-invar (queue' @ [t'], n0) (dom (thr s))* **by** *simp*  
**ultimately show** ?thesis **by** (*rule round-robin-reschedule-invar-None*) *simp*  
**qed**  
**qed**

**lemma** *round-robin-invar-Some:*

**assumes** *deterministic I*  
**and** *rr: round-robin n0 σ s = [(t, [(ta, x', m')], σ')]*  
**and** *invar: round-robin-invar σ (dom (thr s)) s ∈ I*  
**shows** *round-robin-invar σ' (dom (thr s) ∪ {t. ∃ x m. NewThread t x m ∈ set {ta}\_t})*  
**proof** –  
**obtain** *queue n where σ: σ = (queue, n) by (cases σ)*  
**with** *rr have queue ≠ [] by clarsimp*  
**then obtain** *t' queue' where queue: queue = t' # queue'*

```

    by(auto simp add: neq-Nil-conv)
  show ?thesis
proof(cases round-robin-step n0 (t' # queue', n) s t')
  case (Some a)
  with rr queue  $\sigma$  have round-robin-step n0 (t' # queue', n) s t' =  $\lfloor (t, \lfloor (ta, x', m') \rfloor, \sigma') \rfloor$  by simp
  thus ?thesis using invar unfolding  $\sigma$  queue by(rule round-robin-step-invar-Some[OF  $\langle$ deterministic  $I\rangle$ ])
next
  case None
  with rr queue  $\sigma$  have round-robin-reschedule t' (queue' @ [t']) n0 s =  $\lfloor (t, \lfloor (ta, x', m') \rfloor, \sigma') \rfloor$  by
simp
  moreover from invar queue  $\sigma$ 
  have round-robin-invar (queue' @ [t'], n0) (dom (thr s)) by simp
  ultimately show ?thesis by(rule round-robin-reschedule-invar-Some[OF  $\langle$ deterministic  $I\rangle$ ])(simp-all
add:  $\langle s \in I \rangle$ )
qed
qed
end

locale round-robin-base =
  scheduler-base-aux
  final r convert-RA
  thr- $\alpha$  thr-invar thr-lookup thr-update
  ws- $\alpha$  ws-invar ws-lookup
  is- $\alpha$  is-invar is-memb is-ins is-delete
  for final :: 'x  $\Rightarrow$  bool
  and r :: 't  $\Rightarrow$  ('x  $\times$  'm)  $\Rightarrow$  (('l, 't, 'x, 'm, 'w, 'o) thread-action  $\times$  'x  $\times$  'm) Predicate.pred
  and convert-RA :: 'l released-locks  $\Rightarrow$  'o list
  and output :: 'queue round-robin  $\Rightarrow$  't  $\Rightarrow$  ('l, 't, 'x, 'm, 'w, 'o) thread-action  $\Rightarrow$  'q option
  and thr- $\alpha$  :: 'm-t  $\Rightarrow$  ('l, 't, 'x) thread-info
  and thr-invar :: 'm-t  $\Rightarrow$  bool
  and thr-lookup :: 't  $\Rightarrow$  'm-t  $\rightarrow$  ('x  $\times$  'l released-locks)
  and thr-update :: 't  $\Rightarrow$  'x  $\times$  'l released-locks  $\Rightarrow$  'm-t  $\Rightarrow$  'm-t
  and ws- $\alpha$  :: 'm-w  $\Rightarrow$  ('w, 't) wait-sets
  and ws-invar :: 'm-w  $\Rightarrow$  bool
  and ws-lookup :: 't  $\Rightarrow$  'm-w  $\rightarrow$  'w wait-set-status
  and ws-update :: 't  $\Rightarrow$  'w wait-set-status  $\Rightarrow$  'm-w  $\Rightarrow$  'm-w
  and ws-delete :: 't  $\Rightarrow$  'm-w  $\Rightarrow$  'm-w
  and ws-iterate :: 'm-w  $\Rightarrow$  ('t  $\times$  'w wait-set-status, 'm-w) set-iterator
  and ws-sel :: 'm-w  $\Rightarrow$  ('t  $\times$  'w wait-set-status  $\Rightarrow$  bool)  $\rightarrow$  ('t  $\times$  'w wait-set-status)
  and is- $\alpha$  :: 's-i  $\Rightarrow$  't interrupts
  and is-invar :: 's-i  $\Rightarrow$  bool
  and is-memb :: 't  $\Rightarrow$  's-i  $\Rightarrow$  bool
  and is-ins :: 't  $\Rightarrow$  's-i  $\Rightarrow$  's-i
  and is-delete :: 't  $\Rightarrow$  's-i  $\Rightarrow$  's-i
  +
  fixes queue- $\alpha$  :: 'queue  $\Rightarrow$  't list
  and queue-invar :: 'queue  $\Rightarrow$  bool
  and queue-empty :: unit  $\Rightarrow$  'queue
  and queue-isEmpty :: 'queue  $\Rightarrow$  bool
  and queue-enqueue :: 't  $\Rightarrow$  'queue  $\Rightarrow$  'queue
  and queue-dequeue :: 'queue  $\Rightarrow$  't  $\times$  'queue
  and queue-push :: 't  $\Rightarrow$  'queue  $\Rightarrow$  'queue

```

**begin**

**definition** *queue-rotate1* :: 'queue  $\Rightarrow$  'queue  
**where** *queue-rotate1* = case-prod *queue-enqueue*  $\circ$  *queue-dequeue*

**primrec** *enqueue-new-thread* :: 'queue  $\Rightarrow$  ('t,'x,'m) new-thread-action  $\Rightarrow$  'queue  
**where**  
*enqueue-new-thread* *ts* (NewThread *t x m*) = *queue-enqueue t ts*  
| *enqueue-new-thread* *ts* (ThreadExists *t b*) = *ts*

**definition** *enqueue-new-threads* :: 'queue  $\Rightarrow$  ('t,'x,'m) new-thread-action list  $\Rightarrow$  'queue  
**where**  
*enqueue-new-threads* = foldl *enqueue-new-thread*

**primrec** *round-robin-update-state* :: nat  $\Rightarrow$  'queue round-robin  $\Rightarrow$  't  $\Rightarrow$  ('l,'t,'x,'m,'w,'o) thread-action  
 $\Rightarrow$  'queue round-robin

**where**  
*round-robin-update-state* *n0* (*queue*, *n*) *t ta* =  
(let *queue'* = *enqueue-new-threads queue*  $\llbracket ta \rrbracket_t$   
in if *n* = 0  $\vee$  Yield  $\in$  set  $\llbracket ta \rrbracket_c$  then (*queue-rotate1 queue'*, *n0*) else (*queue'*, *n* - 1))

**abbreviation** *round-robin-step* ::  
nat  $\Rightarrow$  'queue round-robin  $\Rightarrow$  ('l,'t,'m,'m-t,'m-w,'s-i) state-refine  $\Rightarrow$  't  
 $\Rightarrow$  ('t  $\times$  (('l,'t,'x,'m,'w,'o) thread-action  $\times$  'x  $\times$  'm) option  $\times$  'queue round-robin) option  
**where**  
*round-robin-step* *n0*  $\sigma$  *s t*  $\equiv$  step-thread (*round-robin-update-state* *n0*  $\sigma$  *t*) *s t*

**partial-function** (*option*) *round-robin-reschedule* ::  
't  $\Rightarrow$  'queue  $\Rightarrow$  nat  $\Rightarrow$  ('l,'t,'m,'m-t,'m-w,'s-i) state-refine  
 $\Rightarrow$  ('t  $\times$  (('l,'t,'x,'m,'w,'o) thread-action  $\times$  'x  $\times$  'm) option  $\times$  'queue round-robin) option

**where**  
*round-robin-reschedule* *t0 queue n0 s* =  
(let  
(*t*, *queue'*) = *queue-dequeue queue*  
in  
if *t* = *t0* then  
None  
else  
case *round-robin-step* *n0* (*queue-push t queue'*, *n0*) *s t* of  
None  $\Rightarrow$  *round-robin-reschedule* *t0* (*queue-enqueue t queue'*) *n0 s*  
|  $\llbracket ttaxm\sigma \rrbracket \Rightarrow \llbracket ttaxm\sigma \rrbracket$ )

**primrec** *round-robin* :: nat  $\Rightarrow$  ('l,'t,'x,'m,'w,'o,'m-t,'m-w,'s-i,'queue round-robin) scheduler  
**where**

*round-robin* *n0* (*queue*, *n*) *s* =  
(if *queue-isEmpty queue* then None  
else  
let  
(*t*, *queue'*) = *queue-dequeue queue*  
in  
(case *round-robin-step* *n0* (*queue-push t queue'*, *n*) *s t* of  
 $\llbracket ttaxm\sigma \rrbracket \Rightarrow \llbracket ttaxm\sigma \rrbracket$   
| None  $\Rightarrow$  *round-robin-reschedule* *t* (*queue-enqueue t queue'*) *n0 s*))

**primrec** *round-robin-invar* :: 'queue round-robin  $\Rightarrow$  't set  $\Rightarrow$  bool  
**where** *round-robin-invar* (queue, n) T  $\longleftrightarrow$  queue-invar queue  $\wedge$  Round-Robin.round-robin-invar (queue- $\alpha$  queue, n) T

**definition** *round-robin- $\alpha$*  :: 'queue round-robin  $\Rightarrow$  't list round-robin  
**where** *round-robin- $\alpha$*  = apfst queue- $\alpha$

**definition** *round-robin-start* :: nat  $\Rightarrow$  't  $\Rightarrow$  'queue round-robin  
**where** *round-robin-start* n0 t = (queue-enqueue t (queue-empty ()), n0)

**lemma** *round-robin-invar-correct*:  
*round-robin-invar*  $\sigma$  T  $\Longrightarrow$  Round-Robin.round-robin-invar (round-robin- $\alpha$   $\sigma$ ) T  
**by**(cases  $\sigma$ )(simp add: round-robin- $\alpha$ -def)

**end**

**locale** *round-robin* =  
*round-robin-base*  
*final r convert-RA output*  
*thr- $\alpha$  thr-invar thr-lookup thr-update*  
*ws- $\alpha$  ws-invar ws-lookup ws-update ws-delete ws-iterate ws-sel*  
*is- $\alpha$  is-invar is-memb is-ins is-delete*  
*queue- $\alpha$  queue-invar queue-empty queue-isEmpty queue-enqueue queue-dequeue queue-push*  
+  
*scheduler-aux*  
*final r convert-RA*  
*thr- $\alpha$  thr-invar thr-lookup thr-update*  
*ws- $\alpha$  ws-invar ws-lookup*  
*is- $\alpha$  is-invar is-memb is-ins is-delete*  
+  
*ws: map-update ws- $\alpha$  ws-invar ws-update +*  
*ws: map-delete ws- $\alpha$  ws-invar ws-delete +*  
*ws: map-iteratei ws- $\alpha$  ws-invar ws-iterate +*  
*ws: map-sel' ws- $\alpha$  ws-invar ws-sel +*  
*queue: list queue- $\alpha$  queue-invar +*  
*queue: list-empty queue- $\alpha$  queue-invar queue-empty +*  
*queue: list-isEmpty queue- $\alpha$  queue-invar queue-isEmpty +*  
*queue: list-enqueue queue- $\alpha$  queue-invar queue-enqueue +*  
*queue: list-dequeue queue- $\alpha$  queue-invar queue-dequeue +*  
*queue: list-push queue- $\alpha$  queue-invar queue-push*  
**for** *final* :: 'x  $\Rightarrow$  bool  
**and** *r* :: 't  $\Rightarrow$  ('x  $\times$  'm)  $\Rightarrow$  (('l, 't, 'x, 'm, 'w, 'o) thread-action  $\times$  'x  $\times$  'm) Predicate.pred  
**and** *convert-RA* :: 'l released-locks  $\Rightarrow$  'o list  
**and** *output* :: 'queue round-robin  $\Rightarrow$  't  $\Rightarrow$  ('l, 't, 'x, 'm, 'w, 'o) thread-action  $\Rightarrow$  'q option  
**and** *thr- $\alpha$*  :: 'm-t  $\Rightarrow$  ('l, 't, 'x) thread-info  
**and** *thr-invar* :: 'm-t  $\Rightarrow$  bool  
**and** *thr-lookup* :: 't  $\Rightarrow$  'm-t  $\rightarrow$  ('x  $\times$  'l released-locks)  
**and** *thr-update* :: 't  $\Rightarrow$  'x  $\times$  'l released-locks  $\Rightarrow$  'm-t  $\Rightarrow$  'm-t  
**and** *ws- $\alpha$*  :: 'm-w  $\Rightarrow$  ('w, 't) wait-sets  
**and** *ws-invar* :: 'm-w  $\Rightarrow$  bool  
**and** *ws-lookup* :: 't  $\Rightarrow$  'm-w  $\rightarrow$  'w wait-set-status  
**and** *ws-update* :: 't  $\Rightarrow$  'w wait-set-status  $\Rightarrow$  'm-w  $\Rightarrow$  'm-w  
**and** *ws-delete* :: 't  $\Rightarrow$  'm-w  $\Rightarrow$  'm-w  
**and** *ws-iterate* :: 'm-w  $\Rightarrow$  ('t  $\times$  'w wait-set-status, 'm-w) set-iterator

```

and ws-sel :: 'm-w  $\Rightarrow$  ('t  $\times$  'w wait-set-status  $\Rightarrow$  bool)  $\rightarrow$  ('t  $\times$  'w wait-set-status)
and is- $\alpha$  :: 's-i  $\Rightarrow$  't interrupts
and is-invar :: 's-i  $\Rightarrow$  bool
and is-memb :: 't  $\Rightarrow$  's-i  $\Rightarrow$  bool
and is-ins :: 't  $\Rightarrow$  's-i  $\Rightarrow$  's-i
and is-delete :: 't  $\Rightarrow$  's-i  $\Rightarrow$  's-i
and queue- $\alpha$  :: 'queue  $\Rightarrow$  't list
and queue-invar :: 'queue  $\Rightarrow$  bool
and queue-empty :: unit  $\Rightarrow$  'queue
and queue-isEmpty :: 'queue  $\Rightarrow$  bool
and queue-enqueue :: 't  $\Rightarrow$  'queue  $\Rightarrow$  'queue
and queue-dequeue :: 'queue  $\Rightarrow$  't  $\times$  'queue
and queue-push :: 't  $\Rightarrow$  'queue  $\Rightarrow$  'queue
begin

```

**lemma** *queue-rotate1-correct*:

```

  assumes queue-invar queue queue- $\alpha$  queue  $\neq$  []
  shows queue- $\alpha$  (queue-rotate1 queue) = rotate1 (queue- $\alpha$  queue)
  and queue-invar (queue-rotate1 queue)
using assms
apply(auto simp add: queue-rotate1-def split-beta queue.dequeue-correct queue.enqueue-correct)
by(cases queue- $\alpha$  queue) simp-all

```

**lemma** *enqueue-thread-correct*:

```

  assumes queue-invar queue
  shows queue- $\alpha$  (enqueue-new-thread queue nta) = Round-Robin.enqueue-new-thread (queue- $\alpha$  queue)
  nta
  and queue-invar (enqueue-new-thread queue nta)
using assms
by(case-tac [!]) nta)(simp-all add: queue.enqueue-correct)

```

**lemma** *enqueue-threads-correct*:

```

  assumes queue-invar queue
  shows queue- $\alpha$  (enqueue-new-threads queue ntas) = Round-Robin.enqueue-new-threads (queue- $\alpha$ 
queue) ntas
  and queue-invar (enqueue-new-threads queue ntas)
using assms
apply(induct ntas arbitrary: queue)
apply(simp-all add: enqueue-new-threads-def Round-Robin.enqueue-new-threads-def enqueue-thread-correct)
done

```

**lemma** *round-robin-update-thread-correct*:

```

  assumes round-robin-invar  $\sigma$  T t'  $\in$  T
  shows round-robin- $\alpha$  (round-robin-update-state n0  $\sigma$  t ta) = Round-Robin.round-robin-update-state
n0 (round-robin- $\alpha$   $\sigma$ ) t ta
using assms
apply(cases  $\sigma$ )
apply(auto simp add: round-robin- $\alpha$ -def queue-rotate1-correct enqueue-threads-correct del: conjI)
apply(subst (1 2) queue-rotate1-correct)
apply(auto simp add: enqueue-threads-correct)
done

```

**lemma** *round-robin-step-correct*:

```

  assumes det:  $\alpha$ .deterministic I

```

**and** *invar*: *round-robin-invar*  $\sigma$  (*dom* (*thr- $\alpha$*  (*thr s*))) *state-invar s state- $\alpha$  s*  $\in I$   
**shows**  
*map-option* (*apsnd* (*apsnd round-robin- $\alpha$* )) (*round-robin-step* *n0*  $\sigma$  *s t*) =  
 $\alpha$ .*round-robin-step* *n0* (*round-robin- $\alpha$*   $\sigma$ ) (*state- $\alpha$  s*) *t* (**is** *?thesis1*)  
**and** *case-option* *True* ( $\lambda(t, \text{taxm}, \sigma).$  *round-robin-invar*  $\sigma$  (*case taxm of* *None*  $\Rightarrow$  *dom* (*thr- $\alpha$*  (*thr s*)) |  
*Some* (*ta, x', m'*)  $\Rightarrow$  *dom* (*thr- $\alpha$*  (*thr s*))  $\cup \{t. \exists x m. \text{NewThread } t x m \in \text{set } \{ta\}_t\}$ )) (*round-robin-step*  
*n0*  $\sigma$  *s t*)  
*(is ?thesis2)*  
**proof** –  
**have** *?thesis1*  $\wedge$  *?thesis2*  
**proof**(*cases* *dom* (*thr- $\alpha$*  (*thr s*)) =  $\{\}$ )  
**case** *True*  
**thus** *?thesis* **using** *invar*  
**apply**(*cases*  $\sigma$ )  
**apply**(*auto* *dest*: *step-thread-Some-NoneD*[*OF det*] *step-thread-Some-SomeD*[*OF det*])  
**apply**(*fastforce simp add*:  $\alpha$ .*step-thread-eq-None-conv elim*:  $\alpha$ .*active-threads.cases intro*: *sym*)  
**done**  
**next**  
**case** *False*  
**then obtain** *t'* **where** *t'*: *t'  $\in$  dom* (*thr- $\alpha$*  (*thr s*)) **by** *blast*  
**hence** *?thesis1*  
**using** *step-thread-correct*(1)[*of I round-robin-invar*  $\sigma$  *s round-robin- $\alpha$  round-robin-update-state*  
*n0*  $\sigma$  *t t, OF det invar*]  
**unfolding** *o-def* **using** *invar*  
**by**(*subst* (*asm*) *round-robin-update-thread-correct*) *auto*  
**moreover**  
**{** **fix** *ta* :: (*'l, 't, 'x, 'm, 'w, 'o*) *thread-action*  
**assume** *FWThread.thread-oks* (*thr- $\alpha$*  (*thr s*))  $\{ta\}_t$   
**moreover from** *t' invar* **have** *queue- $\alpha$*  (*fst*  $\sigma$ )  $\neq []$  **by**(*cases*  $\sigma$ ) *auto*  
**ultimately have** *round-robin-invar* (*round-robin-update-state* *n0*  $\sigma$  *t ta*) (*dom* (*thr- $\alpha$*  (*thr s*))  $\cup$   
 $\{t. \exists x m. \text{NewThread } t x m \in \text{set } \{ta\}_t\}$ )  
**using** *invar t'* **by**(*cases*  $\sigma$ )(*auto simp add*: *queue-rotate1-correct enqueue-threads-correct*  
*set-enqueue-new-threads iff del*: *domIff intro*: *distinct-enqueue-new-threads*) **}**  
**from** *step-thread-correct*(2)[*OF det, of round-robin-invar*  $\sigma$  *s round-robin-update-state* *n0*  $\sigma$  *t t,*  
*OF invar this*]  
**have** *?thesis2* **using** *t' invar* **by** *simp*  
**ultimately show** *?thesis* **by** *blast*  
**qed**  
**thus** *?thesis1 ?thesis2* **by** *blast+*  
**qed**

**lemma** *round-robin-reschedule-correct*:

**assumes** *det*:  $\alpha$ .*deterministic I*  
**and** *invar*: *round-robin-invar* (*queue, n*) (*dom* (*thr- $\alpha$*  (*thr s*))) *state-invar s state- $\alpha$  s*  $\in I$   
**and** *t0*: *t0*  $\in \text{set}$  (*queue- $\alpha$  queue*)  
**shows** *map-option* (*apsnd* (*apsnd round-robin- $\alpha$* )) (*round-robin-reschedule* *t0 queue n0 s*) =  
 $\alpha$ .*round-robin-reschedule* *t0* (*queue- $\alpha$  queue*) *n0* (*state- $\alpha$  s*)  
**and** *case-option* *True* ( $\lambda(t, \text{taxm}, \sigma).$  *round-robin-invar*  $\sigma$  (*case taxm of* *None*  $\Rightarrow$  *dom* (*thr- $\alpha$*   
(*thr s*)) | *Some* (*ta, x', m'*)  $\Rightarrow$  *dom* (*thr- $\alpha$*  (*thr s*))  $\cup \{t. \exists x m. \text{NewThread } t x m \in \text{set } \{ta\}_t\}$ ))  
(*round-robin-reschedule* *t0 queue n0 s*)  
**using** *t0 invar*  
**proof**(*induct* *queue- $\alpha$  queue arbitrary*: *queue n rule*: *round-robin-reschedule-induct*)  
**case** *head*  
**{** **case** 1 **thus** *?case* **using** *head[symmetric]*

```

    by(subst round-robin-reschedule.simps)(subst  $\alpha$ .round-robin-reschedule.simps, clarsimp simp add:
split-beta queue.dequeue-correct)
  next
    case 2 thus ?case using head[symmetric]
    by(subst round-robin-reschedule.simps)(clarsimp simp add: split-beta queue.dequeue-correct) }
next
case (rotate  $\alpha$ queue' t)
obtain t' queue' where queue': queue-dequeue queue = (t', queue') by(cases queue-dequeue queue)
note [simp] =  $\langle t \# \alpha$ queue' = queue- $\alpha$  queue  $\rangle$ [symmetric]
{ case 1
  with queue' have [simp]: t' = t  $\alpha$ queue' = queue- $\alpha$  queue' queue-invar queue' by(auto elim:
queue.removeE)
  from 1 queue' have invar': round-robin-invar (queue-push t queue', n0) (dom (thr- $\alpha$  (thr s)))
  by(auto simp add: queue.push-correct)
  show ?case
  proof(cases round-robin-step n0 (queue-push t queue', n0) s t)
    case Some thus ?thesis
    using queue'  $\langle t \neq t0 \rangle$  round-robin-step-correct[OF det invar'  $\langle$ state-invar s $\rangle$ , of n0 t] invar'
 $\langle$ state- $\alpha$  s  $\in$  I $\rangle$ 
    by(subst round-robin-reschedule.simps)(subst  $\alpha$ .round-robin-reschedule.simps, auto simp add:
round-robin- $\alpha$ -def queue.push-correct)
  next
  case None
  hence  $\alpha$ None:  $\alpha$ .round-robin-step n0 (queue- $\alpha$  (queue-push t queue'), n0) (state- $\alpha$  s) t = None
  using round-robin-step-correct[OF det invar'  $\langle$ state-invar s $\rangle$ , of n0 t] invar'  $\langle$ state- $\alpha$  s  $\in$  I $\rangle$ 
  by(auto simp add: queue.push-correct round-robin- $\alpha$ -def)
  have  $\alpha$ queue' @ [t] = queue- $\alpha$  (queue-enqueue t queue') by(simp add: queue.enqueue-correct)
  moreover from invar'
  have round-robin-invar (queue-enqueue t queue', n0) (dom (thr- $\alpha$  (thr s)))
  by(auto simp add: queue.enqueue-correct queue.push-correct)
  ultimately
  have map-option (apsnd (apsnd round-robin- $\alpha$ )) (round-robin-reschedule t0 (queue-enqueue t
queue') n0 s) =
     $\alpha$ .round-robin-reschedule t0 (queue- $\alpha$  (queue-enqueue t queue')) n0 (state- $\alpha$  s)
  using  $\langle$ state-invar s $\rangle$   $\langle$ state- $\alpha$  s  $\in$  I $\rangle$  by(rule rotate.hyps)
  thus ?thesis using None  $\alpha$ None  $\langle t \neq t0 \rangle$  invar' queue'
  by(subst round-robin-reschedule.simps)(subst  $\alpha$ .round-robin-reschedule.simps, auto simp add:
queue.enqueue-correct queue.push-correct)
qed
next
case 2
  with queue' have [simp]: t' = t  $\alpha$ queue' = queue- $\alpha$  queue' queue-invar queue' by(auto elim:
queue.removeE)
  from 2 queue' have invar': round-robin-invar (queue-push t queue', n0) (dom (thr- $\alpha$  (thr s)))
  by(auto simp add: queue.push-correct)
  show ?case
  proof(cases round-robin-step n0 (queue-push t queue', n0) s t)
    case Some thus ?thesis
    using queue'  $\langle t \neq t0 \rangle$  round-robin-step-correct[OF det invar'  $\langle$ state-invar s $\rangle$ , of n0 t] invar'
 $\langle$ state- $\alpha$  s  $\in$  I $\rangle$ 
    by(subst round-robin-reschedule.simps)(auto simp add: round-robin- $\alpha$ -def queue.push-correct)
  next
  case None
  have  $\alpha$ queue' @ [t] = queue- $\alpha$  (queue-enqueue t queue') by(simp add: queue.enqueue-correct)

```

```

moreover from invar'
have round-robin-invar (queue-enqueue t queue', n0) (dom (thr-α (thr s)))
  by(auto simp add: queue.enqueue-correct queue.push-correct)
ultimately
have case-option True ( $\lambda(t, \text{taxm}, \sigma). \text{round-robin-invar } \sigma \text{ (case-option (dom (thr-}\alpha \text{ (thr s))) } (\lambda(ta, x', m'). \text{dom (thr-}\alpha \text{ (thr s))} \cup \{t. \exists x m. \text{NewThread } t x m \in \text{set } \{ta\}_t\}) \text{taxm})) (round-robin-reschedule } t0 \text{ (queue-enqueue } t \text{ queue') } n0 \text{ s)}$ )
  using  $\langle \text{state-invar } s \rangle \langle \text{state-}\alpha \text{ } s \in I \rangle$  by(rule rotate.hyps)
  thus ?thesis using None  $\langle t \neq t0 \rangle$  invar' queue'
  by(subst round-robin-reschedule.simps)(auto simp add: queue.enqueue-correct queue.push-correct)
qed
}
qed

```

**lemma** *round-robin-correct*:

```

assumes det:  $\alpha.\text{deterministic } I$ 
and invar: round-robin-invar  $\sigma \text{ (dom (thr-}\alpha \text{ (thr s))) state-invar } s \text{ state-}\alpha \text{ } s \in I$ 
shows map-option (apsnd (apsnd round-robin-α)) (round-robin n0  $\sigma$  s) =
   $\alpha.\text{round-robin } n0 \text{ (round-robin-}\alpha \text{ } \sigma) \text{ (state-}\alpha \text{ } s)$ 
  (is ?thesis1)
and case-option True ( $\lambda(t, \text{taxm}, \sigma). \text{round-robin-invar } \sigma \text{ (case taxm of None } \Rightarrow \text{dom (thr-}\alpha \text{ (thr s))} \mid \text{Some (ta, x', m') } \Rightarrow \text{dom (thr-}\alpha \text{ (thr s))} \cup \{t. \exists x m. \text{NewThread } t x m \in \text{set } \{ta\}_t\}) \text{ (round-robin } n0 \text{ } \sigma \text{ } s)$ )
  (is ?thesis2)
proof –
obtain queue n where  $\sigma: \sigma = (\text{queue}, n)$  by(cases  $\sigma$ )
have ?thesis1  $\wedge$  ?thesis2
proof(cases queue-α queue)
  case Nil thus ?thesis using invar  $\sigma$ 
    by(auto simp add: split-beta queue.isEmpty-correct round-robin-α-def)
next
  case (Cons t αqueue')
  with invar  $\sigma$  obtain queue'
    where [simp]: queue-dequeue queue = (t, queue')  $\alpha\text{queue}' = \text{queue-}\alpha \text{ queue' queue-invar queue'}$ 
    by(auto elim: queue.removeE)
  from invar  $\sigma$  Cons have invar': round-robin-invar (queue-push t queue', n) (dom (thr-α (thr s)))
    by(auto simp add: queue.push-correct)
  from invar  $\sigma$  Cons have invar'': round-robin-invar (queue-enqueue t queue', n0) (dom (thr-α (thr s)))
    (is ?thesis1)
    by(auto simp add: queue.enqueue-correct)
  show ?thesis
proof(cases round-robin-step n0 (queue-push t queue', n) s t)
  case Some
  with  $\sigma$  Cons invar show ?thesis
    using round-robin-step-correct[OF det invar'  $\langle \text{state-invar } s \rangle$ , of n0 t]
    by(auto simp add: queue.isEmpty-correct queue.push-correct round-robin-α-def)
  next
  case None
  from invar  $\sigma$  Cons have  $t \in \text{set (queue-}\alpha \text{ (queue-enqueue } t \text{ queue'))}$ 
    by(auto simp add: queue.enqueue-correct)
  from round-robin-reschedule-correct[OF det invar''  $\langle \text{state-invar } s \rangle$ , OF  $\langle \text{state-}\alpha \text{ } s \in I \rangle$  this, of n0]
  None  $\sigma$  Cons invar
    round-robin-step-correct[OF det invar'  $\langle \text{state-invar } s \rangle$ , of n0 t]
    show ?thesis by(auto simp add: queue.isEmpty-correct queue.push-correct round-robin-α-def)

```



queue.enqueue-correct)

qed

qed

thus ?thesis1 ?thesis2 by simp-all

qed

lemma round-robin-scheduler-spec:

assumes det:  $\alpha$ .deterministic  $I$

shows scheduler-spec final  $r$  (round-robin  $n0$ ) round-robin-invar  $thr\text{-}\alpha$  thr-invar  $ws\text{-}\alpha$   $ws\text{-invar}$  is- $\alpha$   $is\text{-invar}$   $I$

proof

fix  $\sigma$   $s$

assume rr: round-robin  $n0$   $\sigma$   $s = \text{None}$

and invar: round-robin-invar  $\sigma$  (dom ( $thr\text{-}\alpha$  ( $thr$   $s$ ))) state-invar  $s$  state- $\alpha$   $s \in I$

from round-robin-correct[OF det, OF invar, of  $n0$ ] rr

have  $\alpha$ .round-robin  $n0$  (round-robin- $\alpha$   $\sigma$ ) (state- $\alpha$   $s$ ) = None by simp

moreover from invar have Round-Robin.round-robin-invar (round-robin- $\alpha$   $\sigma$ ) (dom ( $thr$  (state- $\alpha$   $s$ )))

by(simp add: round-robin-invar-correct)

ultimately show  $\alpha$ .active-threads (state- $\alpha$   $s$ ) = {} by(rule  $\alpha$ .round-robin-NoneD)

next

fix  $\sigma$   $s$   $t$   $\sigma'$

assume rr: round-robin  $n0$   $\sigma$   $s = \lfloor(t, \text{None}, \sigma')\rfloor$

and invar: round-robin-invar  $\sigma$  (dom ( $thr\text{-}\alpha$  ( $thr$   $s$ ))) state-invar  $s$  state- $\alpha$   $s \in I$

from round-robin-correct[OF det, OF invar, of  $n0$ ] rr

have rr':  $\alpha$ .round-robin  $n0$  (round-robin- $\alpha$   $\sigma$ ) (state- $\alpha$   $s$ ) =  $\lfloor(t, \text{None}, \text{round-robin-}\alpha \sigma')\rfloor$  by simp

then show  $\exists x$  ln  $n$ .  $thr\text{-}\alpha$  ( $thr$   $s$ )  $t = \lfloor(x, \text{ln})\rfloor \wedge 0 < \text{ln} \ \$ \ n \wedge \neg \text{waiting} (ws\text{-}\alpha (wset \ s) \ t) \wedge \text{may-acquire-all} (\text{locks} \ s) \ t \ \text{ln}$

by(rule  $\alpha$ .round-robin-Some-NoneD[where  $s = \text{state-}\alpha \ s$ , unfolded state- $\alpha$ -conv])

next

fix  $\sigma$   $s$   $t$   $ta$   $x'$   $m'$   $\sigma'$

assume rr: round-robin  $n0$   $\sigma$   $s = \lfloor(t, \lfloor(ta, x', m')\rfloor, \sigma')\rfloor$

and invar: round-robin-invar  $\sigma$  (dom ( $thr\text{-}\alpha$  ( $thr$   $s$ ))) state-invar  $s$  state- $\alpha$   $s \in I$

from round-robin-correct[OF det, OF invar, of  $n0$ ] rr

have rr':  $\alpha$ .round-robin  $n0$  (round-robin- $\alpha$   $\sigma$ ) (state- $\alpha$   $s$ ) =  $\lfloor(t, \lfloor(ta, x', m')\rfloor, \text{round-robin-}\alpha \sigma')\rfloor$

by simp

thus  $\exists x$ .  $thr\text{-}\alpha$  ( $thr$   $s$ )  $t = \lfloor(x, \text{no-wait-locks})\rfloor \wedge \text{Predicate.eval} (r \ t \ (x, \text{shr} \ s)) \ (ta, x', m') \wedge \alpha$ .actions-ok (state- $\alpha$   $s$ )  $t \ ta$

using  $\langle \text{state-}\alpha \ s \in I \rangle$  by(rule  $\alpha$ .round-robin-Some-SomeD[OF det, where  $s = \text{state-}\alpha \ s$ , unfolded state- $\alpha$ -conv])

next

fix  $\sigma$   $s$   $t$   $\sigma'$

assume rr: round-robin  $n0$   $\sigma$   $s = \lfloor(t, \text{None}, \sigma')\rfloor$

and invar: round-robin-invar  $\sigma$  (dom ( $thr\text{-}\alpha$  ( $thr$   $s$ ))) state-invar  $s$  state- $\alpha$   $s \in I$

from round-robin-correct[OF det, OF invar, of  $n0$ ] rr

show round-robin-invar  $\sigma'$  (dom ( $thr\text{-}\alpha$  ( $thr$   $s$ ))) by simp

next

fix  $\sigma$   $s$   $t$   $ta$   $x'$   $m'$   $\sigma'$

assume rr: round-robin  $n0$   $\sigma$   $s = \lfloor(t, \lfloor(ta, x', m')\rfloor, \sigma')\rfloor$

and invar: round-robin-invar  $\sigma$  (dom ( $thr\text{-}\alpha$  ( $thr$   $s$ ))) state-invar  $s$  state- $\alpha$   $s \in I$

from round-robin-correct[OF det, OF invar, of  $n0$ ] rr

show round-robin-invar  $\sigma'$  (dom ( $thr\text{-}\alpha$  ( $thr$   $s$ ))  $\cup \{t. \exists x \ m. \text{NewThread} \ t \ x \ m \in \text{set} \ \{ta\}_t\}$ ) by

simp

qed

**lemma** *round-robin-start-invar*:

*round-robin-invar (round-robin-start n0 t0) {t0}*

**by**(*simp add: round-robin-start-def queue.empty-correct queue.enqueue-correct*)

**end**

**sublocale** *round-robin-base* <

*scheduler-base*

*final r convert-RA*

*round-robin n0 output pick-wakeup-via-sel (λs P. ws-sel s (λ(k,v). P k v)) round-robin-invar*

*thr-α thr-invar thr-lookup thr-update*

*ws-α ws-invar ws-lookup ws-update ws-delete ws-iterate*

*is-α is-invar is-memb is-ins is-delete*

**for** *n0* .

**sublocale** *round-robin* <

*pick-wakeup-spec*

*final r convert-RA*

*pick-wakeup-via-sel (λs P. ws-sel s (λ(k,v). P k v)) round-robin-invar*

*thr-α thr-invar*

*ws-α ws-invar*

*is-α is-invar*

**by**(*rule pick-wakeup-spec-via-sel*)(*unfold-locales*)

**context** *round-robin* **begin**

**lemma** *round-robin-scheduler*:

**assumes** *det: α.deterministic I*

**shows**

*scheduler*

*final r convert-RA*

*(round-robin n0) (pick-wakeup-via-sel (λs P. ws-sel s (λ(k,v). P k v))) round-robin-invar*

*thr-α thr-invar thr-lookup thr-update*

*ws-α ws-invar ws-lookup ws-update ws-delete ws-iterate*

*is-α is-invar is-memb is-ins is-delete*

*I*

**proof** –

**interpret** *scheduler-spec*

*final r convert-RA*

*round-robin n0 round-robin-invar*

*thr-α thr-invar*

*ws-α ws-invar*

*is-α is-invar*

*I*

**using** *det* **by**(*rule round-robin-scheduler-spec*)

**show** *?thesis* **by**(*unfold-locales*)(*rule α.deterministic-invariant3p[OF det]*)

**qed**

**end**

**lemmas** [*code*] =

*round-robin-base.queue-rotate1-def*

```

round-robin-base.enqueue-new-thread.simps
round-robin-base.enqueue-new-threads-def
round-robin-base.round-robin-update-state.simps
round-robin-base.round-robin-reschedule.simps
round-robin-base.round-robin.simps
round-robin-base.round-robin-start-def

```

**end**

**theory** *SC-Schedulers*

**imports**

```

Random-Scheduler
Round-Robin
../MM/SC-Collections

../Basic/JT-ICF

```

**begin**

**abbreviation** *sc-start-state-refine* ::

```

'm-t ⇒ (thread-id ⇒ ('x × addr released-locks) ⇒ 'm-t ⇒ 'm-t) ⇒ 'm-w ⇒ 's-i
⇒ (cname ⇒ mname ⇒ ty list ⇒ ty ⇒ 'md ⇒ addr val list ⇒ 'x) ⇒ 'md prog ⇒ cname ⇒ mname
⇒ addr val list
⇒ (addr, thread-id, heap, 'm-t, 'm-w, 's-i) state-refine

```

**where**

```

∧ is-empty.
sc-start-state-refine thr-empty thr-update ws-empty is-empty f P ≡
heap-base.start-state-refine addr2thread-id sc-empty (sc-allocate P) thr-empty thr-update ws-empty
is-empty f P

```

**abbreviation** *sc-state-α* ::

```

('l, 't :: linorder, 'm, ('t, 'x × 'l ⇒ f nat) rm, ('t, 'w wait-set-status) rm, 't rs) state-refine
⇒ ('l, 't, 'x, 'm, 'w) state

```

**where** *sc-state-α* ≡ *state-refine-base.state-α* *rm-α* *rm-α* *rs-α*

**lemma** *sc-state-α-sc-start-state-refine* [simp]:

```

sc-state-α (sc-start-state-refine (rm-empty ()) rm-update (rm-empty ()) (rs-empty ()) f P C M vs) =
sc-start-state f P C M vs

```

**by** (simp add: heap-base.start-state-refine-def state-refine-base.state-α.simps split-beta sc.start-state-def rm-correct rs-correct)

**locale** *sc-scheduler* =

```

scheduler
final r convert-RA
schedule output pick-wakeup σ-invar
rm-α rm-invar rm-lookup rm-update
rm-α rm-invar rm-lookup rm-update rm-delete rm-iteratei
rs-α rs-invar rs-memb rs-ins rs-delete
invariant
for final :: 'x ⇒ bool
and r :: 't ⇒ ('x × 'm) ⇒ (('l, 't :: linorder, 'x, 'm, 'w, 'o) thread-action × 'x × 'm) Predicate.pred
and convert-RA :: 'l released-locks ⇒ 'o list
and schedule :: ('l, 't, 'x, 'm, 'w, 'o, ('t, 'x × 'l ⇒ f nat) rm, ('t, 'w wait-set-status) rm, 't rs, 's) scheduler
and output :: 's ⇒ 't ⇒ ('l, 't, 'x, 'm, 'w, 'o) thread-action ⇒ 'q option
and pick-wakeup :: 's ⇒ 't ⇒ 'w ⇒ ('t, 'w wait-set-status) RBT.rbt ⇒ 't option

```

**and**  $\sigma\text{-invar} :: 's \Rightarrow 't \text{ set} \Rightarrow \text{bool}$   
**and**  $\text{invariant} :: ('l, 't, 'x, 'm, 'w) \text{ state set}$

**locale** *sc-round-robin-base* =

*round-robin-base*

*final r convert-RA output*

*rm- $\alpha$  rm-invar rm-lookup rm-update*

*rm- $\alpha$  rm-invar rm-lookup rm-update rm-delete rm-iteratei rm-sel*

*rs- $\alpha$  rs-invar rs-memb rs-ins rs-delete*

*fifo- $\alpha$  fifo-invar fifo-empty fifo-isEmpty fifo-enqueue fifo-dequeue fifo-push*

**for** *final* ::  $'x \Rightarrow \text{bool}$

**and**  $r :: 't \Rightarrow ('x \times 'm) \Rightarrow (('l, 't :: \text{linorder}, 'x, 'm, 'w, 'o) \text{ thread-action} \times 'x \times 'm) \text{ Predicate.pred}$

**and** *convert-RA* ::  $'l \text{ released-locks} \Rightarrow 'o \text{ list}$

**and** *output* ::  $'t \text{ fifo round-robin} \Rightarrow 't \Rightarrow ('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \Rightarrow 'q \text{ option}$

**locale** *sc-round-robin* =

*round-robin*

*final r convert-RA output*

*rm- $\alpha$  rm-invar rm-lookup rm-update*

*rm- $\alpha$  rm-invar rm-lookup rm-update rm-delete rm-iteratei rm-sel*

*rs- $\alpha$  rs-invar rs-memb rs-ins rs-delete*

*fifo- $\alpha$  fifo-invar fifo-empty fifo-isEmpty fifo-enqueue fifo-dequeue fifo-push*

**for** *final* ::  $'x \Rightarrow \text{bool}$

**and**  $r :: 't \Rightarrow ('x \times 'm) \Rightarrow (('l, 't :: \text{linorder}, 'x, 'm, 'w, 'o) \text{ thread-action} \times 'x \times 'm) \text{ Predicate.pred}$

**and** *convert-RA* ::  $'l \text{ released-locks} \Rightarrow 'o \text{ list}$

**and** *output* ::  $'t \text{ fifo round-robin} \Rightarrow 't \Rightarrow ('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \Rightarrow 'q \text{ option}$

**sublocale** *sc-round-robin* < *sc-round-robin-base* .

**locale** *sc-random-scheduler-base* =

*random-scheduler-base*

*final r convert-RA output*

*rm- $\alpha$  rm-invar rm-lookup rm-update rm-iteratei*

*rm- $\alpha$  rm-invar rm-lookup rm-update rm-delete rm-iteratei rm-sel*

*rs- $\alpha$  rs-invar rs-memb rs-ins rs-delete*

*lsi- $\alpha$  lsi-invar lsi-empty lsi-ins-dj lsi-to-list*

**for** *final* ::  $'x \Rightarrow \text{bool}$

**and**  $r :: 't \Rightarrow ('x \times 'm) \Rightarrow (('l, 't :: \text{linorder}, 'x, 'm, 'w, 'o) \text{ thread-action} \times 'x \times 'm) \text{ Predicate.pred}$

**and** *convert-RA* ::  $'l \text{ released-locks} \Rightarrow 'o \text{ list}$

**and** *output* ::  $\text{random-scheduler} \Rightarrow 't \Rightarrow ('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \Rightarrow 'q \text{ option}$

**locale** *sc-random-scheduler* =

*random-scheduler*

*final r convert-RA output*

*rm- $\alpha$  rm-invar rm-lookup rm-update rm-iteratei*

*rm- $\alpha$  rm-invar rm-lookup rm-update rm-delete rm-iteratei rm-sel*

*rs- $\alpha$  rs-invar rs-memb rs-ins rs-delete*

*lsi- $\alpha$  lsi-invar lsi-empty lsi-ins-dj lsi-to-list*

**for** *final* ::  $'x \Rightarrow \text{bool}$

**and**  $r :: 't \Rightarrow ('x \times 'm) \Rightarrow (('l, 't :: \text{linorder}, 'x, 'm, 'w, 'o) \text{ thread-action} \times 'x \times 'm) \text{ Predicate.pred}$

**and** *convert-RA* ::  $'l \text{ released-locks} \Rightarrow 'o \text{ list}$

**and** *output* ::  $\text{random-scheduler} \Rightarrow 't \Rightarrow ('l, 't, 'x, 'm, 'w, 'o) \text{ thread-action} \Rightarrow 'q \text{ option}$

**sublocale** *sc-random-scheduler* < *sc-random-scheduler-base* .

No spurious wake-ups in generated code

```
overloading sc-spurious-wakeups  $\equiv$  sc-spurious-wakeups
begin
  definition sc-spurious-wakeups [code]: sc-spurious-wakeups  $\equiv$  False
end

end
```

## 9.5 Tabulation for lookup functions

```
theory TypeRelRefine
imports
  ../Common/TypeRel
  HOL-Library.AList-Mapping
begin
```

### 9.5.1 Auxiliary lemmata

```
lemma rtranclp-tranclpE:
  assumes  $\hat{r}^{**} x y$ 
  obtains (refl)  $x = y$ 
  | (trancl)  $\hat{r}^{++} x y$ 
using assms
by(cases)(blast dest: rtranclp-into-tranclp1)+
```

```
lemma map-of-map2: map-of (map ( $\lambda(k, v). (k, f k v)$ ) xs) k = map-option (f k) (map-of xs k)
by(induct xs) auto
```

```
lemma map-of-map-K: map-of (map ( $\lambda k. (k, c)$ ) xs) k = (if  $k \in \text{set } xs$  then Some c else None)
by(induct xs) auto
```

```
lift-definition map-values :: ( $'a \Rightarrow 'b \Rightarrow 'c$ )  $\Rightarrow$  ( $'a, 'b$ ) mapping  $\Rightarrow$  ( $'a, 'c$ ) mapping
is  $\lambda f m k. \text{map-option } (f k) (m k)$ .
```

```
lemma map-values-Mapping [simp]:
  map-values f (Mapping.Mapping m) = Mapping.Mapping ( $\lambda k. \text{map-option } (f k) (m k)$ )
by(rule map-values.abs-eq)
```

```
lemma map-Mapping: Mapping.map f g (Mapping.Mapping m) = Mapping.Mapping (map-option g  $\circ$ 
m  $\circ$  f)
by(rule map.abs-eq)
```

```
abbreviation subclst ::  $'m \text{ prog} \Rightarrow \text{cname} \Rightarrow \text{cname} \Rightarrow \text{bool}$ 
where subclst P  $\equiv$  (subcls1 P)++
```

### 9.5.2 Representation type for tabulated lookup functions

```
type-synonym
   $'m \text{ prog-impl}' =$ 
   $'m \text{ cdecl list} \times$ 
   $(\text{cname}, 'm \text{ class}) \text{ mapping} \times$ 
   $(\text{cname}, \text{cname set}) \text{ mapping} \times$ 
   $(\text{cname}, (\text{vname}, \text{cname} \times \text{ty} \times \text{fmod}) \text{ mapping}) \text{ mapping} \times$ 
```

(cname, (mname, cname  $\times$  ty list  $\times$  ty  $\times$  'm option) mapping) mapping

**lift-definition** *tabulate-class* :: 'm cdecl list  $\Rightarrow$  (cname, 'm class) mapping  
is class  $\circ$  Program .

**lift-definition** *tabulate-subcls* :: 'm cdecl list  $\Rightarrow$  (cname, cname set) mapping  
is  $\lambda P C$ . if is-class (Program P) C then Some {D. Program P  $\vdash$  C  $\preceq^*$  D} else None .

**lift-definition** *tabulate-sees-field* :: 'm cdecl list  $\Rightarrow$  (cname, (vname, cname  $\times$  ty  $\times$  fmod) mapping) mapping  
is  $\lambda P C$ . if is-class (Program P) C then  
    Some ( $\lambda F$ . if  $\exists T fm D$ . Program P  $\vdash$  C sees F:T (fm) in D then Some (field (Program P) C F) else None)  
    else None .

**lift-definition** *tabulate-Method* :: 'm cdecl list  $\Rightarrow$  (cname, (mname, cname  $\times$  ty list  $\times$  ty  $\times$  'm option) mapping) mapping  
is  $\lambda P C$ . if is-class (Program P) C then  
    Some ( $\lambda M$ . if  $\exists Ts T mthd D$ . Program P  $\vdash$  C sees M:Ts $\rightarrow$ T=mthd in D then Some (method (Program P) C M) else None)  
    else None .

**fun** wf-prog-impl' :: 'm prog-impl'  $\Rightarrow$  bool  
**where**

    wf-prog-impl' (P, c, s, f, m)  $\longleftrightarrow$   
    c = tabulate-class P  $\wedge$   
    s = tabulate-subcls P  $\wedge$   
    f = tabulate-sees-field P  $\wedge$   
    m = tabulate-Method P

### 9.5.3 Implementation type for tabulated lookup functions

**typedef** 'm prog-impl = {P :: 'm prog-impl'. wf-prog-impl' P}  
    **morphisms** impl-of ProgRefine  
**proof**  
    **show** ([], Mapping.empty, Mapping.empty, Mapping.empty, Mapping.empty)  $\in$  ?prog-impl  
    **apply** clarsimp  
    **by** transfer (simp-all add: fun-eq-iff is-class-def rel-funI)  
**qed**

**lemma** impl-of-ProgImpl [simp]:  
    wf-prog-impl' P fsm  $\implies$  impl-of (ProgRefine P fsm) = P fsm  
**by**(simp add: ProgRefine-inverse)

**definition** program :: 'm prog-impl  $\Rightarrow$  'm prog  
**where** program = Program  $\circ$  fst  $\circ$  impl-of

**code-datatype** program

**lemma** prog-impl-eq-iff:  
    Pi = Pi'  $\longleftrightarrow$  program Pi = program Pi' **for** Pi Pi'  
**apply**(cases Pi)  
**apply**(cases Pi')  
**apply**(auto simp add: ProgRefine-inverse program-def ProgRefine-inject)

done

**lemma** *wf-prog-impl'-impl-of* [*simp*, *intro!*]:  
*wf-prog-impl' (impl-of Pi) for Pi*  
**using** *impl-of[of Pi]* **by** *simp*

**lemma** *ProgImpl-impl-of* [*simp*, *code abstype*]:  
*ProgRefine (impl-of Pi) = Pi for Pi*  
**by**(*rule impl-of-inverse*)

**lemma** *program-ProgRefine* [*simp*]: *wf-prog-impl' Psfm  $\implies$  program (ProgRefine Psfm) = Program (fst Psfm)*  
**by**(*simp add: program-def*)

**lemma** *classes-program* [*code*]: *classes (program P) = fst (impl-of P)*  
**by**(*simp add: program-def*)

**lemma** *class-program* [*code*]: *class (program Pi) = Mapping.lookup (fst (snd (impl-of Pi))) for Pi*  
**by**(*cases Pi*)(*clarsimp simp add: tabulate-class-def lookup.rep-eq Mapping-inverse*)

#### 9.5.4 Refining sub class and lookup functions to use precomputed mappings

**declare** *subcls'.equation* [*code del*]

**lemma** *subcls'-program* [*code*]:  
*subcls' (program Pi) C D  $\longleftrightarrow$*   
*C = D  $\vee$*   
*(case Mapping.lookup (fst (snd (snd (impl-of Pi)))) C of None  $\Rightarrow$  False*  
*| Some m  $\Rightarrow$  D  $\in$  m) **for** Pi  
**apply**(*cases Pi*)  
**apply**(*clarsimp simp add: subcls'-def tabulate-subcls-def lookup.rep-eq Mapping-inverse*)  
**apply**(*auto elim!: rtranclp-tranclpE dest: subcls-is-class intro: tranclp-into-rtranclp*)  
**done***

**lemma** *subcls'-i-i-i-program* [*code*]:  
*subcls'-i-i-i P C D = (if subcls' P C D then Predicate.single () else bot)*  
**by**(*rule pred-eqI*)(*auto elim: subcls'-i-i-iE intro: subcls'-i-i-iI*)

**lemma** *subcls'-i-i-o-program* [*code*]:  
*subcls'-i-i-o (program Pi) C =*  
*sup (Predicate.single C) (case Mapping.lookup (fst (snd (snd (impl-of Pi)))) C of None  $\Rightarrow$  bot | Some*  
*m  $\Rightarrow$  pred-of-set m) **for** Pi  
**by**(*cases Pi*)(*fastforce simp add: subcls'-i-i-o-def subcls'-def tabulate-subcls-def lookup.rep-eq Mapping-inverse*  
*intro!: pred-eqI split: if-split-asm elim: rtranclp-tranclpE dest: subcls-is-class intro: tranclp-into-rtranclp*)*

**lemma** *rtranclp-FioB-i-i-subcls1-i-i-o-code* [*code-unfold*]:  
*rtranclp-FioB-i-i (subcls1-i-i-o P) = subcls'-i-i-i P*  
**by**(*auto simp add: fun-eq-iff subcls1-i-i-o-def subcls'-def rtranclp-FioB-i-i-def subcls'-i-i-i-def*)

**declare** *Method.equation*[*code del*]

**lemma** *Method-program* [*code*]:  
*program Pi  $\vdash$  C sees M:Ts $\rightarrow$ T=meth in D  $\longleftrightarrow$*   
*(case Mapping.lookup (snd (snd (snd (snd (impl-of Pi)))) C of*  
*None  $\Rightarrow$  False*

```

| Some m ⇒
  (case Mapping.lookup m M of
    None ⇒ False
    | Some (D', Ts', T', meth') ⇒ Ts = Ts' ∧ T = T' ∧ meth = meth' ∧ D = D') for Pi
by(cases Pi)(auto split: if-split-asm dest: sees-method-is-class simp add: tabulate-Method-def lookup.rep-eq
Mapping-inverse)

```

**lemma** *Method-i-i-i-o-o-o-o-program* [code]:  
*Method-i-i-i-o-o-o-o* (program Pi) C M =  
(case Mapping.lookup (snd (snd (snd (impl-of Pi)))) C of  
 None ⇒ bot  
 | Some m ⇒  
 (case Mapping.lookup m M of  
 None ⇒ bot  
 | Some (D, Ts, T, meth) ⇒ Predicate.single (Ts, T, meth, D))) **for** Pi  
**by**(auto simp add: Method-i-i-i-o-o-o-o-def Method-program intro!: pred-eqI)

**lemma** *Method-i-i-i-o-o-o-i-program* [code]:  
*Method-i-i-i-o-o-o-i* (program Pi) C M D =  
(case Mapping.lookup (snd (snd (snd (impl-of Pi)))) C of  
 None ⇒ bot  
 | Some m ⇒  
 (case Mapping.lookup m M of  
 None ⇒ bot  
 | Some (D', Ts, T, meth) ⇒ if D = D' then Predicate.single (Ts, T, meth) else bot)) **for** Pi  
**by**(auto simp add: Method-i-i-i-o-o-o-i-def Method-program intro!: pred-eqI)

**declare** *sees-field.equation*[code del]

**lemma** *sees-field-program* [code]:  
program Pi ⊢ C sees F:T (fd) in D ⟷  
(case Mapping.lookup (fst (snd (snd (impl-of Pi)))) C of  
 None ⇒ False  
 | Some m ⇒  
 (case Mapping.lookup m F of  
 None ⇒ False  
 | Some (D', T', fd') ⇒ T = T' ∧ fd = fd' ∧ D = D')) **for** Pi  
**by**(cases Pi)(auto split: if-split-asm dest: has-visible-field[THEN has-field-is-class] simp add: tabulate-sees-field-def lookup.rep-eq Mapping-inverse)

**lemma** *sees-field-i-i-i-o-o-o-o-program* [code]:  
*sees-field-i-i-i-o-o-o-o* (program Pi) C F =  
(case Mapping.lookup (fst (snd (snd (impl-of Pi)))) C of  
 None ⇒ bot  
 | Some m ⇒  
 (case Mapping.lookup m F of  
 None ⇒ bot  
 | Some (D, T, fd) ⇒ Predicate.single(T, fd, D))) **for** Pi  
**by**(auto simp add: sees-field-program sees-field-i-i-i-o-o-o-o-def intro: pred-eqI)

**lemma** *sees-field-i-i-i-o-o-o-i-program* [code]:  
*sees-field-i-i-i-o-o-o-i* (program Pi) C F D =  
(case Mapping.lookup (fst (snd (snd (impl-of Pi)))) C of  
 None ⇒ bot



| *Some m*  $\Rightarrow$   
 (case *Mapping.lookup m F* of  
   *None*  $\Rightarrow$  *bot*  
 | *Some (D', T, fd)*  $\Rightarrow$  if *D = D'* then *Predicate.single(T, fd)* else *bot*)) **for** *Pi*  
**by**(*auto simp add: sees-field-program sees-field-i-i-i-o-o-i-def intro: pred-eqI*)

**lemma** *field-program* [*code*]:  
*field (program Pi) C F =*  
 (case *Mapping.lookup (fst (snd (snd (impl-of Pi))))* *C* of  
   *None*  $\Rightarrow$  *Code.abort (STR "not-unique") (λ-. Predicate.the bot)*  
 | *Some m*  $\Rightarrow$   
   (case *Mapping.lookup m F* of  
     *None*  $\Rightarrow$  *Code.abort (STR "not-unique") (λ-. Predicate.the bot)*  
   | *Some (D', T, fd)*  $\Rightarrow$  (*D', T, fd*)) **for** *Pi*  
**unfolding** *field-def*  
**by**(*cases Pi*)(*fastforce simp add: Predicate.the-def tabulate-sees-field-def lookup.rep-eq Mapping-inverse*  
*split: if-split-asm intro: arg-cong[where f=The] dest: has-visible-field[THEN has-field-is-class] sees-field-fun*)

### 9.5.5 Implementation for precomputing mappings

**definition** *tabulate-program* :: '*m cdecl list*  $\Rightarrow$  '*m prog-impl*  
**where** *tabulate-program P = ProgRefine (P, tabulate-class P, tabulate-subcls P, tabulate-sees-field P,*  
*tabulate-Method P)*

**lemma** *impl-of-tabulate-program* [*code abstract*]:  
*impl-of (tabulate-program P) = (P, tabulate-class P, tabulate-subcls P, tabulate-sees-field P, tabu-*  
*late-Method P)*  
**by**(*simp add: tabulate-program-def*)

**lemma** *Program-code* [*code*]:  
*Program = program  $\circ$  tabulate-program*  
**by**(*simp add: program-def fun-eq-iff tabulate-program-def*)

*class*

**lemma** *tabulate-class-code* [*code*]:  
*tabulate-class = Mapping.of-alist*  
**by** *transfer (simp add: fun-eq-iff)*

*subcls*

**inductive** *subcls1'* :: '*m cdecl list*  $\Rightarrow$  *cname*  $\Rightarrow$  *cname*  $\Rightarrow$  *bool*  
**where**  
*find: C  $\neq$  Object  $\Rightarrow$  subcls1' ((C, D, rest) # P) C D*  
*| step: [ C  $\neq$  Object; C  $\neq$  C'; subcls1' P C D ]  $\Rightarrow$  subcls1' ((C', D', rest) # P) C D*

**code-pred**  
*(modes: i  $\Rightarrow$  i  $\Rightarrow$  o  $\Rightarrow$  bool)*  
*subcls1'.*

**lemma** *subcls1-into-subcls1'*:  
**assumes** *subcls1 (Program P) C D*  
**shows** *subcls1' P C D*

**proof** –  
**from** *assms obtain rest where map-of P C = [(D, rest)] C  $\neq$  Object* **by** *cases simp*

```

thus ?thesis by(induct P)(auto split: if-split-asm intro: subcls1'.intros)
qed

```

```

lemma subcls1'-into-subcls1:
  assumes subcls1' P C D
  shows subcls1 (Program P) C D
using assms
proof(induct)
  case find thus ?case by(auto intro: subcls1.intros)
next
  case step thus ?case by(auto elim!: subcls1.cases intro: subcls1.intros)
qed

```

```

lemma subcls1-eq-subcls1':
  subcls1 (Program P) = subcls1' P
by(auto simp add: fun-eq-iff intro: subcls1-into-subcls1' subcls1'-into-subcls1)

```

```

definition subcls'' :: 'm cdecl list  $\Rightarrow$  cname  $\Rightarrow$  cname  $\Rightarrow$  bool
where subcls'' P = (subcls1' P)**

```

```

code-pred
  (modes: i  $\Rightarrow$  i  $\Rightarrow$  i  $\Rightarrow$  bool)
  [inductify]
  subcls'' .

```

```

lemma subcls''-eq-subcls: subcls'' P = subcls (Program P)
by(simp add: subcls''-def subcls1-eq-subcls1')

```

```

lemma subclst-snd-classD:
  assumes subclst (Program P) C D
  shows D  $\in$  fst ' snd ' set P
using assms
by(induct)(fastforce elim!: subcls1.cases dest!: map-of-SomeD intro: rev-image-eqI)+

```

```

definition check-acyclicity :: (cname, cname set) mapping  $\Rightarrow$  'm cdecl list  $\Rightarrow$  unit
where check-acyclicity - - = ()

```

```

definition cyclic-class-hierarchy :: unit
where [code del]: cyclic-class-hierarchy = ()

```

```

declare [[code abort: cyclic-class-hierarchy]]

```

```

lemma check-acyclicity-code:
  check-acyclicity mapping P =
    (let - =
      map ( $\lambda(C, D, -).$ 
        if C = Object then ()
        else
          (case Mapping.lookup mapping D of
            None  $\Rightarrow$  ()
            | Some Cs  $\Rightarrow$  if C  $\in$  Cs then cyclic-class-hierarchy else ()))
        P
      in ())
by simp

```

```

lemma tabulate-subcls-code [code]:
  tabulate-subcls P =
    (let cnames = map fst P;
      cnames' = map (fst ∘ snd) P;
      mapping = Mapping.tabulate cnames (λC. set (C # [D ← cnames'. subcls'' P C D]));
      - = check-acyclicity mapping P
    in mapping
  )
apply(auto simp add: tabulate-subcls-def Mapping.tabulate-def fun-eq-iff is-class-def o-def map-of-map2[simplified split-def] Mapping-inject)
apply(subst map-of-map2[simplified split-def])
apply(auto simp add: fun-eq-iff subcls''-eq-subcls map-of-map-K dest: subclst-snd-classD elim: rtranclp-tranclpE)[1]
apply(subst map-of-map2[simplified split-def])
apply(rule sym)
apply simp
apply(case-tac map-of P x)
apply auto
done

```

### *Fields*

Problem: Does not terminate for cyclic class hierarchies! This problem already occurs in Jinja's well-formedness checker: *wf-cdecl* calls *wf-mdecl* before checking for acyclicity, but *wf-J-mdecl* involves the type judgements, which in turn requires *Fields* (via *sees-field*). Checking acyclicity before executing *Fields'* for tabulation is difficult because we would have to intertwine tabulation and well-formedness checking. Possible (local) solution: additional termination parameter (like memoisation for *rtranclp*) and list option as error return parameter.

### **inductive**

```

Fields' :: 'm cdecl list ⇒ cname ⇒ ((vname × cname) × (ty × fmod)) list ⇒ bool
for P :: 'm cdecl list
where
  rec:
    [ map-of P C = Some(D,fs,ms); C ≠ Object; Fields' P D FDTs;
      FDTs' = map (λ(F,Tm). ((F,C),Tm)) fs @ FDTs ]
    ⇒ Fields' P C FDTs'
  | Object:
    [ map-of P Object = Some(D,fs,ms); FDTs = map (λ(F,T). ((F,Object),T)) fs ]
    ⇒ Fields' P Object FDTs

```

### **lemma** *Fields'-into-Fields*:

```

  assumes Fields' P C FDTs
  shows Program P ⊢ C has-fields FDTs
using assms
by induct(auto intro: Fields.intros)

```

### **lemma** *Fields-into-Fields'*:

```

  assumes Program P ⊢ C has-fields FDTs
  shows Fields' P C FDTs
using assms
by induct(auto intro: Fields'.intros)

```

**lemma** *Fields'-eq-Fields*:

*Fields' P = Fields (Program P)*

**by**(*auto simp add: fun-eq-iff intro: Fields'-into-Fields Fields-into-Fields'*)

**code-pred**

(*modes: i  $\Rightarrow$  i  $\Rightarrow$  o  $\Rightarrow$  bool*)

*Fields' .*

**definition** *fields' :: 'm cdecl list  $\Rightarrow$  cname  $\Rightarrow$  ((vname  $\times$  cname)  $\times$  (ty  $\times$  fmod)) list*

**where** *fields' P C = (if  $\exists$  FDTs. Fields' P C FDTs then THE FDTs. Fields' P C FDTs else [])*

**lemma** *eval-Fields'-conv*:

*Predicate.eval (Fields'-i-i-o P C) = Fields' P C*

**by**(*auto intro: Fields'-i-i-oI elim: Fields'-i-i-oE intro!: ext*)

**lemma** *fields'-code [code]*:

*fields' P C =*

*(let FDTs = Fields'-i-i-o P C in if Predicate.holds (FDTs  $\gg$  ( $\lambda$ -. Predicate.single ())) then Predicate.the FDTs else [])*

**by**(*auto simp add: fields'-def holds-eq Fields'-i-i-o-def intro: Fields'-i-i-oI Predicate.the-eqI[THEN sym]*)

**lemma** *The-Fields [simp]*:

*P  $\vdash$  C has-fields FDTs  $\Longrightarrow$  The (Fields P C) = FDTs*

**by**(*auto dest: has-fields-fun*)

**lemma** *tabulate-sees-field-code [code]*:

*tabulate-sees-field P =*

*Mapping.tabulate (map fst P) ( $\lambda$ C. Mapping.of-alist (map ( $\lambda$ ((F, D), Tfm). (F, (D, Tfm))) (fields' P C)))*

**apply**(*simp add: tabulate-sees-field-def tabulate-def is-class-def fields'-def Fields'-eq-Fields Mapping-inject*)

**apply**(*rule ext*)

**apply** *clarsimp*

**apply**(*rule conjI*)

**apply**(*clarsimp simp add: o-def*)

**apply**(*subst map-of-map2[unfolded split-def]*)

**apply** *simp*

**apply** *transfer*

**apply**(*rule conjI*)

**apply** *clarsimp*

**apply**(*rule ext*)

**apply** *clarsimp*

**apply**(*rule conjI*)

**apply**(*clarsimp simp add: sees-field-def Fields'-eq-Fields*)

**apply**(*drule (1) has-fields-fun, clarsimp*)

**apply** *clarify*

**apply**(*rule sym*)

**apply**(*rule ccontr*)

**apply**(*clarsimp simp add: sees-field-def Fields'-eq-Fields*)

**apply** *clarsimp*

**apply**(*rule ext*)

**apply**(*clarsimp simp add: sees-field-def*)

**apply**(*clarsimp simp add: o-def*)

**apply**(*subst map-of-map2[simplified split-def]*)

```

apply(rule sym)
apply(clarsimp)
apply(rule ccontr)
apply simp
done

```

*Methods*

Same termination problem as for *Fields'*

```

inductive Methods' :: 'm cdecl list  $\Rightarrow$  cname  $\Rightarrow$  (mname  $\times$  (ty list  $\times$  ty  $\times$  'm option)  $\times$  cname) list
 $\Rightarrow$  bool
  for P :: 'm cdecl list
where
  [| map-of P Object = Some(D,fs,ms); Mm = map ( $\lambda$ (M, rest). (M, (rest, Object))) ms |]
     $\Rightarrow$  Methods' P Object Mm
| [| map-of P C = Some(D,fs,ms); C  $\neq$  Object; Methods' P D Mm;
   Mm' = map ( $\lambda$ (M, rest). (M, (rest, C))) ms @ Mm |]
     $\Rightarrow$  Methods' P C Mm'

```

```

lemma Methods'-into-Methods:
  assumes Methods' P C Mm
  shows Program P  $\vdash$  C sees-methods (map-of Mm)
using assms
apply induct
apply(clarsimp simp add: o-def split-def)
apply(rule sees-methods-Object)
apply fastforce
apply(rule ext)
apply(subst map-of-map2[unfolded split-def])
apply(simp add: o-def)

```

```

apply(rule sees-methods-rec)
  apply fastforce
  apply simp
  apply assumption
apply(clarsimp simp add: map-add-def map-of-map2)
done

```

```

lemma Methods-into-Methods':
  assumes Program P  $\vdash$  C sees-methods Mm
  shows  $\exists$  Mm'. Methods' P C Mm'  $\wedge$  Mm = map-of Mm'
using assms
by induct(auto intro: Methods'.intros simp add: map-of-map2 map-add-def)

```

```

code-pred
  (modes: i  $\Rightarrow$  i  $\Rightarrow$  o  $\Rightarrow$  bool)
  Methods'
.

```

```

definition methods' :: 'm cdecl list  $\Rightarrow$  cname  $\Rightarrow$  (mname  $\times$  (ty list  $\times$  ty  $\times$  'm option)  $\times$  cname) list
where methods' P C = (if  $\exists$  Mm. Methods' P C Mm then THE Mm. Methods' P C Mm else [])

```

```

lemma methods'-code [code]:
  methods' P C =

```

```

    (let Mm = Methods'-i-i-o P C
      in if Predicate.holds (Mm  $\gg$  ( $\lambda$ -. Predicate.single ())) then Predicate.the Mm else [])
unfolding methods'-def
by(auto simp add: holds-eq Methods'-i-i-o-def Predicate.the-def)

```

```

lemma Methods'-fun:
  assumes Methods' P C Mm
  shows Methods' P C Mm'  $\implies$  Mm = Mm'
using assms
apply(induct arbitrary: Mm')
apply(fastforce elim: Methods'.cases)
apply(rotate-tac -1)
apply(erule Methods'.cases)
apply(fastforce)
apply clarify
apply(simp)
done

```

```

lemma The-Methods' [simp]: Methods' P C Mm  $\implies$  The (Methods' P C) = Mm
by(auto dest: Methods'-fun)

```

```

lemma methods-def2 [simp]: Methods' P C Mm  $\implies$  methods' P C = Mm
by(auto simp add: methods'-def)

```

```

lemma tabulate-Method-code [code]:
  tabulate-Method P =
    Mapping.tabulate (map fst P) ( $\lambda$ C. Mapping.of-alist (map ( $\lambda$ (M, (rest, D)). (M, D, rest)) (methods'
    P C)))
apply(simp add: tabulate-Method-def tabulate-def o-def lookup.rep-eq Mapping-inject)
apply(rule ext)
apply clarsimp
apply(rule conjI)
apply clarify
apply(rule sym)
apply(subst map-of-map2[unfolded split-def])
apply(simp add: is-class-def)
apply transfer
apply(rule ext)
apply(simp add: map-of-map2)
apply(rule conjI)
apply(clarsimp simp add: map-of-map2 Method-def)
apply(drule Methods-into-Methods')
apply clarsimp
apply(simp add: split-def)
apply(subst map-of-map2[unfolded split-def])
apply simp
apply clarify
apply(clarsimp simp add: methods'-def)
apply(frule Methods'-into-Methods)
apply(clarsimp simp add: Method-def)
apply(simp add: split-def)
apply(subst map-of-map2[unfolded split-def])
apply(fastforce intro: ccontr)
apply clarify

```

```

apply(rule sym)
apply(simp add: map-of-eq-None-iff is-class-def)
apply(simp only: set-map[symmetric] map-map o-def fst-conv)
apply simp
done

```

Merge modules TypeRel, Decl and TypeRelRefine to avoid cyclic modules

```

code-identifier
  code-module TypeRel  $\rightarrow$ 
    (SML) TypeRel and (Haskell) TypeRel and (OCaml) TypeRel
| code-module TypeRelRefine  $\rightarrow$ 
    (SML) TypeRel and (Haskell) TypeRel and (OCaml) TypeRel
| code-module Decl  $\rightarrow$ 
    (SML) TypeRel and (Haskell) TypeRel and (OCaml) TypeRel

```

```

ML-val  $\langle @\{code\ Program\} \rangle$ 

```

```

end

```

```

theory PCompilerRefine
imports
  TypeRelRefine
  ../Compiler/PCompiler
begin

```

### 9.5.6 compP

Applying the compiler to a tabulated program either compiles every method twice (once for the program itself and once for method lookup) or recomputes the class and method lookup tabulation from scratch. We follow the second approach.

```

fun compP-code' :: (cname  $\Rightarrow$  mname  $\Rightarrow$  ty list  $\Rightarrow$  ty  $\Rightarrow$  'a  $\Rightarrow$  'b)  $\Rightarrow$  'a prog-impl'  $\Rightarrow$  'b prog-impl'
where
  compP-code' f (P, Cs, s, F, m) =
    (let P' = map (compC f) P
     in (P', tabulate-class P', s, F, tabulate-Method P'))

```

```

definition compP-code :: (cname  $\Rightarrow$  mname  $\Rightarrow$  ty list  $\Rightarrow$  ty  $\Rightarrow$  'a  $\Rightarrow$  'b)  $\Rightarrow$  'a prog-impl'  $\Rightarrow$  'b prog-impl'
where compP-code f P = ProgRefine (compP-code' f (impl-of P))

```

```

declare compP.simps [simp del] compP.simps[symmetric, simp]

```

```

lemma compP-code-code [code abstract]:
  impl-of (compP-code f P) = compP-code' f (impl-of P)
apply(cases P)
apply(simp add: compP-code-def)
apply(subst ProgRefine-inverse)
apply(auto simp add: tabulate-subcls-def tabulate-sees-field-def Mapping-inject intro!: ext)
done

```

```

declare compP.simps [simp] compP.simps[symmetric, simp del]

```

```

lemma compP-program [code]:

```





$(addr, thread-id, heap, (thread-id, (addr\ expr \times addr\ locals) \times addr\ released-locks)\ rm, (thread-id, addr\ wait-set-status)\ rm, thread-id\ rs)\ state-refine$

**where**

$sc-J-start-state-refine \equiv$   
 $sc-start-state-refine$   
 $(rm-empty\ ())\ rm-update\ (rm-empty\ ())\ (rs-empty\ ())$   
 $(\lambda C\ M\ Ts\ T\ (pns, body)\ vs.\ (blocks\ (this\ \# \ pns)\ (Class\ C\ \# \ Ts)\ (Null\ \# \ vs)\ body, Map.empty))$

**lemma**  $eval-sc-red-i-i-i-i-i-Fii-i-oB-Fii-i-i-oB-i-i-i-i-i-o-o-o$ :

$(\lambda t\ xm\ ta\ x'm'.\ Predicate.eval\ (sc-red-i-i-i-i-i-Fii-i-oB-Fii-i-i-oB-i-i-i-i-i-o-o-o\ P\ t\ xm)\ (ta, x'm'))$

$=$

$(\lambda t\ ((e, xs), h)\ ta\ ((e', xs'), h').\ extTA2J\ P, P, t \vdash_{sc} \langle e, (h, xs) \rangle - ta \rightarrow \langle e', (h', xs') \rangle)$

**by**( $auto\ elim!$ :  $red-i-i-i-i-i-Fii-i-oB-Fii-i-i-oB-i-i-i-i-i-o-o-oE\ intro!$ :  $red-i-i-i-i-i-Fii-i-oB-Fii-i-i-oB-i-i-i-i-i-o-o-oI$   
 $ext\ SUP1-I\ simp\ add$ :  $eval-sc-heap-write-i-i-i-i-o\ eval-sc-heap-read-i-i-i-o$ )

**lemma**  $sc-J-start-state-invar$ :  $(\lambda -. True)\ (sc-state-\alpha\ (sc-J-start-state-refine\ P\ C\ M\ vs))$

**by**  $simp$

### 9.6.1 Round-robin scheduler

**interpretation**  $J-rr$ :

$sc-round-robin-base$

$final-expr\ sc-red-i-i-i-i-i-Fii-i-oB-Fii-i-i-oB-i-i-i-i-i-o-o-o\ P\ convert-RA\ Jinja-output$

**for**  $P$

.

**definition**  $sc-rr-J-start-state$  ::  $nat \Rightarrow 'm\ prog \Rightarrow thread-id\ fifo\ round-robin$

**where**  $sc-rr-J-start-state\ n0\ P = J-rr.round-robin-start\ n0\ (sc-start-tid\ P)$

**definition**  $exec-J-rr$  ::

$nat \Rightarrow addr\ J-prog \Rightarrow cname \Rightarrow mname \Rightarrow addr\ val\ list \Rightarrow$

$(thread-id \times (addr, thread-id)\ obs-event\ list,$

$(addr, thread-id)\ locks \times ((thread-id, (addr\ expr \times addr\ locals) \times addr\ released-locks)\ rm \times heap)$

$\times$

$(thread-id, addr\ wait-set-status)\ rm \times thread-id\ rs)\ tl\ list$

**where**

$exec-J-rr\ n0\ P\ C\ M\ vs = J-rr.exec\ P\ n0\ (sc-rr-J-start-state\ n0\ P)\ (sc-J-start-state-refine\ P\ C\ M\ vs)$

**interpretation**  $J-rr$ :

$sc-round-robin$

$final-expr\ sc-red-i-i-i-i-i-Fii-i-oB-Fii-i-i-oB-i-i-i-i-i-o-o-o\ P\ convert-RA\ Jinja-output$

**for**  $P$

**by**( $unfold-locales$ )

**interpretation**  $J-rr$ :

$sc-scheduler$

$final-expr\ sc-red-i-i-i-i-i-Fii-i-oB-Fii-i-i-oB-i-i-i-i-i-o-o-o\ P\ convert-RA$

$J-rr.round-robin\ P\ n0\ Jinja-output\ pick-wakeup-via-sel\ (\lambda s\ P.\ rm-sel\ s\ (\lambda(k,v).\ P\ k\ v))\ J-rr.round-robin-invar$

$UNIV$

**for**  $P\ n0$

**unfolding**  $sc-scheduler-def$

**apply**( $rule\ J-rr.round-robin-scheduler$ )

**apply**( $unfold\ eval-sc-red-i-i-i-i-i-Fii-i-oB-Fii-i-i-oB-i-i-i-i-i-o-o-o$ )

**apply**( $rule\ sc.red-mthr-deterministic[OF\ sc-deterministic-heap-ops]$ )

**apply**(*simp add: sc-spurious-wakeups*)  
**done**

### 9.6.2 Random scheduler

**interpretation** *J-rnd*:

*sc-random-scheduler-base*

*final-expr sc-red-i-i-i-i-i-i-i-Fii-i-oB-Fii-i-i-oB-i-i-i-i-i-o-o-o P convert-RA Jinja-output*  
**for** *P*

.

**definition** *sc-rnd-J-start-state* :: *Random.seed*  $\Rightarrow$  *random-scheduler*

**where** *sc-rnd-J-start-state seed* = *seed*

**definition** *exec-J-rnd* ::

*Random.seed*  $\Rightarrow$  *addr J-prog*  $\Rightarrow$  *cname*  $\Rightarrow$  *mname*  $\Rightarrow$  *addr val list*  $\Rightarrow$

(*thread-id*  $\times$  (*addr*, *thread-id*) *obs-event list*,

(*addr*, *thread-id*) *locks*  $\times$  ((*thread-id*, (*addr expr*  $\times$  *addr locals*)  $\times$  *addr released-locks*) *rm*  $\times$  *heap*)

$\times$

(*thread-id*, *addr wait-set-status*) *rm*  $\times$  *thread-id rs*) *tl**list*

**where**

*exec-J-rnd seed P C M vs* = *J-rnd.exec P (sc-rnd-J-start-state seed) (sc-J-start-state-refine P C M vs)*

**interpretation** *J-rnd*:

*sc-random-scheduler*

*final-expr sc-red-i-i-i-i-i-i-i-Fii-i-oB-Fii-i-i-oB-i-i-i-i-i-o-o-o P convert-RA Jinja-output*  
**for** *P*

**by**(*unfold-locales*)

**interpretation** *J-rnd*:

*sc-scheduler*

*final-expr sc-red-i-i-i-i-i-i-i-Fii-i-oB-Fii-i-i-oB-i-i-i-i-i-o-o-o P convert-RA*

*J-rnd.random-scheduler P Jinja-output pick-wakeup-via-sel* ( $\lambda s P. rm-sel s (\lambda(k,v). P k v)$ )  $\lambda - .$

*True*

*UNIV*

**for** *P*

**unfolding** *sc-scheduler-def*

**apply**(*rule J-rnd.random-scheduler-scheduler*)

**apply**(*unfold eval-sc-red-i-i-i-i-i-Fii-i-oB-Fii-i-i-oB-i-i-i-i-i-o-o-o*)

**apply**(*rule sc.red-mthr-deterministic[OF sc-deterministic-heap-ops]*)

**apply**(*simp add: sc-spurious-wakeups*)

**done**

**ML-val**  $\langle @\{code\} exec-J-rr \rangle$

**ML-val**  $\langle @\{code\} exec-J-rnd \rangle$

**end**

## 9.7 Executable semantics for the JVM

**theory** *ExternalCall-Execute*

**imports**

```

../Common/ExternalCall
../Basic/Set-without-equal
begin

```

### 9.7.1 Translated versions of external calls for the JVM

```

locale heap-execute = addr-base +
  constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id
  and thread-id2addr :: 'thread-id ⇒ 'addr
  fixes spurious-wakeups :: bool
  and empty-heap :: 'heap
  and allocate :: 'heap ⇒ htype ⇒ ('heap × 'addr) set
  and typeof-addr :: 'heap ⇒ 'addr ⇒ htype option
  and heap-read :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val set
  and heap-write :: 'heap ⇒ 'addr ⇒ addr-loc ⇒ 'addr val ⇒ 'heap set

```

```

sublocale heap-execute < execute: heap-base
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr
  λh a ad v. v ∈ heap-read h a ad λh a ad v h'. h' ∈ heap-write h a ad v
.

```

```

context heap-execute begin

```

```

definition heap-copy-loc :: 'addr ⇒ 'addr ⇒ addr-loc ⇒ 'heap ⇒ (('addr, 'thread-id) obs-event list ×
'heap) set
where [simp]:
  heap-copy-loc a a' al h = {(obs, h'). execute.heap-copy-loc a a' al h obs h'}

```

```

lemma heap-copy-loc-code:
  heap-copy-loc a a' al h =
    (do {
      v ← heap-read h a al;
      h' ← heap-write h a' al v;
      {[ReadMem a al v, WriteMem a' al v], h'})
    })
by(auto simp add: execute.heap-copy-loc.simps)

```

```

definition heap-copies :: 'addr ⇒ 'addr ⇒ addr-loc list ⇒ 'heap ⇒ (('addr, 'thread-id) obs-event list
× 'heap) set
where [simp]: heap-copies a a' al h = {(obs, h'). execute.heap-copies a a' al h obs h'}

```

```

lemma heap-copies-code:
  shows heap-copies-Nil:
    heap-copies a a' [] h = {([], h)}
  and heap-copies-Cons:
    heap-copies a a' (al # als) h =
      (do {
        (ob, h') ← heap-copy-loc a a' al h;
        (obs, h'') ← heap-copies a a' als h';
        {(ob @ obs, h'')}
      })
by(fastforce elim!: execute.heap-copies-cases intro: execute.heap-copies.intros)+

```

**definition** *heap-clone* :: 'm prog  $\Rightarrow$  'heap  $\Rightarrow$  'addr  $\Rightarrow$  ('heap  $\times$  (('addr, 'thread-id) obs-event list  $\times$  'addr) option) set

**where** [simp]: *heap-clone* *P h a* = {(*h'*, *obsa*). *execute.heap-clone P h a h' obsa*}

**lemma** *heap-clone-code*:

*heap-clone P h a* =  
 (case typeof-addr *h a* of  
 | [Class-type *C*]  $\Rightarrow$   
 let *HA* = allocate *h* (Class-type *C*)  
 in if *HA* = {} then {(*h*, None)} else do {  
 (*h'*, *a'*)  $\leftarrow$  *HA*;  
*FDTs*  $\leftarrow$  set-of-pred (Fields-i-i-o *P C*);  
 (*obs*, *h''*)  $\leftarrow$  heap-copies *a a'* (map ( $\lambda((F, D), Tfm).$  CField *D F*) *FDTs*) *h'*;  
 {(*h''*, [(NewHeapElem *a'* (Class-type *C*) # *obs*, *a'*))}
 }  
 | [Array-type *T n*]  $\Rightarrow$   
 let *HA* = allocate *h* (Array-type *T n*)  
 in if *HA* = {} then {(*h*, None)} else do {  
 (*h'*, *a'*)  $\leftarrow$  *HA*;  
*FDTs*  $\leftarrow$  set-of-pred (Fields-i-i-o *P Object*);  
 (*obs*, *h''*)  $\leftarrow$  heap-copies *a a'* (map ( $\lambda((F, D), Tfm).$  CField *D F*) *FDTs* @ map ACell [0..*n*])  
*h'*;  
 {(*h''*, [(NewHeapElem *a'* (Array-type *T n*) # *obs*, *a'*))}
 }  
 | -  $\Rightarrow$  {})  
**by** (auto 4 3 elim!: *execute.heap-clone.cases split*: *ty.splits*  
*prod.split-asm htype.splits intro*: *execute.heap-clone.intros*  
*simp add*: *eval-Fields-conv split-beta prod-eq-iff*)  
 (auto *simp add*: *eval-Fields-conv Bex-def*)

**definition** *red-external-aggr* ::

'm prog  $\Rightarrow$  'thread-id  $\Rightarrow$  'addr  $\Rightarrow$  mname  $\Rightarrow$  'addr val list  $\Rightarrow$  'heap  $\Rightarrow$   
 (('addr, 'thread-id, 'heap) external-thread-action  $\times$  'addr extCallRet  $\times$  'heap) set

**where** [simp]:

*red-external-aggr P t a M vs h* = *execute.red-external-aggr P t a M vs h*

**lemma** *red-external-aggr-code*:

*red-external-aggr P t a M vs h* =  
 (if *M* = wait then  
 let *ad-t* = thread-id2addr *t*  
 in {(⟦Unlock $\rightarrow$ *a*, Lock $\rightarrow$ *a*, IsInterrupted *t* True, ClearInterrupt *t*, ObsInterrupted *t*⟧, execute.RetEXC InterruptedException, *h*),  
 (⟦Suspend *a*, Unlock $\rightarrow$ *a*, Lock $\rightarrow$ *a*, ReleaseAcquire $\rightarrow$ *a*, IsInterrupted *t* False, SyncUnlock *a*⟧, RetStaySame, *h*),  
 (⟦UnlockFail $\rightarrow$ *a*⟧, execute.RetEXC IllegalMonitorState, *h*),  
 (⟦Notified⟧, RetVal Unit, *h*),  
 (⟦WokenUp, ClearInterrupt *t*, ObsInterrupted *t*⟧, execute.RetEXC InterruptedException, *h*)}  $\cup$   
 (if spurious-wakeups then {(⟦Unlock $\rightarrow$ *a*, Lock $\rightarrow$ *a*, ReleaseAcquire $\rightarrow$ *a*, IsInterrupted *t* False, SyncUnlock *a*⟧, RetVal Unit, *h*)} else {})  
 else if *M* = notify then  
 {(⟦Notify *a*, Unlock $\rightarrow$ *a*, Lock $\rightarrow$ *a*⟧, RetVal Unit, *h*),  
 (⟦UnlockFail $\rightarrow$ *a*⟧, execute.RetEXC IllegalMonitorState, *h*)}  
 else if *M* = notifyAll then

```

    {( $\{\text{NotifyAll } a, \text{Unlock} \rightarrow a, \text{Lock} \rightarrow a\}$ ,  $\text{RetVal Unit}, h$ ),
      ( $\{\text{UnlockFail} \rightarrow a\}$ ,  $\text{execute.RetEXC IllegalMonitorState}, h$ )}
  else if  $M = \text{clone}$  then
    do {
       $(h', \text{obsa}) \leftarrow \text{heap-clone } P \ h \ a$ ;
      {case  $\text{obsa}$  of  $\text{None} \Rightarrow (\varepsilon, \text{execute.RetEXC OutOfMemory}, h')$ 
        |  $\text{Some } (\text{obs}, a') \Rightarrow ((K\$ \ [], [], [], [], \text{obs}), \text{RetVal (Addr } a'), h')}$ 
      }
    }
  else if  $M = \text{hashcode}$  then {( $\varepsilon$ ,  $\text{RetVal (Intg (word-of-int (hash-addr } a))$ ),  $h$ )}
  else if  $M = \text{print}$  then {( $\{\text{ExternalCall } a \ M \text{ vs Unit}\}$ ,  $\text{RetVal Unit}, h$ )}
  else if  $M = \text{currentThread}$  then {( $\varepsilon$ ,  $\text{RetVal (Addr (thread-id2addr } t))$ ),  $h$ )}
  else if  $M = \text{interrupted}$  then
    {( $\{\text{IsInterrupted } t \text{ True}, \text{ClearInterrupt } t, \text{ObsInterrupted } t\}$ ,  $\text{RetVal (Bool True)}, h$ ),
      ( $\{\text{IsInterrupted } t \text{ False}\}$ ,  $\text{RetVal (Bool False)}, h$ )}
  else if  $M = \text{yield}$  then {( $\{\text{Yield}\}$ ,  $\text{RetVal Unit}, h$ )}
  else
    let  $T = \text{ty-of-htype (the (typeof-addr } h \ a))$ 
    in if  $P \vdash T \leq \text{Class Thread}$  then
      let  $t\text{-}a = \text{addr2thread-id } a$ 
      in if  $M = \text{start}$  then
        {( $\{\text{NewThread } t\text{-}a \text{ (the-Class } T, \text{run}, a) \ h, \text{ThreadStart } t\text{-}a\}$ ,  $\text{RetVal Unit}, h$ ),
          ( $\{\text{ThreadExists } t\text{-}a \text{ True}\}$ ,  $\text{execute.RetEXC IllegalThreadState}, h$ )}
      else if  $M = \text{join}$  then
        {( $\{\text{Join } t\text{-}a, \text{IsInterrupted } t \text{ False}, \text{ThreadJoin } t\text{-}a\}$ ,  $\text{RetVal Unit}, h$ ),
          ( $\{\text{IsInterrupted } t \text{ True}, \text{ClearInterrupt } t, \text{ObsInterrupted } t\}$ ,  $\text{execute.RetEXC InterruptedException}, h$ )}
      else if  $M = \text{interrupt}$  then
        {( $\{\text{ThreadExists } t\text{-}a \text{ True}, \text{WakeUp } t\text{-}a, \text{Interrupt } t\text{-}a, \text{ObsInterrupt } t\text{-}a\}$ ,  $\text{RetVal Unit}, h$ ),
          ( $\{\text{ThreadExists } t\text{-}a \text{ False}\}$ ,  $\text{RetVal Unit}, h$ )}
      else if  $M = \text{isInterrupted}$  then
        {( $\{\text{IsInterrupted } t\text{-}a \text{ False}\}$ ,  $\text{RetVal (Bool False)}, h$ ),
          ( $\{\text{IsInterrupted } t\text{-}a \text{ True}, \text{ObsInterrupted } t\text{-}a\}$ ,  $\text{RetVal (Bool True)}, h$ )}
      else {( $\{\}$ ,  $\text{undefined}$ )}
    else {( $\{\}$ ,  $\text{undefined}$ )}
  by (auto simp add:  $\text{execute.red-external-aggr-def}$ 
    split del:  $\text{option.splits}$ ) auto
end

```

end

```

lemmas [code] =
  heap-execute.heap-copy-loc-code
  heap-execute.heap-copies-code
  heap-execute.heap-clone-code
  heap-execute.red-external-aggr-code

```

end

## 9.8 An optimized JVM

```

theory JVMExec-Execute2
imports
  ../BV/BVNoTypeError
  ExternalCall-Execute

```

**begin**

This JVM must lookup the method declaration of the top call frame at every step to find the next instruction. It is more efficient to refine it such that the instruction list and the exception table are cached in the call frame. Even further, this theory adds keeps track of *drop pc ins*, whose head is the next instruction to execute.

**locale** *JVM-heap-execute* = *heap-execute* +  
**constrains** *addr2thread-id* :: ('addr :: addr)  $\Rightarrow$  'thread-id  
**and** *thread-id2addr* :: 'thread-id  $\Rightarrow$  'addr  
**and** *spurious-wakeups* :: bool  
**and** *empty-heap* :: 'heap  
**and** *allocate* :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set  
**and** *typeof-addr* :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  htype option  
**and** *heap-read* :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val set  
**and** *heap-write* :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap set

**sublocale** *JVM-heap-execute* < *execute*: *JVM-heap-base*  
*addr2thread-id thread-id2addr*  
*spurious-wakeups*  
*empty-heap allocate typeof-addr*  
 $\lambda h \ a \ ad \ v. v \in \text{heap-read } h \ a \ ad \ \lambda h \ a \ ad \ v \ h'. h' \in \text{heap-write } h \ a \ ad \ v$   
.

**type-synonym**

'addr frame' = ('addr instr list  $\times$  'addr instr list  $\times$  ex-table)  $\times$  'addr val list  $\times$  'addr val list  $\times$  cname  
 $\times$  mname  $\times$  pc

**type-synonym**

('addr, 'heap) jvm-state' = 'addr option  $\times$  'heap  $\times$  'addr frame' list

**type-synonym**

'addr jvm-thread-state' = 'addr option  $\times$  'addr frame' list

**type-synonym**

('addr, 'thread-id, 'heap) jvm-thread-action' = ('addr, 'thread-id, 'addr jvm-thread-state', 'heap) Jinja-thread-action

**type-synonym**

('addr, 'thread-id, 'heap) jvm-ta-state' = ('addr, 'thread-id, 'heap) jvm-thread-action'  $\times$  ('addr, 'heap) jvm-state'

**fun** frame'-of-frame :: 'addr jvm-prog  $\Rightarrow$  'addr frame  $\Rightarrow$  'addr frame'

**where**

frame'-of-frame *P* (stk, loc, *C*, *M*, pc) =  
((drop pc (instrs-of *P* *C* *M*), instrs-of *P* *C* *M*, ex-table-of *P* *C* *M*), stk, loc, *C*, *M*, pc)

**fun** jvm-state'-of-jvm-state :: 'addr jvm-prog  $\Rightarrow$  ('addr, 'heap) jvm-state  $\Rightarrow$  ('addr, 'heap) jvm-state'

**where** jvm-state'-of-jvm-state *P* (xcp, h, frs) = (xcp, h, map (frame'-of-frame *P*) frs)

**fun** jvm-thread-state'-of-jvm-thread-state :: 'addr jvm-prog  $\Rightarrow$  'addr jvm-thread-state  $\Rightarrow$  'addr jvm-thread-state'

**where**

jvm-thread-state'-of-jvm-thread-state *P* (xcp, frs) = (xcp, map (frame'-of-frame *P*) frs)

**definition** jvm-thread-action'-of-jvm-thread-action ::

'addr jvm-prog  $\Rightarrow$  ('addr, 'thread-id, 'heap) jvm-thread-action  $\Rightarrow$  ('addr, 'thread-id, 'heap) jvm-thread-action'

**where**

$jvm\text{-}thread\text{-}action'\text{-}of\text{-}jvm\text{-}thread\text{-}action\ P = convert\_extTA\ (jvm\text{-}thread\text{-}state'\text{-}of\text{-}jvm\text{-}thread\text{-}state\ P)$

**fun**  $jvm\text{-}ta\text{-}state'\text{-}of\text{-}jvm\text{-}ta\text{-}state ::$

$'addr\ jvm\text{-}prog \Rightarrow ('addr, 'thread\text{-}id, 'heap)\ jvm\text{-}ta\text{-}state \Rightarrow ('addr, 'thread\text{-}id, 'heap)\ jvm\text{-}ta\text{-}state'$

**where**

$jvm\text{-}ta\text{-}state'\text{-}of\text{-}jvm\text{-}ta\text{-}state\ P\ (ta, s) = (jvm\text{-}thread\text{-}action'\text{-}of\text{-}jvm\text{-}thread\text{-}action\ P\ ta, jvm\text{-}state'\text{-}of\text{-}jvm\text{-}state\ P\ s)$

**abbreviation**  $(input)\ frame\text{-}of\text{-}frame' :: 'addr\ frame' \Rightarrow 'addr\ frame$

**where**  $frame\text{-}of\text{-}frame' \equiv snd$

**definition**  $jvm\text{-}state'\text{-}of\text{-}jvm\text{-}state' :: ('addr, 'heap)\ jvm\text{-}state' \Rightarrow ('addr, 'heap)\ jvm\text{-}state$

**where**  $[simp]:$

$jvm\text{-}state'\text{-}of\text{-}jvm\text{-}state' = map\text{-}prod\ id\ (map\text{-}prod\ id\ (map\ frame\text{-}of\text{-}frame'))$

**definition**  $jvm\text{-}thread\text{-}state'\text{-}of\text{-}jvm\text{-}thread\text{-}state' :: 'addr\ jvm\text{-}thread\text{-}state' \Rightarrow 'addr\ jvm\text{-}thread\text{-}state$

**where**  $[simp]:$

$jvm\text{-}thread\text{-}state'\text{-}of\text{-}jvm\text{-}thread\text{-}state' = map\text{-}prod\ id\ (map\ frame\text{-}of\text{-}frame')$

**definition**  $jvm\text{-}thread\text{-}action'\text{-}of\text{-}jvm\text{-}thread\text{-}action' ::$

$('addr, 'thread\text{-}id, 'heap)\ jvm\text{-}thread\text{-}action' \Rightarrow ('addr, 'thread\text{-}id, 'heap)\ jvm\text{-}thread\text{-}action$

**where**  $[simp]:$

$jvm\text{-}thread\text{-}action'\text{-}of\text{-}jvm\text{-}thread\text{-}action' = convert\_extTA\ jvm\text{-}thread\text{-}state'\text{-}of\text{-}jvm\text{-}thread\text{-}state'$

**definition**  $jvm\text{-}ta\text{-}state'\text{-}of\text{-}jvm\text{-}ta\text{-}state' ::$

$('addr, 'thread\text{-}id, 'heap)\ jvm\text{-}ta\text{-}state' \Rightarrow ('addr, 'thread\text{-}id, 'heap)\ jvm\text{-}ta\text{-}state$

**where**  $[simp]:$

$jvm\text{-}ta\text{-}state'\text{-}of\text{-}jvm\text{-}ta\text{-}state' = map\text{-}prod\ jvm\text{-}thread\text{-}action'\text{-}of\text{-}jvm\text{-}thread\text{-}action'\ jvm\text{-}state'\text{-}of\text{-}jvm\text{-}state'$

**fun**  $frame'\text{-}ok :: 'addr\ jvm\text{-}prog \Rightarrow 'addr\ frame' \Rightarrow bool$

**where**

$frame'\text{-}ok\ P\ ((ins', insxt), stk, loc, C, M, pc) \longleftrightarrow$

$ins' = drop\ pc\ (instrs\text{-}of\ P\ C\ M) \wedge insxt = snd\ (snd\ (the\ (snd\ (snd\ (method\ P\ C\ M))))))$

**lemma**  $frame'\text{-}ok\text{-}frame'\text{-}of\text{-}frame\ [iff]:$

$frame'\text{-}ok\ P\ (frame'\text{-}of\text{-}frame\ P\ f)$

**by**  $(cases\ f)\ (simp)$

**lemma**  $frames'\text{-}ok\text{-}inverse\ [simp]:$

$\forall x \in set\ frs.\ frame'\text{-}ok\ P\ x \implies map\ (frame'\text{-}of\text{-}frame\ P \circ frame\text{-}of\text{-}frame')\ frs = frs$

**by**  $(rule\ map\text{-}idI)\ auto$

**fun**  $jvm\text{-}state'\text{-}ok :: 'addr\ jvm\text{-}prog \Rightarrow ('addr, 'heap)\ jvm\text{-}state' \Rightarrow bool$

**where**  $jvm\text{-}state'\text{-}ok\ P\ (xcp, h, frs) = (\forall f \in set\ frs.\ frame'\text{-}ok\ P\ f)$

**lemma**  $jvm\text{-}state'\text{-}ok\text{-}jvm\text{-}state'\text{-}of\text{-}jvm\text{-}state\ [iff]:$

$jvm\text{-}state'\text{-}ok\ P\ (jvm\text{-}state'\text{-}of\text{-}jvm\text{-}state\ P\ s)$

**by**  $(cases\ s)\ simp$

**fun**  $jvm\text{-}thread\text{-}state'\text{-}ok :: 'addr\ jvm\text{-}prog \Rightarrow 'addr\ jvm\text{-}thread\text{-}state' \Rightarrow bool$

**where**  $jvm\text{-}thread\text{-}state'\text{-}ok\ P\ (xcp, frs) \longleftrightarrow (\forall f \in set\ frs.\ frame'\text{-}ok\ P\ f)$

**lemma**  $jvm\text{-}thread\text{-}state'\text{-}ok\text{-}jvm\text{-}thread\text{-}state'\text{-}of\text{-}jvm\text{-}thread\text{-}state\ [iff]:$

*jvm-thread-state'-ok P (jvm-thread-state'-of-jvm-thread-state P s)*  
**by**(cases s) simp

**definition** *jvm-thread-action'-ok* :: 'addr jvm-prog  $\Rightarrow$  ('addr, 'thread-id, 'heap) jvm-thread-action'  $\Rightarrow$  bool  
**where** *jvm-thread-action'-ok P ta*  $\longleftrightarrow (\forall nt \in \text{set } \{\{ta\}_t. \forall t x h. nt = \text{NewThread } t x h \longrightarrow \text{jvm-thread-state'-ok } P x)$

**lemma** *jvm-thread-action'-ok-jvm-thread-action'-of-jvm-thread-action* [iff]:  
*jvm-thread-action'-ok P (jvm-thread-action'-of-jvm-thread-action P ta)*  
**by**(cases ta)(fastforce dest: sym simp add: jvm-thread-action'-ok-def jvm-thread-action'-of-jvm-thread-action-def)

**lemma** *jvm-thread-action'-ok- $\varepsilon$*  [simp]: *jvm-thread-action'-ok P  $\varepsilon$*   
**by**(simp add: jvm-thread-action'-ok-def)

**fun** *jvm-ta-state'-ok* :: 'addr jvm-prog  $\Rightarrow$  ('addr, 'thread-id, 'heap) jvm-ta-state'  $\Rightarrow$  bool  
**where** *jvm-ta-state'-ok P (ta, s)*  $\longleftrightarrow \text{jvm-thread-action'-ok } P \text{ ta} \wedge \text{jvm-state'-ok } P s$

**lemma** *jvm-ta-state'-ok-jvm-ta-state'-of-jvm-ta-state* [iff]:  
*jvm-ta-state'-ok P (jvm-ta-state'-of-jvm-ta-state P tas)*  
**by**(cases tas)(simp)

**lemma** *frame-of-frame'-inverse* [simp]: *frame-of-frame'  $\circ$  frame'-of-frame P = id*  
**by**(clarsimp simp add: fun-eq-iff)

**lemma** *convert-new-thread-action-frame-of-frame'-inverse* [simp]:  
*convert-new-thread-action (map-prod id (map frame-of-frame'))  $\circ$  convert-new-thread-action (jvm-thread-state'-of-P) = id*  
**by**(auto intro!: convert-new-thread-action-eqI simp add: fun-eq-iff List.map.id)

**primrec** *extRet2JVM'* ::  
'addr instr list  $\Rightarrow$  'addr instr list  $\Rightarrow$  ex-table  
 $\Rightarrow$  nat  $\Rightarrow$  'heap  $\Rightarrow$  'addr val list  $\Rightarrow$  'addr val list  $\Rightarrow$  cname  $\Rightarrow$  mname  $\Rightarrow$  pc  $\Rightarrow$  'addr frame' list  
 $\Rightarrow$  'addr extCallRet  $\Rightarrow$  ('addr, 'heap) jvm-state'  
**where**  
*extRet2JVM' ins' ins xt n h stk loc C M pc frs (RetVal v)* = (None, h, ((tl ins', ins, xt), v # drop (Suc n) stk, loc, C, M, pc + 1) # frs)  
*extRet2JVM' ins' ins xt n h stk loc C M pc frs (RetExc a)* = ([a], h, ((ins', ins, xt), stk, loc, C, M, pc) # frs)  
*extRet2JVM' ins' ins xt n h stk loc C M pc frs RetStaySame* = (None, h, ((ins', ins, xt), stk, loc, C, M, pc) # frs)

**definition** *extNTA2JVM'* :: 'addr jvm-prog  $\Rightarrow$  (cname  $\times$  mname  $\times$  'addr)  $\Rightarrow$  'addr jvm-thread-state'  
**where** *extNTA2JVM' P*  $\equiv (\lambda(C, M, a). \text{let } (D, Ts, T, \text{meth}) = \text{method } P \ C \ M; (mxs, mxl0, ins, xt) = \text{the meth}$   
 $\text{in } (None, [((ins, ins, xt), [], \text{Addr } a \ \# \ \text{replicate } mxl0 \ \text{undefined-value}, D, M, 0)]))$

**abbreviation** *extTA2JVM'* ::  
'addr jvm-prog  $\Rightarrow$  ('addr, 'thread-id, 'heap) external-thread-action  $\Rightarrow$  ('addr, 'thread-id, 'heap) jvm-thread-action'  
**where** *extTA2JVM' P*  $\equiv \text{convert-extTA } (\text{extNTA2JVM'} P)$

**lemma** *jvm-state'-ok-extRet2JVM'* [simp]:  
**assumes** [simp]: *ins = instrs-of P C M xt = ex-table-of P C M  $\forall f \in \text{set frs. frame'-ok } P f$*



**shows**  $jvm\text{-}state'\text{-}ok\ P\ (extRet2JVM'\ (drop\ pc\ ins)\ ins\ xt\ n\ h\ stk\ loc\ C\ M\ pc\ frs\ va)$   
**by**(cases va)(simp-all add: drop-tl drop-Suc)

**lemma**  $jvm\text{-}state'\text{-}of\text{-}jvm\text{-}state\text{-}extRet2JVM\ [simp]:$

**assumes** [simp]:  $ins = instrs\text{-}of\ P\ C\ M\ xt = ex\text{-}table\text{-}of\ P\ C\ M\ \forall f \in set\ frs.\ frame'\text{-}ok\ P\ f$

**shows**

$jvm\text{-}state'\text{-}of\text{-}jvm\text{-}state\ P\ (extRet2JVM\ n\ h'\ stk\ loc\ C\ M\ pc\ (map\ frame\text{-}of\text{-}frame'\ frs)\ va) =$   
 $extRet2JVM'\ (drop\ pc\ (instrs\text{-}of\ P\ C\ M))\ ins\ xt\ n\ h'\ stk\ loc\ C\ M\ pc\ frs\ va$

**by**(cases va)(simp-all add: drop-tl drop-Suc)

**lemma**  $extRet2JVM'\text{-}extRet2JVM\ [simp]:$

$jvm\text{-}state\text{-}of\text{-}jvm\text{-}state'\ (extRet2JVM'\ ins'\ ins\ xt\ n\ h'\ stk\ loc\ C\ M\ pc\ frs\ va) =$

$extRet2JVM\ n\ h'\ stk\ loc\ C\ M\ pc\ (map\ frame\text{-}of\text{-}frame'\ frs)\ va$

**by**(cases va) simp-all

**lemma**  $jvm\text{-}ta\text{-}state'\text{-}ok\text{-}inverse:$

**assumes**  $jvm\text{-}ta\text{-}state'\text{-}ok\ P\ tas$

**shows**  $jvm\text{-}ta\text{-}state\text{-}of\text{-}jvm\text{-}ta\text{-}state'\ tas \in A \longleftrightarrow tas \in jvm\text{-}ta\text{-}state'\text{-}of\text{-}jvm\text{-}ta\text{-}state\ P\ 'A$

**using** assms

**apply**(cases tas)

**apply**(fastforce simp add: o-def jvm-thread-action'\text{-}of\text{-}jvm-thread-action-def jvm-thread-action'\text{-}ok-def  
intro!: map-idI[symmetric] map-idI convert-new-thread-action-eqI dest: bspec intro!: rev-image-eqI elim!:  
rev-iffD1[OF - arg-cong[where  $f=\lambda x. x : A$ ]])

**done**

**context**  $JVM\text{-}heap\text{-}execute\ \mathbf{begin}$

**primrec**  $exec\text{-}instr ::$

$'addr\ instr\ list \Rightarrow 'addr\ instr\ list \Rightarrow ex\text{-}table$

$\Rightarrow 'addr\ instr \Rightarrow 'addr\ jvm\text{-}prog \Rightarrow 'thread\text{-}id \Rightarrow 'heap \Rightarrow 'addr\ val\ list \Rightarrow 'addr\ val\ list$

$\Rightarrow cname \Rightarrow mname \Rightarrow pc \Rightarrow 'addr\ frame'\ list$

$\Rightarrow ((addr, thread\text{-}id, heap)\ jvm\text{-}ta\text{-}state')\ set$

**where**

$exec\text{-}instr\ ins'\ ins\ xt\ (Load\ n)\ P\ t\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$

$\{(\varepsilon, (None, h, ((tl\ ins', ins, xt), (loc\ !\ n)\ \#\ stk, loc, C_0, M_0, pc+1)\ \# frs))\}$

|  $exec\text{-}instr\ ins'\ ins\ xt\ (Store\ n)\ P\ t\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$

$\{(\varepsilon, (None, h, ((tl\ ins', ins, xt), tl\ stk, loc[n:=hd\ stk], C_0, M_0, pc+1)\ \# frs))\}$

|  $exec\text{-}instr\ ins'\ ins\ xt\ (Push\ v)\ P\ t\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$

$\{(\varepsilon, (None, h, ((tl\ ins', ins, xt), v\ \#\ stk, loc, C_0, M_0, pc+1)\ \# frs))\}$

|  $exec\text{-}instr\ ins'\ ins\ xt\ (New\ C)\ P\ t\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$

$(let\ HA = allocate\ h\ (Class\text{-}type\ C)$

$in\ if\ HA = \{\} then\ \{(\varepsilon, [execute.addr\text{-}of\text{-}sys\text{-}xcpt\ OutOfMemory], h, ((ins', ins, xt), stk, loc, C_0, M_0, pc)\ \# frs)\}$

$else\ do\ \{(h', a) \leftarrow HA;$

$\{(\{NewHeapElem\ a\ (Class\text{-}type\ C)\}, None, h', ((tl\ ins', ins, xt), Addr\ a\ \#\ stk, loc, C_0, M_0,$

$pc + 1)\ \# frs)\}\}$

|  $exec\text{-}instr\ ins'\ ins\ xt\ (NewArray\ T)\ P\ t\ h\ stk\ loc\ C_0\ M_0\ pc\ frs =$

$(let\ si = the\text{-}Intg\ (hd\ stk);$

$i = nat\ (sint\ si)$

$in\ if\ si < s\ 0$

$then\ \{(\varepsilon, [execute.addr\text{-}of\text{-}sys\text{-}xcpt\ NegativeArraySize], h, ((ins', ins, xt), stk, loc, C_0, M_0, pc)$

$\# frs)\}$

```

else let HA = allocate h (Array-type T i) in
  if HA = {} then {(\varepsilon, [execute.addr-of-sys-xcpt OutOfMemory], h, ((ins', ins, xt), stk, loc, C0,
M0, pc) # frs)}
  else do { (h', a) \leftarrow HA;
    {(\varepsilon, [NewHeapElem a (Array-type T i)], None, h', ((tl ins', ins, xt), Addr a # tl stk, loc,
C0, M0, pc + 1) # frs)}}
| exec-instr ins' ins xt ALoad P t h stk loc C0 M0 pc frs =
  (let va = hd (tl stk)
  in (if va = Null then {(\varepsilon, [execute.addr-of-sys-xcpt NullPointer], h, ((ins', ins, xt), stk, loc, C0,
M0, pc) # frs)}
  else
    let i = the-Intg (hd stk);
    a = the-Addr va;
    len = alen-of-htype (the (typeof-addr h a))
    in if i < s 0 \vee int len \leq sint i then
      {(\varepsilon, [execute.addr-of-sys-xcpt ArrayIndexOutOfBounds], h, ((ins', ins, xt), stk, loc, C0,
M0, pc) # frs)}
    else do {
      v \leftarrow heap-read h a (ACell (nat (sint i)));
      {(\varepsilon, [ReadMem a (ACell (nat (sint i))) v], None, h, ((tl ins', ins, xt), v # tl (tl stk), loc,
C0, M0, pc + 1) # frs)}
    })))
| exec-instr ins' ins xt AStore P t h stk loc C0 M0 pc frs =
  (let ve = hd stk;
  vi = hd (tl stk);
  va = hd (tl (tl stk))
  in (if va = Null then {(\varepsilon, [execute.addr-of-sys-xcpt NullPointer], h, ((ins', ins, xt), stk, loc, C0,
M0, pc) # frs)}
  else (let i = the-Intg vi;
    idx = nat (sint i);
    a = the-Addr va;
    hT = the (typeof-addr h a);
    T = ty-of-htype hT;
    len = alen-of-htype hT;
    U = the (execute.typeof-h h ve)
    in (if i < s 0 \vee int len \leq sint i then
      {(\varepsilon, [execute.addr-of-sys-xcpt ArrayIndexOutOfBounds], h, ((ins', ins, xt), stk, loc,
C0, M0, pc) # frs)}
    else if P \vdash U \leq the-Array T then
      do {
        h' \leftarrow heap-write h a (ACell idx) ve;
        {(\varepsilon, [WriteMem a (ACell idx) ve], None, h', ((tl ins', ins, xt), tl (tl (tl stk)), loc,
C0, M0, pc+1) # frs)}
      }
    else {(\varepsilon, [execute.addr-of-sys-xcpt ArrayStore], h, ((ins', ins, xt), stk, loc, C0, M0, pc)
# frs))})))
| exec-instr ins' ins xt ALength P t h stk loc C0 M0 pc frs =
  {(\varepsilon, (let va = hd stk
  in if va = Null
    then ([execute.addr-of-sys-xcpt NullPointer], h, ((ins', ins, xt), stk, loc, C0, M0, pc) # frs)
    else (None, h, ((tl ins', ins, xt), Intg (word-of-int (int (alen-of-htype (the (typeof-addr h
(the-Addr va)))))) # tl stk, loc, C0, M0, pc+1) # frs))}
| exec-instr ins' ins xt (Getfield F C) P t h stk loc C0 M0 pc frs =
  (let v = hd stk

```

```

in if  $v = \text{Null}$  then  $\{(\varepsilon, \lfloor \text{execute.addr-of-sys-xcpt NullPointer} \rfloor, h, ((\text{ins}', \text{ins}, \text{xt}), \text{stk}, \text{loc}, C_0, M_0, \text{pc}) \# \text{frs})\}$ 
  else let  $a = \text{the-Addr } v$ 
    in do {
       $v' \leftarrow \text{heap-read } h \ a \ (C\text{Field } C \ F);$ 
       $\{(\lfloor \text{ReadMem } a \ (C\text{Field } C \ F) \ v' \rfloor, \text{None}, h, ((\text{tl } \text{ins}', \text{ins}, \text{xt}), v' \# (\text{tl } \text{stk}), \text{loc}, C_0, M_0, \text{pc} + 1) \# \text{frs})\}$ 
    })
|  $\text{exec-instr ins' ins xt (Putfield } F \ C) \ P \ t \ h \ \text{stk} \ \text{loc} \ C_0 \ M_0 \ \text{pc} \ \text{frs} =$ 
  (let  $v = \text{hd } \text{stk};$ 
     $r = \text{hd } (\text{tl } \text{stk})$ 
    in if  $r = \text{Null}$  then  $\{(\varepsilon, \lfloor \text{execute.addr-of-sys-xcpt NullPointer} \rfloor, h, ((\text{ins}', \text{ins}, \text{xt}), \text{stk}, \text{loc}, C_0, M_0, \text{pc}) \# \text{frs})\}$ 
      else let  $a = \text{the-Addr } r$ 
        in do {
           $h' \leftarrow \text{heap-write } h \ a \ (C\text{Field } C \ F) \ v;$ 
           $\{(\lfloor \text{WriteMem } a \ (C\text{Field } C \ F) \ v \rfloor, \text{None}, h', ((\text{tl } \text{ins}', \text{ins}, \text{xt}), \text{tl } (\text{tl } \text{stk}), \text{loc}, C_0, M_0, \text{pc} + 1) \# \text{frs})\}$ 
        })
|  $\text{exec-instr ins' ins xt (CAS } F \ C) \ P \ t \ h \ \text{stk} \ \text{loc} \ C_0 \ M_0 \ \text{pc} \ \text{frs} =$ 
  (let  $v'' = \text{hd } \text{stk}; v' = \text{hd } (\text{tl } \text{stk}); v = \text{hd } (\text{tl } (\text{tl } \text{stk}))$ 
    in if  $v = \text{Null}$  then  $\{(\varepsilon, \lfloor \text{execute.addr-of-sys-xcpt NullPointer} \rfloor, h, ((\text{ins}', \text{ins}, \text{xt}), \text{stk}, \text{loc}, C_0, M_0, \text{pc}) \# \text{frs})\}$ 
      else let  $a = \text{the-Addr } v$ 
        in do {
           $v''' \leftarrow \text{heap-read } h \ a \ (C\text{Field } C \ F);$ 
          if  $v''' = v'$  then do {
             $h' \leftarrow \text{heap-write } h \ a \ (C\text{Field } C \ F) \ v'';$ 
             $\{(\lfloor \text{ReadMem } a \ (C\text{Field } C \ F) \ v', \text{WriteMem } a \ (C\text{Field } C \ F) \ v'' \rfloor, \text{None}, h', ((\text{tl } \text{ins}', \text{ins}, \text{xt}), \text{Bool True} \# \text{tl } (\text{tl } (\text{tl } \text{stk})), \text{loc}, C_0, M_0, \text{pc} + 1) \# \text{frs})\}$ 
          } else  $\{(\lfloor \text{ReadMem } a \ (C\text{Field } C \ F) \ v''' \rfloor, \text{None}, h, ((\text{tl } \text{ins}', \text{ins}, \text{xt}), \text{Bool False} \# \text{tl } (\text{tl } \text{stk})), \text{loc}, C_0, M_0, \text{pc} + 1) \# \text{frs})\}$ 
        })
|  $\text{exec-instr ins' ins xt (Checkcast } T) \ P \ t \ h \ \text{stk} \ \text{loc} \ C_0 \ M_0 \ \text{pc} \ \text{frs} =$ 
   $\{(\varepsilon, \text{let } U = \text{the } (\text{typeof}_h \ (\text{hd } \text{stk}))$ 
    in if  $P \vdash U \leq T$  then  $(\text{None}, h, ((\text{tl } \text{ins}', \text{ins}, \text{xt}), \text{stk}, \text{loc}, C_0, M_0, \text{pc} + 1) \# \text{frs})$ 
    else  $(\lfloor \text{execute.addr-of-sys-xcpt ClassCast} \rfloor, h, ((\text{ins}', \text{ins}, \text{xt}), \text{stk}, \text{loc}, C_0, M_0, \text{pc}) \# \text{frs}))\}$ 
|  $\text{exec-instr ins' ins xt (Instanceof } T) \ P \ t \ h \ \text{stk} \ \text{loc} \ C_0 \ M_0 \ \text{pc} \ \text{frs} =$ 
   $\{(\varepsilon, \text{None}, h, ((\text{tl } \text{ins}', \text{ins}, \text{xt}), \text{Bool } (\text{hd } \text{stk} \neq \text{Null} \wedge P \vdash \text{the } (\text{typeof}_h \ (\text{hd } \text{stk})) \leq T) \# \text{tl } \text{stk}, \text{loc}, C_0, M_0, \text{pc} + 1) \# \text{frs})\}$ 
|  $\text{exec-instr ins' ins xt (Invoke } M \ n) \ P \ t \ h \ \text{stk} \ \text{loc} \ C_0 \ M_0 \ \text{pc} \ \text{frs} =$ 
  (let  $r = \text{stk} ! \ n$ 
    in (if  $r = \text{Null}$  then  $\{(\varepsilon, \lfloor \text{execute.addr-of-sys-xcpt NullPointer} \rfloor, h, ((\text{ins}', \text{ins}, \text{xt}), \text{stk}, \text{loc}, C_0, M_0, \text{pc}) \# \text{frs})\}$ 
      else (let  $\text{ps} = \text{rev } (\text{take } n \ \text{stk});$ 
         $a = \text{the-Addr } r;$ 
         $T = \text{the } (\text{typeof-addr } h \ a);$ 
         $(D, Ts, T, \text{meth}) = \text{method } P \ (\text{class-type-of } T) \ M$ 
        in case  $\text{meth}$  of
          Native  $\Rightarrow$ 
          do {
             $(\text{ta}, \text{va}, h') \leftarrow \text{red-external-aggr } P \ t \ a \ M \ \text{ps} \ h;$ 
             $\{(\text{extTA2JVM}' \ P \ \text{ta}, \text{extRet2JVM}' \ \text{ins' ins xt } n \ h' \ \text{stk} \ \text{loc} \ C_0 \ M_0 \ \text{pc} \ \text{frs} \ \text{va})\}$ 
          }

```

$$\begin{aligned}
& | \lfloor (m\mathbf{x}s, m\mathbf{x}l_0, ins'', xt'') \rfloor \Rightarrow \\
& \quad \text{let } f' = ((ins'', ins'', xt''), [], [r]@ps@(\text{replicate } m\mathbf{x}l_0 \text{ undefined-value}), D, M, 0) \\
& \quad \text{in } \{(\varepsilon, \text{None}, h, f' \# ((ins', ins, xt), stk, loc, C_0, M_0, pc) \# frs)\}) \\
| \text{exec-instr } ins' \text{ ins } xt \text{ Return } P \text{ t } h \text{ stk}_0 \text{ loc}_0 \text{ } C_0 \text{ } M_0 \text{ pc frs} = \\
& \quad \{(\varepsilon, (\text{if } frs = [] \text{ then } (\text{None}, h, [])) \\
& \quad \text{else} \\
& \quad \quad \text{let } v = \text{hd } stk_0; \\
& \quad \quad ((ins', ins, xt), stk, loc, C, m, pc) = \text{hd } frs; \\
& \quad \quad n = \text{length } (\text{fst } (\text{snd } (\text{method } P \text{ } C_0 \text{ } M_0))) \\
& \quad \quad \text{in } (\text{None}, h, ((\text{tl } ins', ins, xt), v \# (\text{drop } (n+1) \text{ } stk), loc, C, m, pc+1) \# \text{tl } frs))\}) \\
| \text{exec-instr } ins' \text{ ins } xt \text{ Pop } P \text{ t } h \text{ stk loc } C_0 \text{ } M_0 \text{ pc frs} = \\
& \quad \{(\varepsilon, (\text{None}, h, ((\text{tl } ins', ins, xt), \text{tl } stk, loc, C_0, M_0, pc+1) \# frs))\}) \\
| \text{exec-instr } ins' \text{ ins } xt \text{ Dup } P \text{ t } h \text{ stk loc } C_0 \text{ } M_0 \text{ pc frs} = \\
& \quad \{(\varepsilon, (\text{None}, h, ((\text{tl } ins', ins, xt), \text{hd } stk \# stk, loc, C_0, M_0, pc+1) \# frs))\}) \\
| \text{exec-instr } ins' \text{ ins } xt \text{ Swap } P \text{ t } h \text{ stk loc } C_0 \text{ } M_0 \text{ pc frs} = \\
& \quad \{(\varepsilon, (\text{None}, h, ((\text{tl } ins', ins, xt), \text{hd } (\text{tl } stk) \# \text{hd } stk \# \text{tl } (\text{tl } stk), loc, C_0, M_0, pc+1) \# frs))\}) \\
| \text{exec-instr } ins' \text{ ins } xt \text{ (BinOpInstr bop) } P \text{ t } h \text{ stk loc } C_0 \text{ } M_0 \text{ pc frs} = \\
& \quad \{(\varepsilon, \\
& \quad \quad \text{case the (execute.binop bop (hd (tl stk)) (hd stk)) of} \\
& \quad \quad \quad \text{Inl } v \Rightarrow (\text{None}, h, ((\text{tl } ins', ins, xt), v \# \text{tl } (\text{tl } stk), loc, C_0, M_0, pc + 1) \# frs) \\
& \quad \quad \quad | \text{Inr } a \Rightarrow (\text{Some } a, h, ((ins', ins, xt), stk, loc, C_0, M_0, pc) \# frs))\}) \\
| \text{exec-instr } ins' \text{ ins } xt \text{ (IfFalse i) } P \text{ t } h \text{ stk loc } C_0 \text{ } M_0 \text{ pc frs} = \\
& \quad \{(\varepsilon, (\text{let } pc' = \text{if } \text{hd } stk = \text{Bool False} \text{ then } \text{nat}(\text{int } pc+i) \text{ else } pc+1 \\
& \quad \quad \text{in } (\text{None}, h, ((\text{drop } pc' \text{ } ins, ins, xt), \text{tl } stk, loc, C_0, M_0, pc') \# frs))))\}) \\
| \text{exec-instr } ins' \text{ ins } xt \text{ (Goto i) } P \text{ t } h \text{ stk loc } C_0 \text{ } M_0 \text{ pc frs} = \\
& \quad \{ \text{let } pc' = \text{nat}(\text{int } pc+i) \\
& \quad \quad \text{in } (\varepsilon, (\text{None}, h, ((\text{drop } pc' \text{ } ins, ins, xt), stk, loc, C_0, M_0, pc') \# frs)) \} \\
| \text{exec-instr } ins' \text{ ins } xt \text{ ThrowExc } P \text{ t } h \text{ stk loc } C_0 \text{ } M_0 \text{ pc frs} = \\
& \quad \{(\varepsilon, (\text{let } xp' = \text{if } \text{hd } stk = \text{Null} \text{ then } \lfloor \text{execute.addr-of-sys-xcpt NullPointer} \rfloor \text{ else } \lfloor \text{the-Addr}(\text{hd } stk) \rfloor \\
& \quad \quad \text{in } (xp', h, ((ins', ins, xt), stk, loc, C_0, M_0, pc) \# frs))))\}) \\
| \text{exec-instr } ins' \text{ ins } xt \text{ MEnter } P \text{ t } h \text{ stk loc } C_0 \text{ } M_0 \text{ pc frs} = \\
& \quad \{ \text{let } v = \text{hd } stk \\
& \quad \quad \text{in if } v = \text{Null} \\
& \quad \quad \quad \text{then } (\varepsilon, \lfloor \text{execute.addr-of-sys-xcpt NullPointer} \rfloor, h, ((ins', ins, xt), stk, loc, C_0, M_0, pc) \# frs) \\
& \quad \quad \quad \text{else } (\lfloor \text{Lock} \rightarrow \text{the-Addr } v, \text{SyncLock } (\text{the-Addr } v) \rfloor, \text{None}, h, ((\text{tl } ins', ins, xt), \text{tl } stk, loc, C_0, M_0, \\
& \quad \quad \quad pc + 1) \# frs) \} \\
| \text{exec-instr } ins' \text{ ins } xt \text{ MExit } P \text{ t } h \text{ stk loc } C_0 \text{ } M_0 \text{ pc frs} = \\
& \quad (\text{let } v = \text{hd } stk \\
& \quad \quad \text{in if } v = \text{Null} \\
& \quad \quad \quad \text{then } \{(\varepsilon, \lfloor \text{execute.addr-of-sys-xcpt NullPointer} \rfloor, h, ((ins', ins, xt), stk, loc, C_0, M_0, pc) \# frs)\} \\
& \quad \quad \quad \text{else } \{(\lfloor \text{Unlock} \rightarrow \text{the-Addr } v, \text{SyncUnlock } (\text{the-Addr } v) \rfloor, \text{None}, h, ((\text{tl } ins', ins, xt), \text{tl } stk, loc, C_0, \\
& \quad \quad \quad M_0, pc + 1) \# frs), \\
& \quad \quad \quad (\lfloor \text{UnlockFail} \rightarrow \text{the-Addr } v \rfloor, \lfloor \text{execute.addr-of-sys-xcpt IllegalMonitorState} \rfloor, h, ((ins', ins, xt), \\
& \quad \quad \quad stk, loc, C_0, M_0, pc) \# frs)\})
\end{aligned}$$

**fun** *exception-step* :: 'addr jvm-prog  $\Rightarrow$  'addr  $\Rightarrow$  'heap  $\Rightarrow$  'addr frame'  $\Rightarrow$  'addr frame' list  $\Rightarrow$  ('addr, 'heap) jvm-state'

**where**

$$\begin{aligned}
& \text{exception-step } P \text{ a } h ((ins', ins, xt), stk, loc, C, M, pc) \text{ frs} = \\
& \quad (\text{case match-ex-table } P (\text{execute.cname-of } h \text{ a}) \text{ pc } xt \text{ of} \\
& \quad \quad \text{None} \Rightarrow (\lfloor a \rfloor, h, frs) \\
& \quad \quad | \text{Some } (pc', d) \Rightarrow (\text{None}, h, ((\text{drop } pc' \text{ } ins, ins, xt), \text{Addr } a \# \text{drop } (\text{size } stk - d) \text{ } stk, loc, C, \\
& \quad \quad \quad M, pc') \# frs))
\end{aligned}$$

**fun** *exec* :: 'addr jvm-prog  $\Rightarrow$  'thread-id  $\Rightarrow$  ('addr, 'heap) jvm-state'  $\Rightarrow$  ('addr, 'thread-id, 'heap) jvm-ta-state' set

**where**

*exec* *P* *t* (*xcp*, *h*, []) = {}  
| *exec* *P* *t* (*None*, *h*, ((*ins'*, *ins*, *xt*), *stk*, *loc*, *C*, *M*, *pc*) # *frs*) =  
*exec-instr ins' ins xt (hd ins')* *P* *t* *h* *stk* *loc* *C* *M* *pc* *frs*  
| *exec* *P* *t* ([*a*], *h*, *fr* # *frs*) = {( $\varepsilon$ , *exception-step* *P* *a* *h* *fr* *frs*)}

**definition** *exec-1* ::

'addr jvm-prog  $\Rightarrow$  'thread-id  $\Rightarrow$  ('addr, 'heap) jvm-state'  
 $\Rightarrow$  (('addr, 'thread-id, 'heap) jvm-thread-action'  $\times$  ('addr, 'heap) jvm-state') Predicate.pred

**where** *exec-1* *P* *t*  $\sigma$  = pred-of-set (*exec* *P* *t*  $\sigma$ )

**lemma** *check-exec-instr-ok*:

**assumes** *wf*: *wf-prog wf-md* *P*  
**and** *execute.check-instr* *i* *P* *h* *stk* *loc* *C* *M* *pc* (map *frame-of-frame'* *frs*)  
**and** *P*  $\vdash$  *C* *sees* *M*:*Ts* $\rightarrow$ *T* = [*m*] *in* *D*  
**and** *jvm-state'-ok* *P* (*None*, *h*, ((*ins'*, *ins*, *xt*), *stk*, *loc*, *C*, *M*, *pc*) # *frs*)  
**and** *tas*  $\in$  *exec-instr ins' ins xt i* *P* *t* *h* *stk* *loc* *C* *M* *pc* *frs*  
**shows** *jvm-ta-state'-ok* *P* *tas*

**proof** –

**note** [*simp*] = drop-Suc drop-tl split-beta *jvm-thread-action'-ok-def* *has-method-def*

**note** [*split*] = *if-split-asm sum.split*

**from** *assms* **show** ?*thesis*

**proof**(*cases* *i*)

**case** *Return*

**thus** ?*thesis* **using** *assms* **by**(*cases* *frs*) *auto*

**next**

**case** *Invoke*

**thus** ?*thesis* **using** *assms*

**apply**(*cases* *m*)

**apply**(*auto simp add: extNTA2JVM'-def dest: sees-method-idemp execute.red-external-aggr-new-thread-sub-thread sub-Thread-sees-run[OF wf]*)

**apply**(*drule execute.red-external-aggr-new-thread-sub-thread, clarsimp, clarsimp, assumption, clarsimp*)

**apply**(*drule sub-Thread-sees-run[OF wf], clarsimp*)

**apply**(*fastforce dest: sees-method-idemp*)

**apply**(*drule execute.red-external-aggr-new-thread-sub-thread, clarsimp, clarsimp, assumption, clarsimp*)

**apply**(*drule sub-Thread-sees-run[OF wf], clarsimp*)

**apply**(*fastforce dest: sees-method-idemp*)

**done**

**next**

**case** *Goto* **thus** ?*thesis* **using** *assms*

**by**(*cases* *m*) *simp*

**next**

**case** *IfFalse* **thus** ?*thesis* **using** *assms*

**by**(*cases* *m*) *simp*

**qed**(*auto*)

**qed**

**lemma** *check-exec-instr-complete*:

**assumes** *wf*: *wf-prog wf-md* *P*

**and** *execute.check-instr* *i* *P* *h* *stk* *loc* *C* *M* *pc* (map *frame-of-frame'* *frs*)

```

and  $P \vdash C \text{ sees } M:Ts \rightarrow T = \lfloor m \rfloor \text{ in } D$ 
and  $jvm\text{-}state'\text{-}ok\ P\ (None, h, ((ins', ins, xt), stk, loc, C, M, pc) \# frs)$ 
and  $tas \in execute.exec\text{-}instr\ i\ P\ t\ h\ stk\ loc\ C\ M\ pc\ (\text{map}\ frame\text{-}of\text{-}frame'\ frs)$ 
shows  $jvm\text{-}ta\text{-}state'\text{-}of\text{-}jvm\text{-}ta\text{-}state\ P\ tas \in exec\text{-}instr\ ins'\ ins\ xt\ i\ P\ t\ h\ stk\ loc\ C\ M\ pc\ frs$ 
proof –
  note  $[simp] =$ 
     $drop\text{-}Suc\ drop\text{-}tl\ split\text{-}beta\ jvm\text{-}thread\text{-}action'\text{-}ok\text{-}def\ jvm\text{-}thread\text{-}action'\text{-}of\text{-}jvm\text{-}thread\text{-}action\text{-}def$ 
 $has\text{-}method\text{-}def$ 
     $ta\text{-}upd\text{-}simps\ map\text{-}tl$ 
  note  $[split] = if\text{-}split\text{-}asm\ sum.split$ 
  from  $assms$  show  $?thesis$ 
proof (cases  $i$ )
  case  $Return$  thus  $?thesis$  using  $assms$  by (cases  $frs$ ) auto
next
  case  $Goto$  thus  $?thesis$  using  $assms$ 
    by (cases  $m$ )  $simp$ 
next
  case  $IfFalse$  thus  $?thesis$  using  $assms$ 
    by (cases  $m$ )  $simp$ 
next
  case  $Invoke$  thus  $?thesis$  using  $assms$ 
    apply (cases  $m$ )
    apply (auto intro!:  $rev\text{-}be\ I\ convert\text{-}new\text{-}thread\text{-}action\text{-}eq\ I\ simp\ add: extNTA2JVM'\text{-}def\ extNTA2JVM\text{-}def$ 
 $dest: execute.red\text{-}external\text{-}aggr\text{-}new\text{-}thread\text{-}sub\text{-}thread\ sub\text{-}Thread\text{-}sees\text{-}run[OF\ wf]\ sees\text{-}method\text{-}idemp$ )
    apply (drule  $execute.red\text{-}external\text{-}aggr\text{-}new\text{-}thread\text{-}sub\text{-}thread, clarsimp, clarsimp, assumption,$ 
 $clarsimp$ )
    apply (drule  $sub\text{-}Thread\text{-}sees\text{-}run[OF\ wf], clarsimp$ )
    apply (fastforce  $dest: sees\text{-}method\text{-}idemp$ )
    apply (drule  $execute.red\text{-}external\text{-}aggr\text{-}new\text{-}thread\text{-}sub\text{-}thread, clarsimp, clarsimp, assumption,$ 
 $clarsimp$ )
    apply (drule  $sub\text{-}Thread\text{-}sees\text{-}run[OF\ wf], clarsimp$ )
    apply (fastforce  $dest: sees\text{-}method\text{-}idemp$ )
    done
  qed (auto 4 4)
qed

```

**lemma**  $check\text{-}exec\text{-}instr\text{-}refine$ :

```

assumes  $wf: wf\text{-}prog\ wf\text{-}md\ P$ 
and  $execute.check\text{-}instr\ i\ P\ h\ stk\ loc\ C\ M\ pc\ (\text{map}\ frame\text{-}of\text{-}frame'\ frs)$ 
and  $P \vdash C \text{ sees } M:Ts \rightarrow T = \lfloor m \rfloor \text{ in } D$ 
and  $jvm\text{-}state'\text{-}ok\ P\ (None, h, ((ins', ins, xt), stk, loc, C, M, pc) \# frs)$ 
and  $tas \in exec\text{-}instr\ ins'\ ins\ xt\ i\ P\ t\ h\ stk\ loc\ C\ M\ pc\ frs$ 
shows  $tas \in jvm\text{-}ta\text{-}state'\text{-}of\text{-}jvm\text{-}ta\text{-}state\ P\ ' execute.exec\text{-}instr\ i\ P\ t\ h\ stk\ loc\ C\ M\ pc\ (\text{map}\ frame\text{-}of\text{-}frame'\ frs)$ 
proof –
  note  $[simp] =$ 
     $drop\text{-}Suc\ drop\text{-}tl\ split\text{-}beta\ jvm\text{-}thread\text{-}action'\text{-}ok\text{-}def\ jvm\text{-}thread\text{-}action'\text{-}of\text{-}jvm\text{-}thread\text{-}action\text{-}def$ 
 $has\text{-}method\text{-}def$ 
     $ta\text{-}upd\text{-}simps\ map\text{-}tl\ o\text{-}def$ 
  note  $[split] = if\text{-}split\text{-}asm\ sum.split$ 
  from  $assms$  have  $jvm\text{-}ta\text{-}state'\text{-}of\text{-}jvm\text{-}ta\text{-}state'\ tas \in execute.exec\text{-}instr\ i\ P\ t\ h\ stk\ loc\ C\ M\ pc\ (\text{map}\ frame\text{-}of\text{-}frame'\ frs)$ 
proof (cases  $i$ )
  case  $Invoke$  thus  $?thesis$  using  $assms$ 

```

```

  by(fastforce simp add: extNTA2JVM'-def extNTA2JVM-def split-def extRet2JVM'-extRet2JVM[simplified])
next
  case Return thus ?thesis using assms by(auto simp add: neq-Nil-conv)
qed (auto cong del: image-cong-simp)
also from assms have ok': jvm-ta-state'-ok P tas by(rule check-exec-instr-ok)
hence jvm-ta-state-of-jvm-ta-state' tas ∈ execute.exec-instr i P t h stk loc C M pc (map frame-of-frame'
frs) ⟷
  tas ∈ jvm-ta-state'-of-jvm-ta-state P ' execute.exec-instr i P t h stk loc C M pc (map frame-of-frame'
frs)
  by(rule jvm-ta-state'-ok-inverse)
finally show ?thesis .
qed

```

```

lemma exception-step-ok:
  assumes frame'-ok P fr ∀f∈set frs. frame'-ok P f
  shows jvm-state'-ok P (exception-step P a h fr frs)
  and exception-step P a h fr frs = jvm-state'-of-jvm-state P (execute.exception-step P a h (snd fr)
(map frame-of-frame' frs))
using assms
by(cases fr, case-tac the (snd (snd (snd (method P d e))))), clarsimp)+

```

```

lemma exec-step-conv:
  assumes wf-prog wf-md P
  and jvm-state'-ok P s
  and execute.check P (jvm-state-of-jvm-state' s)
  shows exec P t s = jvm-ta-state'-of-jvm-ta-state P ' execute.exec P t (jvm-state-of-jvm-state' s)
using assms
apply(cases s)
apply(rename-tac xcp h frs)
apply(case-tac frs)
  apply(simp)
  apply(case-tac xcp)
  prefer 2
  apply(simp add: jvm-thread-action'-of-jvm-thread-action-def exception-step-ok)
apply(clarsimp simp add: execute.check-def)
apply(rule equalityI)
  apply(clarsimp simp add: has-method-def)
  apply(erule (2) check-exec-instr-refine)
  apply fastforce
  apply(simp add: hd-drop-conv-nth)
  apply(clarsimp simp add: has-method-def)
  apply(drule (2) check-exec-instr-complete)
  apply fastforce
  apply(assumption)
  apply(simp add: hd-drop-conv-nth)
done

```

```

lemma exec-step-ok:
  assumes wf-prog wf-md P
  and jvm-state'-ok P s
  and execute.check P (jvm-state-of-jvm-state' s)
  and tas ∈ exec P t s
  shows jvm-ta-state'-ok P tas
using assms

```

```

apply(cases s)
apply(rename-tac xcp h frs)
apply(case-tac frs)
  apply simp
apply(rename-tac fr frs')
apply(case-tac xcp)
  apply(clarsimp simp add: execute.check-def has-method-def)
  apply(erule (2) check-exec-instr-ok)
  apply fastforce
  apply(simp add: hd-drop-conv-nth)
apply(case-tac fr)
apply(rename-tac cache stk loc C M pc)
apply(case-tac the (snd (snd (snd (method P C M)))))
apply auto
done

```

**end**

```

locale JVM-heap-execute-conf-read = JVM-heap-execute +
  execute: JVM-conf-read
  addr2thread-id thread-id2addr
  spurious-wakeups
  empty-heap allocate typeof-addr
   $\lambda h\ a\ ad\ v. v \in \text{heap-read } h\ a\ ad\ \lambda h\ a\ ad\ v\ h'. h' \in \text{heap-write } h\ a\ ad\ v$ 
  +
  constrains addr2thread-id :: ('addr :: addr)  $\Rightarrow$  'thread-id
  and thread-id2addr :: 'thread-id  $\Rightarrow$  'addr
  and spurious-wakeups :: bool
  and empty-heap :: 'heap
  and allocate :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set
  and typeof-addr :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  htype option
  and heap-read :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val set
  and heap-write :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap set
  and hconf :: 'heap  $\Rightarrow$  bool
  and P :: 'addr jvm-prog
begin

```

**lemma** exec-correct-state:

```

  assumes wt: wf-jvm-prog $\Phi$  P
  and correct: execute.correct-state  $\Phi$  t (jvm-state-of-jvm-state' s)
  and ok: jvm-state'-ok P s
  shows exec P t s = jvm-ta-state'-of-jvm-ta-state P ' execute.exec P t (jvm-state-of-jvm-state' s)
  (is ?thesis1)
  and (ta, s')  $\in$  exec P t s  $\implies$  execute.correct-state  $\Phi$  t (jvm-state-of-jvm-state' s') (is -  $\implies$  ?thesis2)
  and tas  $\in$  exec P t s  $\implies$  jvm-ta-state'-ok P tas

```

**proof** –

```

  from wt obtain wf-md where wf: wf-prog wf-md P by(blast dest: wt-jvm-progD)
  from execute.no-type-error[OF wt correct]
  have check: execute.check P (jvm-state-of-jvm-state' s)
    by(simp add: execute.exec-d-def split: if-split-asm)
  with wf ok show eq: ?thesis1 by(rule exec-step-conv)

```

{ **fix** tas



```

    assume  $tas \in \text{exec } P \ t \ s$ 
    with  $wf \ ok \ check$  show  $jvm\text{-}ta\text{-}state'\text{-}ok \ P \ tas$ 
    by(rule  $\text{exec}\text{-}step\text{-}ok$ ) }
note this[of  $(ta, s')$ ]
moreover
assume  $(ta, s') \in \text{exec } P \ t \ s$ 
moreover
hence  $(ta, s') \in jvm\text{-}ta\text{-}state'\text{-}of\text{-}jvm\text{-}ta\text{-}state \ P \ ' \ \text{execute}.\text{exec } P \ t \ (jvm\text{-}state\text{-}of\text{-}jvm\text{-}state' \ s)$ 
  unfolding  $eq$  by  $simp$ 
ultimately have  $jvm\text{-}ta\text{-}state'\text{-}of\text{-}jvm\text{-}ta\text{-}state' \ (ta, s') \in \text{execute}.\text{exec } P \ t \ (jvm\text{-}state\text{-}of\text{-}jvm\text{-}state' \ s)$ 
  using  $jvm\text{-}ta\text{-}state'\text{-}ok\text{-}inverse$ [of  $P \ (ta, s')$ ] by  $blast$ 
  hence  $\text{execute}.\text{exec}\text{-}1 \ P \ t \ (jvm\text{-}state\text{-}of\text{-}jvm\text{-}state' \ s) \ (jvm\text{-}thread\text{-}action\text{-}of\text{-}jvm\text{-}thread\text{-}action' \ ta)$ 
  (jvm-state-of-jvm-state' s')
  by(simp add:  $\text{execute}.\text{exec}\text{-}1\text{-}iff$ )
  with  $wt \ correct$  show ?thesis2 by(rule  $\text{execute}.\text{BV}\text{-}correct\text{-}1$ )
qed

```

end

```

lemmas [code] =
  JVM-heap-execute.exec-instr.simps
  JVM-heap-execute.exception-step.simps
  JVM-heap-execute.exec.simps
  JVM-heap-execute.exec-1-def

```

end

theory *JVM-Execute2*

imports

```

  SC-Schedulers
  JVMEExec-Execute2
  ../BV/BVProgressThreaded

```

begin

**abbreviation**  $sc\text{-}heap\text{-}read\text{-}cset :: heap \Rightarrow addr \Rightarrow addr\text{-}loc \Rightarrow addr \text{ val } set$   
**where**  $sc\text{-}heap\text{-}read\text{-}cset \ h \ ad \ al \equiv set\text{-}of\text{-}pred \ (sc\text{-}heap\text{-}read\text{-}i\text{-}i\text{-}i\text{-}o \ h \ ad \ al)$

**abbreviation**  $sc\text{-}heap\text{-}write\text{-}cset :: heap \Rightarrow addr \Rightarrow addr\text{-}loc \Rightarrow addr \text{ val } \Rightarrow heap \text{ set}$   
**where**  $sc\text{-}heap\text{-}write\text{-}cset \ h \ ad \ al \ v \equiv set\text{-}of\text{-}pred \ (sc\text{-}heap\text{-}write\text{-}i\text{-}i\text{-}i\text{-}i\text{-}o \ h \ ad \ al \ v)$

**interpretation**  $sc$ :

```

  JVM-heap-execute
  addr2thread-id
  thread-id2addr
  sc-spurious-wakeups
  sc-empty
  sc-allocate  $P$ 
  sc-typeof-addr
  sc-heap-read-cset
  sc-heap-write-cset

```

**rewrites**  $\bigwedge h \ ad \ al \ v. v \in sc\text{-}heap\text{-}read\text{-}cset \ h \ ad \ al \equiv sc\text{-}heap\text{-}read \ h \ ad \ al \ v$   
**and**  $\bigwedge h \ ad \ al \ v \ h'. h' \in sc\text{-}heap\text{-}write\text{-}cset \ h \ ad \ al \ v \equiv sc\text{-}heap\text{-}write \ h \ ad \ al \ v \ h'$   
**for**  $P$

**apply**(*simp-all* *add: eval-sc-heap-read-i-i-i-o eval-sc-heap-write-i-i-i-o*)  
**done**

**interpretation** *sc*:

*JVM-heap-execute-conf-read*

*addr2thread-id*

*thread-id2addr*

*sc-spurious-wakeups*

*sc-empty*

*sc-allocate* *P*

*sc-typeof-addr*

*sc-heap-read-cset*

*sc-heap-write-cset*

*sc-hconf* *P*

*P*

**rewrites**  $\bigwedge h \text{ ad al } v. v \in \text{sc-heap-read-cset } h \text{ ad al} \equiv \text{sc-heap-read } h \text{ ad al } v$

**and**  $\bigwedge h \text{ ad al } v \text{ h'}. h' \in \text{sc-heap-write-cset } h \text{ ad al } v \equiv \text{sc-heap-write } h \text{ ad al } v \text{ h'}$

**for** *P*

**proof** –

**show** *unfolds*:  $\bigwedge h \text{ ad al } v. v \in \text{sc-heap-read-cset } h \text{ ad al} \equiv \text{sc-heap-read } h \text{ ad al } v$

$\bigwedge h \text{ ad al } v \text{ h'}. h' \in \text{sc-heap-write-cset } h \text{ ad al } v \equiv \text{sc-heap-write } h \text{ ad al } v \text{ h'}$

**by**(*simp-all* *add: eval-sc-heap-read-i-i-i-o eval-sc-heap-write-i-i-i-o*)

**show** *JVM-heap-execute-conf-read*

*addr2thread-id thread-id2addr*

*sc-empty* (*sc-allocate* *P*)

*sc-typeof-addr sc-heap-read-cset sc-heap-write-cset*

(*sc-hconf* *P*) *P*

**apply**(*rule JVM-heap-execute-conf-read.intro*)

**apply**(*unfold unfolds*)

**apply**(*unfold-locales*)

**done**

**qed**

**abbreviation** *sc-JVM-start-state* :: *addr jvm-prog*  $\Rightarrow$  *cname*  $\Rightarrow$  *mname*  $\Rightarrow$  *addr val list*  $\Rightarrow$  (*addr,thread-id,addr*  
*jvm-thread-state,heap,addr*) *state*

**where** *sc-JVM-start-state* *P*  $\equiv$  *sc.execute.JVM-start-state* *TYPE(addr jvm-method)* *P P*

**abbreviation** *sc-exec* :: *addr jvm-prog*  $\Rightarrow$  *thread-id*  $\Rightarrow$  (*addr, heap*) *jvm-state'*  $\Rightarrow$  (*addr, thread-id,*  
*heap*) *jvm-ta-state'* *set*

**where** *sc-exec* *P*  $\equiv$  *sc.exec* *TYPE(addr jvm-method)* *P P*

**abbreviation** *sc-execute-mexec* :: *addr jvm-prog*  $\Rightarrow$  *thread-id*  $\Rightarrow$  (*addr jvm-thread-state*  $\times$  *heap*)  
 $\Rightarrow$  (*addr, thread-id, heap*) *jvm-thread-action*  $\Rightarrow$  (*addr jvm-thread-state*  $\times$  *heap*)  $\Rightarrow$  *bool*

**where** *sc-execute-mexec* *P*  $\equiv$  *sc.execute.mexec* *TYPE(addr jvm-method)* *P P*

**fun** *sc-mexec* ::

*addr jvm-prog*  $\Rightarrow$  *thread-id*  $\Rightarrow$  (*addr jvm-thread-state'*  $\times$  *heap*)

$\Rightarrow$  ((*addr, thread-id, heap*) *jvm-thread-action'*  $\times$  *addr jvm-thread-state'*  $\times$  *heap*) *Predicate.pred*

**where**

*sc-mexec* *P t* ((*xcp, frs*), *h*) =

*sc.exec-1* (*TYPE(addr jvm-method)*) *P P t* (*xcp, h, frs*)  $\gg$  ( $\lambda(ta, xcp, h, frs). \text{Predicate.single } (ta,$   
(*xcp, frs*), *h*))

**abbreviation** *sc-jvm-start-state-refine* ::

$addr\ jvm\text{-}prog \Rightarrow cname \Rightarrow mname \Rightarrow addr\ val\ list \Rightarrow$   
 $(addr, thread\text{-}id, heap, (thread\text{-}id, (addr\ jvm\text{-}thread\text{-}state') \times addr\ released\text{-}locks) rbt, (thread\text{-}id, addr\ wait\text{-}set\text{-}status) rbt, thread\text{-}id\ rs)\ state\text{-}refine$

**where**

$sc\text{-}jvm\text{-}start\text{-}state\text{-}refine \equiv$   
 $sc\text{-}start\text{-}state\text{-}refine\ (rm\text{-}empty\ ())\ rm\text{-}update\ (rm\text{-}empty\ ())\ (rs\text{-}empty\ ())\ (\lambda C\ M\ Ts\ T\ (m\!xs, m\!xl0,$   
 $ins, xt)\ vs.\ (None, [((ins, ins, xt), [], Null\ \# vs\ @\ replicate\ m\!xl0\ undefined\text{-}value, C, M, 0)]))$

**fun**  $jvm\text{-}mstate\text{-}of\text{-}jvm\text{-}mstate' ::$

$(addr, thread\text{-}id, addr\ jvm\text{-}thread\text{-}state', heap, addr)\ state \Rightarrow (addr, thread\text{-}id, addr\ jvm\text{-}thread\text{-}state', heap, addr)$   
 $state$

**where**

$jvm\text{-}mstate\text{-}of\text{-}jvm\text{-}mstate'\ (ls, (ts, m), ws) = (ls, (\lambda t.\ map\text{-}option\ (map\text{-}prod\ jvm\text{-}thread\text{-}state\text{-}of\text{-}jvm\text{-}thread\text{-}state'\ id)\ (ts\ t), m), ws)$

**definition**  $sc\text{-}jvm\text{-}state\text{-}invar :: addr\ jvm\text{-}prog \Rightarrow ty_P \Rightarrow (addr, thread\text{-}id, addr\ jvm\text{-}thread\text{-}state', heap, addr)$   
 $state\ set$

**where**

$sc\text{-}jvm\text{-}state\text{-}invar\ P\ \Phi \equiv$   
 $\{s.\ jvm\text{-}mstate\text{-}of\text{-}jvm\text{-}mstate'\ s \in sc.execute.correct\text{-}jvm\text{-}state\ P\ \Phi\} \cap$   
 $\{s.\ ts\text{-}ok\ (\lambda t\ (xcp, frs)\ h.\ jvm\text{-}state'\text{-}ok\ P\ (xcp, h, frs))\ (thr\ s)\ (shr\ s)\}$

**fun**  $JVM\text{-}final' :: 'addr\ jvm\text{-}thread\text{-}state' \Rightarrow bool$

**where**  $JVM\text{-}final'\ (xcp, frs) \longleftrightarrow frs = []$

**lemma**  $shr\text{-}jvm\text{-}mstate\text{-}of\text{-}jvm\text{-}mstate'\ [simp]: shr\ (jvm\text{-}mstate\text{-}of\text{-}jvm\text{-}mstate'\ s) = shr\ s$   
**by**  $(cases\ s)\ clarsimp$

**lemma**  $jvm\text{-}mstate\text{-}of\text{-}jvm\text{-}mstate'\text{-}sc\text{-}start\text{-}state\ [simp]:$

$jvm\text{-}mstate\text{-}of\text{-}jvm\text{-}mstate'$   
 $(sc\text{-}start\text{-}state\ (\lambda C\ M\ Ts\ T\ (m\!xs, m\!xl0, ins, xt)\ vs.\ (None, [((ins, ins, xt), [], Null\ \# vs\ @\ replicate\ m\!xl0\ undefined\text{-}value, C, M, 0)]))\ P\ C\ M\ vs) = sc\text{-}JVM\text{-}start\text{-}state\ P\ C\ M\ vs$   
**by**  $(simp\ add:\ sc.start\text{-}state\text{-}def\ split\text{-}beta\ fun\text{-}eq\text{-}iff)$

**lemma**  $sc\text{-}jvm\text{-}start\text{-}state\text{-}invar:$

**assumes**  $wf\text{-}jvm\text{-}prog_{\Phi}\ P$   
**and**  $sc\text{-}wf\text{-}start\text{-}state\ P\ C\ M\ vs$   
**shows**  $sc\text{-}state\text{-}\alpha\ (sc\text{-}jvm\text{-}start\text{-}state\text{-}refine\ P\ C\ M\ vs) \in sc\text{-}jvm\text{-}state\text{-}invar\ P\ \Phi$

**unfolding**  $sc\text{-}jvm\text{-}state\text{-}invar\text{-}def\ Int\text{-}iff\ mem\text{-}Collect\text{-}eq$

**apply**  $(rule\ conjI)$

**apply**  $(simp\ add:\ sc.execute.correct\text{-}jvm\text{-}state\text{-}initial[OF\ assms])$

**apply**  $(rule\ ts\text{-}okI)$

**using**  $\langle sc\text{-}wf\text{-}start\text{-}state\ P\ C\ M\ vs \rangle$

**apply**  $(auto\ simp\ add:\ sc.start\text{-}state\text{-}def\ split\text{-}beta\ sc\text{-}wf\text{-}start\text{-}state\text{-}iff\ split:\ if\text{-}split\text{-}asm\ dest:\ sees\text{-}method\text{-}idemp)$   
**done**

**lemma**  $invariant3p\text{-}sc\text{-}jvm\text{-}state\text{-}invar:$

**assumes**  $wf\text{-}jvm\text{-}prog_{\Phi}\ P$   
**shows**  $invariant3p\ (multithreaded\text{-}base.redT\ JVM\text{-}final'\ (\lambda t\ xm\ ta\ x'm'. Predicate.eval\ (sc\text{-}mexec\ P\ t\ xm)\ (ta, x'm'))\ convert\text{-}RA)\ (sc\text{-}jvm\text{-}state\text{-}invar\ P\ \Phi)$

**proof**  $(rule\ invariant3pI)$

**fix**  $s\ tl\ s'$

**assume**  $red:\ multithreaded\text{-}base.redT\ JVM\text{-}final'\ (\lambda t\ xm\ ta\ x'm'. Predicate.eval\ (sc\text{-}mexec\ P\ t\ xm)\ (ta, x'm'))\ convert\text{-}RA\ s\ tl\ s'$

```

and invar:  $s \in \text{sc-jvm-state-invar } P \ \Phi$ 
obtain  $t \ ta$  where  $tl = (t, ta)$  by(cases  $tl$ )
from red have  $\text{red}'$ : multithreaded-base.redT JVM-final (sc-execute-mexec  $P$ ) convert-RA (jvm-mstate-of-jvm-mst
 $s$ ) ( $t, \text{jvm-thread-action-of-jvm-thread-action}' \ ta$ ) (jvm-mstate-of-jvm-mstate'  $s'$ )
proof(cases rule: multithreaded-base.redT.cases[consumes 1, case-names normal acquire])
  case (acquire  $s \ t \ x \ ln \ n \ s'$ )
    thus ?thesis using  $tl$  by(cases  $s$ )(auto intro!: multithreaded-base.redT.redT-acquire)
  next
    case (normal  $t \ x \ s \ ta \ x' \ m' \ s'$ )
      obtain  $xcp \ frs$  where  $x: x = (xcp, frs)$  by(cases  $x$ )
      with invar normal  $tl$ 
      have correct: sc.execute.correct-state  $P \ \Phi \ t$  (jvm-state-of-jvm-state' ( $xcp, shr \ s, frs$ ))
        and ok: jvm-state'-ok  $P$  ( $xcp, shr \ s, frs$ )
        apply –
        apply(case-tac [!]  $s$ )
        apply(fastforce simp add: sc-jvm-state-invar-def sc.execute.correct-jvm-state-def dest: ts-okD) +
        done
      note  $eq = \text{sc.exec-correct-state}(1)[OF \ \text{assms this}]$ 
      with normal  $x \ tl$ 
      have sc-execute-mexec  $P \ t$  (jvm-thread-state-of-jvm-thread-state'  $x, shr$  (jvm-mstate-of-jvm-mstate'
 $s$ )) (jvm-thread-action-of-jvm-thread-action'  $ta$ ) (jvm-thread-state-of-jvm-thread-state'  $x', m'$ )
        by(auto simp add: sc.exec-1-def eq jvm-thread-action'-of-jvm-thread-action-def sc.execute.exec-1-iff)
        with normal  $tl$  show ?thesis
        by(cases  $s$ )(fastforce intro!: multithreaded-base.redT.redT-normal simp add: final-thread.actions-ok-iff
fun-eq-iff map-redT-updTs elim: rev-iffD1[OF - thread-oks-ts-change] cond-action-oks-final-change)
      qed
      moreover from invar
      have sc.execute.correct-state-ts  $P \ \Phi$  (thr (jvm-mstate-of-jvm-mstate'  $s$ )) (shr (jvm-mstate-of-jvm-mstate'
 $s$ ))
        and lock-thread-ok (locks (jvm-mstate-of-jvm-mstate'  $s$ )) (thr (jvm-mstate-of-jvm-mstate'  $s$ ))
        by(simp-all add: sc-jvm-state-invar-def sc.execute.correct-jvm-state-def)
      ultimately have sc.execute.correct-state-ts  $P \ \Phi$  (thr (jvm-mstate-of-jvm-mstate'  $s'$ )) (shr (jvm-mstate-of-jvm-mst
 $s'$ ))
        and lock-thread-ok (locks (jvm-mstate-of-jvm-mstate'  $s'$ )) (thr (jvm-mstate-of-jvm-mstate'  $s'$ ))
        by(blast intro: lifting-wf.redT-preserves[OF sc.execute.lifting-wf-correct-state, OF assms] sc.execute.exec-mthr.re)
      hence jvm-mstate-of-jvm-mstate'  $s' \in \text{sc.execute.correct-jvm-state}$   $P \ \Phi$ 
        by(simp add: sc.execute.correct-jvm-state-def)
      moreover from red have ts-ok ( $\lambda t \ (xcp, frs) \ h. \forall f \in \text{set } frs. \text{frame}'\text{-ok } P \ f$ ) (thr  $s'$ ) (shr  $s'$ ) unfolding
 $tl$ 
      proof(cases rule: multithreaded-base.redT.cases[consumes 1, case-names normal acquire])
        case acquire thus ?thesis using invar
          by(fastforce simp add: sc-jvm-state-invar-def intro!: ts-okI dest: ts-okD bspec split: if-split-asm)
        next
          case (normal  $t \ x \ s \ ta \ x' \ m' \ s'$ )
            obtain  $xcp \ frs$  where  $x: x = (xcp, frs)$  by(cases  $x$ )
            with invar normal  $tl$ 
            have correct: sc.execute.correct-state  $P \ \Phi \ t$  (jvm-state-of-jvm-state' ( $xcp, shr \ s, frs$ ))
              and ok: jvm-state'-ok  $P$  ( $xcp, shr \ s, frs$ )
              apply –
              apply(case-tac [!]  $s$ )
              apply(fastforce simp add: sc-jvm-state-invar-def sc.execute.correct-jvm-state-def dest: ts-okD) +
              done
            from normal  $x$  invar show ?thesis
            apply(auto simp add: sc.exec-1-def final-thread.actions-ok-iff jvm-thread-action'-ok-def sc-jvm-state-invar-def)

```

```

apply hypsubst-thin
apply(drule sc.exec-correct-state(3)[OF assms correct ok])
apply(rule ts-okI)
apply(clarsimp split: if-split-asm simp add: jvm-thread-action'-ok-def)
apply(drule (1) bspec)
apply simp
apply(case-tac thr s t)
apply(drule (2) redT-updTs-new-thread)
apply clarsimp
apply(drule (1) bspec)
apply simp
apply(drule (1) bspec)
apply simp
apply(erule thin-rl)
apply(frule (1) redT-updTs-Some)
apply(fastforce dest: ts-okD)
done
qed
ultimately show  $s' \in \text{sc-jvm-state-invar } P \ \Phi$  by(simp add: sc-jvm-state-invar-def)
qed

lemma sc-exec-deterministic:
  assumes wf-jvm-prog $\Phi$  P
  shows multithreaded-base.deterministic JVM-final' ( $\lambda t \ x \ m \ ta \ x' \ m'. \text{Predicate.eval } (\text{sc-mexec } P \ t \ x \ m)$ 
    (ta, x' m')) convert-RA
    (sc-jvm-state-invar P  $\Phi$ )
proof –
  from assms sc-deterministic-heap-ops
  have det: multithreaded-base.deterministic JVM-final' (sc-execute-mexec P) convert-RA {s. sc.execute.correct-state-ts
P  $\Phi$  (thr s) (shr s)}
  by(rule sc.execute.mexec-deterministic)(simp add: sc-spurious-wakeups)
  show ?thesis
proof(rule multithreaded-base.deterministicI)
  fix s t x ta' x' m' ta'' x'' m''
  assume inv: s  $\in$  sc-jvm-state-invar P  $\Phi$ 
  and tst: thr s t = [(x, no-wait-locks)]
  and exec1: Predicate.eval (sc-mexec P t (x, shr s)) (ta', x', m')
  and exec2: Predicate.eval (sc-mexec P t (x, shr s)) (ta'', x'', m'')
  and aok1: final-thread.actions-ok JVM-final' s t ta'
  and aok2: final-thread.actions-ok JVM-final' s t ta''
  obtain xcp frs where x: x = (xcp, frs) by(cases x)
  from inv tst x have correct: sc.execute.correct-state P  $\Phi$  t (jvm-state-of-jvm-state' (xcp, shr s, frs))
  and ok: jvm-state'-ok P (xcp, shr s, frs)
  by(cases s, fastforce simp add: sc-jvm-state-invar-def sc.execute.correct-jvm-state-def dest: ts-okD)+
  note eq = sc.exec-correct-state(1)[OF assms this]

  from exec1 exec2 x
  have sc-execute-mexec P t (jvm-thread-state-of-jvm-thread-state' x, shr (jvm-mstate-of-jvm-mstate'
s)) (jvm-thread-action-of-jvm-thread-action' ta') (jvm-thread-state-of-jvm-thread-state' x', m')
  and sc-execute-mexec P t (jvm-thread-state-of-jvm-thread-state' x, shr (jvm-mstate-of-jvm-mstate'
s)) (jvm-thread-action-of-jvm-thread-action' ta'') (jvm-thread-state-of-jvm-thread-state' x'', m'')
  by(auto simp add: sc.exec-1-def eq jvm-thread-action'-of-jvm-thread-action-def sc.execute.exec-1-iff)
  moreover have thr (jvm-mstate-of-jvm-mstate' s) t = [(jvm-thread-state-of-jvm-thread-state' x,
no-wait-locks)]

```

```

    using tst by(cases s) clarsimp
  moreover have final-thread.actions-ok JVM-final (jvm-mstate-of-jvm-mstate' s) t (jvm-thread-action-of-jvm-thread-action' ta')
    and final-thread.actions-ok JVM-final (jvm-mstate-of-jvm-mstate' s) t (jvm-thread-action-of-jvm-thread-action' ta'')
    using aok1 aok2
    by  $-(\text{case-tac } [!]\ s, \text{auto simp add: final-thread.actions-ok-iff elim: rev-iffD1[OF - thread-oks-ts-change] cond-action-oks-final-change})$ 
  moreover have sc.execute.correct-state-ts P  $\Phi$  (thr (jvm-mstate-of-jvm-mstate' s)) (shr (jvm-mstate-of-jvm-mstate' s))
    using inv
    by(cases s)(auto intro!: ts-okI simp add: sc-jvm-state-invar-def sc.execute.correct-jvm-state-def dest: ts-okD)
  ultimately
  have jvm-thread-action-of-jvm-thread-action' ta' = jvm-thread-action-of-jvm-thread-action' ta''  $\wedge$  jvm-thread-state-of-jvm-thread-state' x' = jvm-thread-state-of-jvm-thread-state' x''  $\wedge$  m' = m''
    by  $-(\text{drule } (4)\ \text{multithreaded-base.deterministicD[OF det], simp-all})$ 
  moreover from exec1 exec2 x
  have (ta', (fst x', m', snd x'))  $\in$  sc-exec P t (xcp, shr s, frs)
    and (ta'', (fst x'', m'', snd x''))  $\in$  sc-exec P t (xcp, shr s, frs)
    by(auto simp add: sc-exec-1-def)
  hence jvm-ta-state'-ok P (ta', (fst x', m', snd x'))
    and jvm-ta-state'-ok P (ta'', (fst x'', m'', snd x''))
    by(blast intro: sc-exec-correct-state[OF assms correct ok])+
  hence ta' = jvm-thread-action'-of-jvm-thread-action P (jvm-thread-action-of-jvm-thread-action' ta')
    and ta'' = jvm-thread-action'-of-jvm-thread-action P (jvm-thread-action-of-jvm-thread-action' ta'')
    and x' = jvm-thread-state'-of-jvm-thread-state P (jvm-thread-state-of-jvm-thread-state' x')
    and x'' = jvm-thread-state'-of-jvm-thread-state P (jvm-thread-state-of-jvm-thread-state' x'')
    apply  $-$ 
    apply(case-tac [!] ta')
    apply(case-tac [!] ta'')
    apply(case-tac [!] x')
    apply(case-tac [!] x'')
    apply(fastforce simp add: jvm-thread-action'-of-jvm-thread-action-def jvm-thread-action'-ok-def intro!: map-idI[symmetric] convert-new-thread-action-eqI dest: bspec)+
  done
  ultimately
  show ta' = ta''  $\wedge$  x' = x''  $\wedge$  m' = m'' by simp
qed(rule invariant3p-sc-jvm-state-invar[OF assms])
qed

```

### 9.8.1 Round-robin scheduler

**interpretation** *JVM-rr*:

*sc-round-robin-base*

*JVM-final' sc-mexec P convert-RA Jinja-output*

for *P*

.

**definition** *sc-rr-JVM-start-state* :: *nat*  $\Rightarrow$  '*m prog*  $\Rightarrow$  *thread-id* *fifo* *round-robin*

where *sc-rr-JVM-start-state n0 P = JVM-rr.round-robin-start n0 (sc-start-tid P)*

**definition** *exec-JVM-rr* ::

*nat*  $\Rightarrow$  *addr jvm-prog*  $\Rightarrow$  *cname*  $\Rightarrow$  *mname*  $\Rightarrow$  *addr val list*  $\Rightarrow$   
 (*thread-id*  $\times$  (*addr, thread-id*) *obs-event list*,  
 (*addr, thread-id*) *locks*  $\times$  ((*thread-id, addr jvm-thread-state'*  $\times$  *addr released-locks*) *RBT.rbt*  $\times$  *heap*)  
 $\times$   
 (*thread-id, addr wait-set-status*) *RBT.rbt*  $\times$  *thread-id rs*) *tl**list*

**where**

*exec-JVM-rr* *n0 P C M vs* = *JVM-rr.exec* *P n0* (*sc-rr-JVM-start-state* *n0 P*) (*sc-jvm-start-state-refine* *P C M vs*)

**interpretation** *JVM-rr*:

*sc-round-robin*  
*JVM-final'* *sc-mexec* *P* *convert-RA* *Jinja-output*  
**for** *P*  
**by**(*unfold-locales*)

**lemma** *JVM-rr*:

**assumes** *wf-jvm-prog* $\Phi$  *P*  
**shows**  
*sc-scheduler*  
*JVM-final'* (*sc-mexec* *P*) *convert-RA*  
 (*JVM-rr.round-robin* *P n0*) (*pick-wakeup-via-sel* ( $\lambda s P. rm-sel s (\lambda(k,v). P k v)$ )) *JVM-rr.round-robin-invar*  
 (*sc-jvm-state-invar* *P*  $\Phi$ )  
**unfolding** *sc-scheduler-def*  
**apply**(*rule JVM-rr.round-robin-scheduler*)  
**apply**(*rule sc-exec-deterministic*[*OF* *assms*])  
**done**

### 9.8.2 Random scheduler

**interpretation** *JVM-rnd*:

*sc-random-scheduler-base*  
*JVM-final'* *sc-mexec* *P* *convert-RA* *Jinja-output*  
**for** *P*  
**.**

**definition** *sc-rnd-JVM-start-state* :: *Random.seed*  $\Rightarrow$  *random-scheduler*

**where** *sc-rnd-JVM-start-state* *seed* = *seed*

**definition** *exec-JVM-rnd* ::

*Random.seed*  $\Rightarrow$  *addr jvm-prog*  $\Rightarrow$  *cname*  $\Rightarrow$  *mname*  $\Rightarrow$  *addr val list*  $\Rightarrow$   
 (*thread-id*  $\times$  (*addr, thread-id*) *obs-event list*,  
 (*addr, thread-id*) *locks*  $\times$  ((*thread-id, addr jvm-thread-state'*  $\times$  *addr released-locks*) *RBT.rbt*  $\times$  *heap*)  
 $\times$   
 (*thread-id, addr wait-set-status*) *RBT.rbt*  $\times$  *thread-id rs*) *tl**list*  
**where** *exec-JVM-rnd* *seed P C M vs* = *JVM-rnd.exec* *P* (*sc-rnd-JVM-start-state* *seed*) (*sc-jvm-start-state-refine* *P C M vs*)

**interpretation** *JVM-rnd*:

*sc-random-scheduler*  
*JVM-final'* *sc-mexec* *P* *convert-RA* *Jinja-output*  
**for** *P*  
**by**(*unfold-locales*)

```

lemma JVM-rnd:
  assumes wf-jvm-progΦ P
  shows
    sc-scheduler
      JVM-final' (sc-mexec P) convert-RA
      (JVM-rnd.random-scheduler P) (pick-wakeup-via-sel ( $\lambda s$  P. rm-sel s ( $\lambda(k,v)$ . P k v))) ( $\lambda-$  True)
      (sc-jvm-state-invar P  $\Phi$ )
  unfolding sc-scheduler-def
  apply(rule JVM-rnd.random-scheduler-scheduler)
  apply(rule sc-exec-deterministic[OF assms])
  done

ML-val  $\langle @\{code\} exec-JVM-rr \rangle$ 

ML-val  $\langle @\{code\} exec-JVM-rnd \rangle$ 

end

```

## 9.9 Code generator setup

```

theory Code-Generation
imports
  J-Execute
  JVM-Execute2
  ../Compiler/Preprocessor
  ../BV/BCVExec
  ../Compiler/Compiler
  Coinductive.Lazy-TLList
  HOL-Library.Code-Cardinality
  HOL-Library.Code-Target-Int
  HOL-Library.Code-Target-Numeral
begin

  Avoid module dependency cycles.

code-identifier
  code-module More-Set  $\rightarrow$  (SML) Set
| code-module Set  $\rightarrow$  (SML) Set
| code-module Complete-Lattices  $\rightarrow$  (SML) Set
| code-module Complete-Partial-Order  $\rightarrow$  (SML) Set

  new code equation for insort-insert-key to avoid module dependency cycle with set.

lemma insort-insert-key-code [code]:
  insort-insert-key f x xs =
    (if List.member (map f xs) (f x) then xs else insort-key f x xs)
by(simp add: insort-insert-key-def List.member-def split del: if-split)

  equations on predicate operations for code inlining

lemma eq-i-o-conv-single: eq-i-o = Predicate.single
by(rule ext)(simp add: Predicate.single-bind eq.equation)

lemma eq-o-i-conv-single: eq-o-i = Predicate.single
by(rule ext)(simp add: Predicate.single-bind eq.equation)

```



**lemma** *sup-case-exp-case-exp-same:*

```

sup-class.sup
  (case-exp cNew cNewArray cCast cInstanceOf cVal cBinOp cVar cLAss cAAss cALen cFAss
  cFAss cCAS cCall cBlock cSync cInSync cSeq cCond cWhile cThrow cTry e)
  (case-exp cNew' cNewArray' cCast' cInstanceOf' cVal' cBinOp' cVar' cLAss' cAAss' cALen'
  cFAss' cFAss' cCAS' cCall' cBlock' cSync' cInSync' cSeq' cCond' cWhile' cThrow' cTry' e)
=
(case e of
  new C ⇒ sup-class.sup (cNew C) (cNew' C)
| newArray T e ⇒ sup-class.sup (cNewArray T e) (cNewArray' T e)
| Cast T e ⇒ sup-class.sup (cCast T e) (cCast' T e)
| InstanceOf e T ⇒ sup-class.sup (cInstanceOf e T) (cInstanceOf' e T)
| Val v ⇒ sup-class.sup (cVal v) (cVal' v)
| BinOp e bop e' ⇒ sup-class.sup (cBinOp e bop e') (cBinOp' e bop e')
| Var V ⇒ sup-class.sup (cVar V) (cVar' V)
| LAss V e ⇒ sup-class.sup (cLAss V e) (cLAss' V e)
| AAss a e ⇒ sup-class.sup (cAAss a e) (cAAss' a e)
| AAss a i e ⇒ sup-class.sup (cAAss a i e) (cAAss' a i e)
| ALen a ⇒ sup-class.sup (cALen a) (cALen' a)
| FAss e F D ⇒ sup-class.sup (cFAss e F D) (cFAss' e F D)
| FAss e F D e' ⇒ sup-class.sup (cFAss e F D e') (cFAss' e F D e')
| CompareAndSwap e D F e' e'' ⇒ sup-class.sup (cCAS e D F e' e'') (cCAS' e D F e' e'')
| Call e M es ⇒ sup-class.sup (cCall e M es) (cCall' e M es)
| Block V T vo e ⇒ sup-class.sup (cBlock V T vo e) (cBlock' V T vo e)
| Synchronized v e e' ⇒ sup-class.sup (cSync v e e') (cSync' v e e')
| InSynchronized v a e ⇒ sup-class.sup (cInSync v a e) (cInSync' v a e)
| Seq e e' ⇒ sup-class.sup (cSeq e e') (cSeq' e e')
| Cond b e e' ⇒ sup-class.sup (cCond b e e') (cCond' b e e')
| While b e ⇒ sup-class.sup (cWhile b e) (cWhile' b e)
| throw e ⇒ sup-class.sup (cThrow e) (cThrow' e)
| TryCatch e C V e' ⇒ sup-class.sup (cTry e C V e') (cTry' e C V e'))
apply(cases e)
apply(simp-all)
done

```

**lemma** *sup-case-exp-case-exp-other:*

```

fixes p :: 'a :: semilattice-sup shows
sup-class.sup
  (case-exp cNew cNewArray cCast cInstanceOf cVal cBinOp cVar cLAss cAAss cALen cFAss
  cFAss cCAS cCall cBlock cSync cInSync cSeq cCond cWhile cThrow cTry e)
  (sup-class.sup (case-exp cNew' cNewArray' cCast' cInstanceOf' cVal' cBinOp' cVar' cLAss' cAAss'
  cAAss' cALen' cFAss' cFAss' cCAS' cCall' cBlock' cSync' cInSync' cSeq' cCond' cWhile' cThrow'
  cTry' e) p) =
sup-class.sup (case e of
  new C ⇒ sup-class.sup (cNew C) (cNew' C)
| newArray T e ⇒ sup-class.sup (cNewArray T e) (cNewArray' T e)
| Cast T e ⇒ sup-class.sup (cCast T e) (cCast' T e)
| InstanceOf e T ⇒ sup-class.sup (cInstanceOf e T) (cInstanceOf' e T)
| Val v ⇒ sup-class.sup (cVal v) (cVal' v)
| BinOp e bop e' ⇒ sup-class.sup (cBinOp e bop e') (cBinOp' e bop e')
| Var V ⇒ sup-class.sup (cVar V) (cVar' V)
| LAss V e ⇒ sup-class.sup (cLAss V e) (cLAss' V e)
| AAss a e ⇒ sup-class.sup (cAAss a e) (cAAss' a e)
| AAss a i e ⇒ sup-class.sup (cAAss a i e) (cAAss' a i e)

```

```

| ALen a ⇒ sup-class.sup (cALen a) (cALen' a)
| FAcc e F D ⇒ sup-class.sup (cFAcc e F D) (cFAcc' e F D)
| FAss e F D e' ⇒ sup-class.sup (cFAss e F D e') (cFAss' e F D e')
| CompareAndSwap e D F e' e'' ⇒ sup-class.sup (cCAS e D F e' e'') (cCAS' e D F e' e'')
| Call e M es ⇒ sup-class.sup (cCall e M es) (cCall' e M es)
| Block V T vo e ⇒ sup-class.sup (cBlock V T vo e) (cBlock' V T vo e)
| Synchronized v e e' ⇒ sup-class.sup (cSync v e e') (cSync' v e e')
| InSynchronized v a e ⇒ sup-class.sup (cInSync v a e) (cInSync' v a e)
| Seq e e' ⇒ sup-class.sup (cSeq e e') (cSeq' e e')
| Cond b e e' ⇒ sup-class.sup (cCond b e e') (cCond' b e e')
| While b e ⇒ sup-class.sup (cWhile b e) (cWhile' b e)
| throw e ⇒ sup-class.sup (cThrow e) (cThrow' e)
| TryCatch e C V e' ⇒ sup-class.sup (cTry e C V e') (cTry' e C V e') p
apply(cases e)
apply(simp-all add: inf-sup-aci sup.assoc)
done

```

**lemma** *sup-bot1*: *sup-class.sup bot a = (a :: 'a :: {semilattice-sup, order-bot})*  
**by**(rule *sup-absorb2*)*auto*

**lemma** *sup-bot2*: *sup-class.sup a bot = (a :: 'a :: {semilattice-sup, order-bot})*  
**by**(rule *sup-absorb1*) *auto*

**lemma** *sup-case-val-case-val-same*:

```

sup-class.sup (case-val cUnit cNull cBool cIntg cAddr v) (case-val cUnit' cNull' cBool' cIntg' cAddr'
v) =
  (case v of
    Unit ⇒ sup-class.sup cUnit cUnit'
  | Null ⇒ sup-class.sup cNull cNull'
  | Bool b ⇒ sup-class.sup (cBool b) (cBool' b)
  | Intg i ⇒ sup-class.sup (cIntg i) (cIntg' i)
  | Addr a ⇒ sup-class.sup (cAddr a) (cAddr' a))
apply(cases v)
apply simp-all
done

```

**lemma** *sup-case-bool-case-bool-same*:

```

sup-class.sup (case-bool t f b) (case-bool t' f' b) =
  (if b then sup-class.sup t t' else sup-class.sup f f')
by simp

```

**lemmas** *predicate-code-inline* [*code-unfold*] =

```

Predicate.single-bind Predicate.bind-single split
eq-i-o-conv-single eq-o-i-conv-single
sup-case-exp-case-exp-same sup-case-exp-case-exp-other unit.case
sup-bot1 sup-bot2 sup-case-val-case-val-same sup-case-bool-case-bool-same

```

**lemma** *op-case-ty-case-ty-same*:

```

f (case-ty cVoid cBoolean cInteger cNT cClass cArray e)
  (case-ty cVoid' cBoolean' cInteger' cNT' cClass' cArray' e) =
  (case e of
    Void ⇒ f cVoid cVoid'
  | Boolean ⇒ f cBoolean cBoolean'
  | Integer ⇒ f cInteger cInteger'

```

```

| NT ⇒ f cNT cNT'
| Class C ⇒ f (cClass C) (cClass' C)
| Array T ⇒ f (cArray T) (cArray' T))
by(simp split: ty.split)

```

**declare** *op-case-ty-case-ty-same*[**where**  $f = \text{sup-class.sup}$ , *code-unfold*]

**lemma** *op-case-bop-case-bop-same*:

```

f (case-bop cEq cNotEq cLessThan cLessOrEqual cGreaterThan cGreaterOrEqual cAdd cSubtract
cMult cDiv cMod cBinAnd cBinOr cBinXor cShiftLeft cShiftRightZeros cShiftRightSigned bop)
(case-bop cEq' cNotEq' cLessThan' cLessOrEqual' cGreaterThan' cGreaterOrEqual' cAdd' cSub-
tract' cMult' cDiv' cMod' cBinAnd' cBinOr' cBinXor' cShiftLeft' cShiftRightZeros' cShiftRightSigned'
bop)
= case-bop (f cEq cEq') (f cNotEq cNotEq') (f cLessThan cLessThan') (f cLessOrEqual cLessOrE-
qual') (f cGreaterThan cGreaterThan') (f cGreaterOrEqual cGreaterOrEqual') (f cAdd cAdd') (f cSub-
tract cSubtract') (f cMult cMult') (f cDiv cDiv') (f cMod cMod') (f cBinAnd cBinAnd') (f cBinOr cBi-
nOr') (f cBinXor cBinXor') (f cShiftLeft cShiftLeft') (f cShiftRightZeros cShiftRightZeros') (f cShiftRight-
Signed cShiftRightSigned') bop
by(simp split: bop.split)

```

**lemma** *sup-case-bop-case-bop-other* [*code-unfold*]:

```

fixes p :: 'a :: semilattice-sup shows
sup-class.sup (case-bop cEq cNotEq cLessThan cLessOrEqual cGreaterThan cGreaterOrEqual cAdd
cSubtract cMult cDiv cMod cBinAnd cBinOr cBinXor cShiftLeft cShiftRightZeros cShiftRightSigned
bop)
(sup-class.sup (case-bop cEq' cNotEq' cLessThan' cLessOrEqual' cGreaterThan' cGreaterOrE-
qual' cAdd' cSubtract' cMult' cDiv' cMod' cBinAnd' cBinOr' cBinXor' cShiftLeft' cShiftRightZeros'
cShiftRightSigned' bop) p)
= sup-class.sup (case-bop (sup-class.sup cEq cEq') (sup-class.sup cNotEq cNotEq') (sup-class.sup
cLessThan cLessThan') (sup-class.sup cLessOrEqual cLessOrEqual') (sup-class.sup cGreaterThan cGreaterThan')
(sup-class.sup cGreaterOrEqual cGreaterOrEqual') (sup-class.sup cAdd cAdd') (sup-class.sup cSub-
tract cSubtract') (sup-class.sup cMult cMult') (sup-class.sup cDiv cDiv') (sup-class.sup cMod cMod')
(sup-class.sup cBinAnd cBinAnd') (sup-class.sup cBinOr cBinOr') (sup-class.sup cBinXor cBinXor')
(sup-class.sup cShiftLeft cShiftLeft') (sup-class.sup cShiftRightZeros cShiftRightZeros') (sup-class.sup
cShiftRightSigned cShiftRightSigned') bop) p
apply(cases bop)
apply(simp-all add: inf-sup-aci sup.assoc)
done

```

**declare** *op-case-bop-case-bop-same*[**where**  $f = \text{sup-class.sup}$ , *code-unfold*]

**end**

**theory** *JVMExec-Execute*

**imports**

*../JVM/JVMExec*  
*ExternalCall-Execute*

**begin**

### 9.9.1 Manual translation of the JVM to use sets instead of predicates

```

locale JVM-heap-execute = heap-execute +
constrains addr2thread-id :: ('addr :: addr) ⇒ 'thread-id

```

**and** *thread-id2addr* :: 'thread-id  $\Rightarrow$  'addr  
**and** *spurious-wakeups* :: bool  
**and** *empty-heap* :: 'heap  
**and** *allocate* :: 'heap  $\Rightarrow$  htype  $\Rightarrow$  ('heap  $\times$  'addr) set  
**and** *typeof-addr* :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  htype option  
**and** *heap-read* :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val set  
**and** *heap-write* :: 'heap  $\Rightarrow$  'addr  $\Rightarrow$  addr-loc  $\Rightarrow$  'addr val  $\Rightarrow$  'heap set

**sublocale** *JVM-heap-execute* < *execute*: *JVM-heap-base*  
*addr2thread-id thread-id2addr*  
*spurious-wakeups*  
*empty-heap allocate typeof-addr*  
 $\lambda h \ a \ ad \ v. v \in \text{heap-read } h \ a \ ad \ \lambda h \ a \ ad \ v \ h'. h' \in \text{heap-write } h \ a \ ad \ v$   
 .

**context** *JVM-heap-execute* **begin**

**definition** *exec-instr* ::

'addr instr  $\Rightarrow$  'addr jvm-prog  $\Rightarrow$  'thread-id  $\Rightarrow$  'heap  $\Rightarrow$  'addr val list  $\Rightarrow$  'addr val list  
 $\Rightarrow$  cname  $\Rightarrow$  mname  $\Rightarrow$  pc  $\Rightarrow$  'addr frame list  
 $\Rightarrow$  (('addr, 'thread-id, 'heap) jvm-thread-action  $\times$  ('addr, 'heap) jvm-state) set

**where** [simp]: *exec-instr* = *execute.exec-instr*

**lemma** *exec-instr-code* [code]:

*exec-instr* (Load *n*) *P t h stk loc C<sub>0</sub> M<sub>0</sub> pc frs* =  
 {( $\varepsilon$ , (None, *h*, ((loc ! *n*) # stk, loc, C<sub>0</sub>, M<sub>0</sub>, pc+1)#frs))}  
*exec-instr* (Store *n*) *P t h stk loc C<sub>0</sub> M<sub>0</sub> pc frs* =  
 {( $\varepsilon$ , (None, *h*, (tl stk, loc[n:=hd stk], C<sub>0</sub>, M<sub>0</sub>, pc+1)#frs))}  
*exec-instr* (Push *v*) *P t h stk loc C<sub>0</sub> M<sub>0</sub> pc frs* =  
 {( $\varepsilon$ , (None, *h*, (v # stk, loc, C<sub>0</sub>, M<sub>0</sub>, pc+1)#frs))}  
*exec-instr* (New *C*) *P t h stk loc C<sub>0</sub> M<sub>0</sub> pc frs* =  
 (let *HA* = *allocate h (Class-type C)* in  
 if *HA* = {} then {( $\varepsilon$ , [execute.addr-of-sys-xcpt OutOfMemory], *h*, (stk, loc, C<sub>0</sub>, M<sub>0</sub>, pc) # frs)}  
 else do { (*h'*, *a*)  $\leftarrow$  *HA*; {( $\varepsilon$ , [NewHeapElem *a* (Class-type C)], None, *h'*, (Addr *a* # stk, loc, C<sub>0</sub>, M<sub>0</sub>, pc + 1)#frs)} } )  
*exec-instr* (NewArray *T*) *P t h stk loc C<sub>0</sub> M<sub>0</sub> pc frs* =  
 (let *si* = the-Intg (hd stk);  
*i* = nat (sint *si*)  
 in if *si* < *s* 0  
 then {( $\varepsilon$ , [execute.addr-of-sys-xcpt NegativeArraySize], *h*, (stk, loc, C<sub>0</sub>, M<sub>0</sub>, pc) # frs)}  
 else let *HA* = *allocate h (Array-type T i)* in  
 if *HA* = {} then {( $\varepsilon$ , [execute.addr-of-sys-xcpt OutOfMemory], *h*, (stk, loc, C<sub>0</sub>, M<sub>0</sub>, pc) # frs)}  
 else do { (*h'*, *a*)  $\leftarrow$  *HA*; {( $\varepsilon$ , [NewHeapElem *a* (Array-type T i)], None, *h'*, (Addr *a* # tl stk, loc, C<sub>0</sub>, M<sub>0</sub>, pc + 1) # frs)} } )  
*exec-instr* ALoad *P t h stk loc C<sub>0</sub> M<sub>0</sub> pc frs* =  
 (let *va* = hd (tl stk)  
 in (if *va* = Null then {( $\varepsilon$ , [execute.addr-of-sys-xcpt NullPointer], *h*, (stk, loc, C<sub>0</sub>, M<sub>0</sub>, pc) # frs)}  
 else  
 let *i* = the-Intg (hd stk);  
*a* = the-Addr *va*;  
*len* = alen-of-htype (the (typeof-addr *h a*))  
 in if *i* < *s* 0  $\vee$  int *len*  $\leq$  sint *i* then  
 {( $\varepsilon$ , [execute.addr-of-sys-xcpt ArrayIndexOutOfBounds], *h*, (stk, loc, C<sub>0</sub>, M<sub>0</sub>, pc) # frs)}

```

else do {
  v ← heap-read h a (ACell (nat (sint i)));
  {(⌈ReadMem a (ACell (nat (sint i))) v⌋, None, h, (v # tl (tl stk), loc, C0, M0, pc +
1) # frs)}
})))
exec-instr AStore P t h stk loc C0 M0 pc frs =
(let ve = hd stk;
 vi = hd (tl stk);
 va = hd (tl (tl stk))
 in (if va = Null then {(ε, ⌊execute.addr-of-sys-xcpt NullPointer⌋, h, (stk, loc, C0, M0, pc) # frs)}
 else (let i = the-Intg vi;
 idx = nat (sint i);
 a = the-Addr va;
 hT = the (typeof-addr h a);
 T = ty-of-htype hT;
 len = alen-of-htype hT;
 U = the (execute.typeof-h h ve)
 in (if i < s 0 ∨ int len ≤ sint i then
 {(ε, ⌊execute.addr-of-sys-xcpt ArrayIndexOutOfBounds⌋, h, (stk, loc, C0, M0, pc) #
frs)}
 else if P ⊢ U ≤ the-Array T then
 do {
 h' ← heap-write h a (ACell idx) ve;
 {(⌈WriteMem a (ACell idx) ve⌋, None, h', (tl (tl (tl stk)), loc, C0, M0, pc+1) #
frs)}
}
 else {(ε, (⌊execute.addr-of-sys-xcpt ArrayStore⌋, h, (stk, loc, C0, M0, pc) # frs)}))))))
exec-instr ALength P t h stk loc C0 M0 pc frs =
{(ε, (let va = hd stk
 in if va = Null
 then (⌊execute.addr-of-sys-xcpt NullPointer⌋, h, (stk, loc, C0, M0, pc) # frs)
 else (None, h, (Intg (word-of-int (int (alen-of-htype (the (typeof-addr h (the-Addr va)))))) #
tl stk, loc, C0, M0, pc+1) # frs))})}
exec-instr (Getfield F C) P t h stk loc C0 M0 pc frs =
(let v = hd stk
 in if v = Null then {(ε, ⌊execute.addr-of-sys-xcpt NullPointer⌋, h, (stk, loc, C0, M0, pc) # frs)}
 else let a = the-Addr v
 in do {
 v' ← heap-read h a (CField C F);
 {(⌈ReadMem a (CField C F) v'⌋, None, h, (v' # (tl stk), loc, C0, M0, pc + 1) # frs)}
})}
exec-instr (Putfield F C) P t h stk loc C0 M0 pc frs =
(let v = hd stk;
 r = hd (tl stk)
 in if r = Null then {(ε, ⌊execute.addr-of-sys-xcpt NullPointer⌋, h, (stk, loc, C0, M0, pc) # frs)}
 else let a = the-Addr r
 in do {
 h' ← heap-write h a (CField C F) v;
 {(⌈WriteMem a (CField C F) v⌋, None, h', (tl (tl stk), loc, C0, M0, pc + 1) # frs)}
})}
exec-instr (Checkcast T) P t h stk loc C0 M0 pc frs =
{(ε, let U = the (typeofh (hd stk))
 in if P ⊢ U ≤ T then (None, h, (stk, loc, C0, M0, pc + 1) # frs)
 else (⌊execute.addr-of-sys-xcpt ClassCast⌋, h, (stk, loc, C0, M0, pc) # frs))}

```

```

exec-instr (Instanceof T) P t h stk loc C0 M0 pc frs =
  {(ε, None, h, (Bool (hd stk ≠ Null ∧ P ⊢ the (typeofh (hd stk)) ≤ T) # tl stk, loc, C0, M0, pc + 1) # frs)}
exec-instr (Invoke M n) P t h stk loc C0 M0 pc frs =
  (let r = stk ! n
   in (if r = Null then {(ε, [execute.addr-of-sys-xcpt NullPointer], h, (stk, loc, C0, M0, pc) # frs)}
      else (let ps = rev (take n stk);
              a = the-Addr r;
              T = the (typeof-addr h a);
              (D, M', Ts, meth) = method P (class-type-of T) M
              in case meth of
                Native ⇒
                  do {
                    (ta, va, h') ← red-external-aggr P t a M ps h;
                    {(extTA2JVM P ta, extRet2JVM n h' stk loc C0 M0 pc frs va)}
                  }
                | [(mxs, mxl0, ins, xt)] ⇒
                  let f' = ([], [r]@ps@(replicate mxl0 undefined-value), D, M, 0)
                  in {(ε, None, h, f' # (stk, loc, C0, M0, pc) # frs)})))
exec-instr Return P t h stk0 loc0 C0 M0 pc frs =
  {(ε, (if frs=[] then (None, h, []))
   else
    let v = hd stk0;
    (stk, loc, C, m, pc) = hd frs;
    n = length (fst (snd (method P C0 M0)))
    in (None, h, (v#(drop (n+1) stk), loc, C, m, pc+1) # tl frs))}
exec-instr Pop P t h stk loc C0 M0 pc frs = {(ε, (None, h, (tl stk, loc, C0, M0, pc+1) # frs))}
exec-instr Dup P t h stk loc C0 M0 pc frs = {(ε, (None, h, (hd stk # stk, loc, C0, M0, pc+1) # frs))}
exec-instr Swap P t h stk loc C0 M0 pc frs = {(ε, (None, h, (hd (tl stk) # hd stk # tl (tl stk), loc, C0, M0, pc+1) # frs))}
exec-instr (BinOpInstr bop) P t h stk loc C0 M0 pc frs =
  {(ε,
   case the (execute.binop bop (hd (tl stk)) (hd stk)) of
     Inl v ⇒ (None, h, (v # tl (tl stk), loc, C0, M0, pc + 1) # frs)
     | Inr a ⇒ (Some a, h, (stk, loc, C0, M0, pc) # frs))}
exec-instr (IfFalse i) P t h stk loc C0 M0 pc frs =
  {(ε, (let pc' = if hd stk = Bool False then nat(int pc+i) else pc+1
        in (None, h, (tl stk, loc, C0, M0, pc') # frs))}
exec-instr (Goto i) P t h stk loc C0 M0 pc frs = {(ε, (None, h, (stk, loc, C0, M0, nat(int pc+i)) # frs))}
exec-instr ThrowExc P t h stk loc C0 M0 pc frs =
  {(ε, (let xp' = if hd stk = Null then [execute.addr-of-sys-xcpt NullPointer] else [the-Addr(hd stk)]
        in (xp', h, (stk, loc, C0, M0, pc) # frs))}
exec-instr MEnter P t h stk loc C0 M0 pc frs =
  {(let v = hd stk
   in if v = Null
    then (ε, [execute.addr-of-sys-xcpt NullPointer], h, (stk, loc, C0, M0, pc) # frs)
    else ({Lock → the-Addr v, SyncLock (the-Addr v)}, None, h, (tl stk, loc, C0, M0, pc + 1) # frs))}
exec-instr MExit P t h stk loc C0 M0 pc frs =
  (let v = hd stk
   in if v = Null
    then {(ε, [execute.addr-of-sys-xcpt NullPointer], h, (stk, loc, C0, M0, pc) # frs)}
    else ({Unlock → the-Addr v, SyncUnlock (the-Addr v)}, None, h, (tl stk, loc, C0, M0, pc + 1) # frs),
  # frs),

```

( $\llbracket \text{UnlockFail} \rightarrow \text{the-Addr } v \rrbracket$ ,  $\llbracket \text{execute.addr-of-sys-xcpt IllegalMonitorState} \rrbracket$ ,  $h$ ,  $(\text{stk}, \text{loc}, C_0, M_0, \text{pc}) \# \text{frs}\}$ )  
**by**(*auto* 4 4 *intro*: *rev-bexI*)

**definition** *exec* :: '*addr jvm-prog*  $\Rightarrow$  '*thread-id*  $\Rightarrow$  ('*addr*, '*heap*) *jvm-state*  $\Rightarrow$  ('*addr*, '*thread-id*, '*heap*)  
*jvm-ta-state set*  
**where** *exec* = *execute.exec*

**lemma** *exec-code*:  
*exec P t (xcp, h, [])* = {}  
*exec P t (None, h, (stk, loc, C, M, pc) # frs)* = *exec-instr (instrs-of P C M ! pc) P t h stk loc C M*  
*pc frs*  
*exec P t ([a], h, fr # frs)* = {( $\varepsilon$ , *execute.exception-step P a h fr frs*)}  
**by**(*simp-all add: exec-def*)

**definition** *exec-1* ::  
'*addr jvm-prog*  $\Rightarrow$  '*thread-id*  $\Rightarrow$  ('*addr*, '*heap*) *jvm-state*  
 $\Rightarrow$  (('*addr*, '*thread-id*, '*heap*) *jvm-thread-action*  $\times$  ('*addr*, '*heap*) *jvm-state*) *Predicate.pred*  
**where** *exec-1 P t  $\sigma$*  = *pred-of-set (exec P t  $\sigma$ )*

**lemma** *exec-1I*: *execute.exec-1 P t  $\sigma$  ta  $\sigma'$*   $\Longrightarrow$  *Predicate.eval (exec-1 P t  $\sigma$ ) (ta,  $\sigma'$ )*  
**by**(*erule execute.exec-1.cases*)(*simp add: exec-1-def exec-def*)

**lemma** *exec-1E*:  
**assumes** *Predicate.eval (exec-1 P t  $\sigma$ ) (ta,  $\sigma'$ )*  
**obtains** *execute.exec-1 P t  $\sigma$  ta  $\sigma'$*   
**using** *assms*  
**by**(*auto simp add: exec-1-def exec-def intro: execute.exec-1.intros*)

**lemma** *exec-1-eq* [*simp*]:  
*Predicate.eval (exec-1 P t  $\sigma$ ) (ta,  $\sigma'$ )*  $\longleftrightarrow$  *execute.exec-1 P t  $\sigma$  ta  $\sigma'$*   
**by**(*auto intro: exec-1I elim: exec-1E*)

**lemma** *exec-1-eq'*:  
*Predicate.eval (exec-1 P t  $\sigma$ )* = ( $\lambda(ta, \sigma').$  *execute.exec-1 P t  $\sigma$  ta  $\sigma'$* )  
**by**(*rule ext*)(*simp split: prod.split*)

**end**

**lemmas** [*code*] =  
*JVM-heap-execute.exec-instr-code*  
*JVM-heap-base.exception-step.simps*  
*JVM-heap-execute.exec-code*  
*JVM-heap-execute.exec-1-def*

**end**

**theory** *JVM-Execute*  
**imports**  
*SC-Schedulers*  
*JVMExec-Execute*  
*../BV/BVProgressThreaded*  
**begin**

**abbreviation**  $sc\text{-heap-read-cset} :: \text{heap} \Rightarrow \text{addr} \Rightarrow \text{addr-loc} \Rightarrow \text{addr val set}$   
**where**  $sc\text{-heap-read-cset } h \text{ ad al} \equiv \text{set-of-pred } (sc\text{-heap-read-i-i-i-o } h \text{ ad al})$

**abbreviation**  $sc\text{-heap-write-cset} :: \text{heap} \Rightarrow \text{addr} \Rightarrow \text{addr-loc} \Rightarrow \text{addr val} \Rightarrow \text{heap set}$   
**where**  $sc\text{-heap-write-cset } h \text{ ad al } v \equiv \text{set-of-pred } (sc\text{-heap-write-i-i-i-i-o } h \text{ ad al } v)$

**interpretation**  $sc$ :

$JVM\text{-heap-execute}$   
 $addr2thread\text{-id}$   
 $thread\text{-id}2addr$   
 $sc\text{-spurious-wakeups}$   
 $sc\text{-empty}$   
 $sc\text{-allocate } P$   
 $sc\text{-typeof-addr}$   
 $sc\text{-heap-read-cset}$   
 $sc\text{-heap-write-cset}$

**rewrites**  $\bigwedge h \text{ ad al } v. v \in sc\text{-heap-read-cset } h \text{ ad al} \equiv sc\text{-heap-read } h \text{ ad al } v$   
**and**  $\bigwedge h \text{ ad al } v \text{ h'}. h' \in sc\text{-heap-write-cset } h \text{ ad al } v \equiv sc\text{-heap-write } h \text{ ad al } v \text{ h'}$   
**for**  $P$

**apply**( $\text{simp-all add: eval-}sc\text{-heap-read-i-i-i-o eval-}sc\text{-heap-write-i-i-i-i-o}$ )  
**done**

**interpretation**  $sc\text{-execute}$ :

$JVM\text{-conf-read}$   
 $addr2thread\text{-id}$   
 $thread\text{-id}2addr$   
 $sc\text{-spurious-wakeups}$   
 $sc\text{-empty}$   
 $sc\text{-allocate } P$   
 $sc\text{-typeof-addr}$   
 $sc\text{-heap-read}$   
 $sc\text{-heap-write}$   
 $sc\text{-hconf } P$

**for**  $P$

**by**( $\text{unfold-locales}$ )

**fun**  $sc\text{-mexec} ::$

$addr \text{ jvm-prog} \Rightarrow thread\text{-id} \Rightarrow (addr \text{ jvm-thread-state} \times \text{heap})$   
 $\Rightarrow ((addr, thread\text{-id}, \text{heap}) \text{ jvm-thread-action} \times addr \text{ jvm-thread-state} \times \text{heap}) \text{ Predicate.pred}$

**where**

$sc\text{-mexec } P \text{ t } ((xcp, frs), h) =$   
 $sc.\text{exec-1 } (TYPE(addr \text{ jvm-method})) P P \text{ t } (xcp, h, frs) \gg (\lambda(ta, xcp, h, frs). \text{Predicate.single } (ta,$   
 $(xcp, frs), h))$

**abbreviation**  $sc\text{-jvm-start-state-refine} ::$

$addr \text{ jvm-prog} \Rightarrow cname \Rightarrow mname \Rightarrow addr \text{ val list} \Rightarrow$   
 $(addr, thread\text{-id}, \text{heap}, (thread\text{-id}, (addr \text{ jvm-thread-state}) \times addr \text{ released-locks}) \text{ rm}, (thread\text{-id}, addr$   
 $\text{wait-set-status}) \text{ rm}, thread\text{-id } rs) \text{ state-refine}$

**where**

$sc\text{-jvm-start-state-refine} \equiv$   
 $sc\text{-start-state-refine } (rm\text{-empty } ()) \text{ rm-update } (rm\text{-empty } ()) (rs\text{-empty } ()) (\lambda C M Ts T (mxs, mxl0,$   
 $b) \text{ vs. } (None, [(), Null \# \text{vs} @ \text{replicate mxl0 undefined-value, } C, M, 0]))$



**abbreviation**  $sc\text{-}jvm\text{-}state\text{-}invar :: addr\ jvm\text{-}prog \Rightarrow typ \Rightarrow (addr, thread\text{-}id, addr\ jvm\text{-}thread\text{-}state, heap, addr)$   
 $state\ set$

**where**  $sc\text{-}jvm\text{-}state\text{-}invar\ P\ \Phi \equiv \{s. sc\text{-}execute.correct\text{-}state\text{-}ts\ P\ \Phi\ (thr\ s)\ (shr\ s)\}$

**lemma**  $eval\text{-}sc\text{-}mexec$ :

$(\lambda t\ xm\ ta\ x'm'. Predicate.eval\ (sc\text{-}mexec\ P\ t\ xm)\ (ta, x'm')) =$   
 $(\lambda t\ ((xcp, frs), h)\ ta\ ((xcp', frs'), h'). sc.execute.exec\text{-}1\ (TYPE(addr\ jvm\text{-}method))\ P\ P\ t\ (xcp, h,$   
 $frs)\ ta\ (xcp', h', frs'))$

**by**( $rule\ ext$ )+(fastforce intro!: SUP1-I simp add: sc.exec-1-eq')

**lemma**  $sc\text{-}jvm\text{-}start\text{-}state\text{-}invar$ :

**assumes**  $wf\text{-}jvm\text{-}prog_{\Phi}\ P$

**and**  $sc\text{-}wf\text{-}start\text{-}state\ P\ C\ M\ vs$

**shows**  $sc\text{-}state\text{-}\alpha\ (sc\text{-}jvm\text{-}start\text{-}state\text{-}refine\ P\ C\ M\ vs) \in sc\text{-}jvm\text{-}state\text{-}invar\ P\ \Phi$

**using**  $sc\text{-}execute.correct\text{-}jvm\text{-}state\text{-}initial[OF\ assms]$

**by**(simp add: sc.execute.correct-jvm-state-def)

## 9.9.2 Round-robin scheduler

**interpretation**  $JVM\text{-}rr$ :

$sc\text{-}round\text{-}robin\text{-}base$

$JVM\text{-}final\ sc\text{-}mexec\ P\ convert\text{-}RA\ Jinja\text{-}output$

**for**  $P$

.

**definition**  $sc\text{-}rr\text{-}JVM\text{-}start\text{-}state :: nat \Rightarrow 'm\ prog \Rightarrow thread\text{-}id\ fifo\ round\text{-}robin$

**where**  $sc\text{-}rr\text{-}JVM\text{-}start\text{-}state\ n0\ P = JVM\text{-}rr.round\text{-}robin\text{-}start\ n0\ (sc\text{-}start\text{-}tid\ P)$

**definition**  $exec\text{-}JVM\text{-}rr ::$

$nat \Rightarrow addr\ jvm\text{-}prog \Rightarrow cname \Rightarrow mname \Rightarrow addr\ val\ list \Rightarrow$

$(thread\text{-}id \times (addr, thread\text{-}id)\ obs\text{-}event\ list,$

$(addr, thread\text{-}id)\ locks \times ((thread\text{-}id, addr\ jvm\text{-}thread\text{-}state \times addr\ released\text{-}locks)\ rm \times heap) \times$

$(thread\text{-}id, addr\ wait\text{-}set\text{-}status)\ rm \times thread\text{-}id\ rs)\ tl\ list$

**where**

$exec\text{-}JVM\text{-}rr\ n0\ P\ C\ M\ vs = JVM\text{-}rr.exec\ P\ n0\ (sc\text{-}rr\text{-}JVM\text{-}start\text{-}state\ n0\ P)\ (sc\text{-}jvm\text{-}start\text{-}state\text{-}refine\ P\ C\ M\ vs)$

**interpretation**  $JVM\text{-}rr$ :

$sc\text{-}round\text{-}robin$

$JVM\text{-}final\ sc\text{-}mexec\ P\ convert\text{-}RA\ Jinja\text{-}output$

**for**  $P$

**by**( $unfold\text{-}locales$ )

**lemma**  $JVM\text{-}rr$ :

**assumes**  $wf\text{-}jvm\text{-}prog_{\Phi}\ P$

**shows**

$sc\text{-}scheduler$

$JVM\text{-}final\ (sc\text{-}mexec\ P)\ convert\text{-}RA$

$(JVM\text{-}rr.round\text{-}robin\ P\ n0)\ (pick\text{-}wake\text{-}up\text{-}via\text{-}sel\ (\lambda s\ P. rm\text{-}sel\ s\ (\lambda(k,v). P\ k\ v)))\ JVM\text{-}rr.round\text{-}robin\text{-}invar$

$(sc\text{-}jvm\text{-}state\text{-}invar\ P\ \Phi)$

**unfolding**  $sc\text{-}scheduler\text{-}def$

**apply**( $rule\ JVM\text{-}rr.round\text{-}robin\text{-}scheduler$ )

**apply**( $unfold\ eval\text{-}sc\text{-}mexec$ )

**apply**( $rule\ sc\text{-}execute.mexec\text{-}deterministic[OF\ assms\ sc\text{-}deterministic\text{-}heap\text{-}ops]$ )

**apply**(simp add: sc-spurious-wakeups)  
**done**

### 9.9.3 Random scheduler

**interpretation** *JVM-rnd*:  
*sc-random-scheduler-base*  
*JVM-final sc-mexec P convert-RA Jinja-output*  
**for** *P*  
**.**

**definition** *sc-rnd-JVM-start-state* :: *Random.seed*  $\Rightarrow$  *random-scheduler*  
**where** *sc-rnd-JVM-start-state seed* = *seed*

**definition** *exec-JVM-rnd* ::  
*Random.seed*  $\Rightarrow$  *addr jvm-prog*  $\Rightarrow$  *cname*  $\Rightarrow$  *mname*  $\Rightarrow$  *addr val list*  $\Rightarrow$   
*(thread-id*  $\times$  *(addr, thread-id) obs-event list,*  
*(addr, thread-id) locks*  $\times$  *((thread-id, addr jvm-thread-state*  $\times$  *addr released-locks) rm*  $\times$  *heap)*  $\times$   
*(thread-id, addr wait-set-status) rm*  $\times$  *thread-id rs) tllist*  
**where** *exec-JVM-rnd seed P C M vs* = *JVM-rnd.exec P (sc-rnd-JVM-start-state seed) (sc-jvm-start-state-refine*  
*P C M vs)*

**interpretation** *JVM-rnd*:  
*sc-random-scheduler*  
*JVM-final sc-mexec P convert-RA Jinja-output*  
**for** *P*  
**by**(*unfold-locales*)

**lemma** *JVM-rnd*:  
**assumes** *wf-jvm-prog <sub>$\Phi$</sub>  P*  
**shows**  
*sc-scheduler*  
*JVM-final (sc-mexec P) convert-RA*  
*(JVM-rnd.random-scheduler P) (pick-wakeup-via-sel ( $\lambda s P. rm-sel s (\lambda(k,v). P k v))) (\lambda -. True)$*   
*(sc-jvm-state-invar P  $\Phi$ )*  
**unfolding** *sc-scheduler-def*  
**apply**(*rule JVM-rnd.random-scheduler-scheduler*)  
**apply**(*unfold eval-sc-mexec*)  
**apply**(*rule sc-execute.mexec-deterministic[OF assms sc-deterministic-heap-ops]*)  
**apply**(simp add: sc-spurious-wakeups)  
**done**

**ML-val**  $\langle @\{\text{code } exec-JVM-rr\} \rangle$

**ML-val**  $\langle @\{\text{code } exec-JVM-rnd\} \rangle$

**end**

## 9.10 String representation of types

**theory** *ToString* **imports**  
*../J/Expr*  
*../JVM/JVMInstructions*

```

../Basic/JT-ICF
begin

class toString =
  fixes toString :: 'a ⇒ String.literal

instantiation bool :: toString begin
definition [code]: toString b = (case b of True ⇒ STR "True" | False ⇒ STR "False")
instance proof qed
end

instantiation char :: toString begin
definition [code]: toString (c :: char) = String.implode [c]
instance proof qed
end

instantiation String.literal :: toString begin
definition [code]: toString (s :: String.literal) = s
instance proof qed
end

fun list-toString :: String.literal ⇒ 'a :: toString list ⇒ String.literal list
where
  list-toString sep [] = []
| list-toString sep [x] = [toString x]
| list-toString sep (x#xs) = toString x # sep # list-toString sep xs

instantiation list :: (toString) toString begin
definition [code]:
  toString (xs :: 'a list) = sum-list (STR "[" # list-toString (STR ",") xs @ [STR "]" ])
instance proof qed
end

definition digit-toString :: int ⇒ String.literal
where
  digit-toString k = (if k = 0 then STR "0"
    else if k = 1 then STR "1"
    else if k = 2 then STR "2"
    else if k = 3 then STR "3"
    else if k = 4 then STR "4"
    else if k = 5 then STR "5"
    else if k = 6 then STR "6"
    else if k = 7 then STR "7"
    else if k = 8 then STR "8"
    else if k = 9 then STR "9"
    else undefined)

function int-toString :: int ⇒ String.literal list
where
  int-toString n =
    (if n < 0 then STR "-" # int-toString (- n)
    else if n < 10 then [digit-toString n]
    else int-toString (n div 10) @ [digit-toString (n mod 10)])
by pat-completeness simp

```

termination by *size-change*

**instantiation** *int* :: *toString* **begin**

**definition** [code]: *toString* *i* = *sum-list* (*int-toString* *i*)

**instance proof qed**

**end**

**instantiation** *nat* :: *toString* **begin**

**definition** [code]: *toString* *n* = *toString* (*int* *n*)

**instance proof qed**

**end**

**instantiation** *option* :: (*toString*) *toString* **begin**

**primrec** *toString-option* :: 'a *option*  $\Rightarrow$  *String.literal* **where**

*toString* *None* = *STR* "None"

| *toString* (*Some* *a*) = *sum-list* [*STR* "Some (", *toString* *a*, *STR* ")"]

**instance proof qed**

**end**

**instantiation** *finfun* :: ({*toString*, *card-UNIV*, *equal*, *linorder*}, *toString*) *toString* **begin**

**definition** [code]:

*toString* (*f* :: 'a  $\Rightarrow$  'b) =

*sum-list*

(*STR* "("

# *toString* (*finfun-default* *f*)

# *concat* (*map* ( $\lambda x$ . [*STR* ", ", *toString* *x*, *STR* "|->", *toString* (*f* \$ *x*)])) (*finfun-to-list* *f*))

@ [*STR* ")"])

**instance proof qed**

**end**

**instantiation** *word* :: (*len*) *toString* **begin**

**definition** [code]: *toString* (*w* :: 'a *word*) = *toString* (*sint* *w*)

**instance proof qed**

**end**

**instantiation** *fun* :: (*type*, *type*) *toString* **begin**

**definition** [code]: *toString* (*f* :: 'a  $\Rightarrow$  'b) = *STR* "fn"

**instance proof qed**

**end**

**instantiation** *val* :: (*toString*) *toString* **begin**

**fun** *toString-val* :: ('a :: *toString*) *val*  $\Rightarrow$  *String.literal*

**where**

*toString* *Unit* = *STR* "Unit"

| *toString* *Null* = *STR* "Null"

| *toString* (*Bool* *b*) = *sum-list* [*STR* "Bool ", *toString* *b*]

| *toString* (*Intg* *i*) = *sum-list* [*STR* "Intg ", *toString* *i*]

| *toString* (*Addr* *a*) = *sum-list* [*STR* "Addr ", *toString* *a*]

**instance proof qed**

**end**

**instantiation** *ty* :: *toString* **begin**

**primrec** *toString-ty* :: *ty*  $\Rightarrow$  *String.literal*

**where**

```

  toString Void = STR "Void"
| toString Boolean = STR "Boolean"
| toString Integer = STR "Integer"
| toString NT = STR "NT"
| toString (Class C) = sum-list [STR "Class ", toString C]
| toString (T[]) = sum-list [toString T, STR "[]"]
instance proof qed
end

```

```

instantiation bop :: toString begin
primrec toString-bop :: bop ⇒ String.literal where
  toString Eq = STR "=="
| toString NotEq = STR "!="
| toString LessThan = STR "<"
| toString LessOrEqual = STR "<="
| toString GreaterThan = STR ">"
| toString GreaterOrEqual = STR ">="
| toString Add = STR "+"
| toString Subtract = STR "-"
| toString Mult = STR "*"
| toString Div = STR "/"
| toString Mod = STR "%"
| toString BinAnd = STR "&"
| toString BinOr = STR "|"
| toString BinXor = STR "^"
| toString ShiftLeft = STR "<<"
| toString ShiftRightZeros = STR ">>"
| toString ShiftRightSigned = STR ">>>"
instance proof qed
end

```

```

instantiation addr-loc :: toString begin
primrec toString-addr-loc :: addr-loc ⇒ String.literal where
  toString (CField C F) = sum-list [STR "CField ", F, STR "{", C, STR "}"]
| toString (ACell n) = sum-list [STR "ACell ", toString n]
instance proof qed
end

```

```

instantiation htype :: toString begin
fun toString-htype :: htype ⇒ String.literal where
  toString (Class-type C) = C
| toString (Array-type T n) = sum-list [toString T, STR "[", toString n, STR "]"]
instance proof qed
end

```

```

instantiation obs-event :: (toString, toString) toString begin
primrec toString-obs-event :: ('a :: toString, 'b :: toString) obs-event ⇒ String.literal
where
  toString (ExternalCall ad M vs v) =
    sum-list [STR "ExternalCall ", M, STR "(", toString vs, STR ") = ", toString v]
| toString (ReadMem ad al v) =
  sum-list [STR "ReadMem ", toString ad, STR "@", toString al, STR "=", toString v]
| toString (WriteMem ad al v) =
  sum-list [STR "WriteMem ", toString ad, STR "@", toString al, STR "=", toString v]

```

```

| toString (NewHeapElem ad hT) = sum-list [STR "Allocate ", toString ad, STR ":", toString hT]
| toString (ThreadStart t) = sum-list [STR "ThreadStart ", toString t]
| toString (ThreadJoin t) = sum-list [STR "ThreadJoin ", toString t]
| toString (SyncLock ad) = sum-list [STR "SyncLock ", toString ad]
| toString (SyncUnlock ad) = sum-list [STR "SyncUnlock ", toString ad]
| toString (ObsInterrupt t) = sum-list [STR "Interrupt ", toString t]
| toString (ObsInterrupted t) = sum-list [STR "Interrupted ", toString t]
instance proof qed
end

instantiation prod :: (toString, toString) toString begin
definition toString = (λ(a, b). sum-list [STR "(" , toString a, STR ", ", toString b, STR ")"])
instance proof qed
end

instantiation fmod-ext :: (toString) toString begin
definition toString fd = sum-list [STR "{|volatile=", toString (volatile fd), STR ", ", toString
(fmod.more fd), STR "|}"']
instance proof qed
end

instantiation unit :: toString begin
definition toString (u :: unit) = STR "()"
instance proof qed
end

instantiation exp :: (toString, toString, toString) toString begin
fun toString-exp :: ('a :: toString, 'b :: toString, 'c :: toString) exp ⇒ String.literal
where
  toString (new C) = sum-list [STR "new ", C]
| toString (newArray T e) = sum-list [STR "new ", toString T, STR "[", toString e, STR "]"']
| toString (Cast T e) = sum-list [STR "(" , toString T, STR ") (" , toString e, STR ")"]
| toString (InstanceOf e T) = sum-list [STR "(" , toString e, STR ") instanceof ", toString T]
| toString (Val v) = sum-list [STR "Val (" , toString v, STR ")"]
| toString (e1 «bop» e2) = sum-list [STR "(" , toString e1, STR ") ", toString bop, STR " (" , toString
e2, STR ")"]
| toString (Var V) = sum-list [STR "Var ", toString V]
| toString (V := e) = sum-list [toString V, STR " := (" , toString e, STR ")"]
| toString (AAcc a i) = sum-list [STR "(" , toString a, STR ")[" , toString i, STR "]"']
| toString (AAss a i e) = sum-list [STR "(" , toString a, STR ")[" , toString i, STR "]" := (" , toString
e, STR ")"]
| toString (ALen a) = sum-list [STR "(" , toString a, STR ").length"]
| toString (FAcc e F D) = sum-list [STR "(" , toString e, STR ").", F, STR "{", D, STR "}"]
| toString (FAss e F D e') = sum-list [STR "(" , toString e, STR ").", F, STR "{", D, STR "} := (" , toString
e', STR ")"]
| toString (Call e M es) = sum-list ([STR "(" , toString e, STR ").", M, STR "("] @ map toString es
@ [STR ")"])
| toString (Block V T vo e) = sum-list ([STR "{", toString V, STR ":", toString T] @ (case vo of
None ⇒ [] | Some v ⇒ [STR "=", toString v]) @ [STR "; ", toString e, STR "]"])
| toString (Synchronized V e e') = sum-list [STR "synchronized-", toString V, STR "-(" , toString e,
STR ") {" , toString e', STR "}"]
| toString (InSynchronized V ad e) = sum-list [STR "insynchronized-", toString V, STR "-(" , toString
ad, STR ") {" , toString e, STR "}"]
| toString (e;;e') = sum-list [toString e, STR "; ", toString e']

```

```

| toString (if (e) e' else e'') = sum-list [STR "if (" , toString e, STR ") { " , toString e', STR " } else
{ " , toString e'', STR " }"]
| toString (while (e) e') = sum-list [STR "while (" , toString e, STR ") { " , toString e', STR " }"]
| toString (throw e) = sum-list [STR "throw (" , toString e, STR ")"]
| toString (try e catch (C V) e') = sum-list [STR "try { " , toString e, STR " } catch (" , C, STR " "
" , toString V, STR " ) { " , toString e', STR " }"]
instance proof qed
end

```

```

instantiation instr :: (toString) toString begin
primrec toString-instr :: 'a instr  $\Rightarrow$  String.literal where
  toString (Load i) = sum-list [STR "Load (" , toString i, STR ")"]
| toString (Store i) = sum-list [STR "Store (" , toString i, STR ")"]
| toString (Push v) = sum-list [STR "Push (" , toString v, STR ")"]
| toString (New C) = sum-list [STR "New " , toString C]
| toString (NewArray T) = sum-list [STR "NewArray " , toString T]
| toString ALoad = STR "ALoad"
| toString AStore = STR "AStore"
| toString ALength = STR "ALength"
| toString (Getfield F D) = sum-list [STR "Getfield " , toString F, STR " " , toString D]
| toString (Putfield F D) = sum-list [STR "Putfield " , toString F, STR " " , toString D]
| toString (Checkcast T) = sum-list [STR "Checkcast " , toString T]
| toString (Instanceof T) = sum-list [STR "Instanceof " , toString T]
| toString (Invoke M n) = sum-list [STR "Invoke " , toString M, STR " " , toString n]
| toString Return = STR "Return"
| toString Pop = STR "Pop"
| toString Dup = STR "Dup"
| toString Swap = STR "Swap"
| toString (BinOpInstr bop) = sum-list [STR "BinOpInstr " , toString bop]
| toString (Goto i) = sum-label [STR "Goto " , toString i]
| toString (IfFalse i) = sum-label [STR "IfFalse " , toString i]
| toString ThrowExc = STR "ThrowExc"
| toString MEnter = STR "monitorenter"
| toString MExit = STR "monitorexit"
instance proof qed
end

```

```

instantiation trie :: (toString, toString) toString begin
definition [code]: toString (t :: ('a, 'b) trie) = toString (tm-to-list t)
instance proof qed
end

```

```

instantiation rbt :: ({toString, linorder}, toString) toString begin
definition [code]:
  toString (t :: ('a, 'b) rbt) =
    sum-list (list-toString (STR "⌈" ) (rm-to-list t))
instance proof qed
end

```

```

instantiation assoc-list :: (toString, toString) toString begin
definition [code]: toString = toString  $\circ$  Assoc-List.impl-of
instance proof qed
end

```

```

code-printing
  class-instance String.literal :: toString  $\rightarrow$  (Haskell) -
end

```

## 9.11 Setup for converter Java2Jinja

```

theory Java2Jinja
imports
  Code-Generation
  ToString
begin

```

```

code-identifier
  code-module Java2Jinja  $\rightarrow$  (SML) Code-Generation

```

```

definition j-Program :: addr J-mb cdecl list  $\Rightarrow$  addr J-prog
where j-Program = Program

```

```

export-code wf-J-prog' j-Program in SML file  $\langle$ JWellForm.ML $\rangle$ 

```

Functions for extracting calls to the native print method

```

definition purge where
   $\bigwedge$ run.
  purge run =
    lmap ( $\lambda$ obs. case obs of ExternalCall - - (Cons (Intg i) -) v  $\Rightarrow$  i)
    (lfilter
      ( $\lambda$ obs. case obs of ExternalCall - M (Cons (Intg i) Nil) -  $\Rightarrow$  M = print | -  $\Rightarrow$  False)
      (lconcat (lmap (llist-of  $\circ$  snd) (llist-of-tllist run))))

```

Various other functions

```

instantiation heapobj :: toString begin
primrec toString-heapobj :: heapobj  $\Rightarrow$  String.literal where
  toString (Obj C fs) = sum-list [STR "(Obj ", toString C, STR ", ", toString fs, STR ")"]
| toString (Arr T si fs el) =
  sum-list [STR "([", toString si, STR "]", toString T, STR ", ", toString fs, STR ", ", toString
(map snd (rm-to-list el)), STR ")"]
instance proof qed
end

```

```

definition case-llist' where case-llist' = case-llist
definition case-tllist' where case-tllist' = case-tllist
definition terminal' where terminal' = terminal
definition llist-of-tllist' where llist-of-tllist' = llist-of-tllist
definition thr' where thr' = thr
definition shr' where shr' = shr

```

```

definition heap-toString :: heap  $\Rightarrow$  String.literal
where heap-toString = toString

```

```

definition thread-toString :: (thread-id, (addr expr  $\times$  addr locals)  $\times$  (addr  $\Rightarrow$  f nat)) rbt  $\Rightarrow$  String.literal
where thread-toString = toString

```

```

definition thread-toString' :: (thread-id, addr jvm-thread-state'  $\times$  (addr  $\Rightarrow$  f nat)) rbt  $\Rightarrow$  String.literal

```



**where** *thread-toString'* = *toString*

**definition** *trace-toString* :: *thread-id*  $\times$  (*addr*, *thread-id*) *obs-event list*  $\Rightarrow$  *String.literal*

**where** *trace-toString* = *toString*

**code-identifier**

**code-module** *Cardinality*  $\rightarrow$  (*SML*) *Set*

**code-module** *Code-Cardinality*  $\rightarrow$  (*SML*) *Set*

**code-module** *Conditionally-Complete-Lattices*  $\rightarrow$  (*SML*) *Set*

**code-module** *List*  $\rightarrow$  (*SML*) *Set*

**code-module** *Predicate*  $\rightarrow$  (*SML*) *Set*

**code-module** *Parity*  $\rightarrow$  (*SML*) *Bit-Operations*

**type-class** *semiring-parity*  $\rightarrow$  (*SML*) *Bit-Operations.semiring-parity*

**class-instance** *int* :: *semiring-parity*  $\rightarrow$  (*SML*) *Bit-Operations.semiring-parity-int*

**class-instance** *int* :: *ring-parity*  $\rightarrow$  (*SML*) *Bit-Operations.semiring-parity-int*

**constant** *member-i-i*  $\rightarrow$  (*SML*) *Set.member-i-i*

**export-code**

*wf-J-prog'* *exec-J-rr* *exec-J-rnd*

*j-Program*

*purge case-llist'* *case-tllist'* *terminal'* *llist-of-tllist'*

*thr'* *shr'* *heap-toString* *thread-toString* *trace-toString*

**in** *SML*

**file**  $\langle J\text{-Execute}.ML \rangle$

**definition** *j2jvm* :: *addr J-prog*  $\Rightarrow$  *addr jvm-prog* **where** *j2jvm* = *J2JVM*

**export-code**

*wf-jvm-prog'* *exec-JVM-rr* *exec-JVM-rnd* *j2jvm*

*j-Program*

*purge case-llist'* *case-tllist'* *terminal'* *llist-of-tllist'*

*thr'* *shr'* *heap-toString* *thread-toString'* *trace-toString*

**in** *SML*

**file**  $\langle JVM\text{-Execute2}.ML \rangle$

**end**

**theory** *Execute-Main*

**imports**

*SC-Schedulers*

*PCompilerRefine*

*Code-Generation*

*JVM-Execute*

*Java2Jinja*

**begin**

**end**



# Chapter 10

## Examples

### 10.1 Apprentice challenge

```
theory ApprenticeChallenge
imports
  ../Execute/Code-Generation
begin
```

This theory implements the apprentice challenge by Porter and Moore [5].

```
definition ThreadC :: addr J-mb cdecl
where
  ThreadC =
    (Thread, Object, [],
     [(run, [], Void, [([], unit)]),
      (start, [], Void, Native),
      (join, [], Void, Native),
      (interrupt, [], Void, Native),
      (isInterrupted, [], Boolean, Native)])
```

```
definition Container :: cname
where Container = STR "Container"
```

```
definition ContainerC :: addr J-mb cdecl
where ContainerC = (Container, Object, [(STR "counter", Integer, (volatile=False))], [])
```

```
definition String :: cname
where String = STR "String"
```

```
definition StringC :: addr J-mb cdecl
where
  StringC = (String, Object, [], [])
```

```
definition Job :: cname
where Job = STR "Job"
```

```
definition JobC :: addr J-mb cdecl
where
  JobC =
    (Job, Thread, [(STR "objref", Class Container, (volatile=False))],
     [(STR "incr", [], Class Job, [([],
```

```

    sync(Var (STR "objref"))
    ((Var (STR "objref"))•STR "counter"{STR ""} := ((Var (STR "objref"))•STR "counter"{STR
""} «Add» Val (Intg 1))));
    Var this));
    (STR "setref", [Class Container], Void, ⌊([STR "o"],
    LAss (STR "objref") (Var (STR "o")))),
    (run, [], Void, ⌊([,
    while (true) (Var this•STR "incr"([])))]
    ])

```

**definition** *Apprentice* :: *cname*  
**where** *Apprentice* = STR "Apprentice"

**definition** *ApprenticeC* :: *addr J-mb cdecl*  
**where**  
*ApprenticeC* =  
(*Apprentice*, *Object*, [],  
⌊([STR "main", [Class String[]], Void, ⌊([STR "args",  
{STR "container":Class Container=None;  
(STR "container" := new Container);;  
(while (true)  
{STR "job":Class Job=None;  
(STR "job" := new Job);;  
(Var (STR "job")•STR "setref"([Var (STR "container")])));;  
(Var (STR "job")•Type.start([]))  
}
)
}]]))

**definition** *ApprenticeChallenge*  
**where**  
*ApprenticeChallenge* = Program (SystemClasses @ [StringC, ThreadC, ContainerC, JobC, Apprent-  
ticeC])

**definition** *ApprenticeChallenge-annotated*  
**where** *ApprenticeChallenge-annotated* = *annotate-prog-code* *ApprenticeChallenge*

**lemma** *wf-J-prog* *ApprenticeChallenge-annotated*  
**by** *eval*

**lemmas** [code-unfold] =  
Container-def Job-def String-def Apprentice-def

**definition** *main* :: *String.literal* **where** *main* = STR "main"

**ML-val** <  
*val* - = *tracing started*;  
*val* *program* = @{code *ApprenticeChallenge-annotated*};  
*val* - = *tracing prg*;  
*val* *compiled* = @{code J2JVM} *program*;  
*val* - = *tracing compiled*;  
  
@{code *exec-J-rr*}  
@{code 1 :: nat}

```

    program
    @{code Apprentice}
    @{code main}
    [ @{code Null}];

    val - = tracing J-rr;
    @{code exec-JVM-rr}
    @{code 1 :: nat}
    compiled
    @{code Apprentice}
    @{code main}
    [ @{code Null}];
    val - = tracing JVM-rr;
  }

end

```

## 10.2 Buffer example

```

theory BufferExample imports
  ../Execute/Code-Generation
begin

```

```

definition ThreadC :: addr J-mb cdecl
where
  ThreadC =
    (Thread, Object, [],
     [(run, [], Void, [([], unit)]),
      (start, [], Void, Native),
      (join, [], Void, Native),
      (interrupt, [], Void, Native),
      (isInterrupted, [], Boolean, Native)])

```

```

definition IntegerC :: addr J-mb cdecl
where IntegerC = (STR "Integer", Object, [(STR "value", Integer, (volatile=False))], [])

```

```

definition Buffer :: cname
where Buffer = STR "Buffer"

```

```

definition BufferC :: addr J-mb cdecl
where
  BufferC =
    (Buffer, Object,
     [(STR "buffer", Class Object[], (volatile=False)),
      (STR "front", Integer, (volatile=False)),
      (STR "back", Integer, (volatile=False)),
      (STR "size", Integer, (volatile=False))],
     [(STR "constructor", [Integer], Void, [(STR "size",
      (STR "buffer" := newA (Class Object)[ Var (STR "size")]);
      (STR "front" := Val (Intg 0));
      (STR "back" := Val (Intg (- 1))));
      (Var this.(STR "size"){STR ""} := Val (Intg 0)))]),
     (STR "empty", [], Boolean, [([], sync(Var this) (Var (STR "size") «Eq» Val (Intg 0)))]),

```

```

(STR "full", [], Boolean, [([]),
  sync(Var this) (Var (STR "size") «Eq» ((Var (STR "buffer")).length))),
(STR "get", [], Class Object, [([]),
  sync(Var this) (
    (while (Var this.(STR "empty")())
      (try (Var this.wait()) catch(InterruptedExceptio (STR "e") unit));
    (STR "size" := (Var (STR "size") «Subtract» Val (Intg 1))));
  {(STR "result"):Class Object=None;
    ((STR "result") := ((Var (STR "buffer"))[Var (STR "front")]));
    (STR "front" := (Var (STR "front") «Add» Val (Intg 1))));
    if ((Var (STR "front")) «Eq» ((Var (STR "buffer")).length))
      (STR "front" := Val (Intg 0))
    else unit);;
    (Var this.notifyAll());;
    Var (STR "result")
  }
  )]),
(STR "put", [Class Object], Void, [(STR "o"],
  sync(Var this) (
    (while (Var this.STR "full"())
      (try (Var this.wait()) catch(InterruptedExceptio STR "e") unit));
    (STR "back" := (Var (STR "back") «Add» Val (Intg 1))));
    if (Var (STR "back") «Eq» ((Var (STR "buffer")).length))
      (STR "back" := Val (Intg 0))
    else unit);;
    (AAss (Var (STR "buffer")) (Var (STR "back")) (Var (STR "o")));;
    (STR "size" := ((Var (STR "size") «Add» Val (Intg 1))));
    (Var this.notifyAll())
  )])
])

```

**definition** *Producer* :: cname  
**where** *Producer* = STR "Producer"

**definition** *ProducerC* :: int  $\Rightarrow$  addr J-mb cdecl  
**where**  
*ProducerC* n =  
 (Producer, Thread, [(STR "buffer", Class Buffer, (volatile=False))],  
 [(run, [], Void, [([]),  
 {STR "i":Integer=[Intg 0];  
 while (Var (STR "i") «NotEq» Val (Intg (word-of-int n))) (
 (Var (STR "buffer")).STR "put"([STR "temp":Class (STR "Integer")=None; (STR "temp"  
:= new (STR "Integer");; ((FAss (Var (STR "temp")) (STR "value") (STR "")) (Var (STR "i")));;  
Var (STR "temp")))] } ]));  
 STR "i" := (Var (STR "i") «Add» (Val (Intg 1)))]  
 ]))])

**definition** *Consumer* :: cname  
**where** *Consumer* = STR "Consumer"

**definition** *ConsumerC* :: int  $\Rightarrow$  addr J-mb cdecl  
**where**  
*ConsumerC* n =  
 (Consumer, Thread, [(STR "buffer", Class Buffer, (volatile=False))],

```

[(run, [], Void, [([,
  {STR "i":Integer=[Intg 0];
  while (Var (STR "i") «NotEq» Val (Intg (word-of-int n))) (
    {STR "o":Class Object=None;
    Seq (STR "o" := ((Var (STR "buffer"))•STR "get"([])))
      (STR "i" := (Var (STR "i") «Add» Val (Intg 1))))}
  )]]))

```

**definition** *String* :: *cname*  
**where** *String* = STR "String"

**definition** *StringC* :: *addr J-mb cdecl*  
**where**  
*StringC* = (*String*, *Object*, [], [])

**definition** *Test* :: *cname*  
**where** *Test* = STR "Test"

**definition** *TestC* :: *addr J-mb cdecl*  
**where**  
*TestC* =  
(*Test*, *Object*, [],  
[(STR "main", [Class String[]], Void, [([STR "args",  
 {STR "b":Class Buffer=None; (STR "b" := new Buffer);;  
 (Var (STR "b")•STR "constructor"([Val (Intg 10)]));;  
 {STR "p":Class Producer=None; STR "p" := new Producer;;  
 {STR "c":Class Consumer=None;  
 (STR "c" := new Consumer);;  
 (Var (STR "c")•STR "buffer"{STR ""} := Var (STR "b"));;  
 (Var (STR "p")•STR "buffer"{STR ""} := Var (STR "b"));;  
 (Var (STR "c")•Type.start([]));(Var (STR "p")•Type.start([]))
 }
 }
 ])]))

**definition** *BufferExample*  
**where**  
*BufferExample* *n* = Program (SystemClasses @ [ThreadC, StringC, IntegerC, BufferC, ProducerC  
*n*, ConsumerC *n*, TestC])

**definition** *BufferExample-annotated*  
**where**  
*BufferExample-annotated* *n* = annotate-prog-code (*BufferExample* *n*)

**lemmas** [code-unfold] =  
IntegerC-def Buffer-def Producer-def Consumer-def Test-def  
String-def

**lemma** *wf-J-prog* (*BufferExample-annotated* 10)  
**by** *eval*

**definition** *main* **where** *main* = STR "main"  
**definition** *five* :: *int* **where** *five* = 5

**ML-val** <

```

    val program = @{\code BufferExample-annotated} @{\code five};
    val compiled = @{\code J2JVM} program;

```

```

    val run =
      @{\code exec-J-rr}
      @{\code 1 :: nat}
      program
      @{\code Test}
      @{\code main}
      [ @{\code Null}];
    val - = @{\code terminal} run;

```

```

    val jvm-run =
      @{\code exec-JVM-rr}
      @{\code 1 :: nat}
      compiled
      @{\code Test}
      @{\code main}
      [ @{\code Null}];
    val - = @{\code terminal} run;

```

&gt;

**end****theory** *Examples-Main***imports***ApprenticeChallenge**BufferExample***begin****end****theory** *JinjaThreads***imports***Basic/Basic-Main**Common/Common-Main**J/J-Main**JVM/JVM-Main**BV/BV-Main**Compiler/Compiler-Main**MM/MM-Main**Execute/Execute-Main**Examples/Examples-Main***begin****end**



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